

Layout Note:

All SDRAM traces 50 ohm controlled impedance, surface routed, 6 mil, 1/2 oz copper.
Place r232,r233,c360 as close to pins of U9 as possible.
Minimize distance of PCI_CLK between Chips(1 inch). Route in a Daisy chain w/minimized stubbs
For U9, Minplane of 2.5v under chip, 1 via per ball on GND/Power pins
All Nets, including No connect pins shall be routed to a test pad.

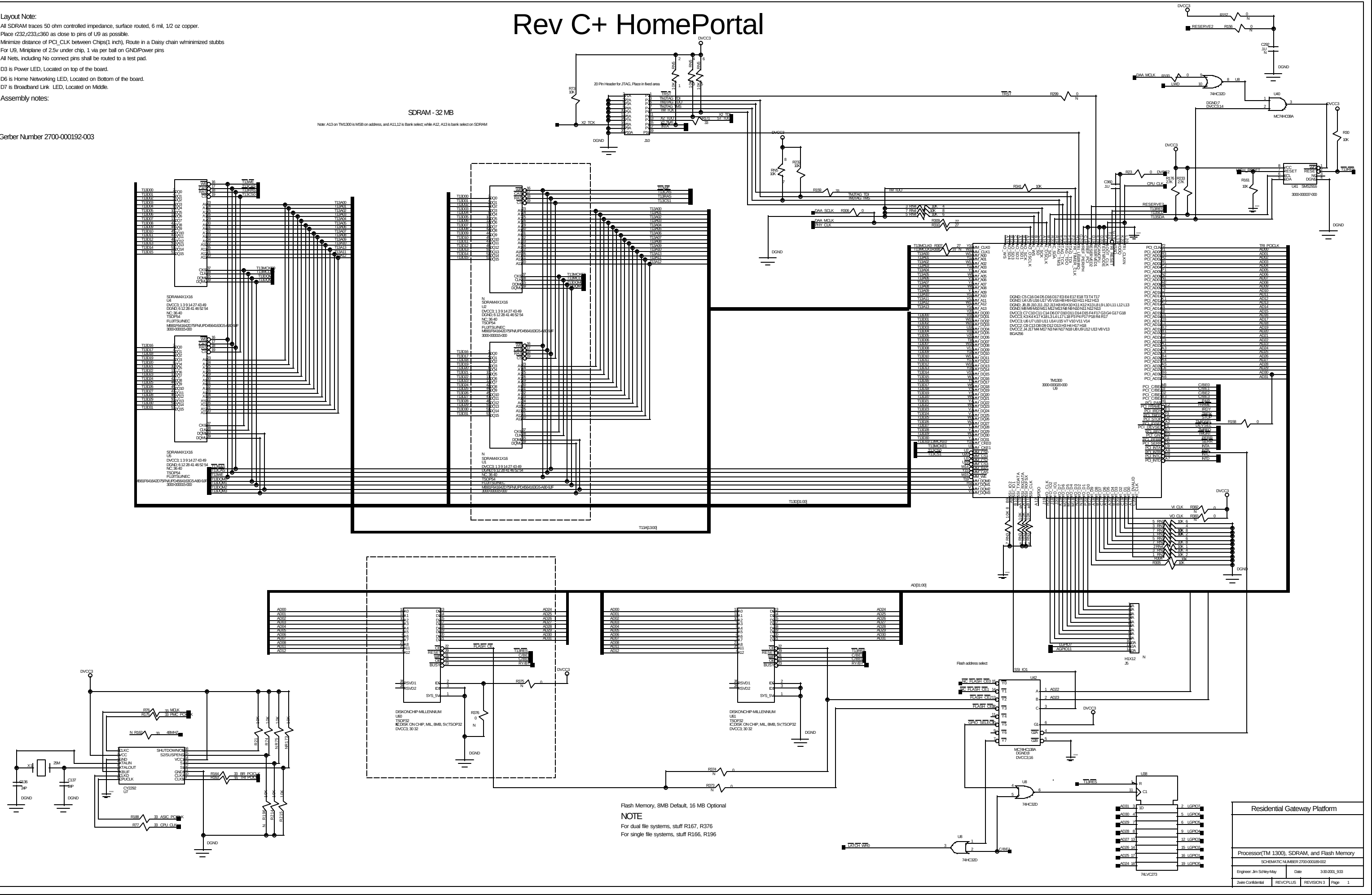
D3 is Power LED, Located on top of the board.

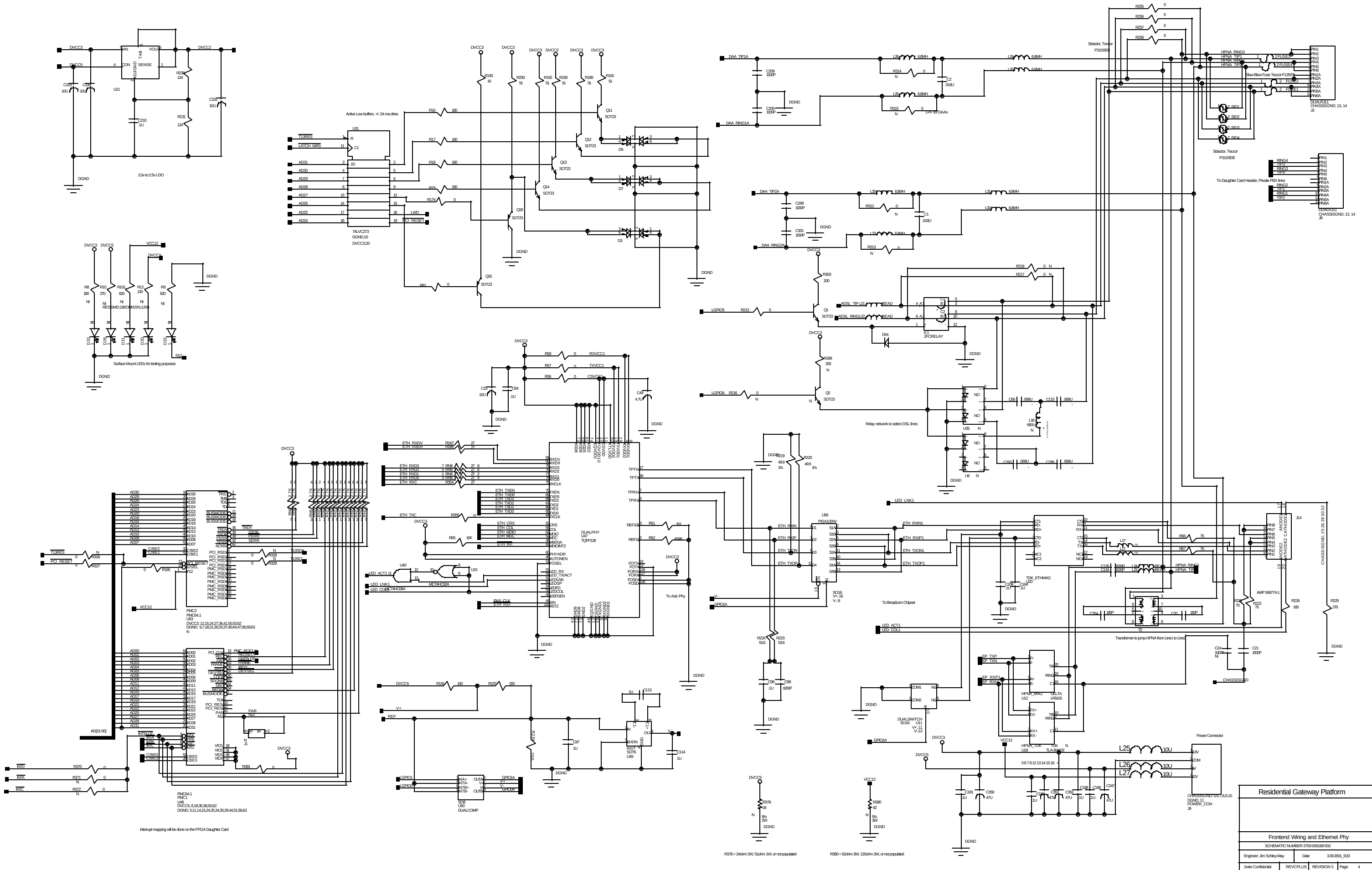
D6 is Home Networking LED, Located on Bottom of the board.

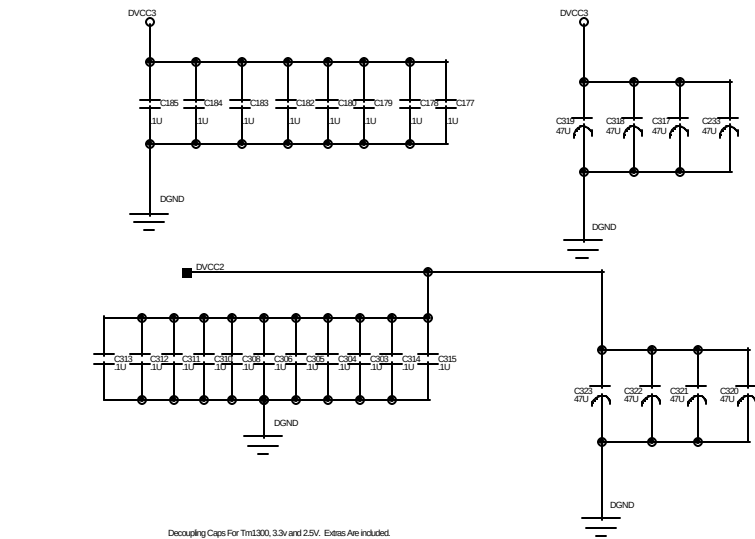
D7 is Broadband Link LED, Located on Middle.

Assembly notes:

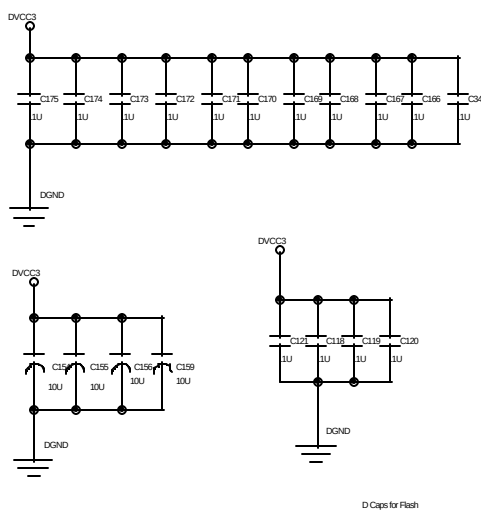
Gerber Number 2700-000192-003



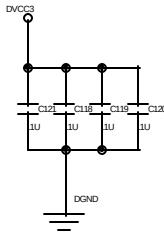




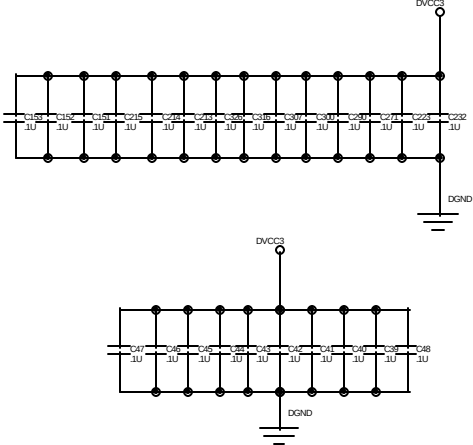
Decoupling Caps For Tm3300, 3.3v and 2.5v. Extras Are Included.



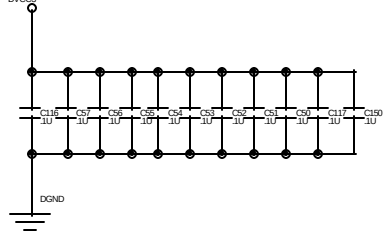
Decoupling Caps For SDRAM



D Caps for Flash



Decoupling Caps For Epigram



Decoupling Caps For ASIC

