

ALINCO

Alinco, Inc. Electronics Division

Yodoyabashi Dai Bldg. 13F, 4-4-9 Koraibashi, Chuo-ku, Osaka 541-0043 Japan

Tel: 0081-6-7636-2360 Fax: 0081-6-6208-3801

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Federal Communications Commissions

RE: PH3 DJNX40T / 800MHz analog cellular telephone band blocking

Dear Sir or Madam,

This is to declare that the device in application PH3 DJNX40T has been blocked for any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz.

The device uses a double super heterodyne for FM as receiver circuits and frequencies are generated by a PLL synthesizer circuitry. The first local oscillation frequencies are determined by the N value data of the CPU, and such values can't be changed by any means.

Receiving Freq.range (MHz)	1 st local oscillation freq. (MHz)	Image freq. (MHz)
450.000~512.000	399.150~461.150	348.300~ 410.300

The band-pass filters and tuning circuits are used to filter out the unwanted bands.

The CPU used in this device, our parts code XA1511, vender's code R5F64112DFB#U0 is exclusively programmed and burned for this US export model. Alinco, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the declared cellular frequencies.

To my best of knowledge being informed by the chief-engineer in charge of PH3 DJNX40T, above declared is true.

Sincerely,



Mitsuru Matsumoto

General manager, Production Section,
Alinco, Inc. Electronics Division