

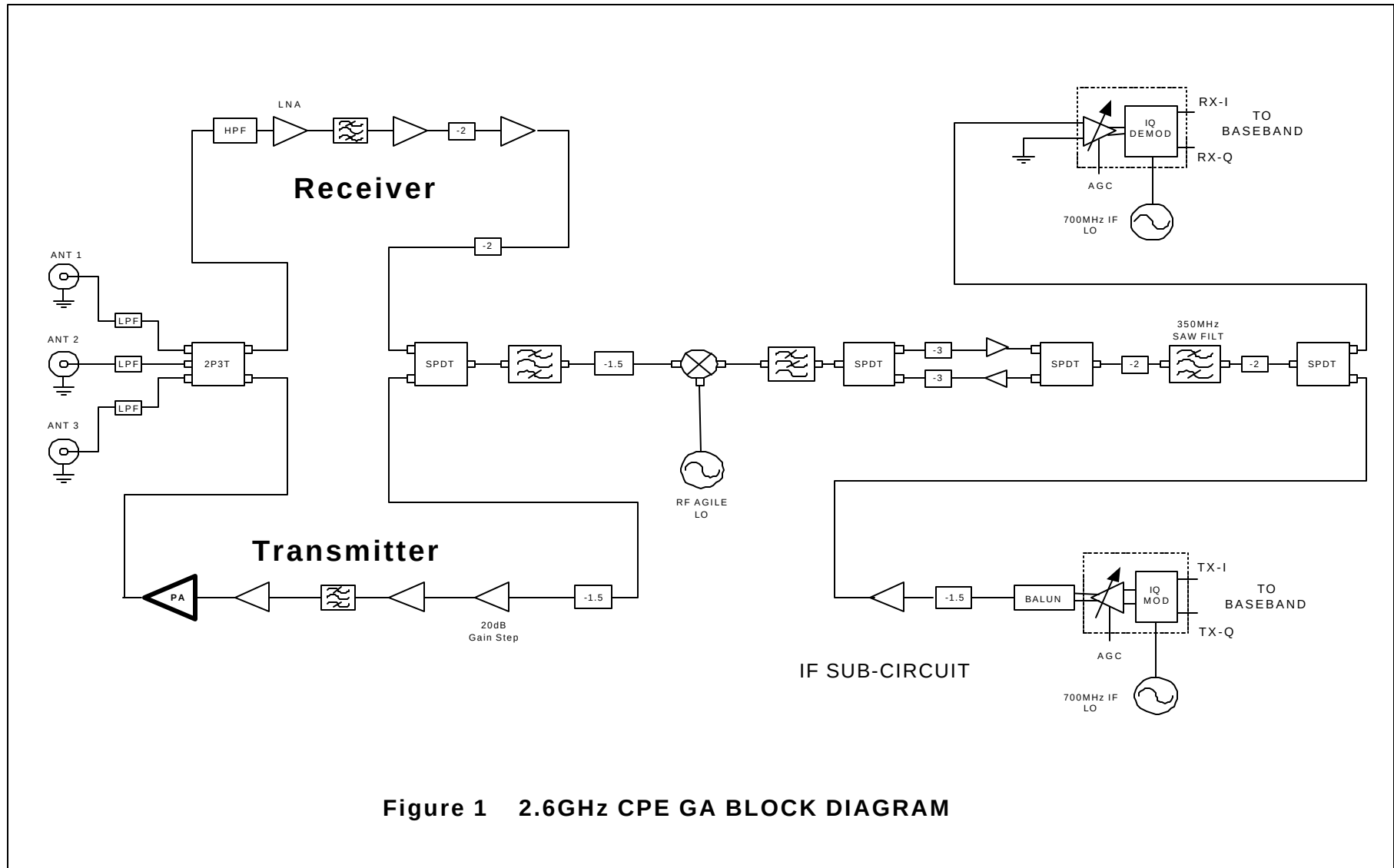


Figure 1 2.6GHz CPE GA BLOCK DIAGRAM