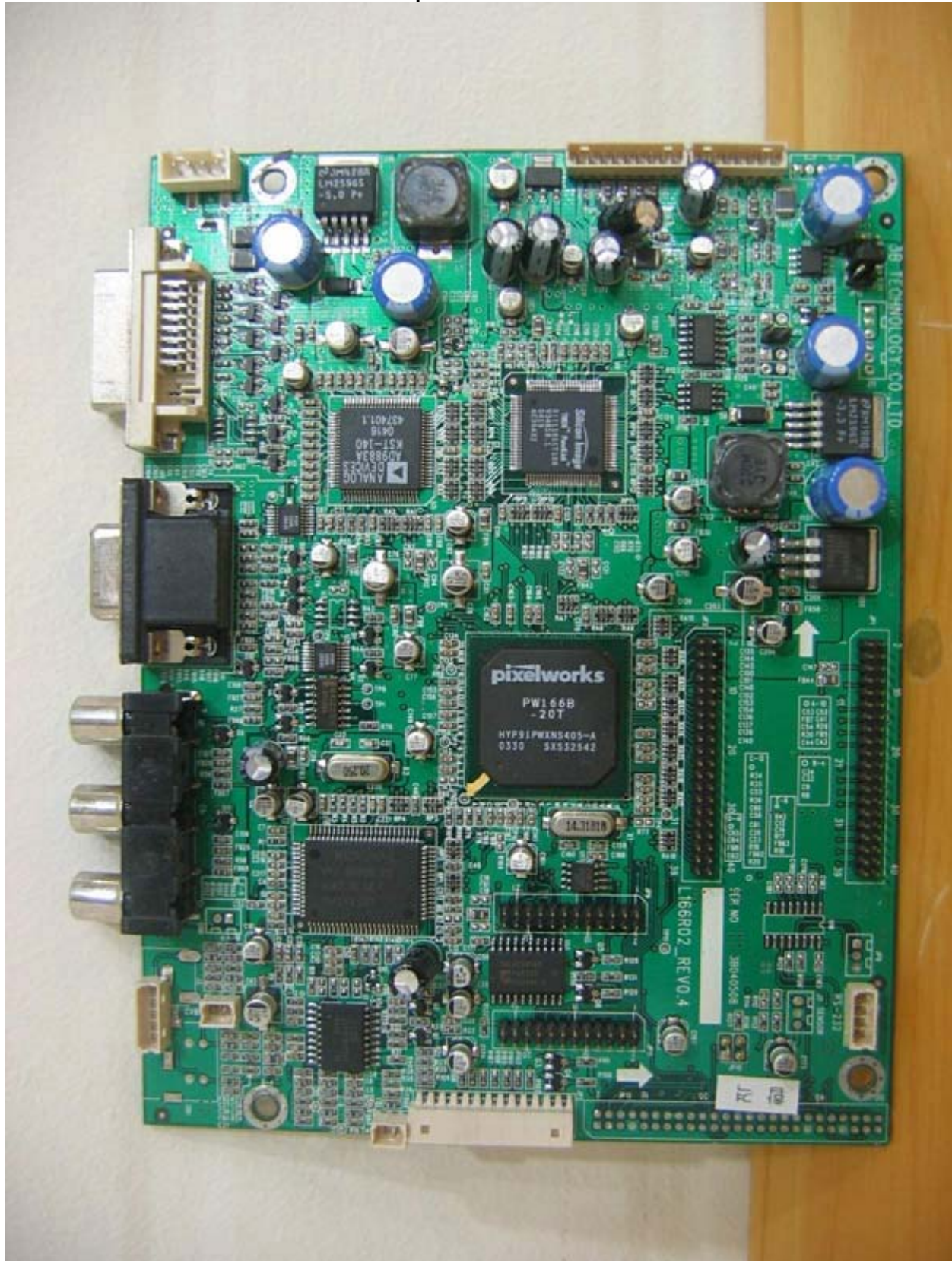
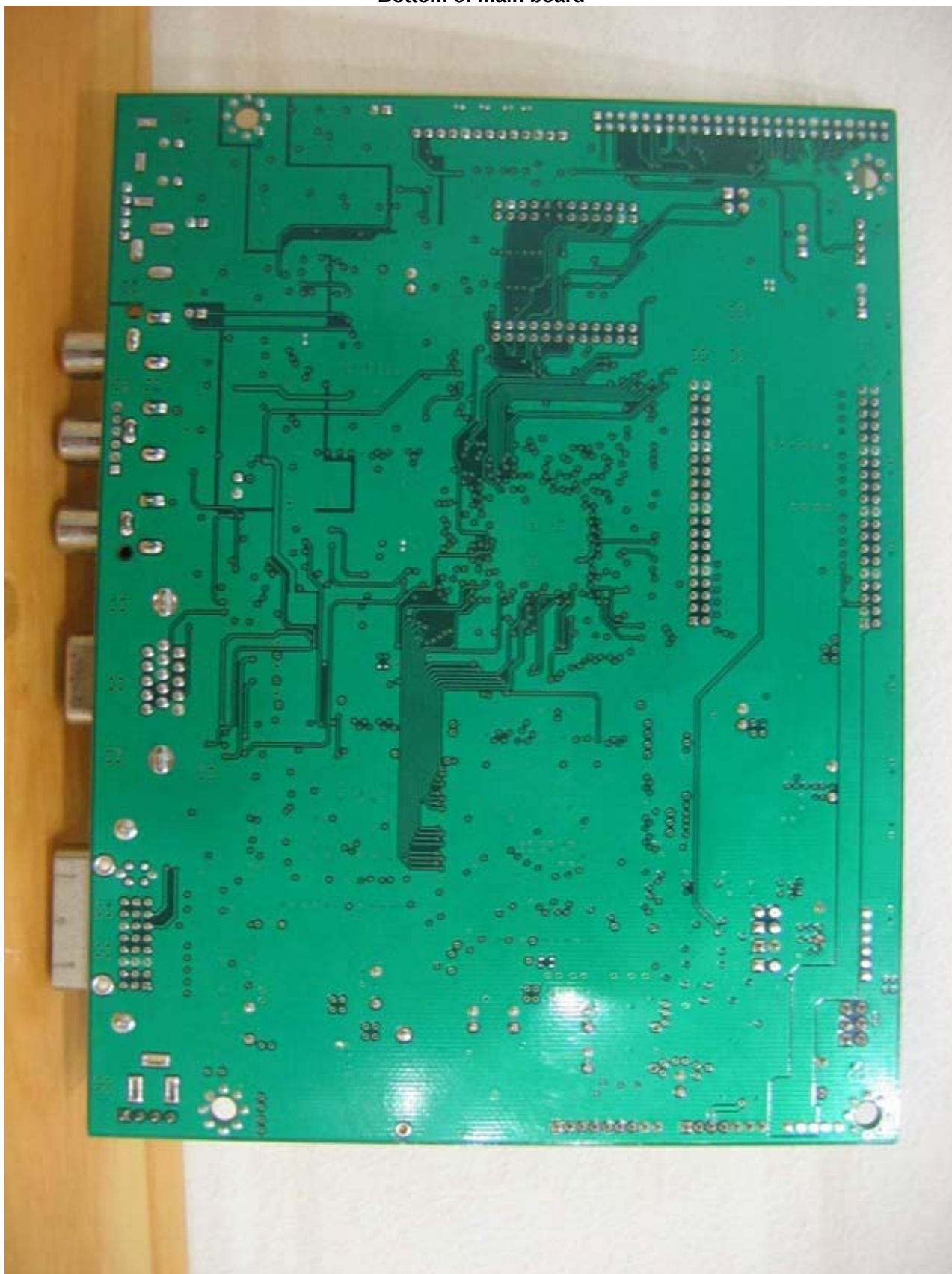


Appendix 4. Internal Photographs of EUT

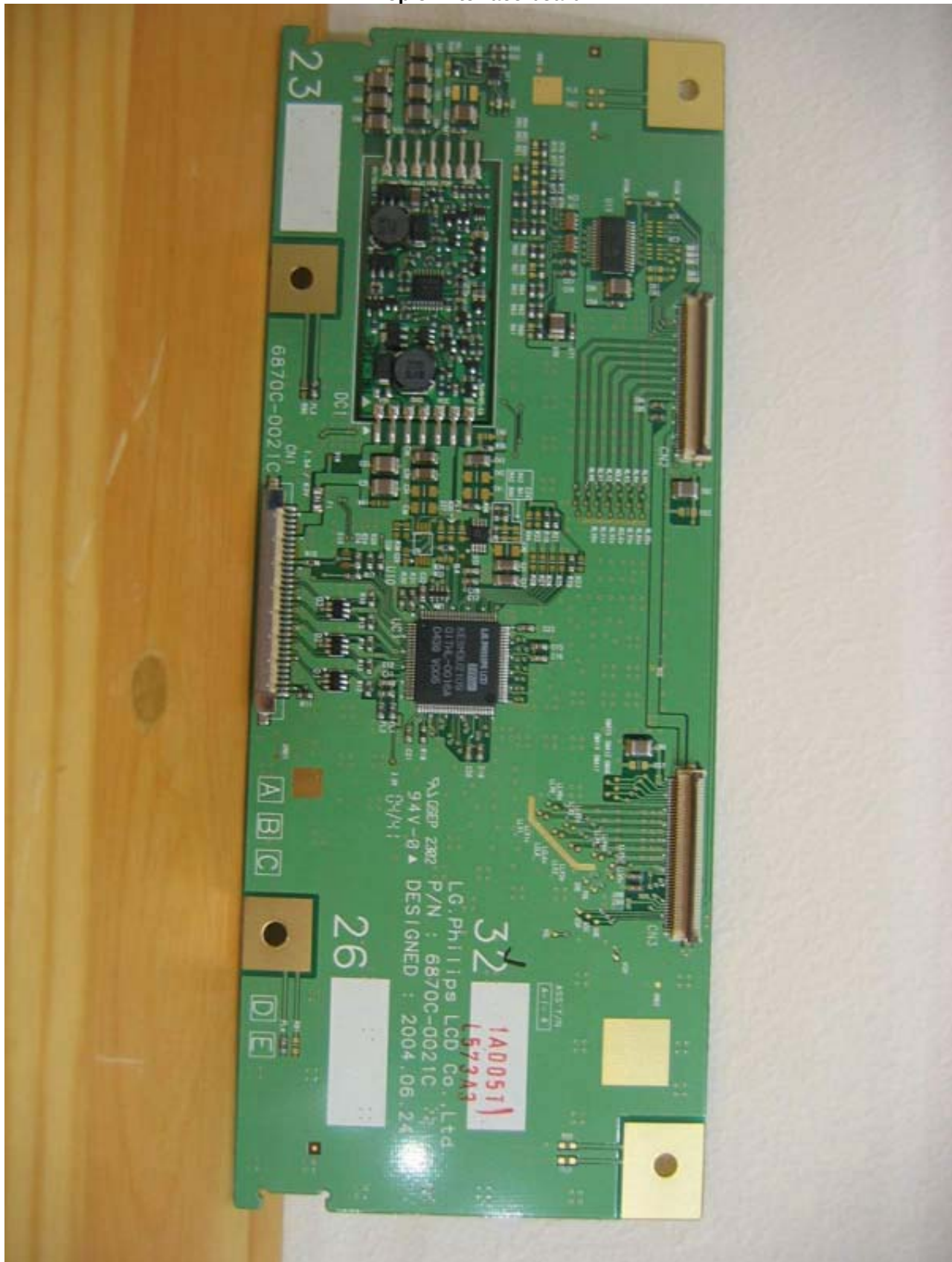
Top of main board



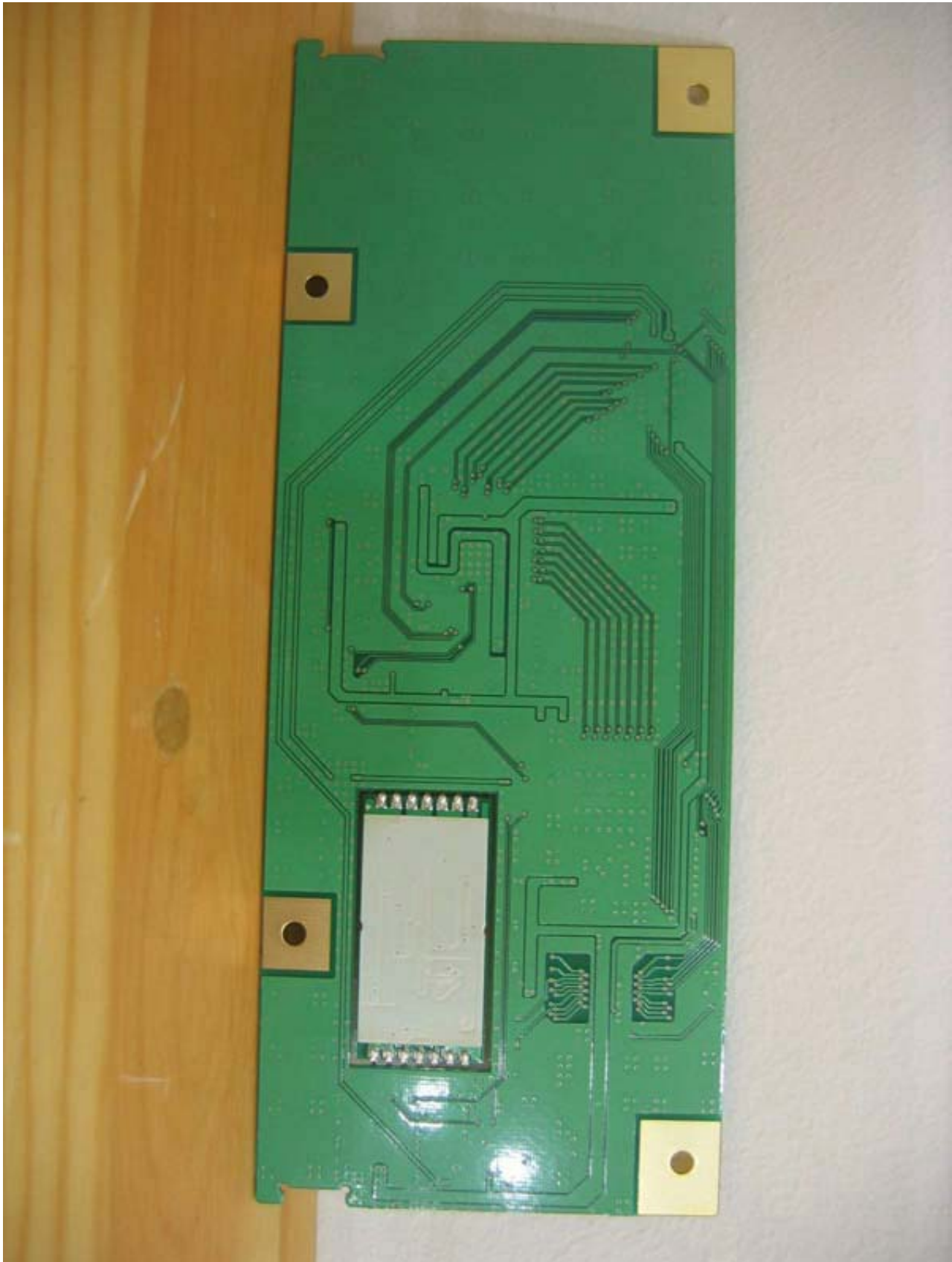
Bottom of main board



Top of interface board



Bottom of interface board



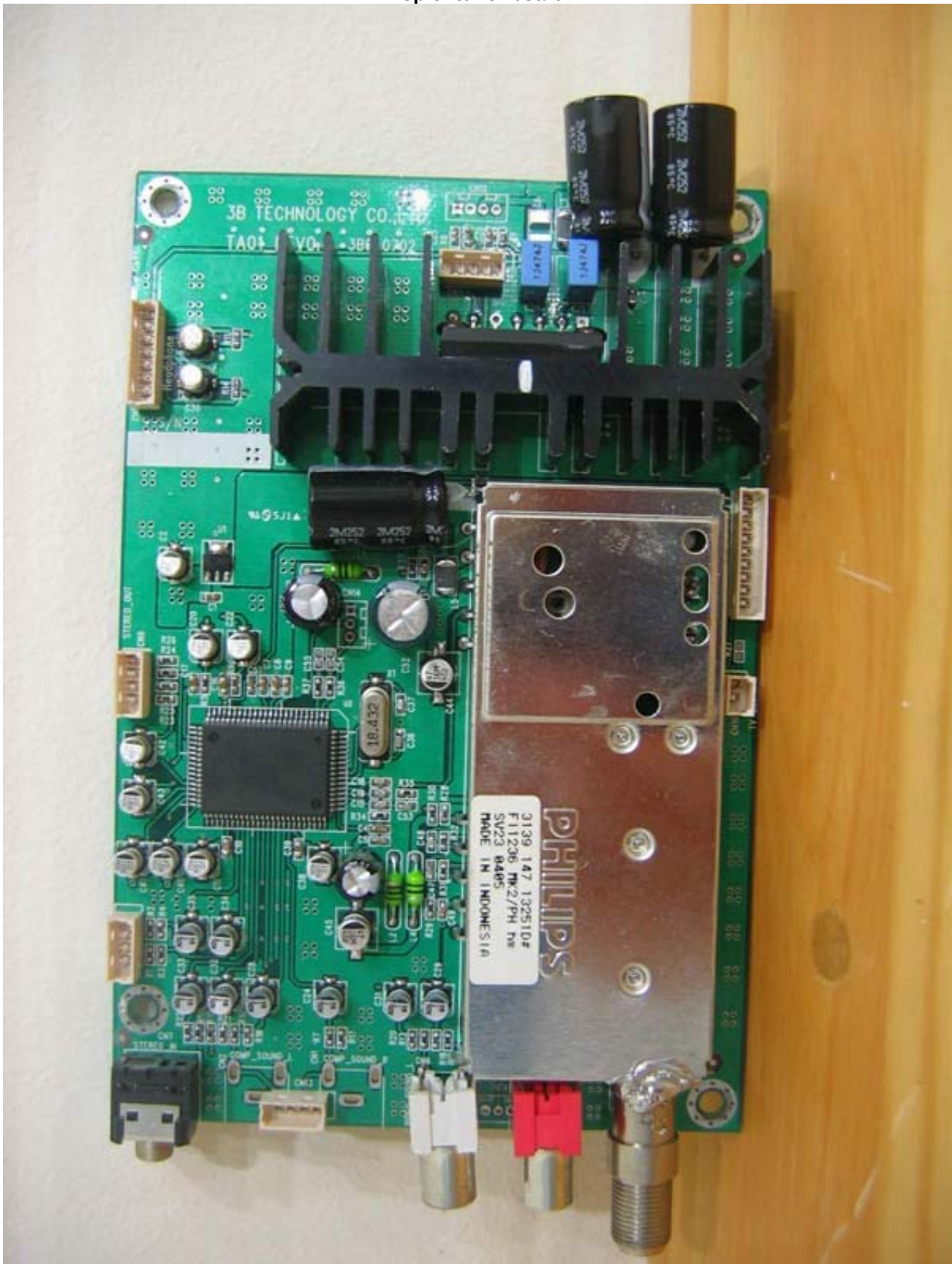
Top of interface board



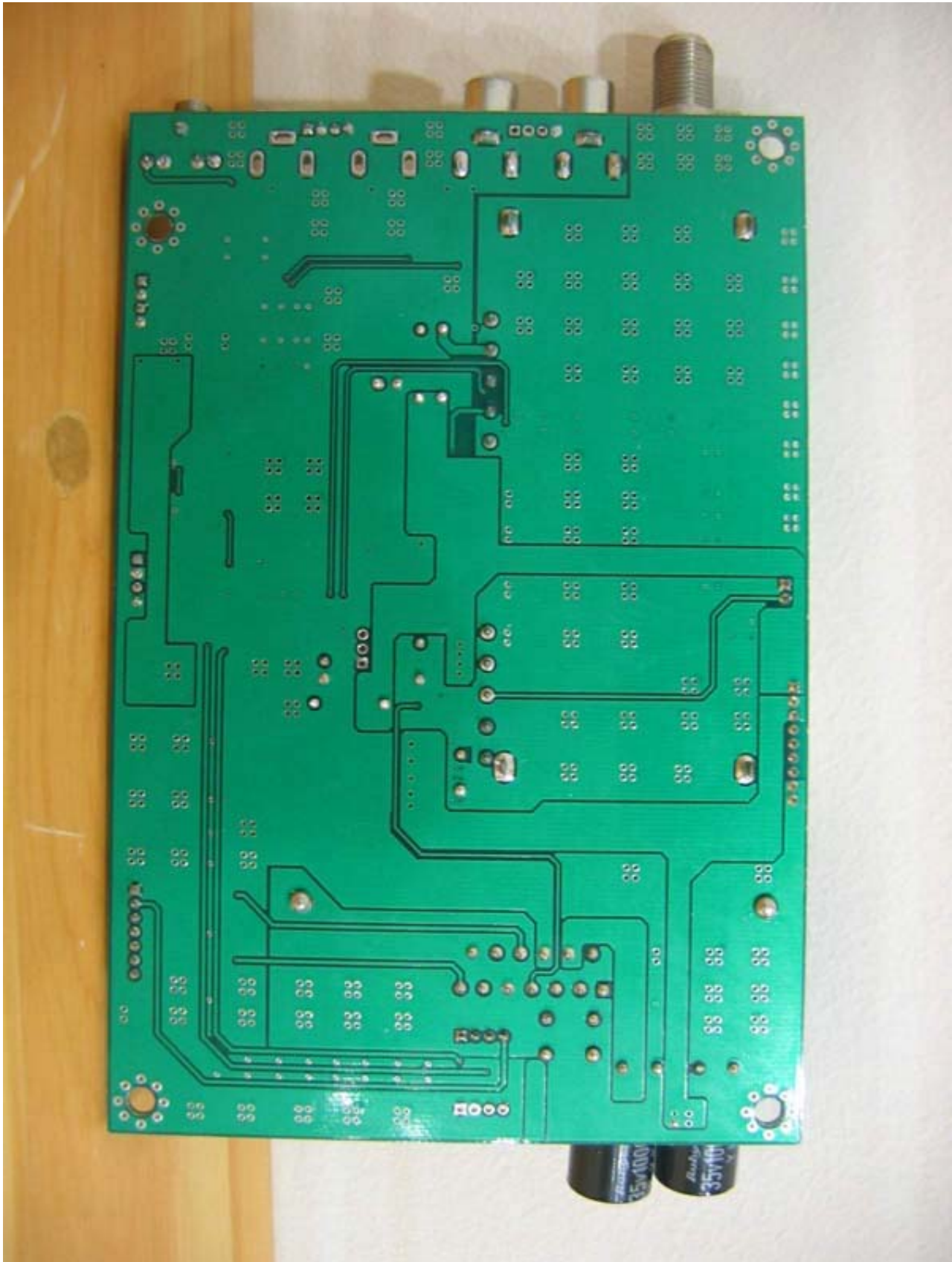
Bottom of interface board



Top of tuner board



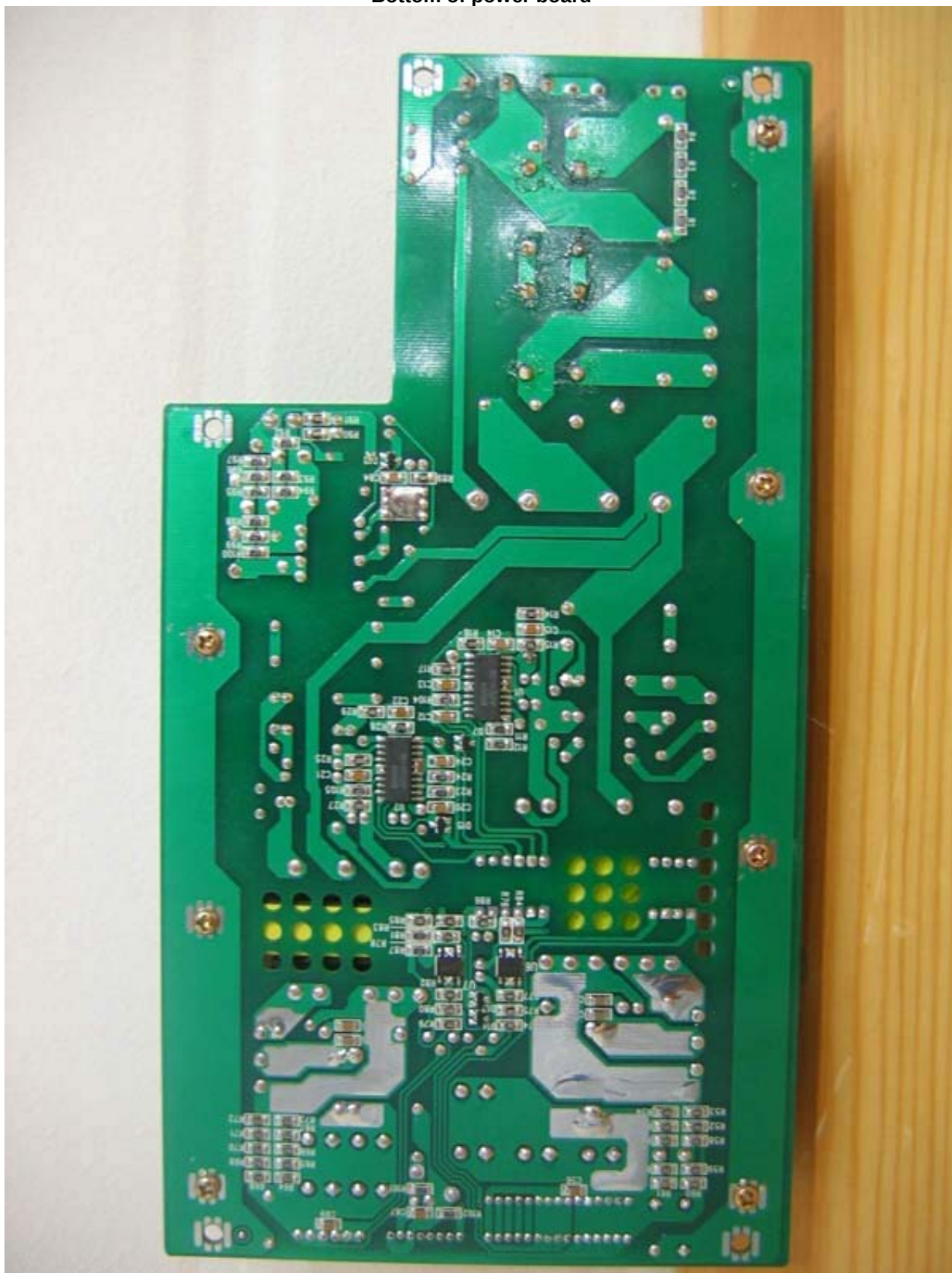
Bottom of tuner board



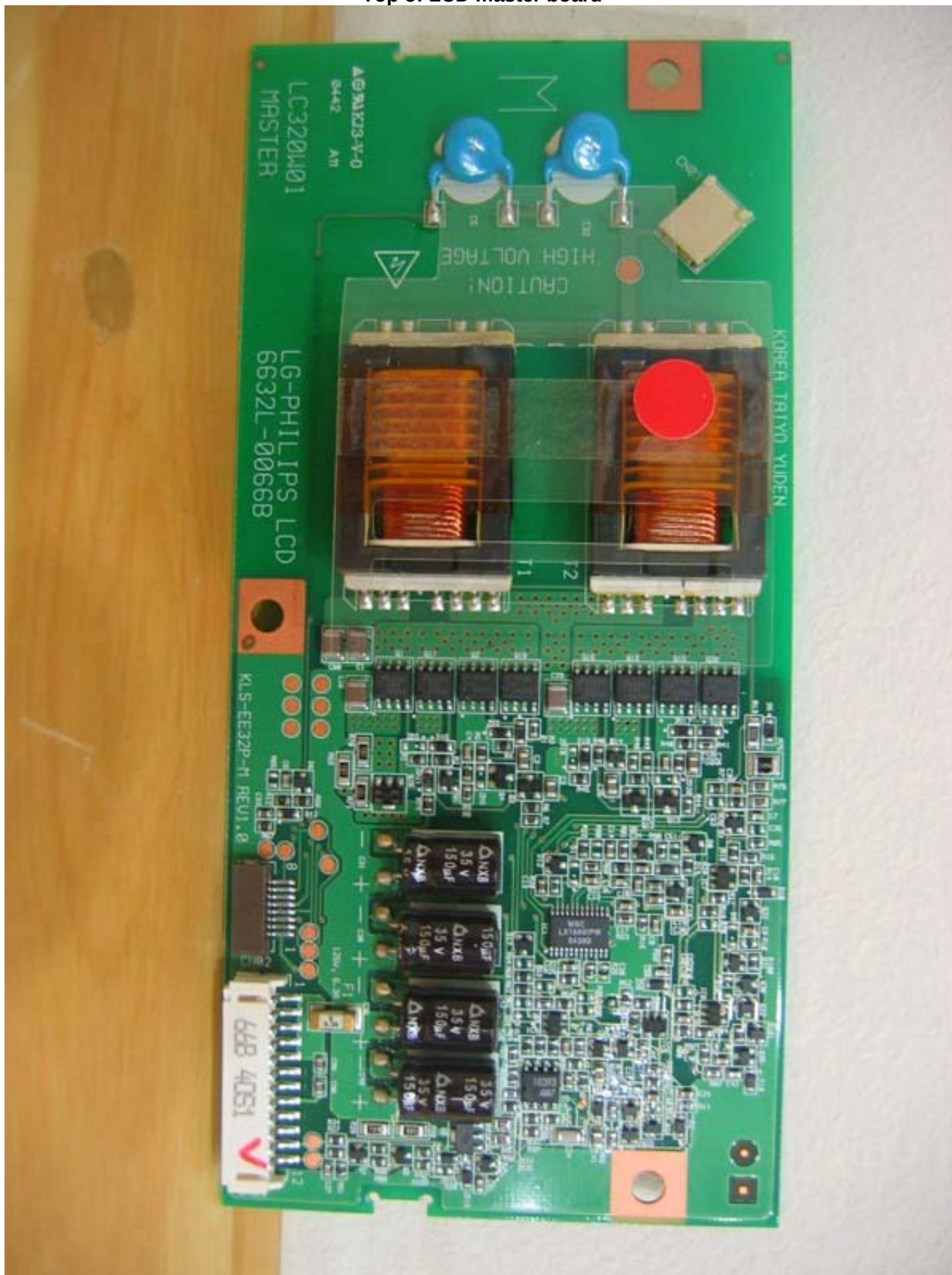
Top of power board



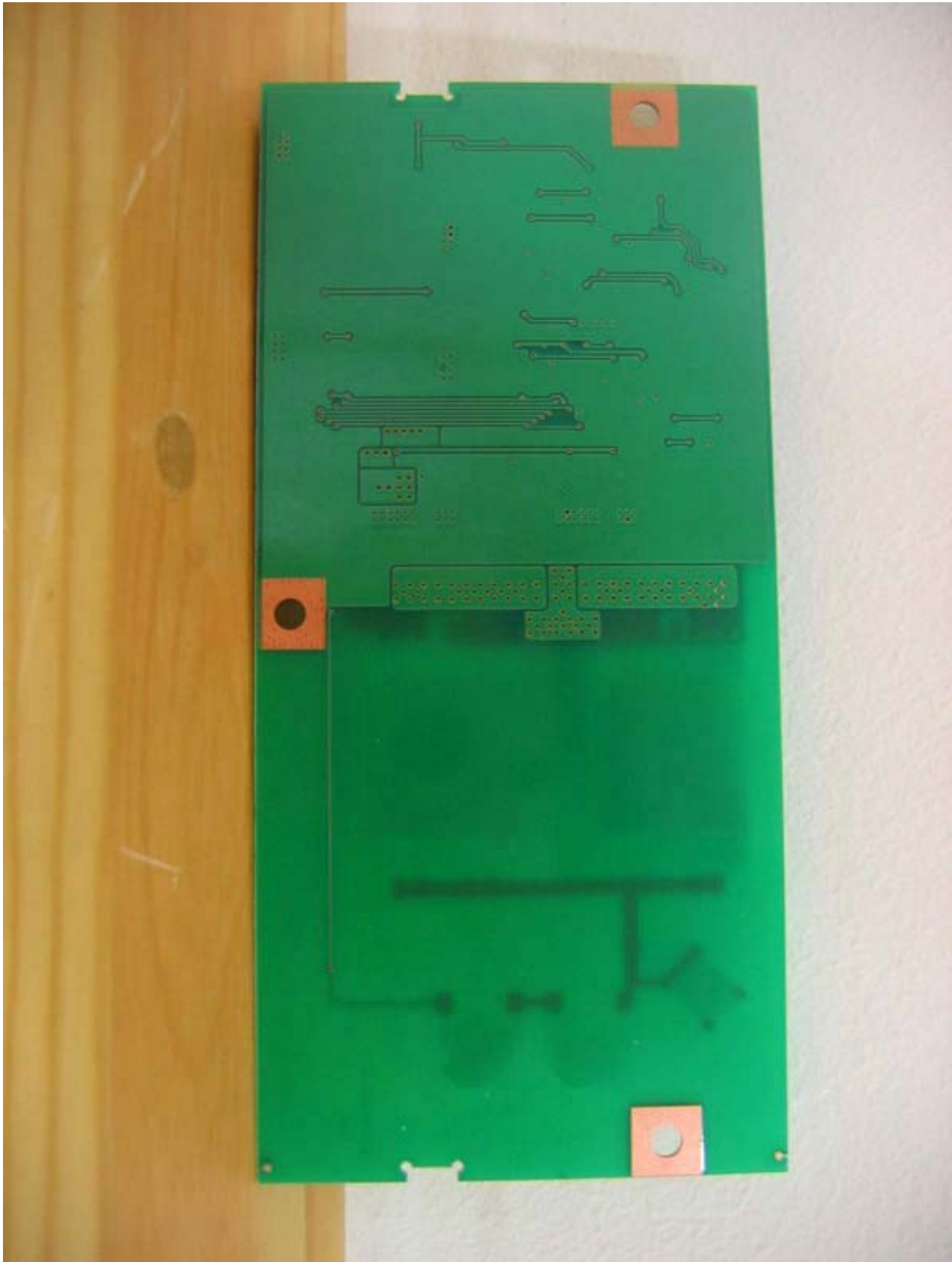
Bottom of power board



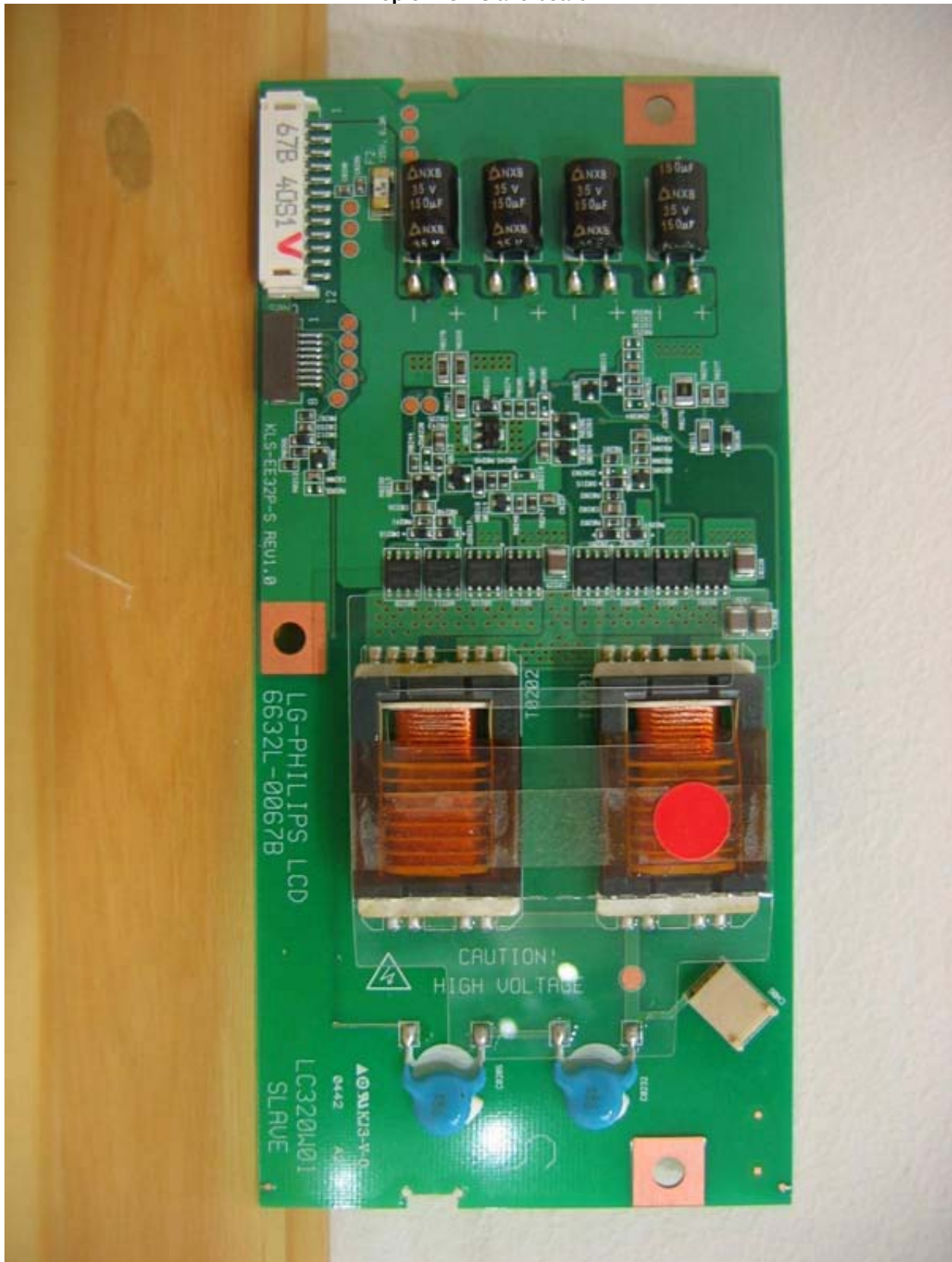
Top of LCD master board



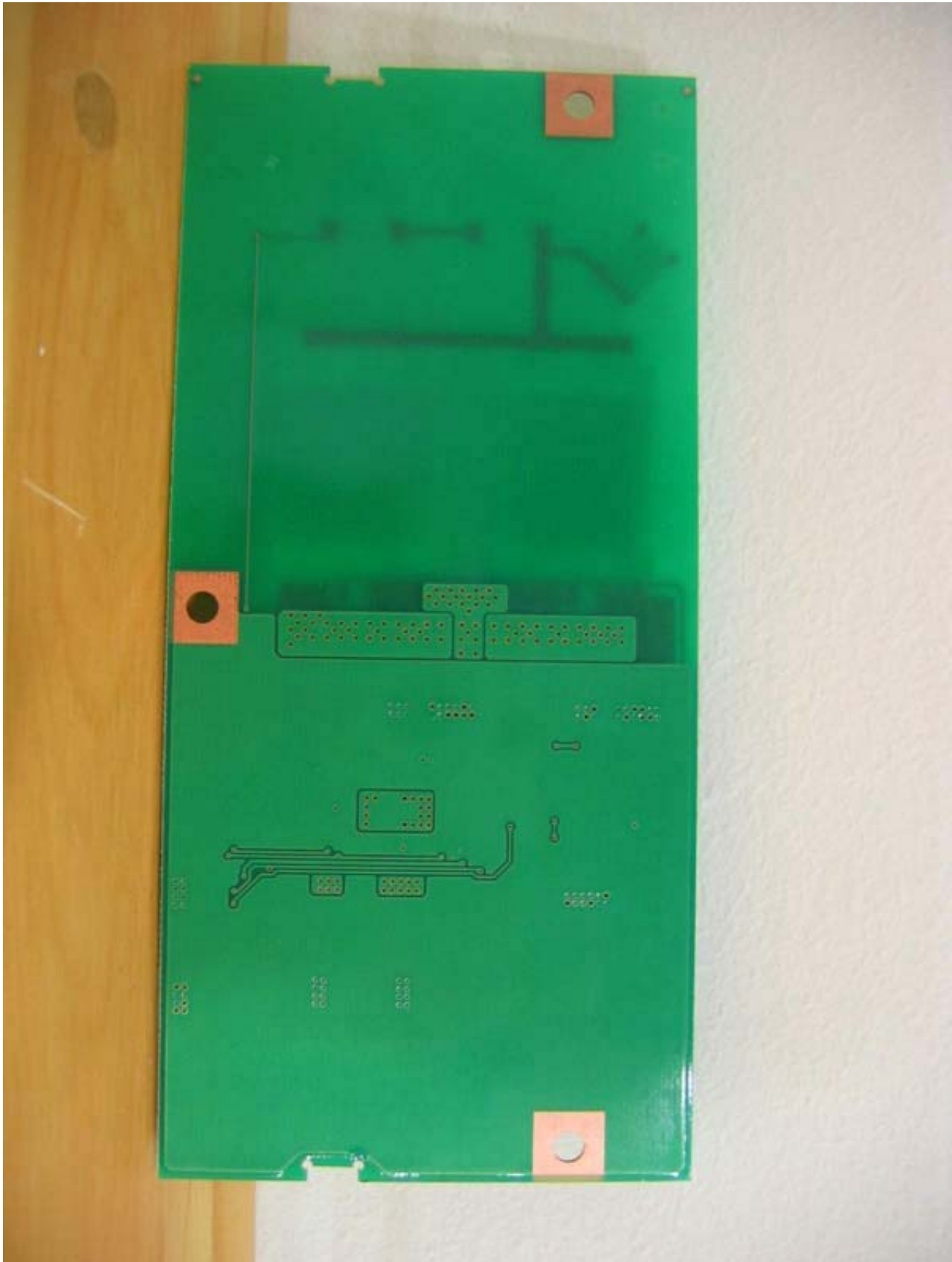
Bottom of LCD master board



Top of LCD slave board



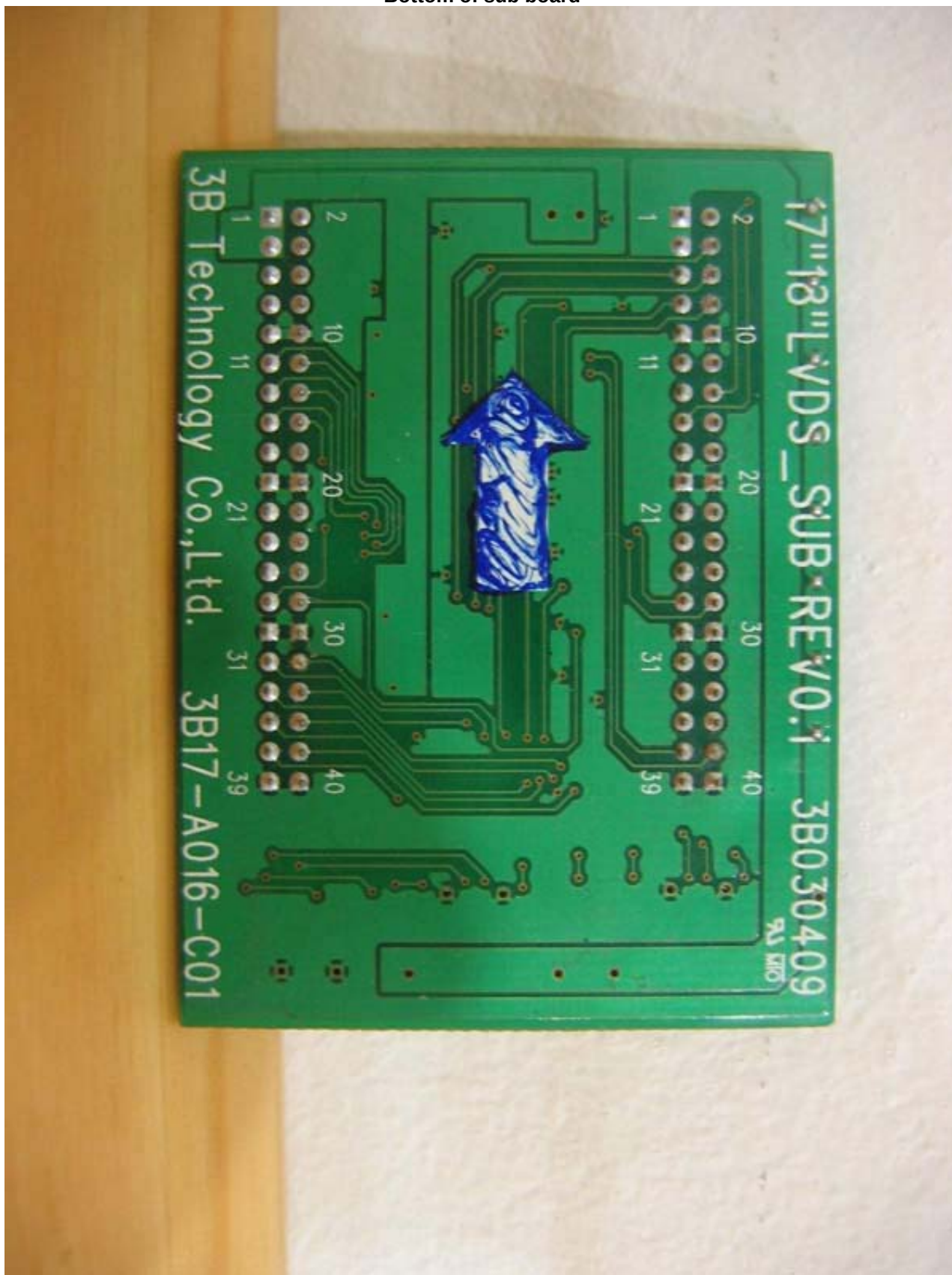
Bottom of LCD slave board



Top of sub board



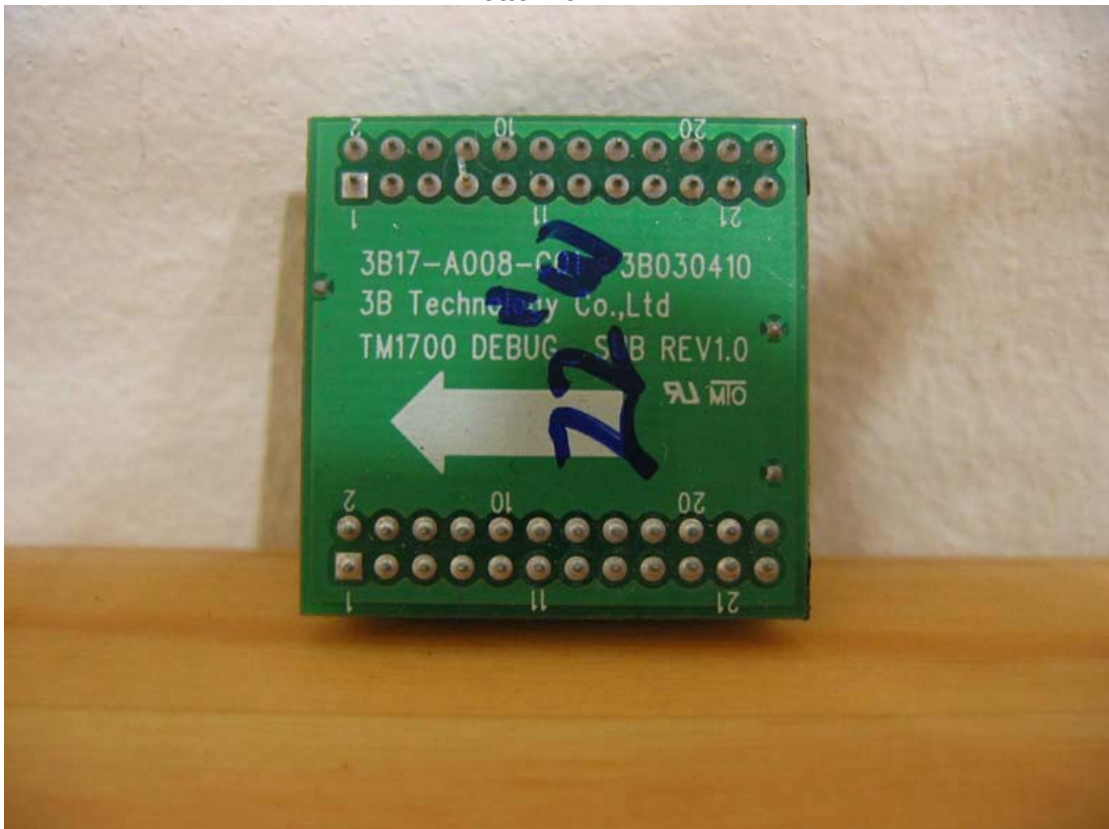
Bottom of sub board



Top of RAM



Bottom of RAM



Noise Filter

