

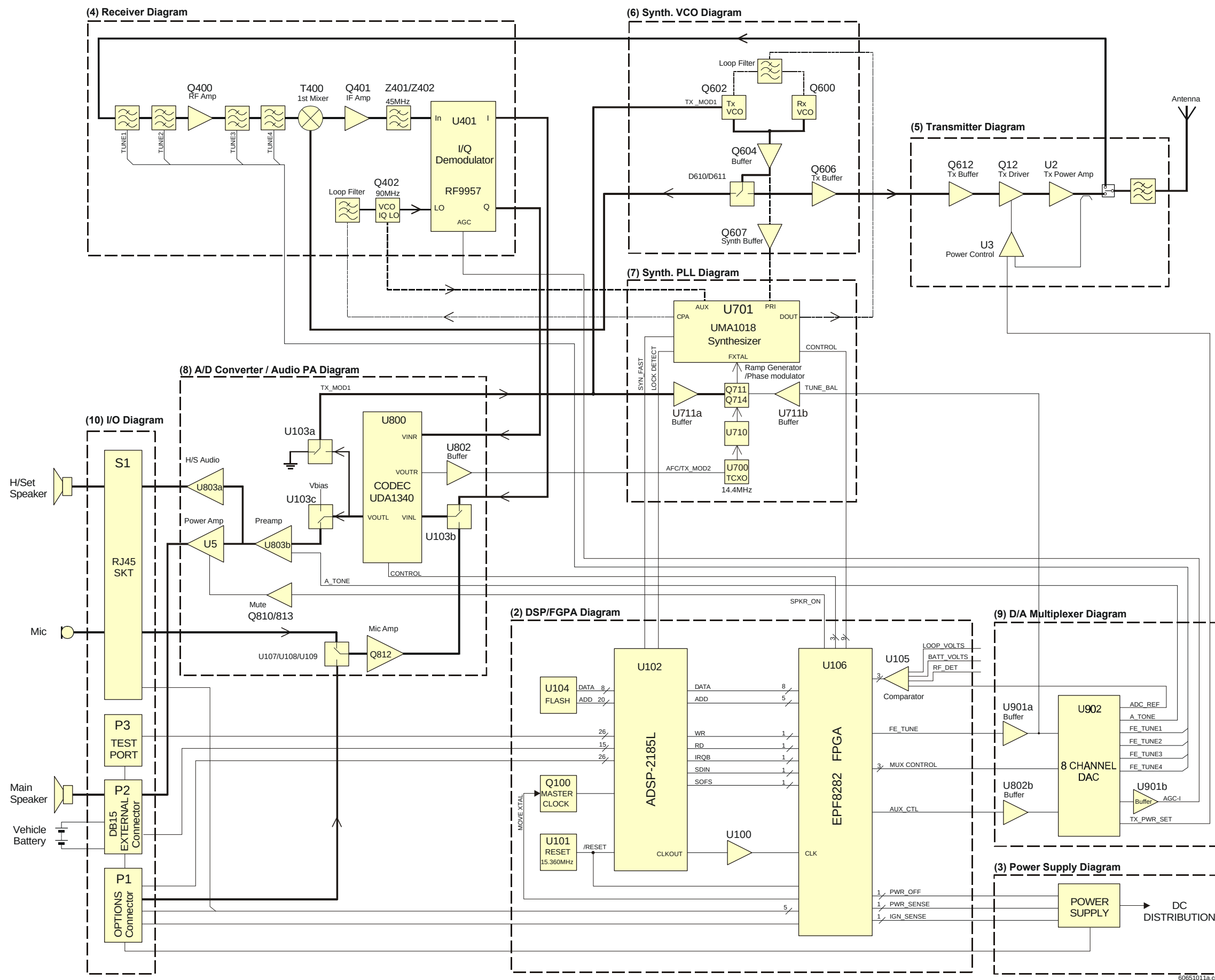


Figure 6-1 SRM9000 Block Diagram

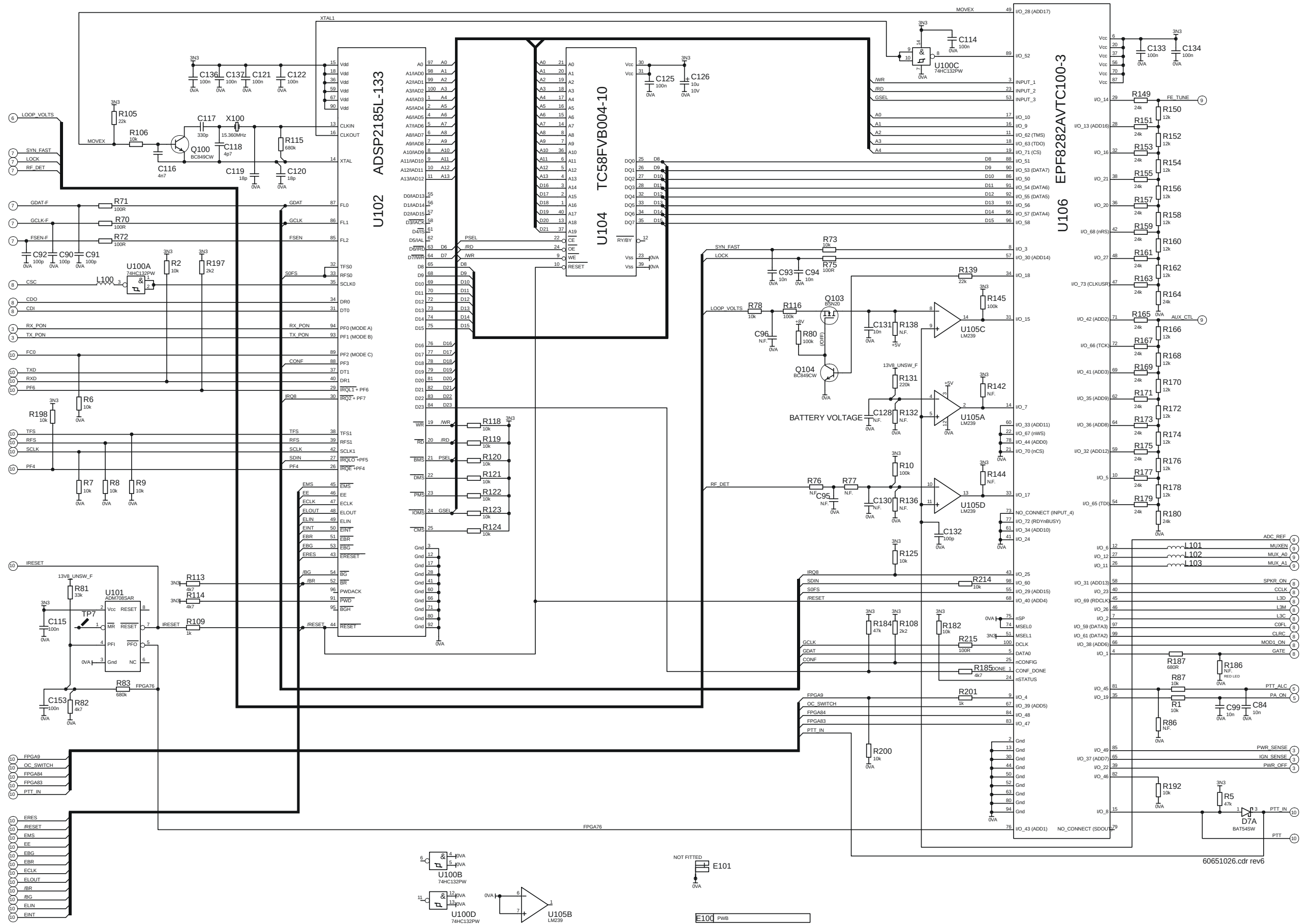
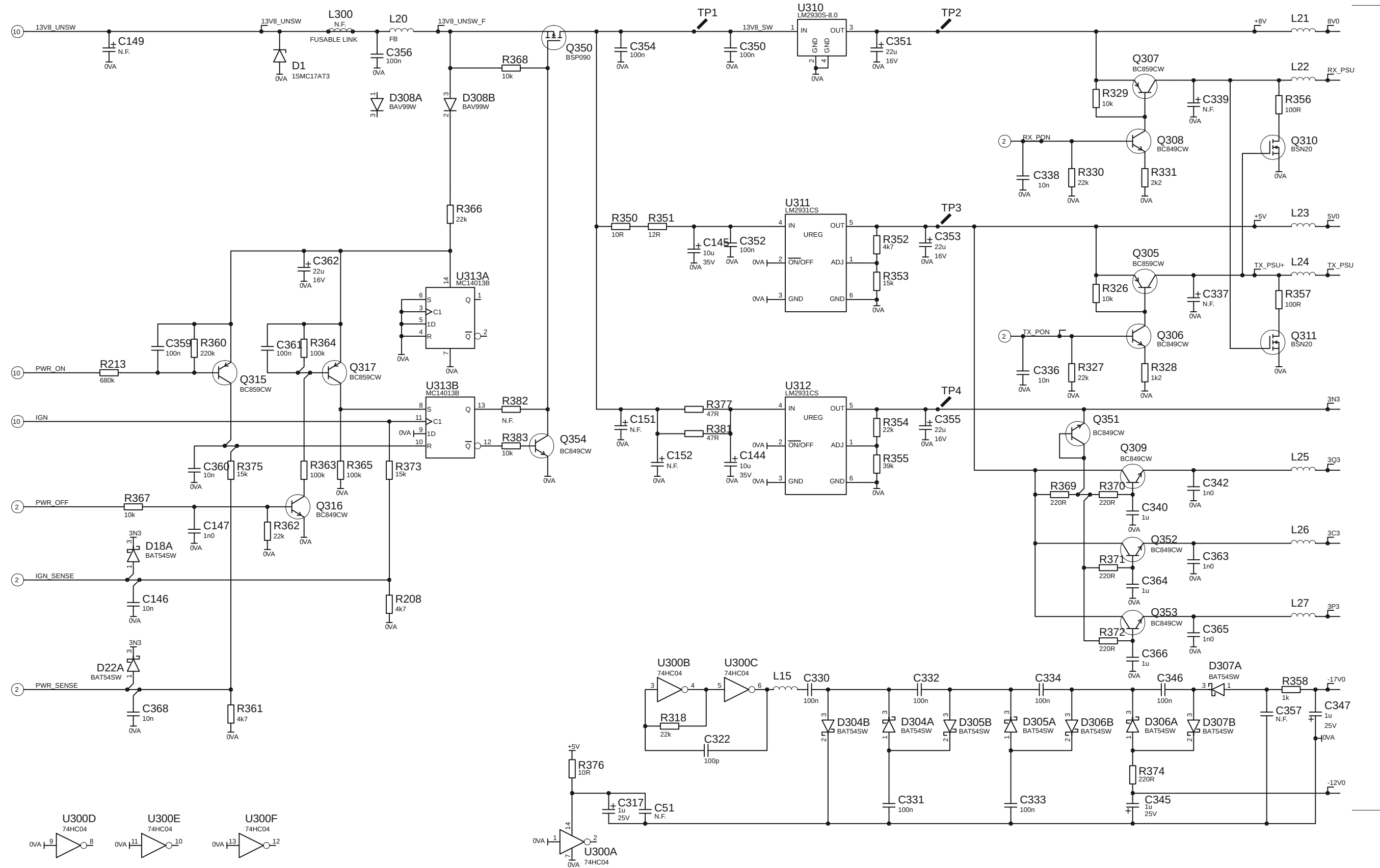


Figure 6-2 DSP and FPGA Schematic

DRAFT D



DC supply is distributed to all other schematics

Figure 6-3 Power Supply Schematic

E0 Band (66-88MHz)

DRAFT D

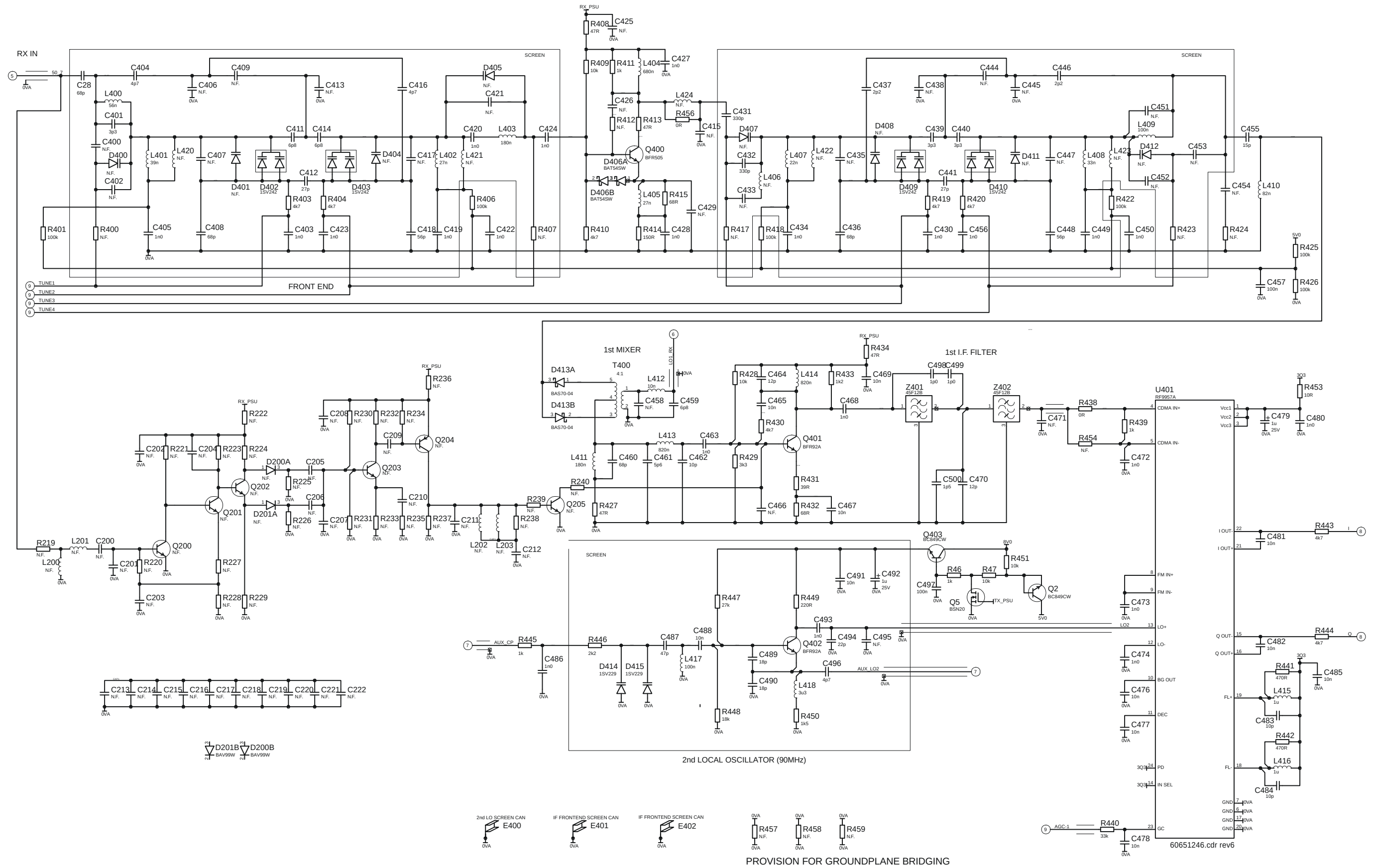


Figure 6-4AC Receiver Circuit Schematic
AC Band (136 - 174MHz)

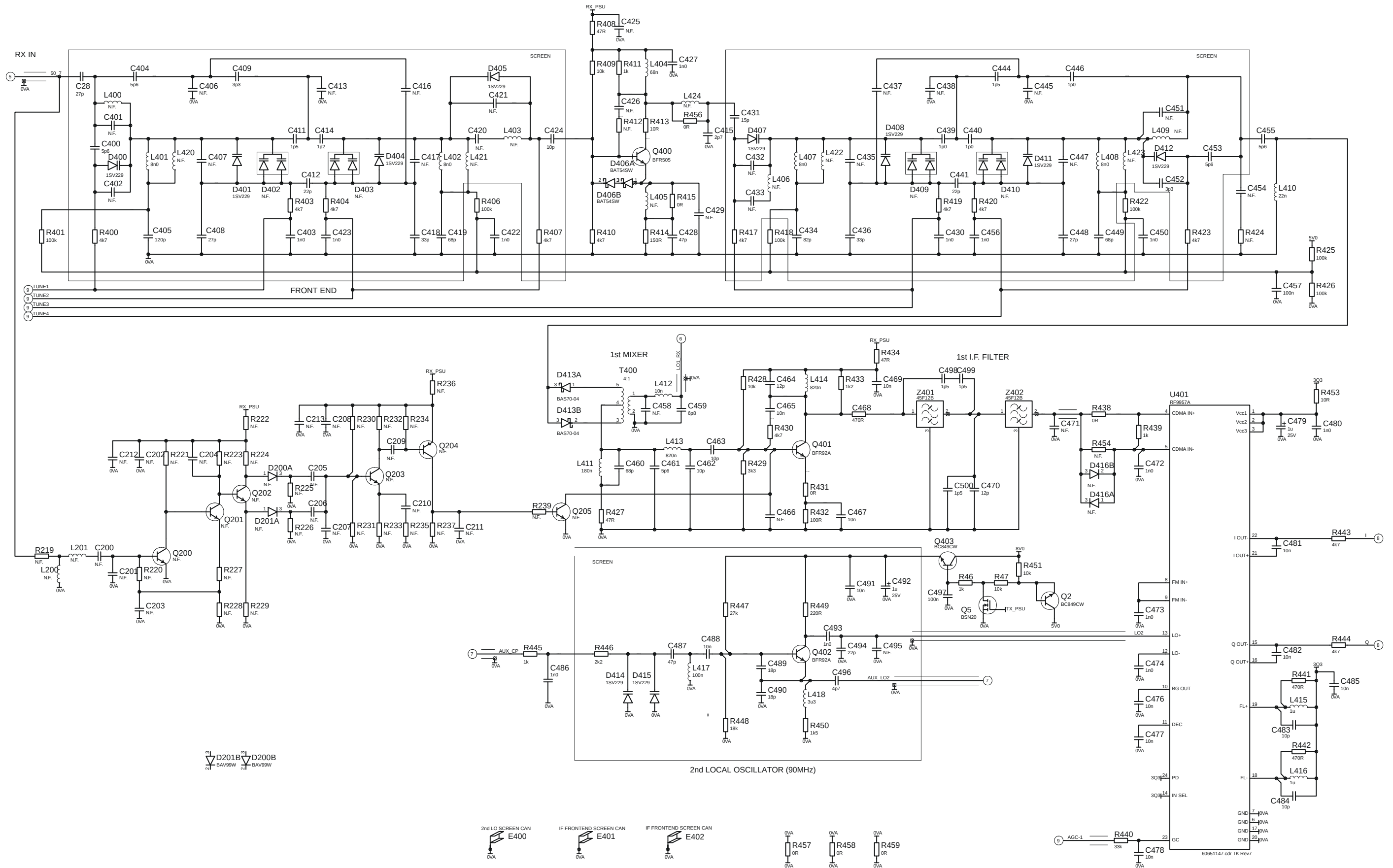


Figure 6-4TK Receiver Circuit Schematic

TK Band (400- 450MHz)

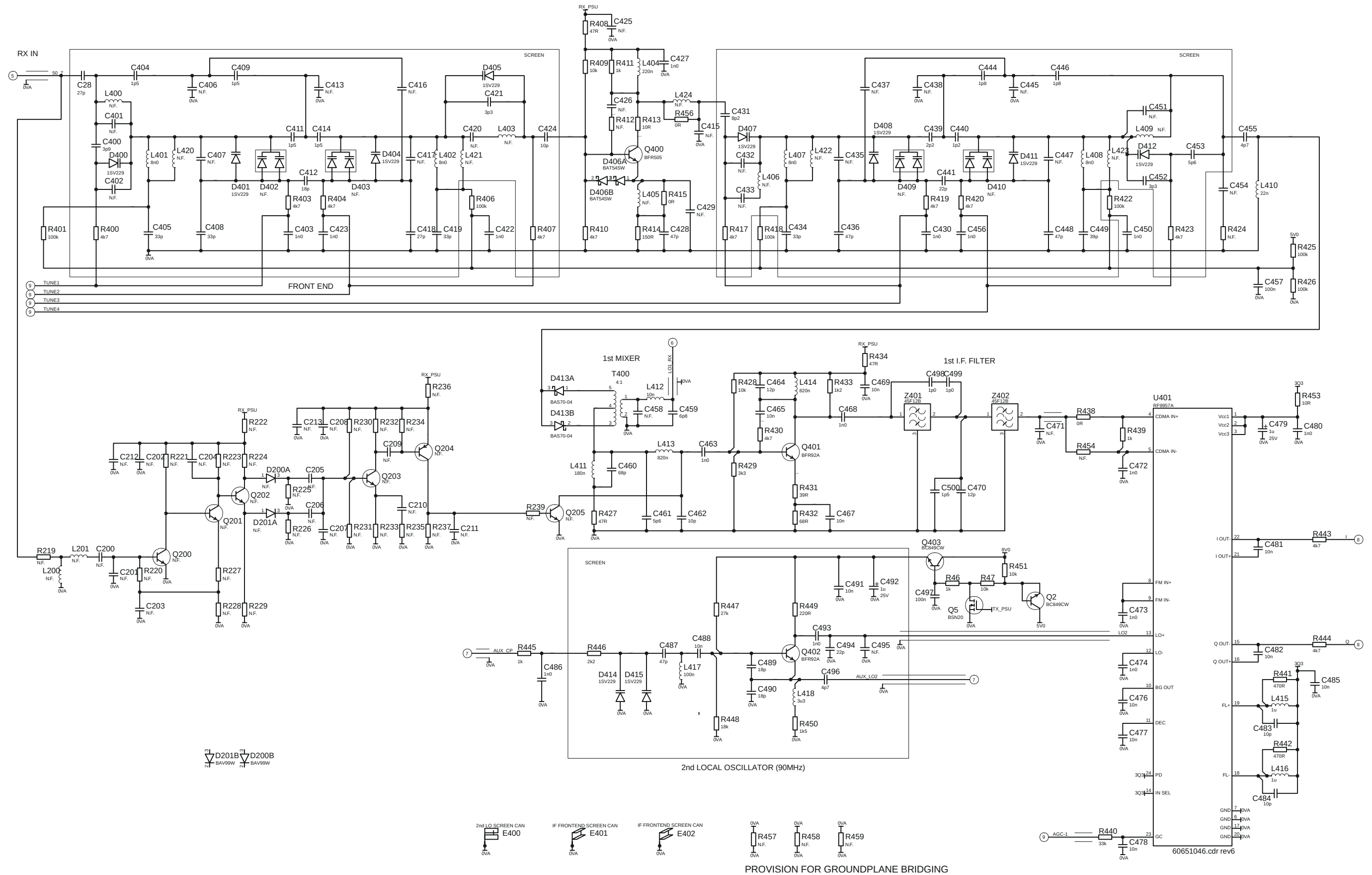


Figure 6-4UW Receiver Circuit Schematic

UW Band (440- 500MHz)

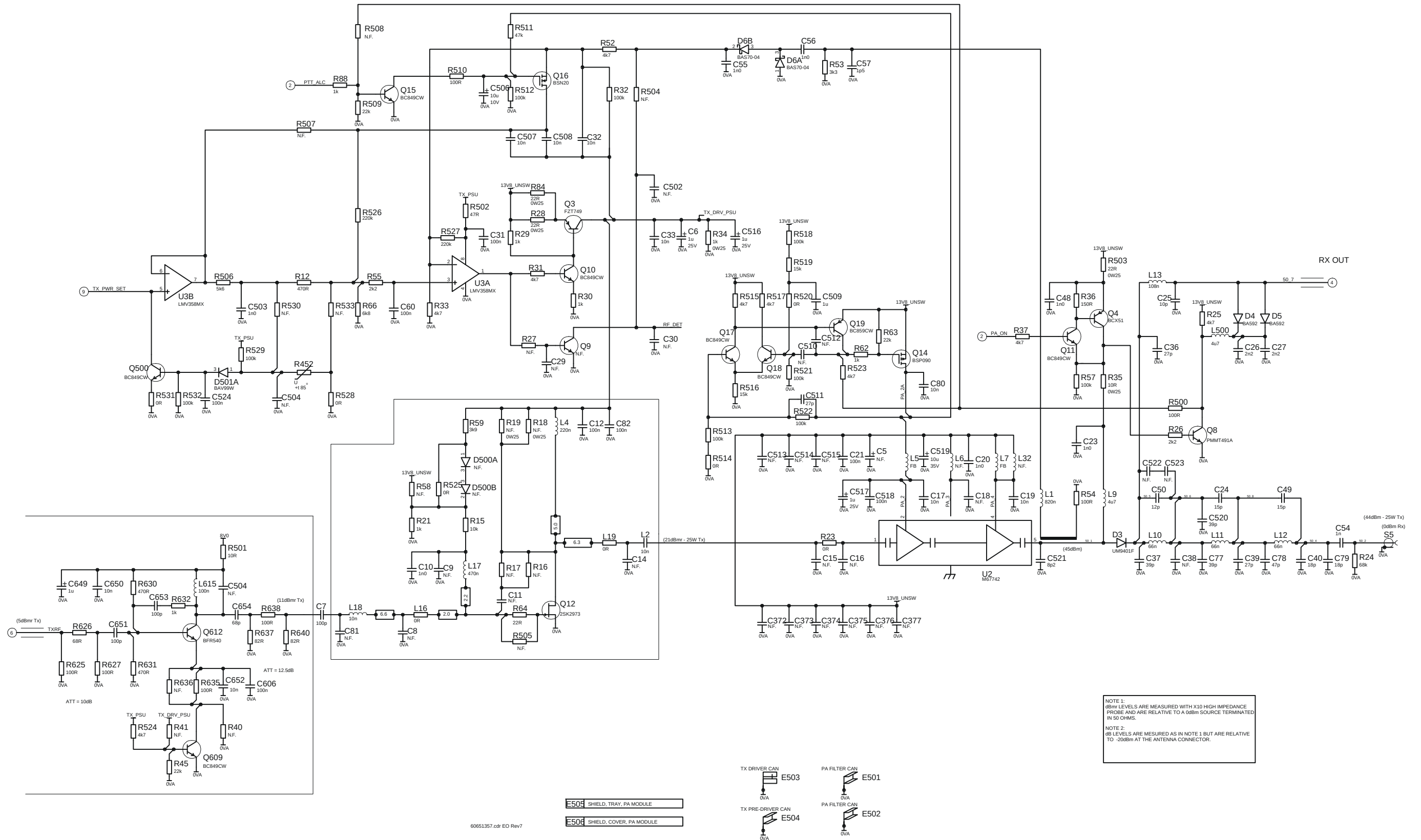


Figure 6-5E0 Transmitter Circuit Schematic

E0 Band (66-88MHz)

DRAFT D

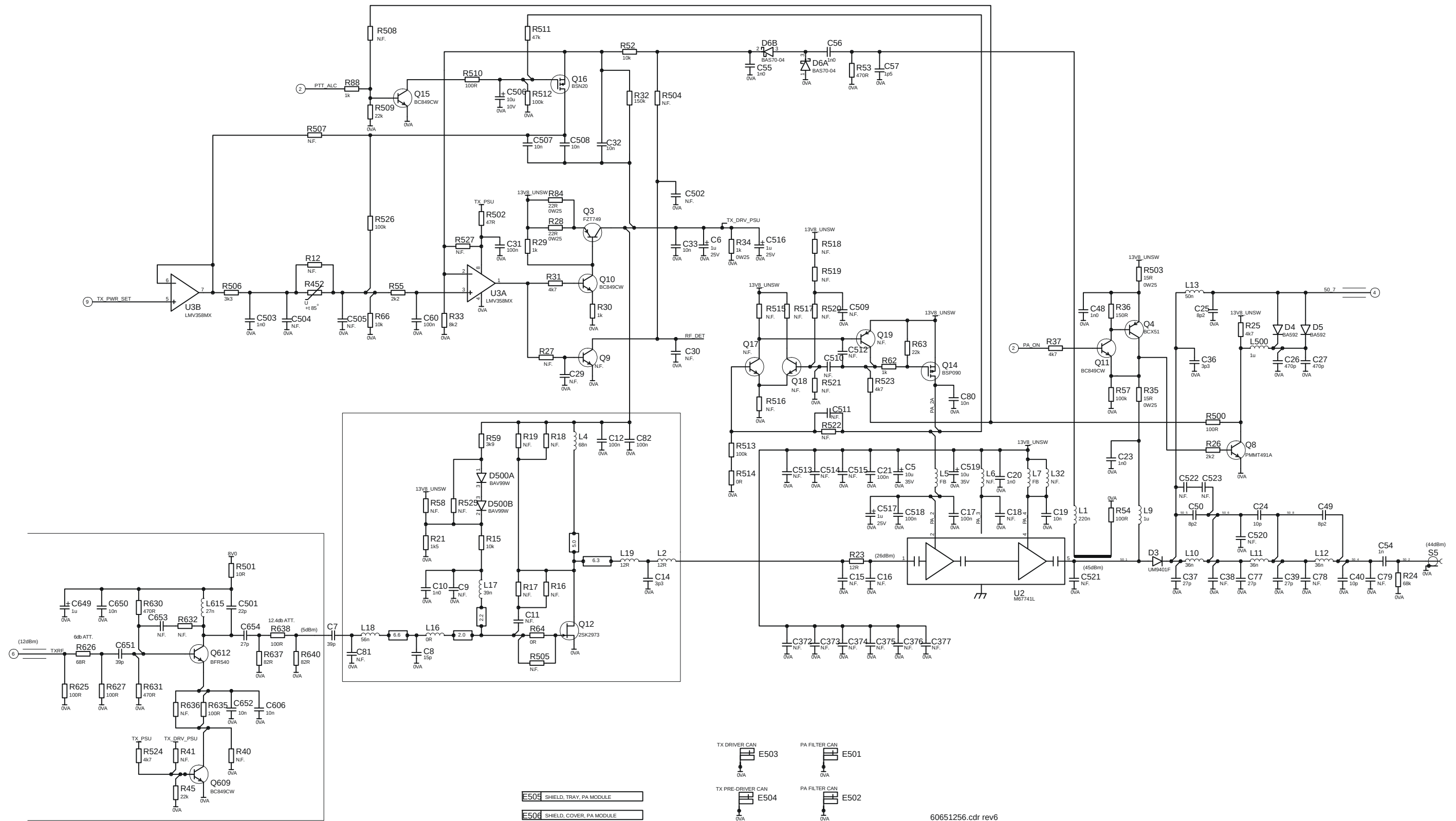


Figure 6-5AC Transmitter Circuit Schematic
AC Band (136 - 174MHz)

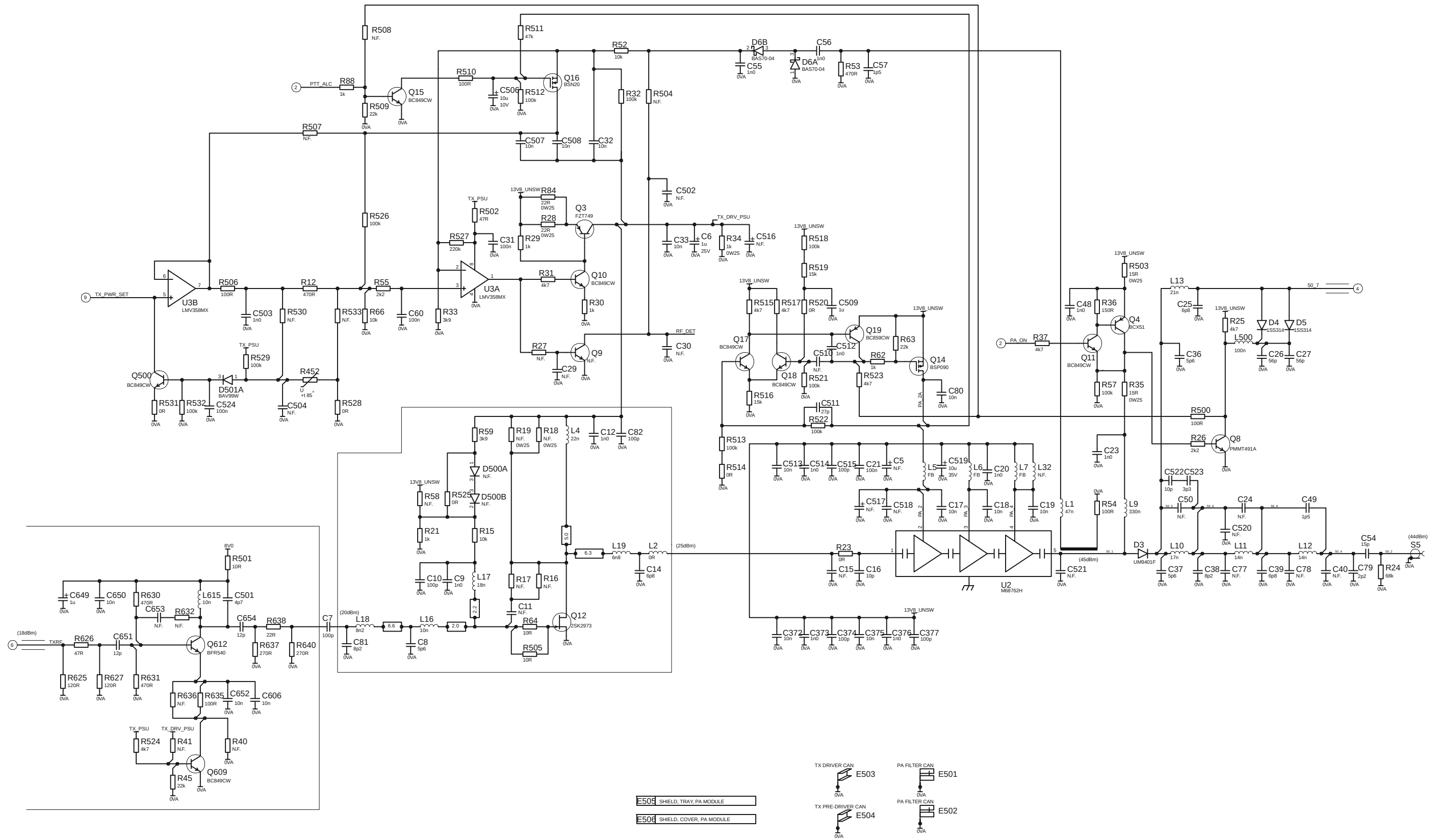


Figure 6-5TK Transmitter Circuit Schematic
TK Band (400- 450MHz)

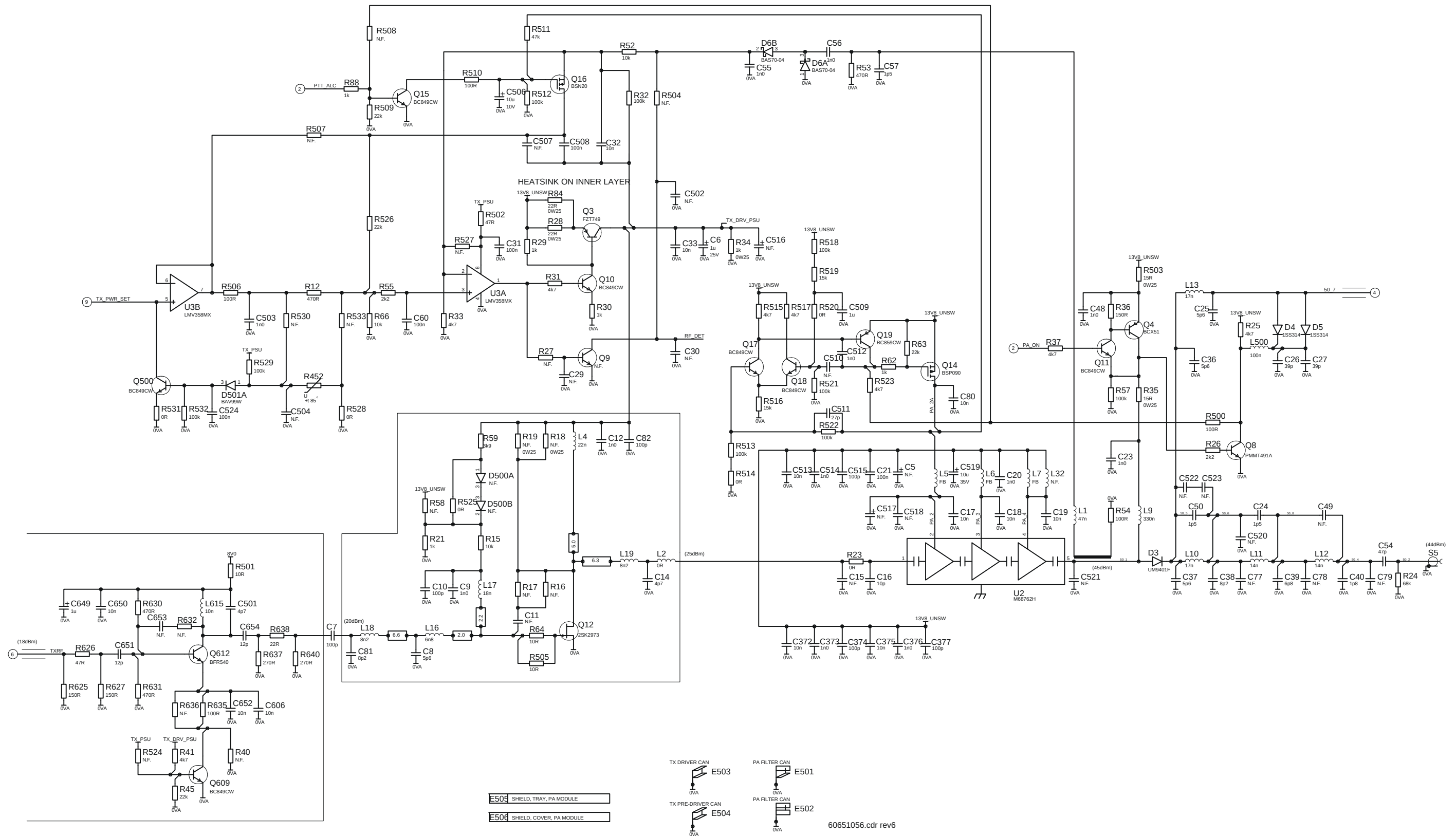


Figure 6-5UW Transmitter Circuit Schematic
UW Band (440- 500MHz)

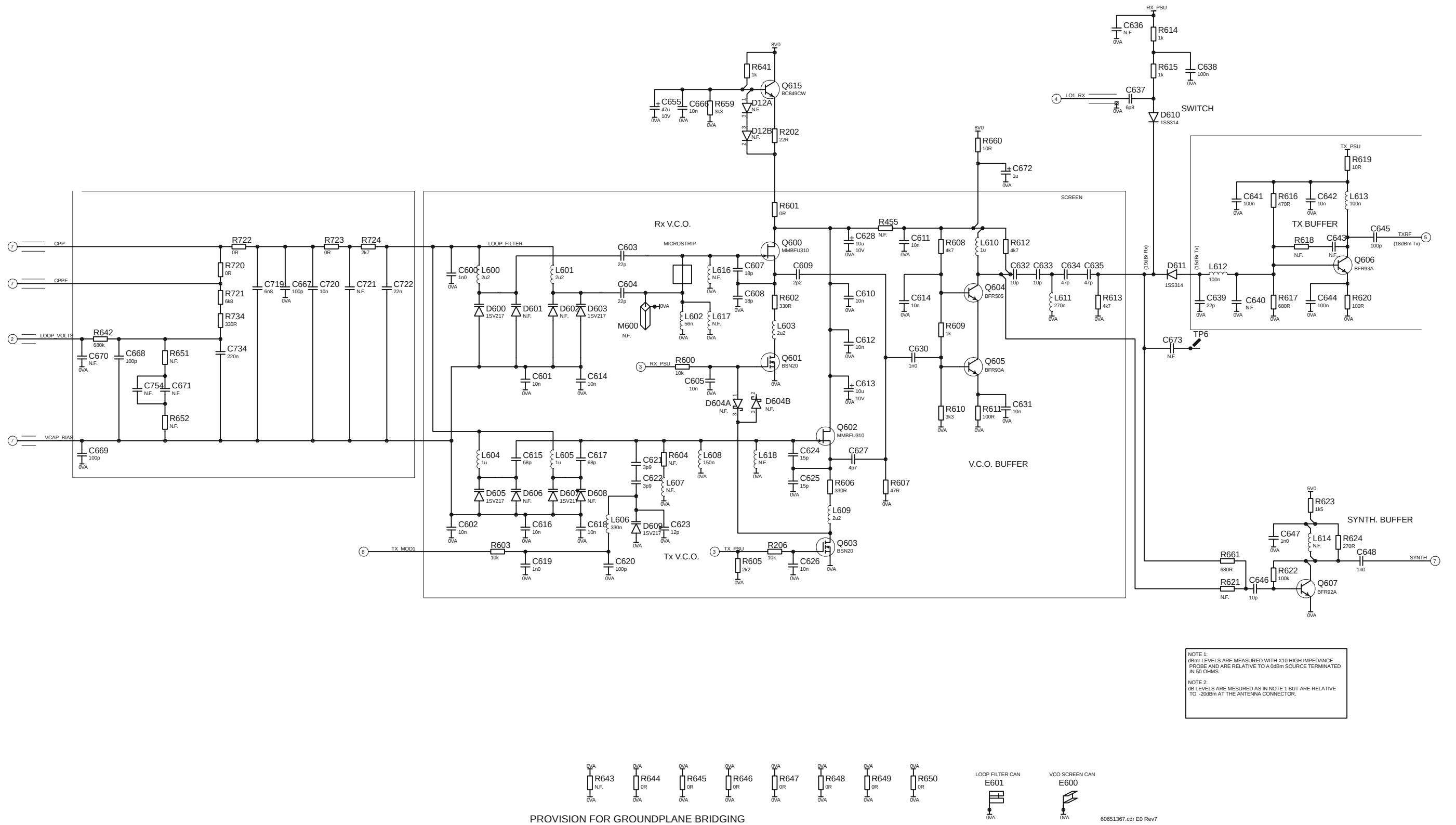


Figure 6-E0 Synthesiser - VCO Schematic

E0 Band (66-88MHz)

DRAFT D

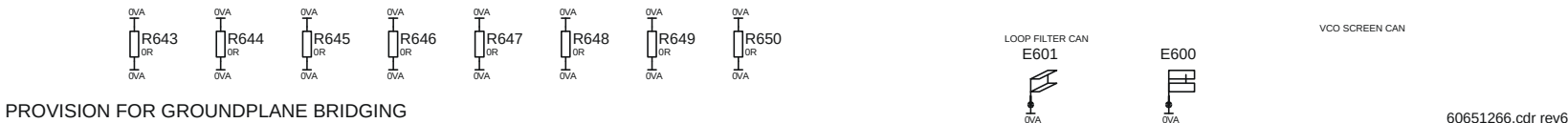


Figure 6-6AC Synthesiser - VCO Schematic
AC Band (136 - 174MHz)

DRAFT D

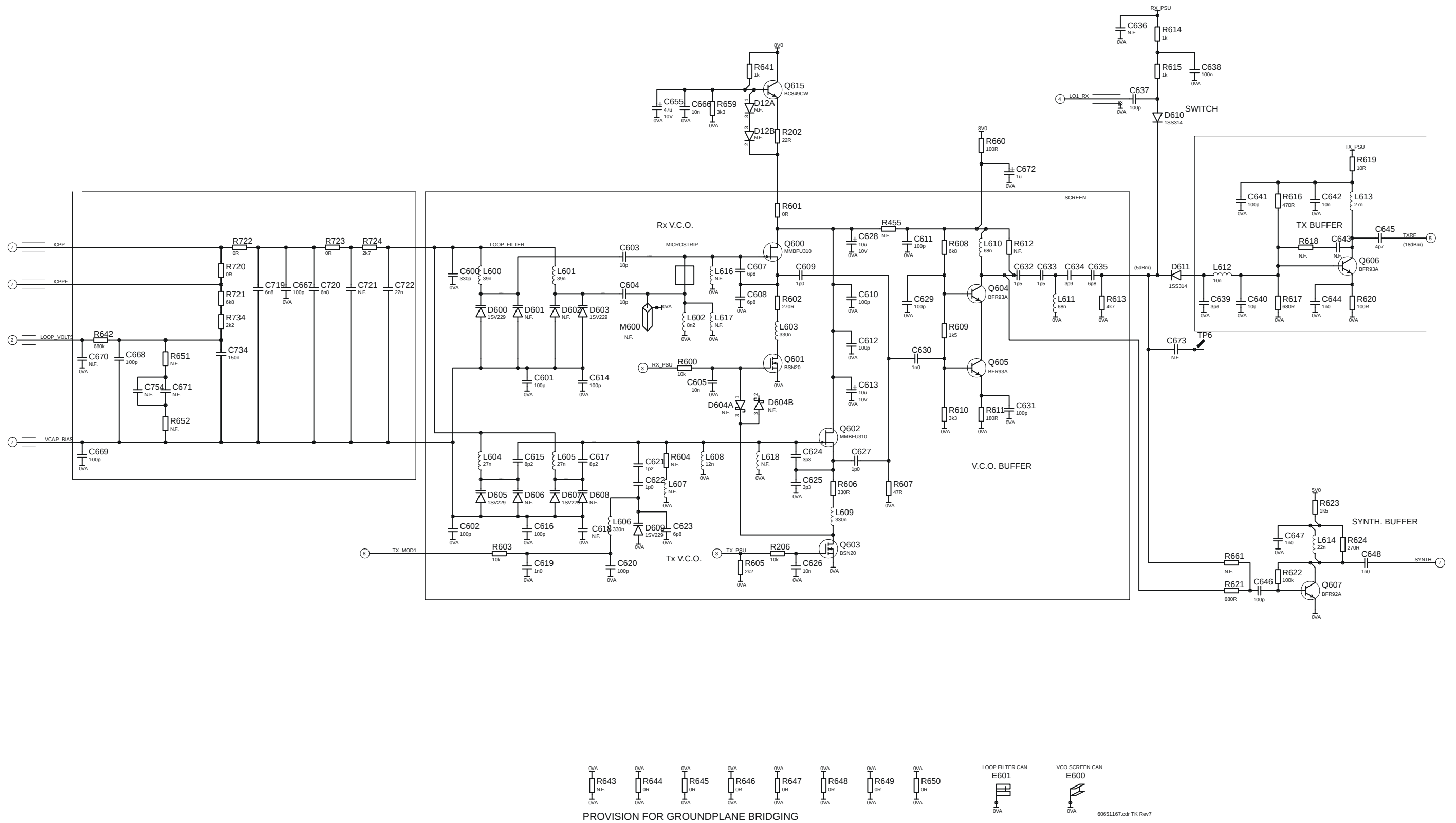


Figure 6-6TK Synthesiser – VCO Schematic
TK Band (400- 450MHz)

DRAFT D

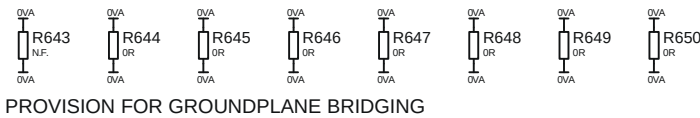


Figure 6-6UW Synthesiser – VCO Schematic
UW Band (440- 500MHz)

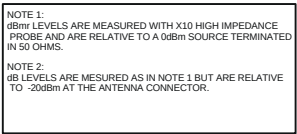


Figure 6-7E0 Synthesiser - PLL Schematic

E0 Band (66-88MHz)

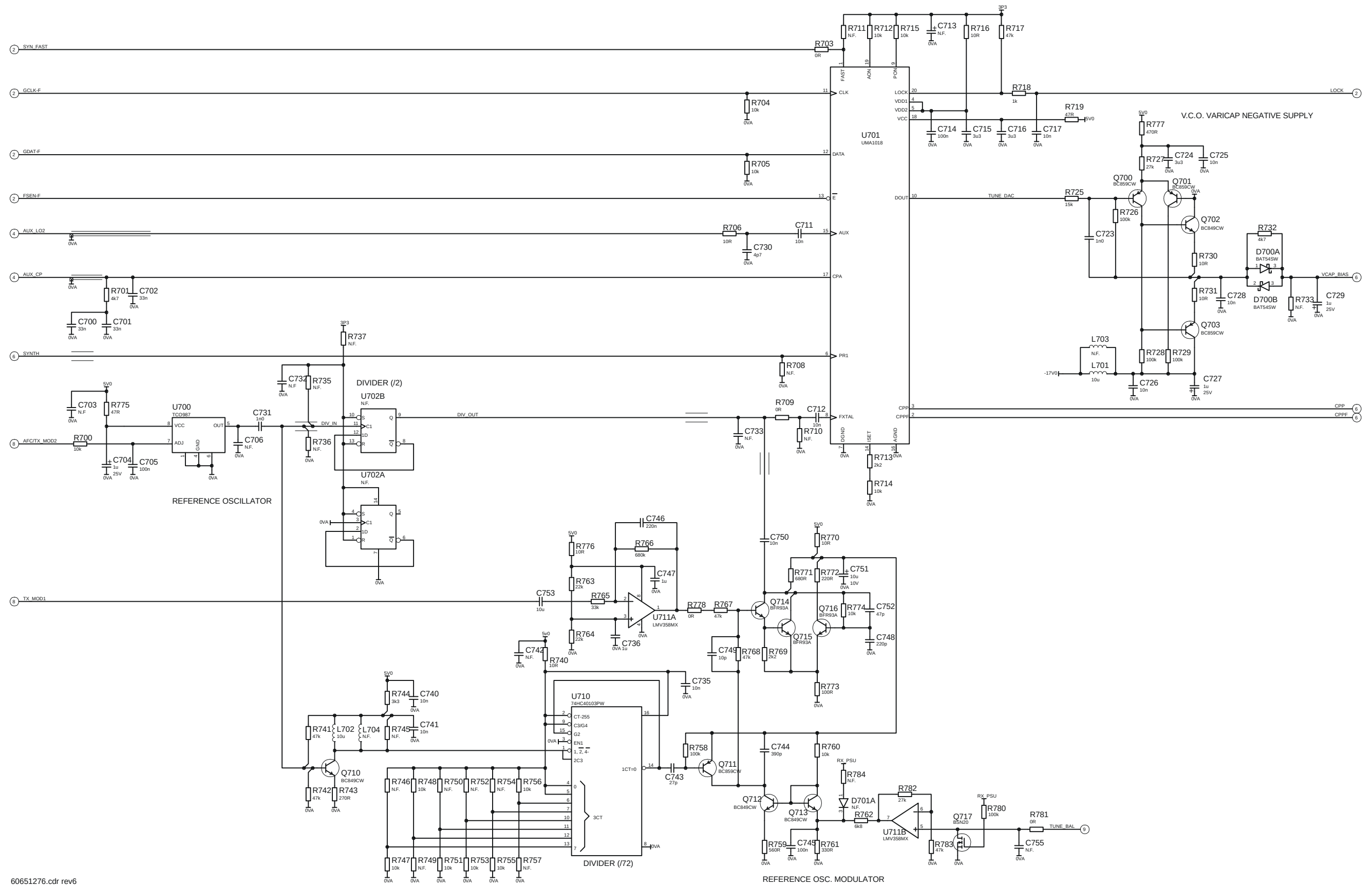


Figure 6-7AC Synthesiser – PLL Schematic
AC Band (136 - 174MHz)

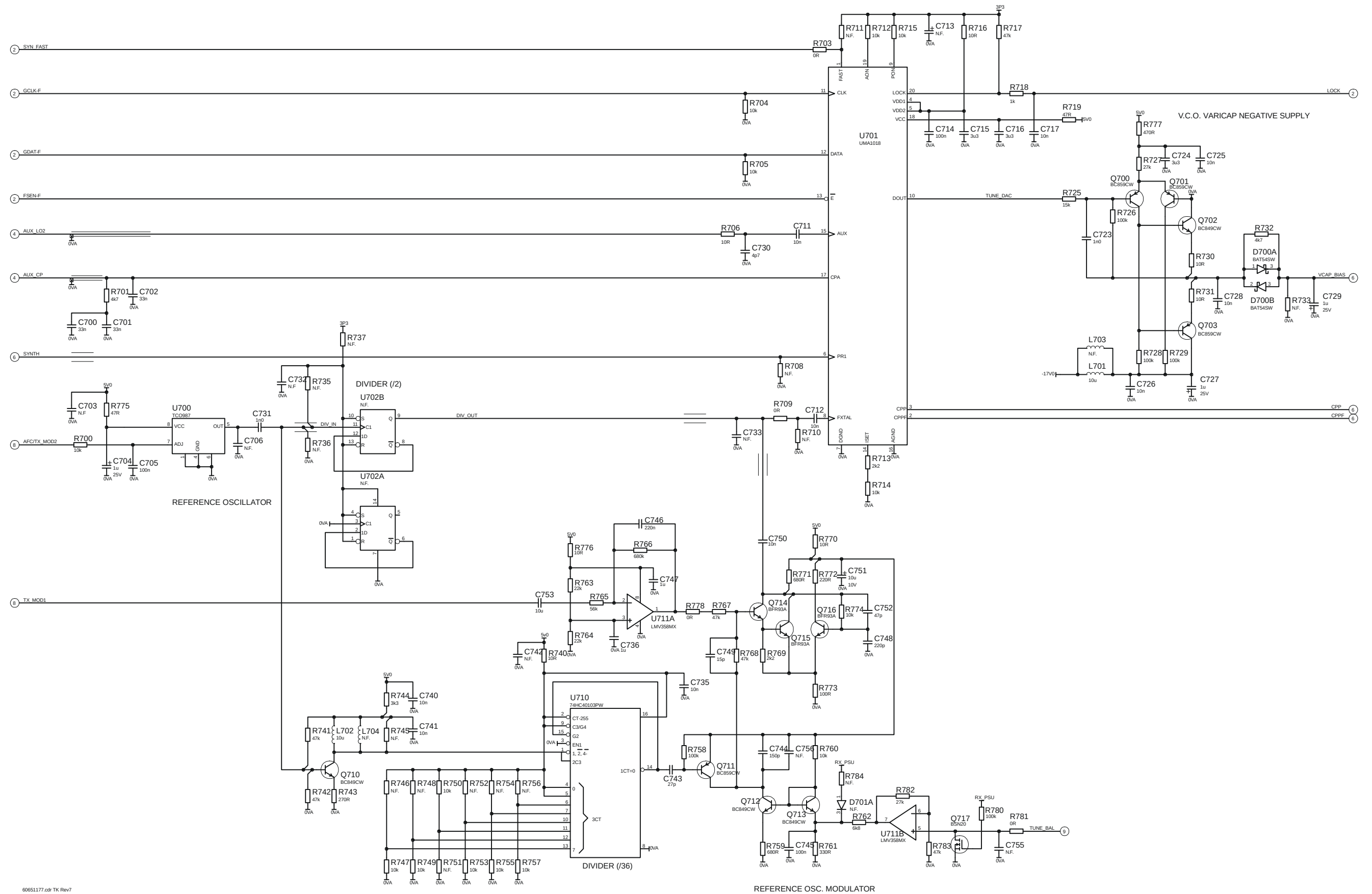


Figure 6-7TK Synthesiser – PLL Schematic
TK Band (400- 450MHz)

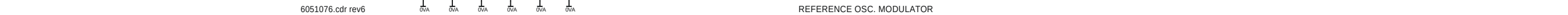
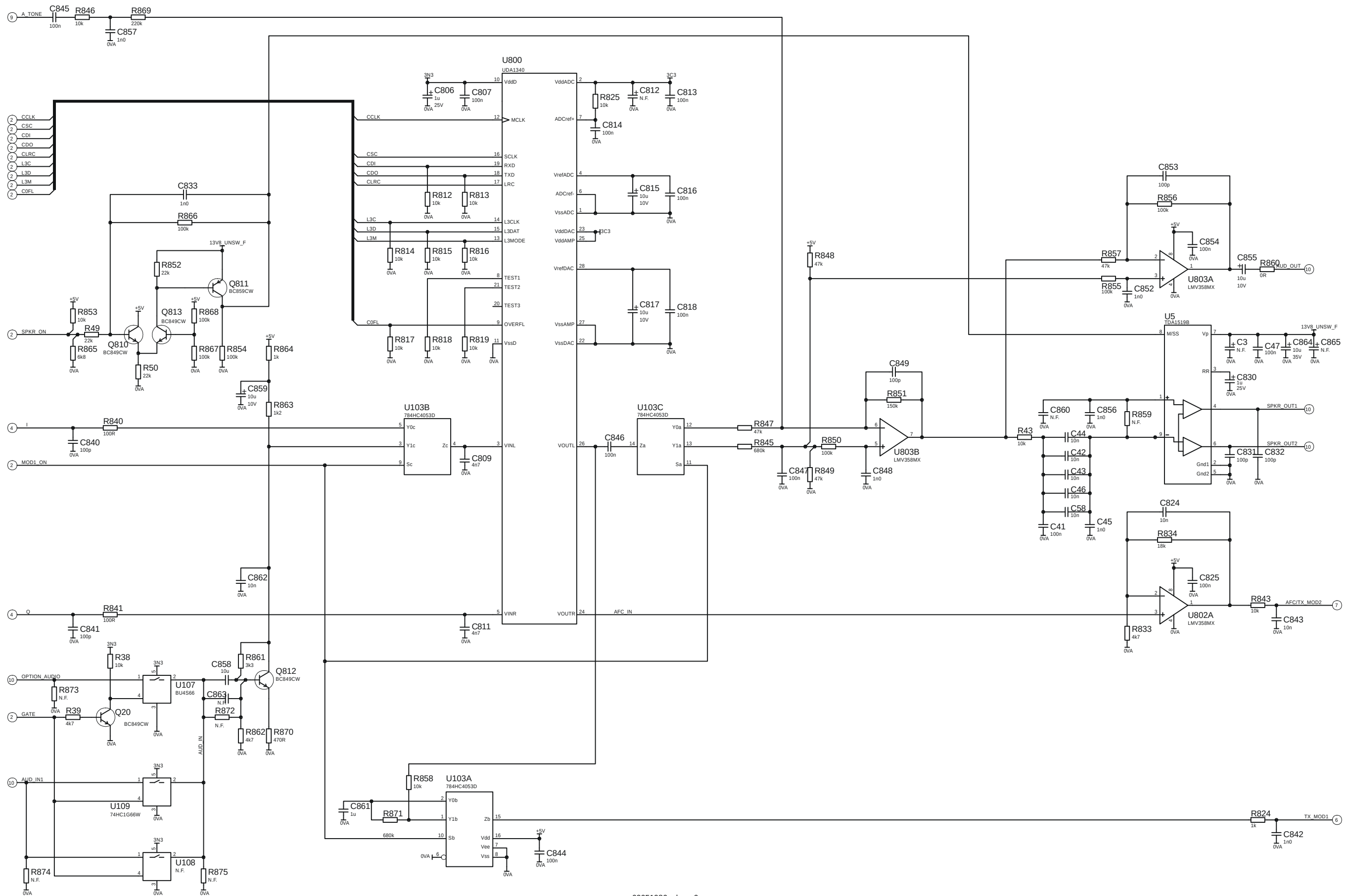


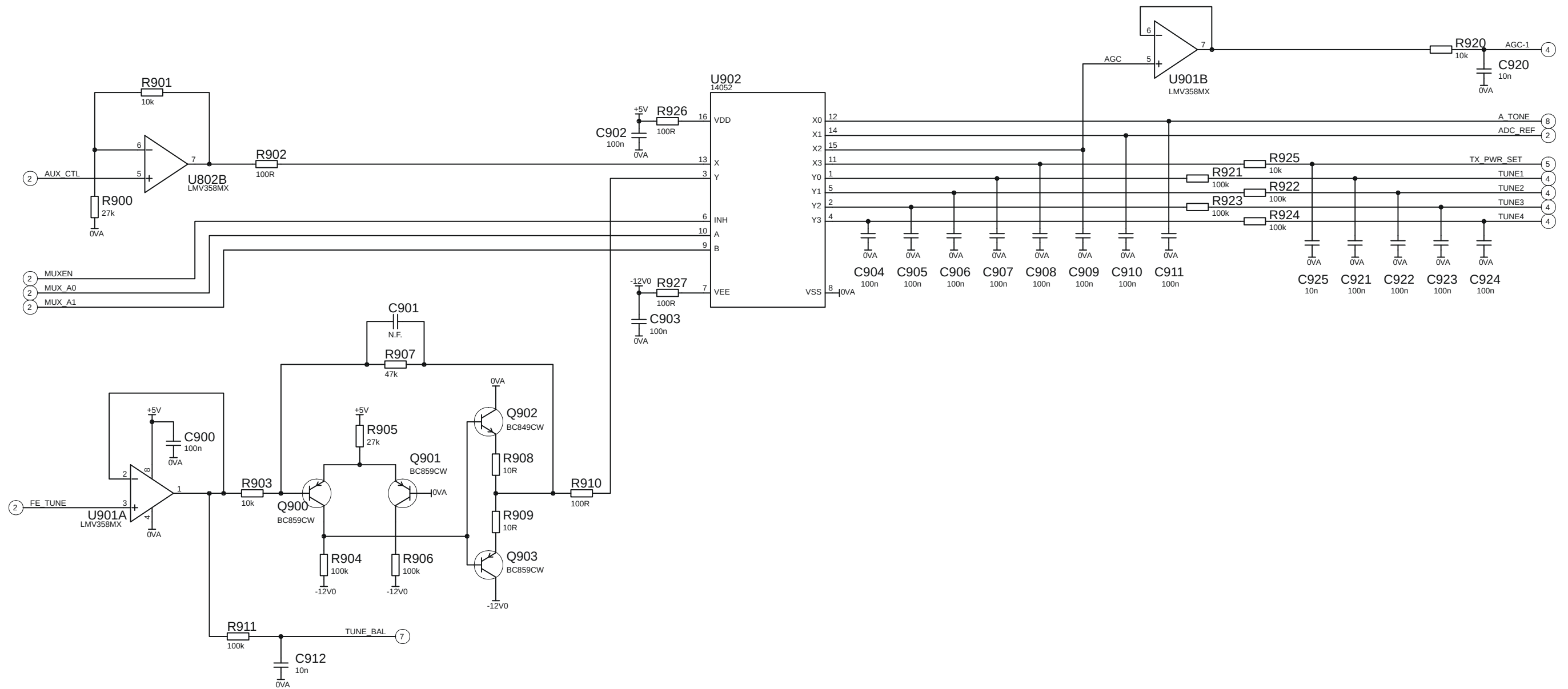
Figure 6-7UW Synthesiser – PLL Schematic
UW Band (440- 500MHz)

DRAFT D



60651086.cdr rev6

Figure 6-8 A/D Converter Schematic



60651096.cdr rev6

Figure 6-9 D/A Multiplexer Schematic

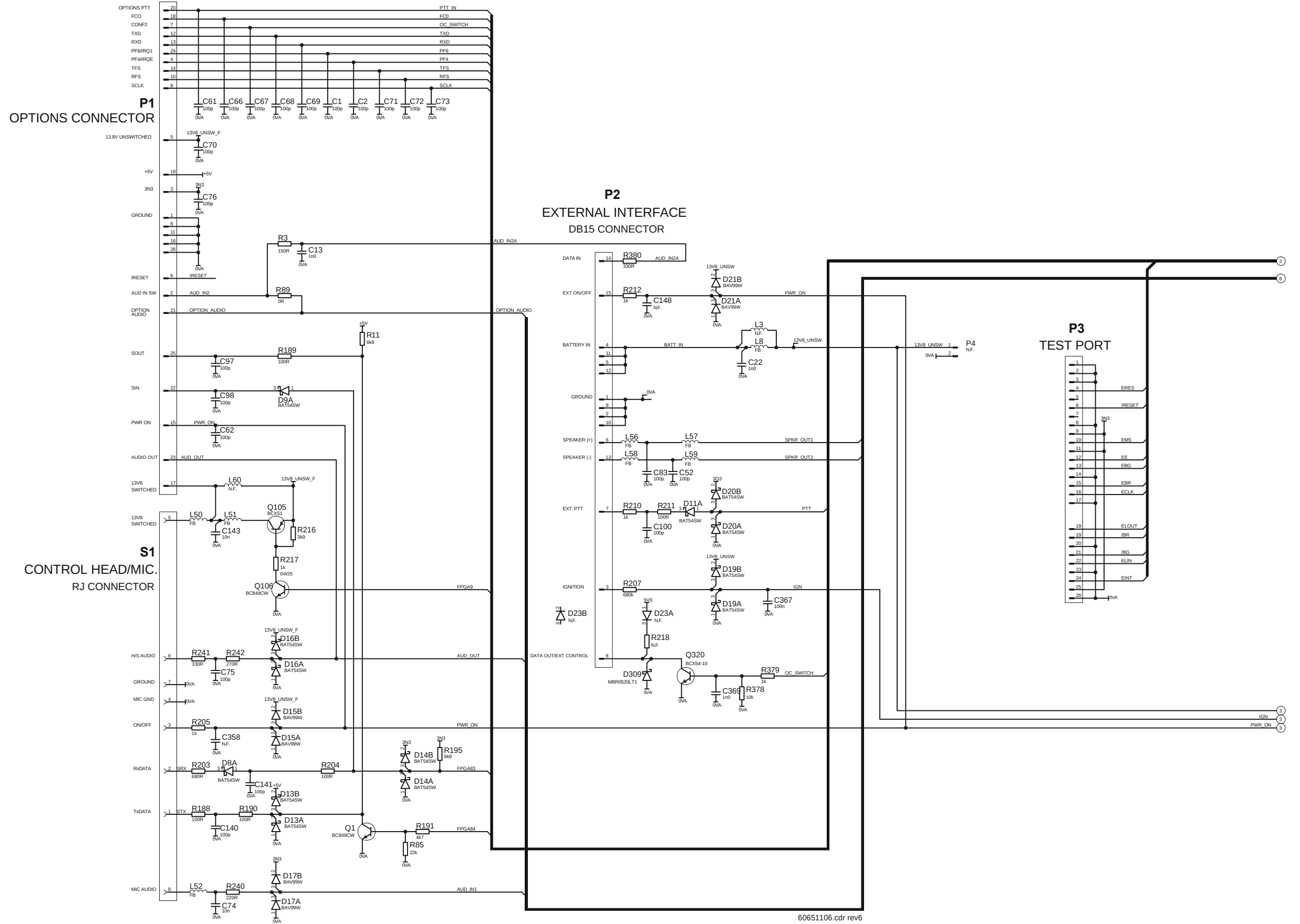


Figure 6-10 I/O Connections Schematic



Figure 6-11 SRM9000 PCB Layout, Top (1/2)

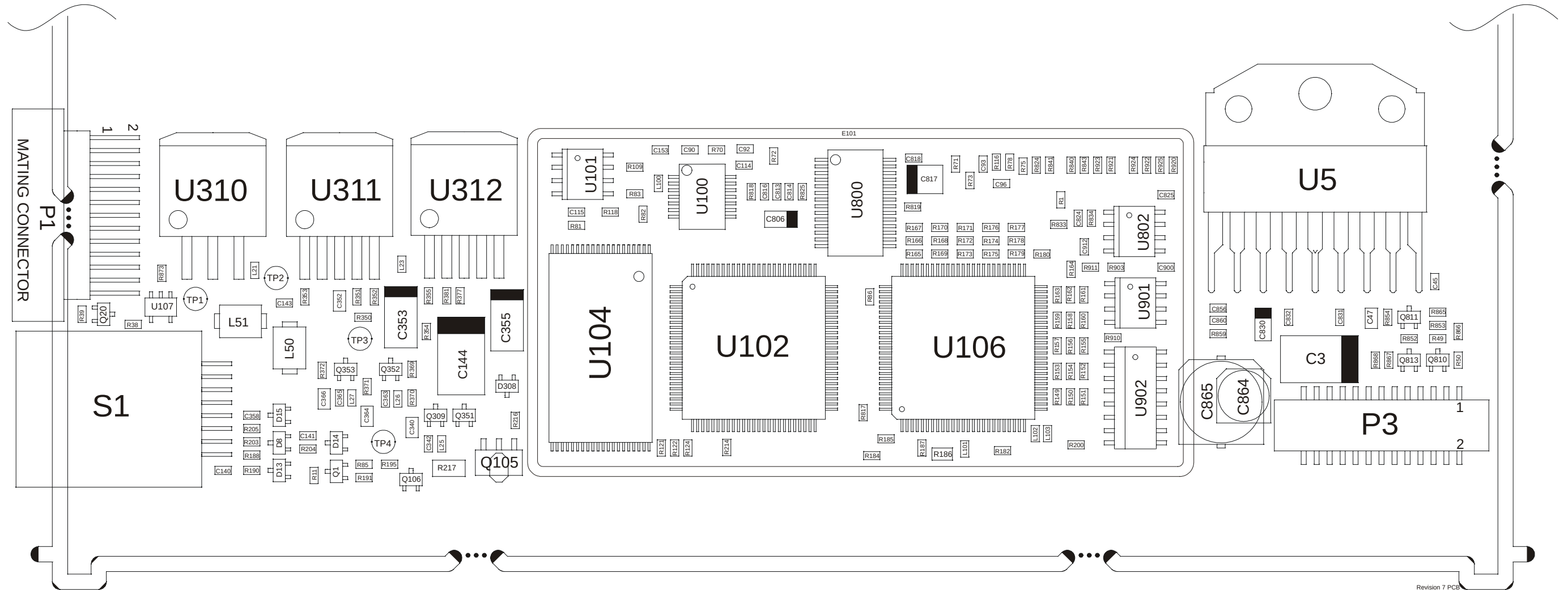


Figure 6-12 SRM9000 PCB Layout, Top (2/2)



Figure 6-13 SRM9000 PCB Layout, Bottom (1/2)

