

FCC PART 15.247 TEST REPORT

For

Shenzhen Rapoo Technology Co., Ltd.

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FCC ID: PP2A300

Report Type: Original Report	Product Type: Bluetooth Mini NFC Speaker
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GENERAL INFORMATION

Product Description for Equipment under Test (EUT)

The *Shenzhen Rapoo Technology Co., Ltd.*'s product, model number: *A300 (FCC ID: PP2A300)* (the "EUT") in this report was a *Bluetooth Mini NFC Speaker*, which was measured approximately: 6.0 cm (L) x 6.0 cm (W) x 6.5 cm (H), rated input voltage: DC 3.7 V from lithium battery or DC 5.0V from system.

** All measurement and test data in this report was gathered from production sample serial number: 140106017 (Assigned by BACL.Dongguan). The EUT was received on 2014-01-21*

Objective

This report is prepared on behalf of *Shenzhen Rapoo Technology Co., Ltd.* in accordance with Part 2, Subpart J, Part 15, Subparts A, B and C of the Federal Communications Commission's rules

The tests were performed in order to determine the Bluetooth BDR and EDR mode of EUT compliance with FCC Part 15, Subpart C, and section 15.203, 15.205, 15.207, 15.209 and 15.247 rules.

Related Submittal(s)/Grant(s)

FCC Part 15C DTS submissions with FCC ID: PP2A300 for Bluetooth LE mode.

Test Methodology

All measurements contained in this report were conducted with ANSI C63.4-2003, American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the range of 9 kHz to 40 GHz.

All emissions measurement was performed and Bay Area Compliance Laboratories Corp. (Dongguan).

Test Facility

The Test site used by Bay Area Compliance Laboratories Corp. (Dongguan) to collect test data is located on the No.69 Pulongcun, Puxinhu Industrial Zone, Tangxia, Dongguan, Guangdong, China

Test site at Bay Area Compliance Laboratories Corp. (Dongguan) has been fully described in reports submitted to the Federal Communications Commission (FCC). The details of these reports have been found to be in compliance with the requirements of Section 2.948 of the FCC Rules on February 02, 2012. The facility also complies with the radiated and AC line conducted test site criteria set forth in ANSI C63.4-2003.

The Federal Communications Commission has the reports on file and is listed under FCC Registration No.: 273710. The test site has been approved by the FCC for public use and is listed in the FCC Public Access Link (PAL) database.

Additionally, Bay Area Compliance Laboratories Corp. (Dongguan) is an ISO/IEC 17025 accredited laboratory, and is accredited by National Voluntary Laboratory Accredited Program (Lab Code 500069-0).



The current scope of accreditations can be found at <http://ts.nist.gov/standards/scopes/5000690.htm>

SYSTEM TEST CONFIGURATION

Description of Test Configuration

The system was configured for testing in an engineering mode, which was provided by manufacturer.

EUT Exercise Software

The software “CSR Bluesuite 2.4.8” was used for testing, which was provided by manufacturer. The worst condition (maximum power) was setting by the software as following table:

Test Software Version	CSR Bluesuite 2.4.8		
Test Frequency	2402MHz	2441MHz	2480MHz
GFSK	60	60	60
$\pi/4$ -DPSK	100	100	100
8-DPSK	100	100	100

Equipment Modifications

No modification was made to the EUT.

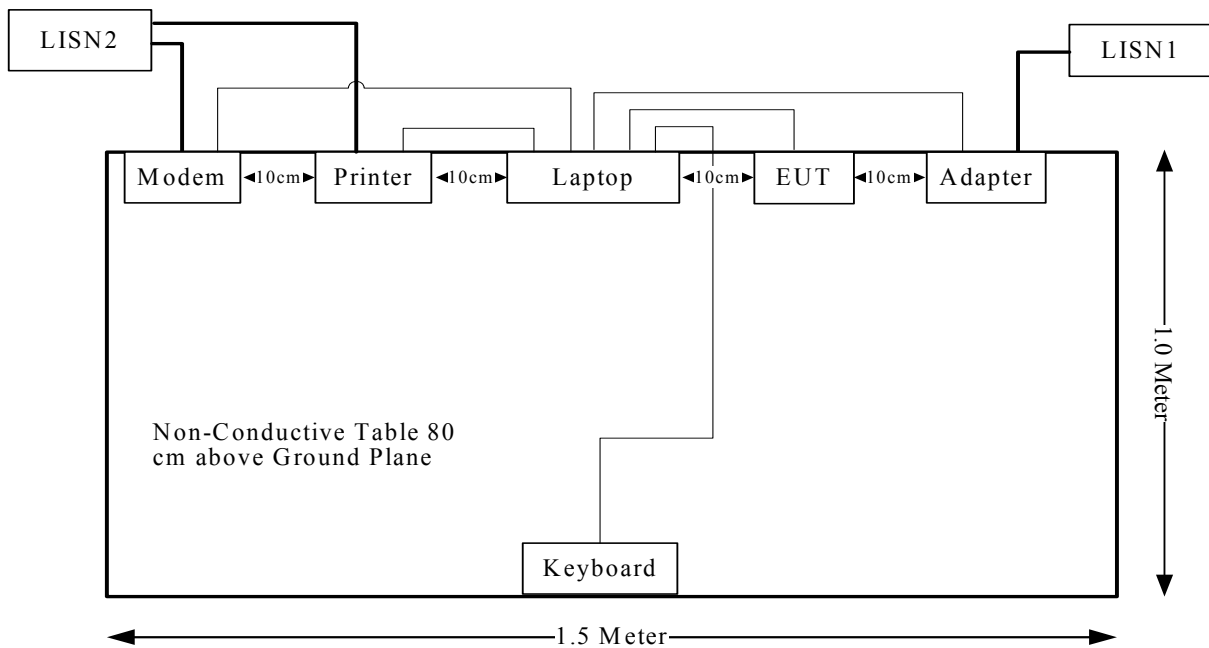
Support Equipment List and Details

Manufacturer	Description	Model	Serial Number
DELL	Laptop	PP11L	QDS-BRCM1017
HP	Printer	C3941A	JPTVOB2337
DELL	Keyboard	L100	CNORH656658907BL05DC
SAST	Modem	AEM-2100	0293

External I/O Cable

Cable Description	Shielding Type	Ferrite Core	Length (m)	From Port	To
Serial Cable	yes	no	1.2	Serial Port of Laptop	Printer
Parallel Cable	yes	no	1.2	Parallel Port of Laptop	Modem
Keyboard Cable	yes	no	1.5	USB port of Laptop	Keyboard
USB Cable	yes	no	0.6	USB Port of Laptop	EUT

Block Diagram of Test Setup



SUMMARY OF TEST RESULTS

FCC Rules	Description of Test	Result
FCC §15.247 (i) & §1.1310 & §2.1093	RF Exposure	Compliance
§15.203	Antenna Requirement	Compliance
§15.207 (a)	Conducted Emissions	Compliance
§15.205, §15.209, §15.247(d)	Radiated Emissions	Compliance
§15.247 (a)(1)	20 dB Bandwidth	Compliance
§15.247(a)(1)	Channel Separation Test	Compliance
§15.247(a)(1)(iii)	Time of Occupancy (Dwell Time)	Compliance
§15.247(a)(1)(iii)	Quantity of hopping channel Test	Compliance
§15.247(b)(1)	Peak Output Power Measurement	Compliance
§15.247(d)	Band Edges	Compliance

FCC §15.247 (i) & §1.1310 & §2.1093- RF EXPOSURE

Applicable Standard

According to §15.247(i) and §1.1310, systems operating under the provisions of this section shall be operated in a manner that ensure that the public is not exposed to radio frequency energy level in excess of the Commission's guideline.

According to KDB447498 D01 General RF Exposure Guidance v05r02 Appendix A, SAR can be exempted if the output power is less than the SAR exclusion threshold:

For $f=2450\text{MHz}$, the output power is less 10mW at distance of 5mm.

Measurement Result

Peak conducted output power= 9.78 dBm

SAR exclusion threshold 10 mW (10dBm) > 9.78 dBm

So the SAR evaluation is not necessary.

FCC §15.203 - ANTENNA REQUIREMENT

Applicable Standard

According to FCC § 15.203, an intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator shall be considered sufficient to comply with the provisions of this Section. The manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

Antenna Connector Construction

The EUT has an internal antenna meeting the requirements, the maximum gain is 3.1 dBi, please refer to the internal photos.

Result: Compliance.

FCC §15.207 (a) – AC LINE CONDUCTED EMISSIONS

Applicable Standard

FCC§15.207

Measurement Uncertainty

Compliance or non-compliance with a disturbance limit shall be determined in the following manner:

If U_{lab} is less than or equal to U_{cisp} of Table 1, then:

- compliance is deemed to occur if no measured disturbance level exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level exceeds the disturbance limit.

If U_{lab} is greater than U_{cisp} of Table 1, then:

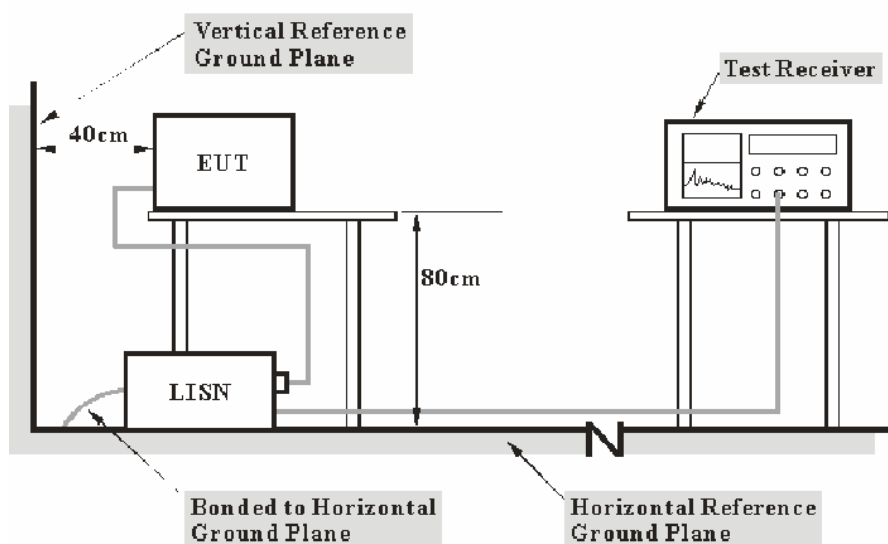
- compliance is deemed to occur if no measured disturbance level, increased by $(U_{lab} - U_{cisp})$, exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level, increased by $(U_{lab} - U_{cisp})$, exceeds the disturbance limit.

Based on CISPR 16-4-2: 2011, measurement uncertainty of conducted disturbance at mains port using AMN at Bay Area Compliance Laboratories Corp. (Dongguan) is 3.46 dB (150 kHz to 30 MHz).

Table 1 – Values of U_{cisp}

Measurement	U_{cisp}
Conducted disturbance at mains port using AMN (150 kHz to 30 MHz)	3.4 dB

EUT Setup



Note: 1. Support units were connected to second LISN.
2. Both of LISNs (AMN) 80 cm from EUT and at the least 80 cm from other units and other metal planes support units.

The setup of EUT is according with per ANSI C63.4-2003 measurement procedure. The specification used was with the FCC Part 15.207 limits.

The spacing between the peripherals was 10 cm.

The adapter was connected to a 120 VAC/60 Hz power source

EMI Test Receiver Setup

The EMI test receiver was set to investigate the spectrum from 150 kHz to 30 MHz.

During the conducted emission test, the EMI test receiver was set with the following configurations:

Frequency Range	IF B/W
150 kHz – 30 MHz	9 kHz

Test Procedure

During the conducted emission test, the adapter was connected to the outlet of the first LISN and the other support equipments were connected to the outlet of the second LISN.

Maximizing procedure was performed on the six (6) highest emissions of the EUT.

All data was recorded in the Quasi-peak and average detection mode.

Corrected Amplitude & Margin Calculation

The basic equation is as follows:

$$V_C = V_R + A_C + VDF$$

Herein,

V_C : corrected voltage amplitude

V_R : reading voltage amplitude

A_C : attenuation caused by cable loss

VDF: voltage division factor of AMN or ISN

The “**Margin**” column of the following data tables indicates the degree of compliance within the applicable limit. For example, a margin of 7dB means the emission is 7dB below the maximum limit. The equation for margin calculation is as follows:

$$\text{Margin} = \text{Limit} - \text{Corrected Amplitude}$$

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	EMI Test Receiver	ESCS 30	830245/006	2013-11-20	2014-11-19
R&S	Two-line V-network	ENV216	3560.6550.12	2014-01-22	2015-01-21
R&S	L.I.S.N	ESH3-Z5	100113	N/A	N/A
BACL	Test Software	BACL-EMC	V1.0-2010	N/A	N/A

* Statement of Traceability: Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed in accordance to NVLAP requirements, traceable to National Primary Standards and International System of Units (SI).

Test Results Summary

According to the recorded data in following table, the EUT complied with the FCC Part 15.207, with the worst margin reading of:

5.32 dB at **0.320 MHz** in the **Neutral** conducted mode

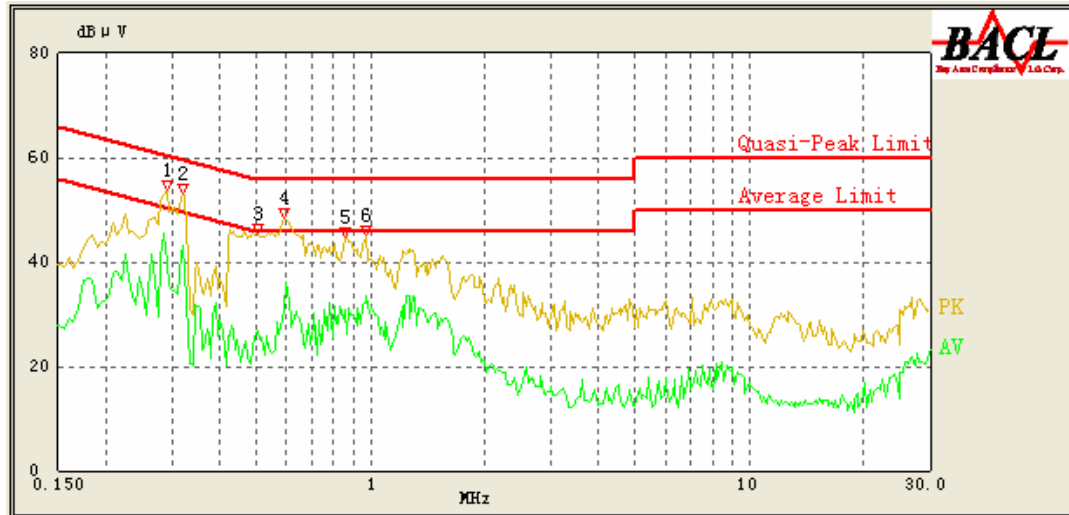
Test Data**Environmental Conditions**

Temperature:	23.5 °C
Relative Humidity:	51 %
ATM Pressure:	101.4 kPa

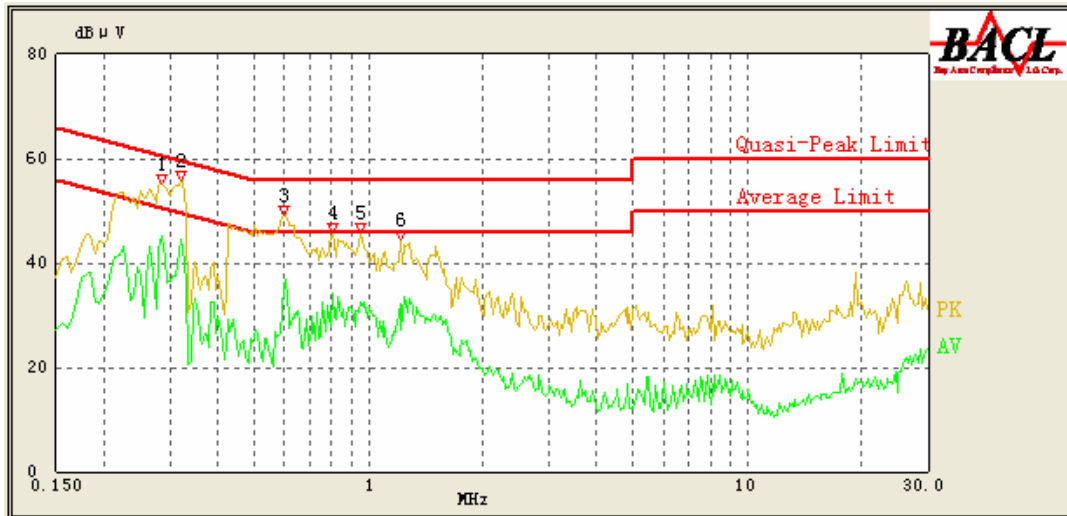
The testing was performed by Allen Qiao on 2014-01-26.

Test Mode: Transmitting

120 V, 60 Hz, Line:



Frequency (MHz)	Cord. Reading (dB μV)	Correction Factor (dB)	Limit (dB μV)	Margin (dB)	Detector (PK/AV/QP)
0.290	47.59	10.13	60.52	12.93	QP
0.290	39.87	10.13	50.52	10.65	AV
0.320	50.96	10.10	59.71	8.75	QP
0.320	43.25	10.10	49.71	6.46	AV
0.505	40.27	9.95	56.00	15.73	QP
0.505	26.97	9.95	46.00	19.03	AV
0.590	45.78	9.89	56.00	10.22	QP
0.590	30.67	9.89	46.00	15.33	AV
0.855	40.09	9.77	56.00	15.91	QP
0.855	30.80	9.77	46.00	15.20	AV
0.975	38.76	9.73	56.00	17.24	QP
0.975	33.45	9.73	46.00	12.55	AV

120 V, 60 Hz, Neutral:

Frequency (MHz)	Cord. Reading (dBμV)	Correction Factor (dB)	Limit (dBμV)	Margin (dB)	Detector (PK/AV/QP)
0.285	50.98	10.56	60.67	9.69	QP
0.285	45.07	10.56	50.67	5.60	AV
0.320	54.39	10.46	59.71	5.32	QP
0.320	44.36	10.46	49.71	5.35	AV
0.600	45.79	9.89	56.00	10.21	QP
0.600	35.69	9.89	46.00	10.31	AV
0.805	37.45	9.82	56.00	18.55	QP
0.805	34.12	9.82	46.00	11.88	AV
0.950	39.16	9.82	56.00	16.84	QP
0.950	32.47	9.82	46.00	13.53	AV
1.215	37.83	9.80	56.00	18.17	QP
1.215	32.28	9.80	46.00	13.72	AV

FCC §15.209, §15.205 & §15.247(d) - SPURIOUS EMISSIONS

Applicable Standard

FCC §15.247 (d); §15.209; §15.205;

Measurement Uncertainty

Compliance or non-compliance with a disturbance limit shall be determined in the following manner:

If U_{lab} is less than or equal to U_{cisp} of Table 1, then:

- compliance is deemed to occur if no measured disturbance level exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level exceeds the disturbance limit.

If U_{lab} is greater than U_{cisp} of Table 1, then:

- compliance is deemed to occur if no measured disturbance level, increased by $(U_{lab} - U_{cisp})$, exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level, increased by $(U_{lab} - U_{cisp})$, exceeds the disturbance limit.

Based on CISPR 16-4-2: 2011, measurement uncertainty of radiated emission at a distance of 3m at Bay Area Compliance Laboratories Corp. (Dongguan) is:

30M~200MHz: 5.0 dB

200M~1GHz: 6.2 dB

1G~6GHz: 4.45 dB

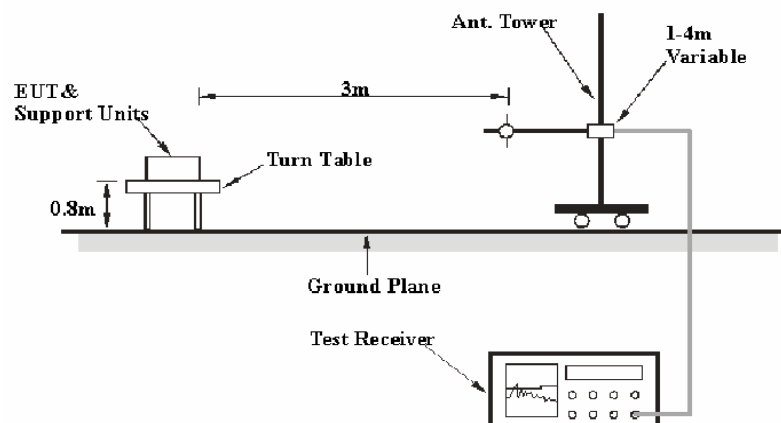
6G~18GHz: 5.23 dB

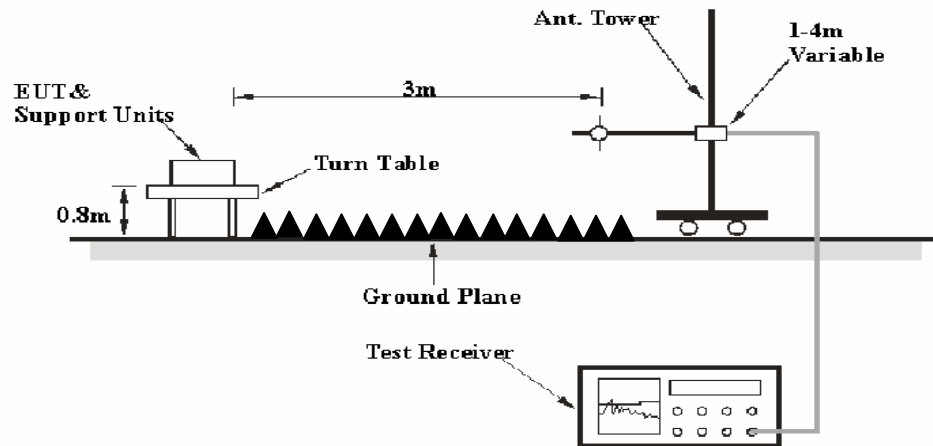
Table 1 – Values of U_{cisp}

Measurement	U_{cisp}
Radiated disturbance (electric field strength at an OATS or in a SAC) (30 MHz to 1000 MHz)	6.3 dB
Radiated disturbance (electric field strength in a FAR) (1 GHz to 6 GHz)	5.2 dB
Radiated disturbance (electric field strength in a FAR) (6 GHz to 18 GHz)	5.5 dB

EUT Setup

Below 1GHz:



Above 1GHz:

The radiated emission tests were performed in the 3 meters test site, using the setup accordance with the ANSI C63.4-2003. The specification used was the FCC 15.209, and FCC 15.247 limits.

The external I/O cables were draped along the test table and formed a bundle 30 to 40 cm long in the middle.

The spacing between the peripherals was 10 cm.

The adapter was connected to a 120 VAC/60 Hz power source

EMI Test Receiver & Spectrum Analyzer Setup

The system was investigated from 30 MHz to 25 GHz.

During the radiated emission test, the EMI test receiver & Spectrum Analyzer Setup were set with the following configurations:

Frequency Range	RBW	Video B/W	IF B/W	Detector
30MHz – 1000 MHz	120 kHz	300 kHz	120kHz	QP
Above 1 GHz	1MHz	3 MHz	/	PK
	1MHz	10 Hz	/	Ave.

Test Procedure

During the radiated emissions, the adapter was connected to the AC floor outlet and the other support equipments were connected to the second AC floor outlet. #

Maximizing procedure was performed on the highest emissions to ensure that the EUT complied with all installation combinations.

Data was recorded in Quasi-peak detection mode for frequency range of 30 MHz - 1 GHz, peak and Average detection modes for frequencies above 1 GHz.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	EMI Test Receiver	ESCI	100224	2013-05-06	2014-05-05
Sunol Sciences	Antenna	JB3	A060611-1	2011-09-06	2014-09-05
HP	Amplifier	8447E	2434A02181	2013-09-06	2014-09-05
R&S	Spectrum Analyzer	FSEM	DE31388	2013-05-07	2014-05-06
ETS LINDGREN	Horn Antenna	3115	000 527 35	2012-09-06	2015-09-05
Mini-Circuit	Amplifier	ZVA-213-S+	054201245	2013-02-19	2014-02-18
R&S	Spectrum Analyzer	FSP 38	100478	2013-06-16	2014-06-15
Ducommun Technologies	Horn Antenna	ARH-4223-02	1007726-01 1304	2013-06-16	2014-06-15
Quinstar	Amplifier	QLW-18405536-JO	15964001001	2013-09-06	2014-09-05

* Statement of Traceability: Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed in accordance to NVLAP requirements, traceable to National Primary Standards and International System of Units (SI).

Corrected Amplitude & Margin Calculation

The Corrected Amplitude is calculated by adding the Antenna Factor and Cable Loss, and subtracting the Amplifier Gain from the Meter Reading. The basic equation is as follows:

$$\text{Corrected Amplitude} = \text{Meter Reading} + \text{Antenna Factor} + \text{Cable Loss} - \text{Amplifier Gain}$$

The “**Margin**” column of the following data tables indicates the degree of compliance with the applicable limit. For example, a margin of 7dB means the emission is 7dB below the limit. The equation for margin calculation is as follows:

$$\text{Margin} = \text{Limit} - \text{Corrected Amplitude}$$

Test Results Summary

According to the recorded data in following table, the EUT complied with the FCC Title 47, Part 15, Subpart C, and section 15.205, 15.209 and 15.247, with the worst margin reading of:

6.80 dB at 2483.5 MHz in the Vertical polarization of EDR Mode (8-DPSK)

Test Data

Environmental Conditions

Temperature:	19.7 °C
Relative Humidity:	49 %
ATM Pressure:	101.4kPa

* The testing was performed by Allen Qiao on 2014-01-26.
Mode: Transmitting

BDR Mode (GFSK):

Frequency	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
(MHz)	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB/m)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	71.24	PK	H	25.65	4.42	0.00	101.31	N/A	N/A
2402	59.81	AV	H	25.65	4.42	0.00	89.88	N/A	N/A
2402	71.56	PK	V	25.65	4.42	0.00	101.63	N/A	N/A
2402	60.13	AV	V	25.65	4.42	0.00	90.20	N/A	N/A
2390	27.53	PK	V	25.61	4.39	0.00	57.53	74.00	16.47
2390	13.93	AV	V	25.61	4.39	0.00	43.93	54.00	10.07
4804	42.68	PK	H	30.59	5.98	27.26	51.99	74.00	22.01
4804	35.33	AV	H	30.59	5.98	27.26	44.64	54.00	9.36
7206	34.59	PK	H	34.09	7.45	26.30	49.83	74.00	24.17
7206	26.53	AV	H	34.09	7.45	26.30	41.77	54.00	12.23
9608	31.96	PK	V	35.96	8.80	26.22	50.50	74.00	23.50
9608	17.94	AV	V	35.96	8.80	26.22	36.48	54.00	17.52
1946.4	36.56	PK	H	24.49	3.78	27.10	37.73	74.00	36.27
1946.4	22.26	AV	H	24.49	3.78	27.10	23.43	54.00	30.57
482.02	25.2	QP	H	18.06	2.67	21.98	23.95	46.00	22.05
Middle Channel: 2441 MHz									
2441	72.13	PK	H	25.75	4.40	0.00	102.28	N/A	N/A
2441	60.58	AV	H	25.75	4.40	0.00	90.73	N/A	N/A
2441	72.06	PK	V	25.75	4.40	0.00	102.21	N/A	N/A
2441	60.37	AV	V	25.75	4.40	0.00	90.52	N/A	N/A
4882	41.35	PK	H	30.79	6.08	27.26	50.96	74.00	23.04
4882	33.26	AV	H	30.79	6.08	27.26	42.87	54.00	11.13
7323	34.12	PK	H	34.38	7.51	26.53	49.48	74.00	24.52
7323	26.21	AV	H	34.38	7.51	26.53	41.57	54.00	12.43
9764	31.81	PK	V	36.33	8.83	25.62	51.35	74.00	22.65
9764	17.87	AV	V	36.33	8.83	25.62	37.41	54.00	16.59
1948.3	36.02	PK	H	24.50	3.78	27.10	37.20	74.00	36.80
1948.3	22.15	AV	H	24.50	3.78	27.10	23.33	54.00	30.67
1521	34.71	PK	H	23.64	3.11	26.99	34.47	74.00	39.53
1521	20.65	AV	H	23.64	3.11	26.99	20.41	54.00	33.59
482.39	25.6	QP	H	18.06	2.67	21.98	24.35	46.00	21.65
High Channel: 2480 MHz									
2480	71.31	PK	H	25.85	4.48	0.00	101.64	N/A	N/A
2480	59.22	AV	H	25.85	4.48	0.00	89.55	N/A	N/A
2480	71.66	PK	V	25.85	4.48	0.00	101.99	N/A	N/A
2480	57.71	AV	V	25.85	4.48	0.00	88.04	N/A	N/A
2483.5	27.91	PK	V	25.86	4.49	0.00	58.26	74.00	15.74
2483.5	16.57	AV	V	25.86	4.49	0.00	46.92	54.00	7.08
4960	40.69	PK	H	31.00	5.90	27.27	50.32	74.00	23.68
4960	32.64	AV	H	31.00	5.90	27.27	42.27	54.00	11.73
7440	36.32	PK	H	34.66	7.58	26.56	52.00	74.00	22.00
7440	27.02	AV	H	34.66	7.58	26.56	42.70	54.00	11.30
9920	34.32	PK	V	36.71	8.87	25.50	54.40	74.00	19.60
9920	20.61	AV	V	36.71	8.87	25.50	40.69	54.00	13.31
1949.6	36.37	PK	H	24.50	3.79	27.10	37.56	74.00	36.44
1949.6	22.25	AV	H	24.50	3.79	27.10	23.44	54.00	30.56
482.97	25.9	QP	H	18.06	2.67	21.98	24.65	46.00	21.35

EDR Mode ($\pi/4$ -DQPSK):

Frequency	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
(MHz)	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB/m)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	70.53	PK	H	25.65	4.42	0.00	100.60	N/A	N/A
2402	56.27	AV	H	25.65	4.42	0.00	86.34	N/A	N/A
2402	70.66	PK	V	25.65	4.42	0.00	100.73	N/A	N/A
2402	56.59	AV	V	25.65	4.42	0.00	86.66	N/A	N/A
2390	27.09	PK	V	25.61	4.39	0.00	57.09	74.00	16.91
2390	13.98	AV	V	25.61	4.39	0.00	43.98	54.00	10.02
4804	40.96	PK	H	30.59	5.98	27.26	50.27	74.00	23.73
4804	30.15	AV	H	30.59	5.98	27.26	39.46	54.00	14.54
7206	36.26	PK	H	34.09	7.45	26.30	51.50	74.00	22.50
7206	26.32	AV	H	34.09	7.45	26.30	41.56	54.00	12.44
9608	33.12	PK	V	35.96	8.80	26.22	51.66	74.00	22.34
9608	17.95	AV	V	35.96	8.80	26.22	36.49	54.00	17.51
1946.4	36.68	PK	H	24.49	3.78	27.10	37.85	74.00	36.15
1946.4	22.13	AV	H	24.49	3.78	27.10	23.30	54.00	30.70
482.02	24.8	QP	H	18.06	2.67	21.98	23.55	46.00	22.45
Middle Channel: 2441 MHz									
2441	71.11	PK	H	25.75	4.40	0.00	101.26	N/A	N/A
2441	56.73	AV	H	25.75	4.40	0.00	86.88	N/A	N/A
2441	71.19	PK	V	25.75	4.40	0.00	101.34	N/A	N/A
2441	56.82	AV	V	25.75	4.40	0.00	86.97	N/A	N/A
4882	40.86	PK	H	30.79	6.08	27.26	50.47	74.00	23.53
4882	30.05	AV	H	30.79	6.08	27.26	39.66	54.00	14.34
7323	36.15	PK	H	34.38	7.51	26.53	51.51	74.00	22.49
7323	26.03	AV	H	34.38	7.51	26.53	41.39	54.00	12.61
9764	31.79	PK	V	36.33	8.83	25.62	51.33	74.00	22.67
9764	17.88	AV	V	36.33	8.83	25.62	37.42	54.00	16.58
1948.3	35.75	PK	H	24.50	3.78	27.10	36.93	74.00	37.07
1948.3	22.18	AV	H	24.50	3.78	27.10	23.36	54.00	30.64
1521	35.28	PK	H	23.64	3.11	26.99	35.04	74.00	38.96
1521	20.69	AV	H	23.64	3.11	26.99	20.45	54.00	33.55
482.39	25.8	QP	H	18.06	2.67	21.98	24.55	46.00	21.45
High Channel: 2480 MHz									
2480	70.04	PK	H	25.85	4.48	0.00	100.37	N/A	N/A
2480	55.92	AV	H	25.85	4.48	0.00	86.25	N/A	N/A
2480	70.49	PK	V	25.85	4.48	0.00	100.82	N/A	N/A
2480	56.03	AV	V	25.85	4.48	0.00	86.36	N/A	N/A
2483.5	28.99	PK	V	25.86	4.49	0.00	59.34	74.00	14.66
2483.5	16.84	AV	V	25.86	4.49	0.00	47.19	54.00	6.81
4960	40.22	PK	H	31.00	5.90	27.27	49.85	74.00	24.15
4960	29.86	AV	H	31.00	5.90	27.27	39.49	54.00	14.51
7440	35.26	PK	H	34.66	7.58	26.56	50.94	74.00	23.06
7440	25.98	AV	H	34.66	7.58	26.56	41.66	54.00	12.34
9920	32.04	PK	V	36.71	8.87	25.50	52.12	74.00	21.88
9920	17.79	AV	V	36.71	8.87	25.50	37.87	54.00	16.13
1949.6	36.73	PK	H	24.50	3.79	27.10	37.92	74.00	36.08
1949.6	22.17	AV	H	24.50	3.79	27.10	23.36	54.00	30.64
482.97	25.3	QP	H	18.06	2.67	21.98	24.05	46.00	21.95

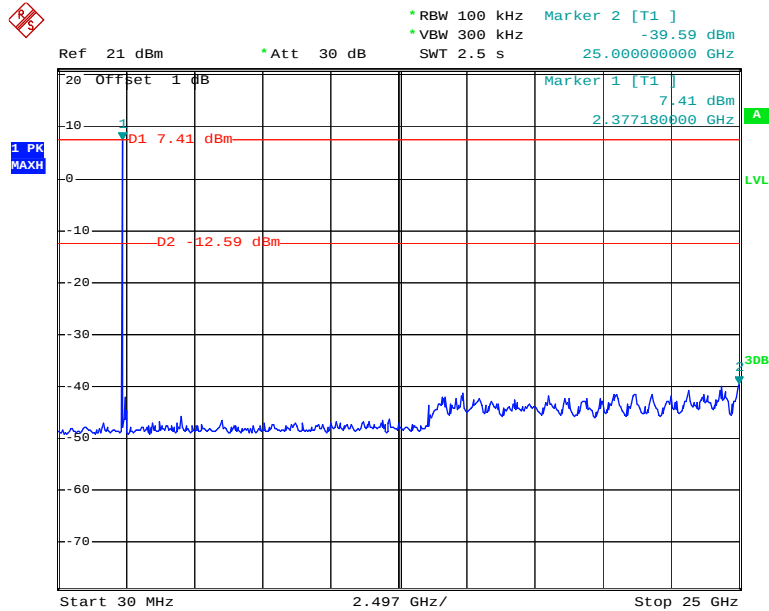
EDR Mode (8-DPSK):

Frequency	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
(MHz)	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB/m)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	71.44	PK	H	25.65	4.42	0.00	101.51	N/A	N/A
2402	56.93	AV	H	25.65	4.42	0.00	87.00	N/A	N/A
2402	71.93	PK	V	25.65	4.42	0.00	102.00	N/A	N/A
2402	55.75	AV	V	25.65	4.42	0.00	85.82	N/A	N/A
2390	27.42	PK	V	25.61	4.39	0.00	57.42	74.00	16.58
2390	13.94	AV	V	25.61	4.39	0.00	43.94	54.00	10.06
4804	41.92	PK	H	30.59	5.98	27.26	51.23	74.00	22.77
4804	31.02	AV	H	30.59	5.98	27.26	40.33	54.00	13.67
7206	34.28	PK	H	34.09	7.45	26.30	49.52	74.00	24.48
7206	25.91	AV	H	34.09	7.45	26.30	41.15	54.00	12.85
9608	31.85	PK	V	35.96	8.80	26.22	50.39	74.00	23.61
9608	17.97	AV	V	35.96	8.80	26.22	36.51	54.00	17.49
1946.4	36.01	PK	H	24.49	3.78	27.10	37.18	74.00	36.82
1946.4	22.09	AV	H	24.49	3.78	27.10	23.26	54.00	30.74
482.02	24.7	QP	H	18.06	2.67	21.98	23.45	46.00	22.55
Middle Channel: 2441 MHz									
2441	71.16	PK	H	25.75	4.40	0.00	101.31	N/A	N/A
2441	56.37	AV	H	25.75	4.40	0.00	86.52	N/A	N/A
2441	71.53	PK	V	25.75	4.40	0.00	101.68	N/A	N/A
2441	56.43	AV	V	25.75	4.40	0.00	86.58	N/A	N/A
4882	40.68	PK	H	30.79	6.08	27.26	50.29	74.00	23.71
4882	30.23	AV	H	30.79	6.08	27.26	39.84	54.00	14.16
7323	34.16	PK	H	34.38	7.51	26.53	49.52	74.00	24.48
7323	25.31	AV	H	34.38	7.51	26.53	40.67	54.00	13.33
9764	31.31	PK	V	36.33	8.83	25.62	50.85	74.00	23.15
9764	17.86	AV	V	36.33	8.83	25.62	37.40	54.00	16.60
1948.3	36.59	PK	H	24.50	3.78	27.10	37.77	74.00	36.23
1948.3	22.22	AV	H	24.50	3.78	27.10	23.40	54.00	30.60
1521	34.06	PK	H	23.64	3.11	26.99	33.82	74.00	40.18
1521	20.73	AV	H	23.64	3.11	26.99	20.49	54.00	33.51
482.39	24.5	QP	H	18.06	2.67	21.98	23.25	46.00	22.75
High Channel: 2480 MHz									
2480	70.99	PK	H	25.85	4.48	0.00	101.32	N/A	N/A
2480	55.67	AV	H	25.85	4.48	0.00	86.00	N/A	N/A
2480	71.64	PK	V	25.85	4.48	0.00	101.97	N/A	N/A
2480	55.88	AV	V	25.85	4.48	0.00	86.21	N/A	N/A
2483.5	28.87	PK	V	25.86	4.49	0.00	59.22	74.00	14.78
2483.5	16.85	AV	V	25.86	4.49	0.00	47.20	54.00	6.80
4960	39.85	PK	H	31.00	5.90	27.27	49.48	74.00	24.52
4960	29.93	AV	H	31.00	5.90	27.27	39.56	54.00	14.44
7440	34.59	PK	H	34.66	7.58	26.56	50.27	74.00	23.73
7440	25.13	AV	H	34.66	7.58	26.56	40.81	54.00	13.19
9920	31.39	PK	V	36.71	8.87	25.50	51.47	74.00	22.53
9920	17.81	AV	V	36.71	8.87	25.50	37.89	54.00	16.11
1949.6	36.67	PK	H	24.50	3.79	27.10	37.86	74.00	36.14
1949.6	22.2	AV	H	24.50	3.79	27.10	23.39	54.00	30.61
482.97	25.6	QP	H	18.06	2.67	21.98	24.35	46.00	21.65

Conducted Spurious Emissions at Antenna Port

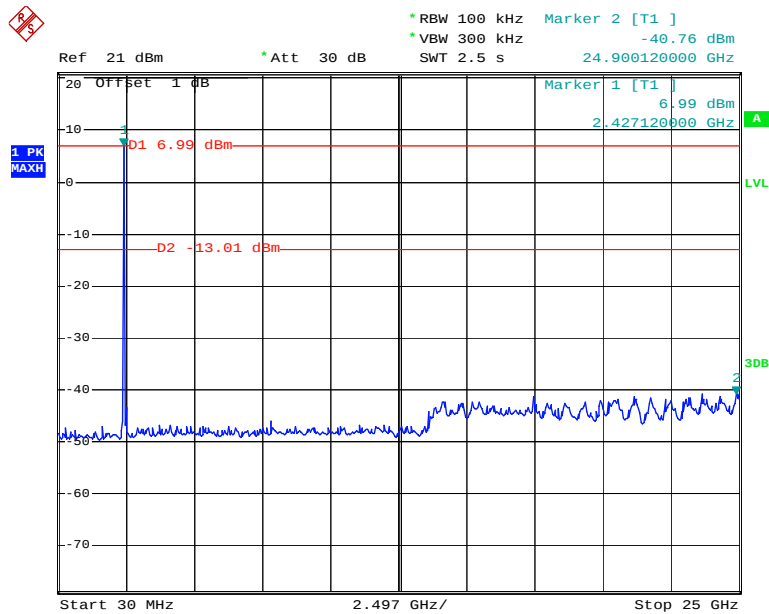
BDR Mode (GFSK):

Low Channel



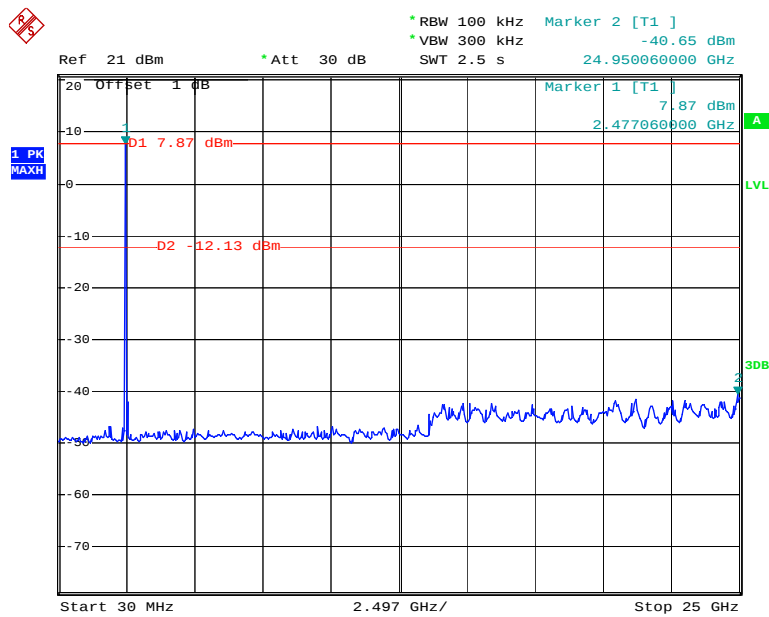
Date: 26.JAN.2014 15:20:13

Middle Channel



Date: 26.JAN.2014 15:27:10

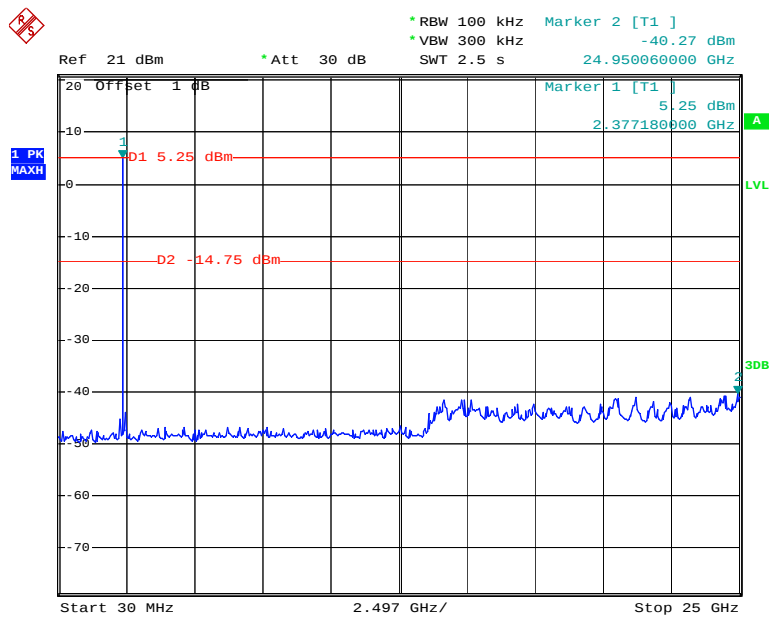
High Channel



Date: 26.JAN.2014 15:28:24

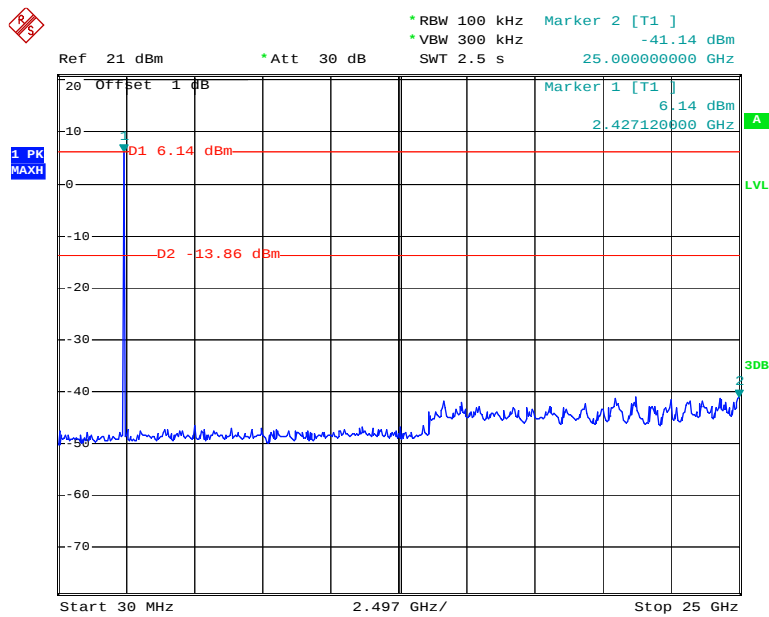
EDR Mode ($\pi/4$ -DQPSK):

Low Channel



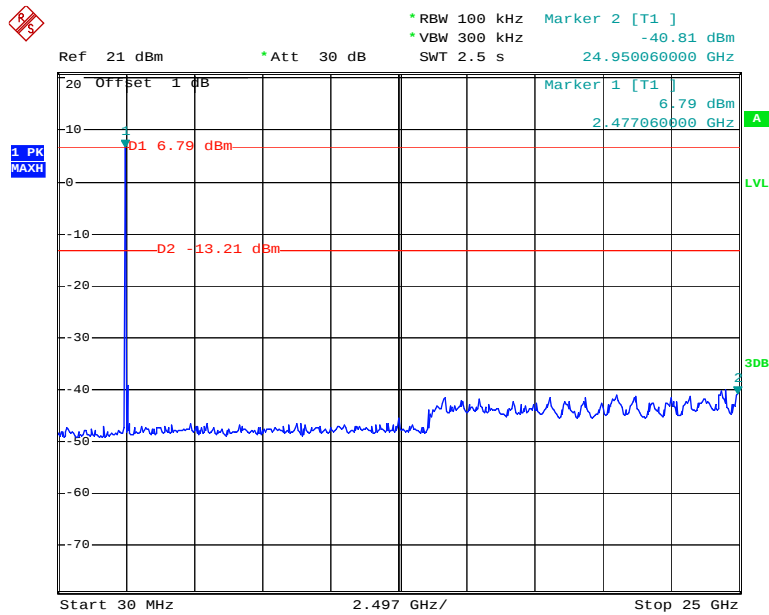
Date: 26.JAN.2014 15:22:20

Middle Channel



Date: 26.JAN.2014 15:26:05

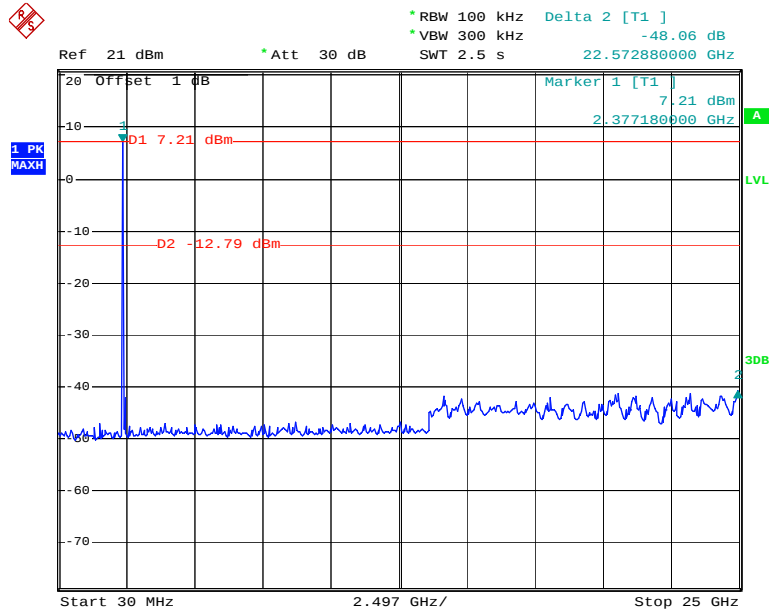
High Channel



Date: 26.JAN.2014 15:30:35

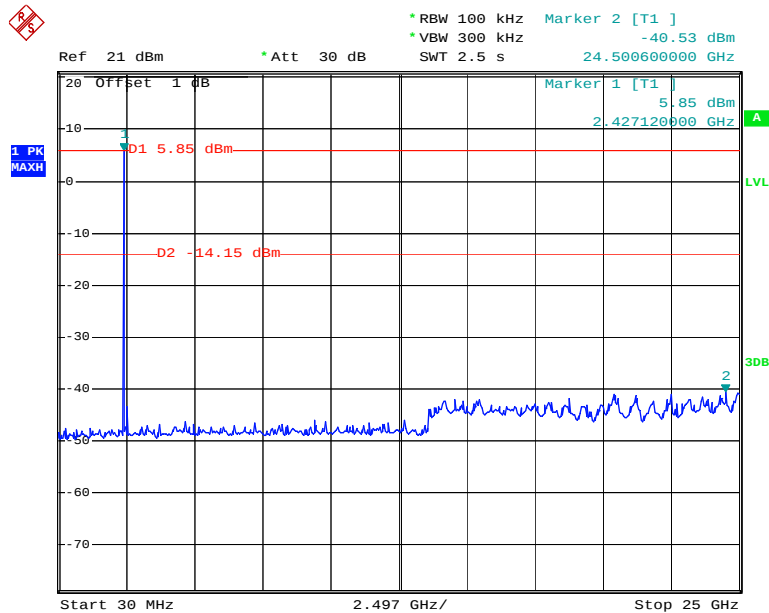
EDR Mode (8-DPSK):

Low Channel



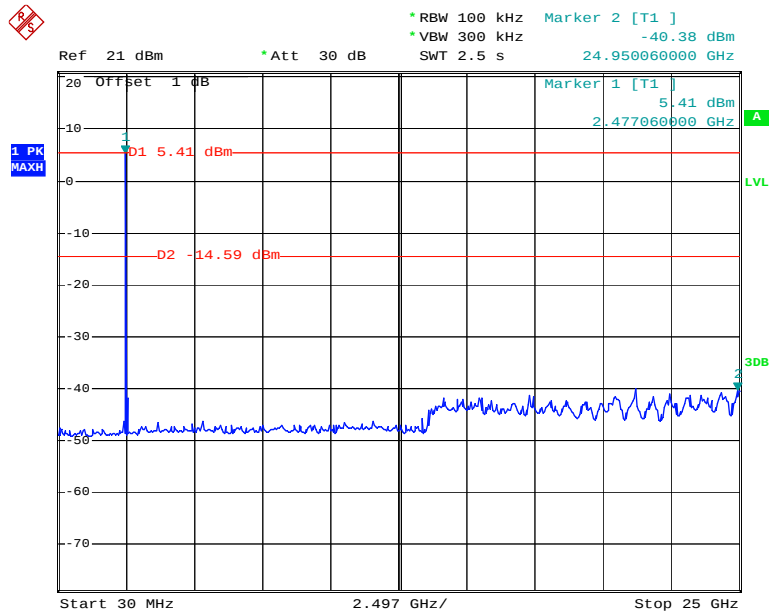
Date: 26.JAN.2014 15:23:06

Middle Channel



Date: 26.JAN.2014 15:24:27

High Channel



Date: 26.JAN.2014 15:32:31

FCC §15.247(a) (1) - CHANNEL SEPARATION TEST**Applicable Standard**

Frequency hopping systems shall have hopping channel carrier frequencies separated by a minimum of 25 kHz or the 20dB bandwidth of the hopping channel, whichever is greater. Alternatively, frequency hopping systems operating in the 2400-2483.50 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20dB bandwidth of the hopping channel, whichever is greater provided the systems operate with an output power no greater than 125 mW.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2013-06-16	2014-06-15

* Statement of Traceability: Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed in accordance to NVLAP requirements, traceable to National Primary Standards and International System of Units (SI).

Test Procedure

1. Set the EUT in transmitting mode, spectrum Bandwidth was set at 100 kHz, maxhold the channel.
2. Set the adjacent channel of the EUT maxhold another truce
3. Measure the channel separation.

Test Data**Environmental Conditions**

Temperature:	23.5 ° C
Relative Humidity:	51 %
ATM Pressure:	101.4 kPa

* The testing was performed by Allen Qiao on 2014-01-26.

Test Result: Compliance.

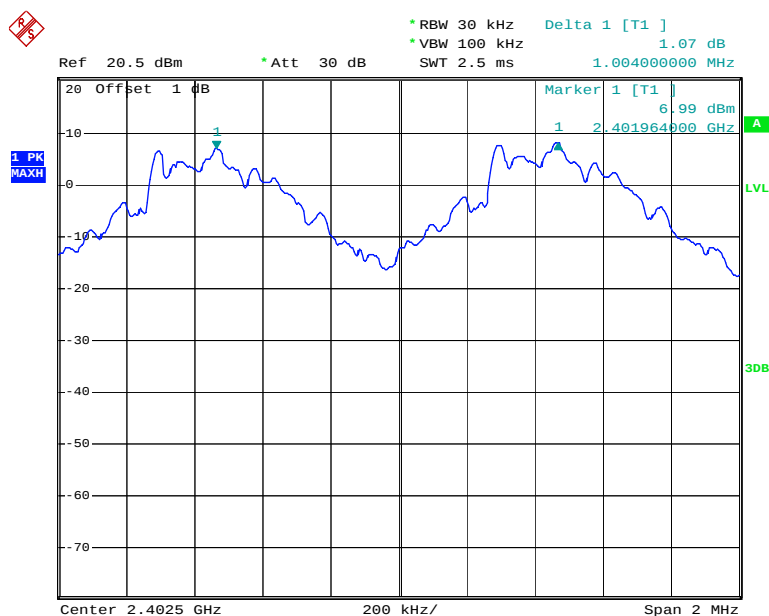
Please refer to following tables and plots

Test Mode: Transmitting

Mode	Channel	Frequency (MHz)	Channel Separation (MHz)	Limit (MHz)	Result
BDR Mode (GFSK)	Low	2402	1.004	0.613	Pass
	Adjacent	2403			
	Middle	2441	1.004	0.613	Pass
	Adjacent	2440			
	High	2480	1.004	0.613	Pass
	Adjacent	2479			
EDR Mode ($\pi/4$ -DQPSK):	Low	2402	1.000	0.816	Pass
	Adjacent	2403			
	Middle	2441	1.000	0.816	Pass
	Adjacent	2440			
	High	2480	1.004	0.816	Pass
	Adjacent	2479			
EDR Mode (8-DPSK):	Low	2402	1.004	0.811	Pass
	Adjacent	2403			
	Middle	2441	1.000	0.811	Pass
	Adjacent	2440			
	High	2480	1.000	0.811	Pass
	Adjacent	2479			

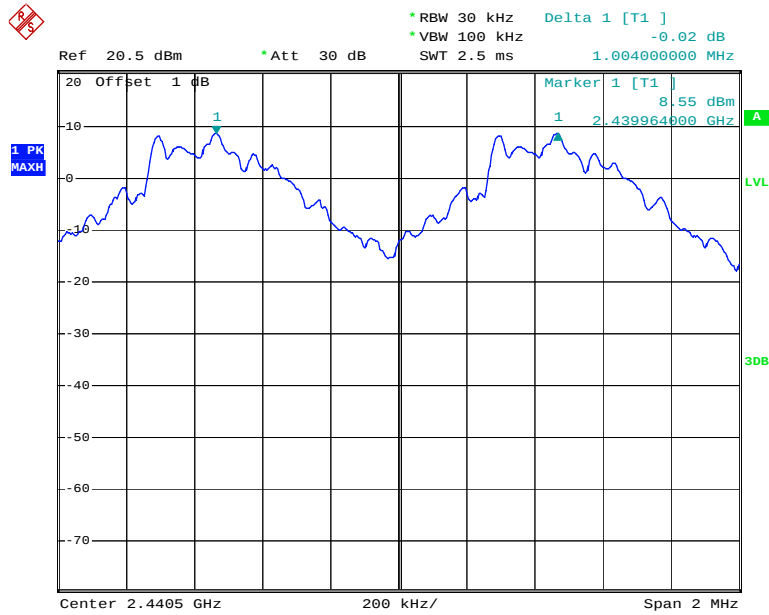
BDR Mode (GFSK):

Low Channel



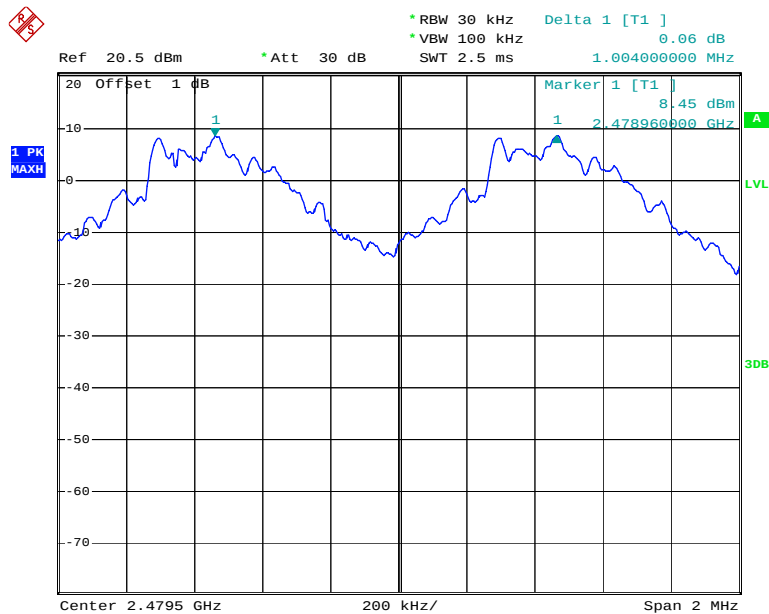
Date: 26.JAN.2014 16:06:24

Middle Channel



Date: 26.JAN.2014 16:07:32

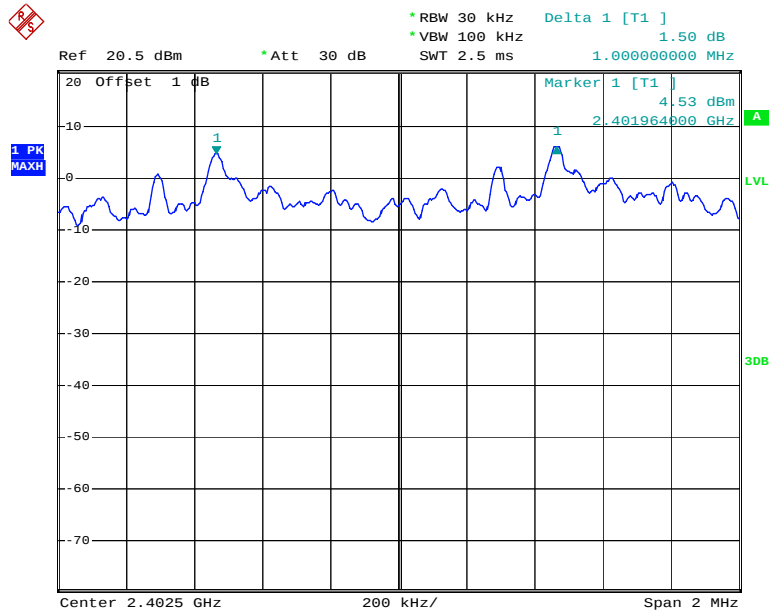
High Channel



Date: 26.JAN.2014 16:07:56

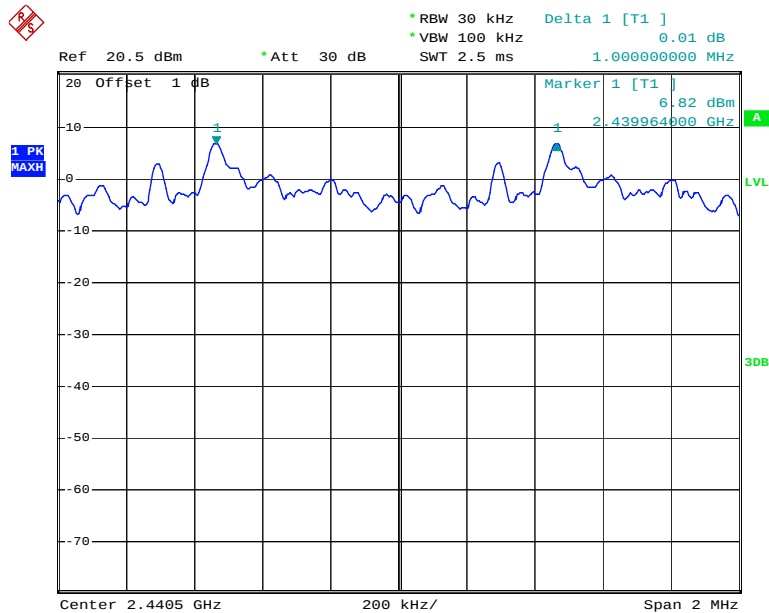
EDR Mode ($\pi/4$ -DQPSK):

Low Channel



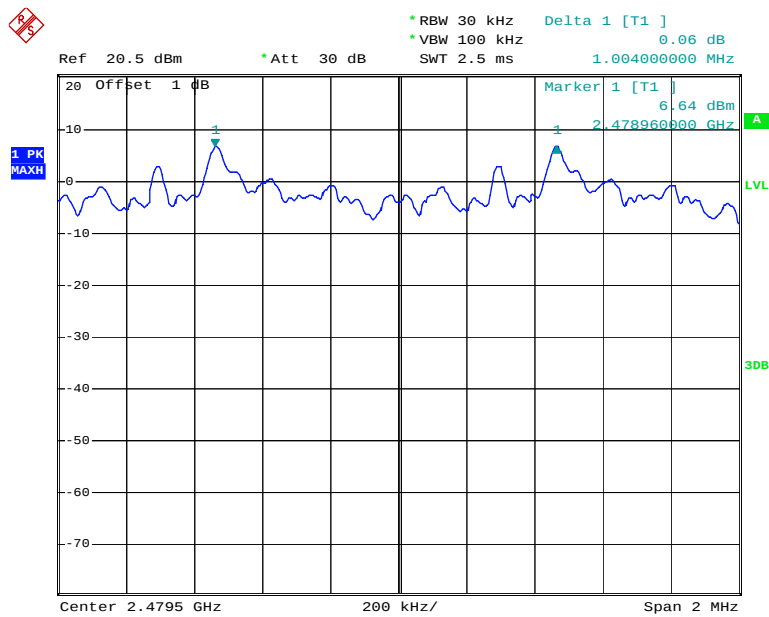
Date: 26.JAN.2014 16:25:31

Middle Channel



Date: 26.JAN.2014 16:24:17

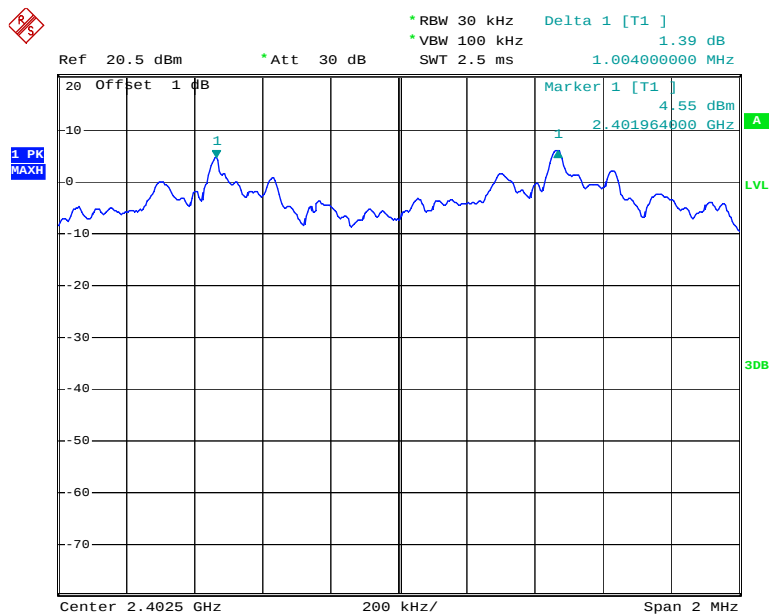
High Channel



Date: 26.JAN.2014 16:23:03

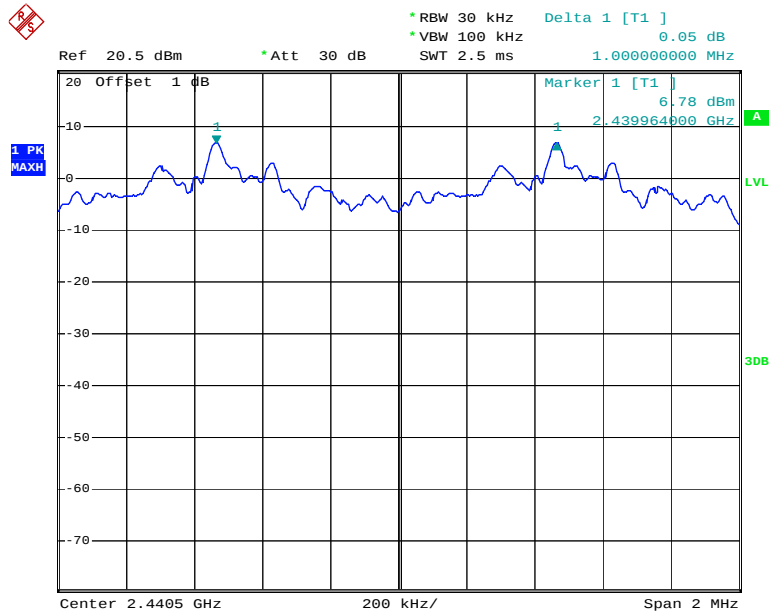
EDR Mode (8-DPSK):

Low Channel



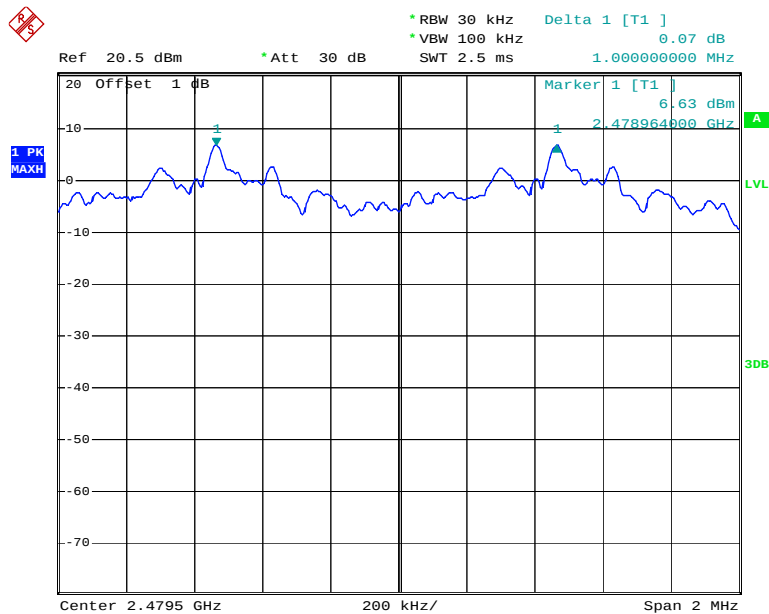
Date: 26.JAN.2014 16:26:43

Middle Channel



Date: 26.JAN.2014 16:27:12

High Channel



Date: 26.JAN.2014 16:27:51

FCC §15.247(a) (1) – 20 dB BANDWIDTH TESTING**Applicable Standard**

Alternatively, frequency hopping systems operating in the 2400–2483.5 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20 dB bandwidth of the hopping channel, whichever is greater, provided the systems operate with an output power no greater than 125 mW.

Test Procedure

1. Check the calibration of the measuring instrument using either an internal calibrator or a known signal from an external generator.
2. Position the EUT on the test table without connection to measurement instrument. Turn on the EUT. Then set it to any one convenient frequency within its operating range. Set a reference level on the measuring instrument equal to the highest peak value.
3. Measure the frequency difference of two frequencies that were attenuated 20 dB from the reference level. Record the frequency difference as the emission bandwidth.
4. Repeat above procedures until all frequencies measured were complete.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2013-06-16	2014-06-15

* Statement of Traceability: Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed in accordance to NVLAP requirements, traceable to National Primary Standards and International System of Units (SI).

Test Data**Environmental Conditions**

Temperature:	23.5 ° C
Relative Humidity:	51 %
ATM Pressure:	101.4 kPa

* The testing was performed by Allen Qiao on 2014-01-26.

Test Result: Compliance.

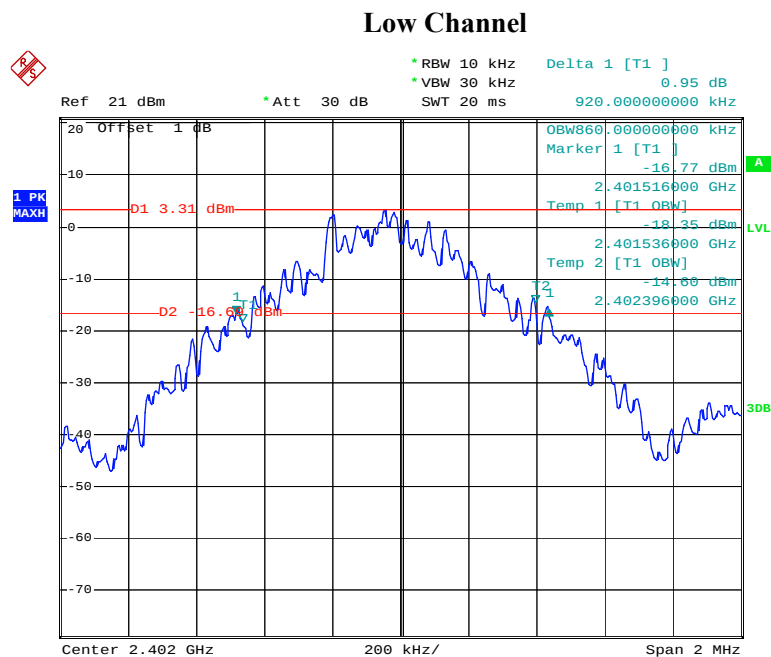
Please refer to following tables and plots

Test Mode: Transmitting

Mode	Channel	Frequency (MHz)	20 dB Bandwidth (MHz)
BDR Mode (GFSK)	Low	2402	0.920
	Middle	2441	0.920
	High	2480	0.920
EDR Mode ($\pi/4$ -DQPSK):	Low	2402	1.224
	Middle	2441	1.216
	High	2480	1.220
EDR Mode (8-DPSK):	Low	2402	1.208
	Middle	2441	1.208
	High	2480	1.216

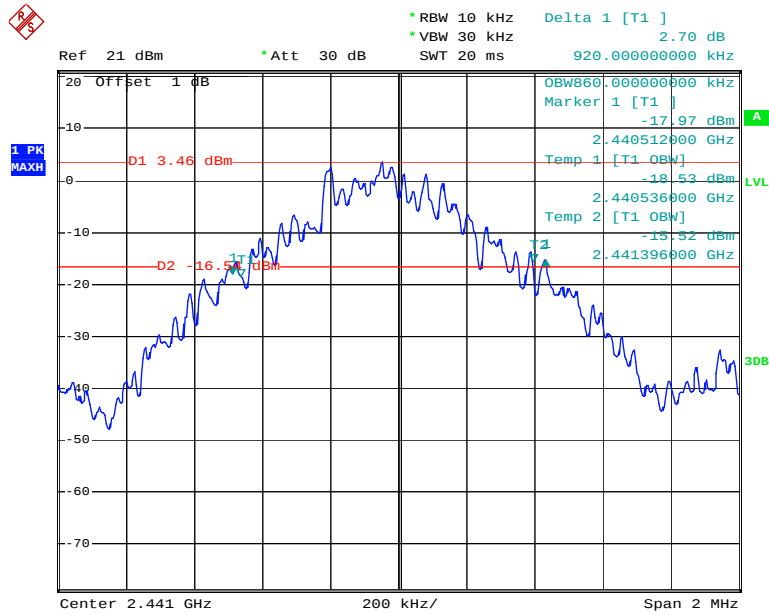
Please refer to the following plots.

BDR Mode (GFSK):



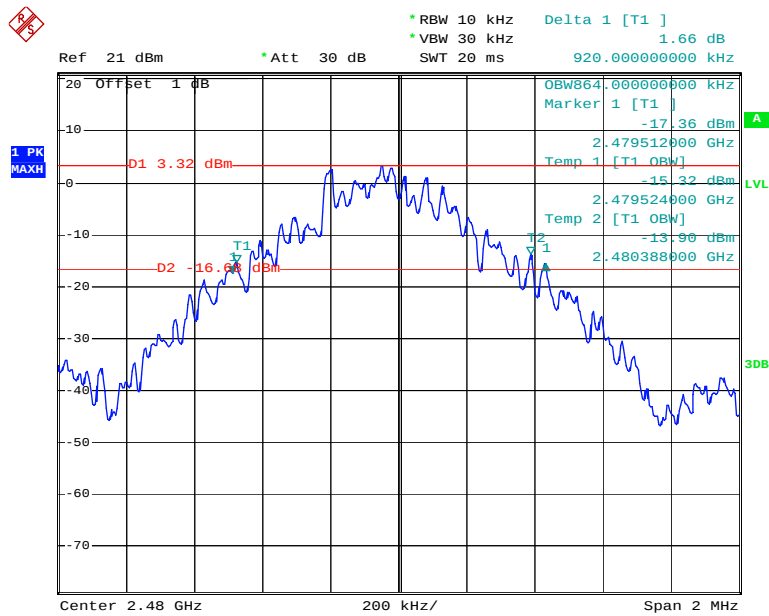
Date: 26.JAN.2014 14:59:14

Middle Channel

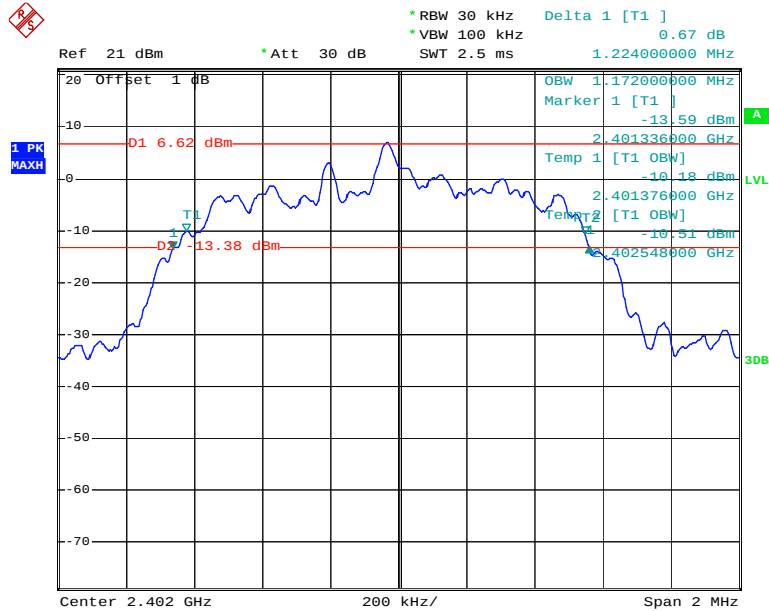


Date: 26.JAN.2014 14:54:27

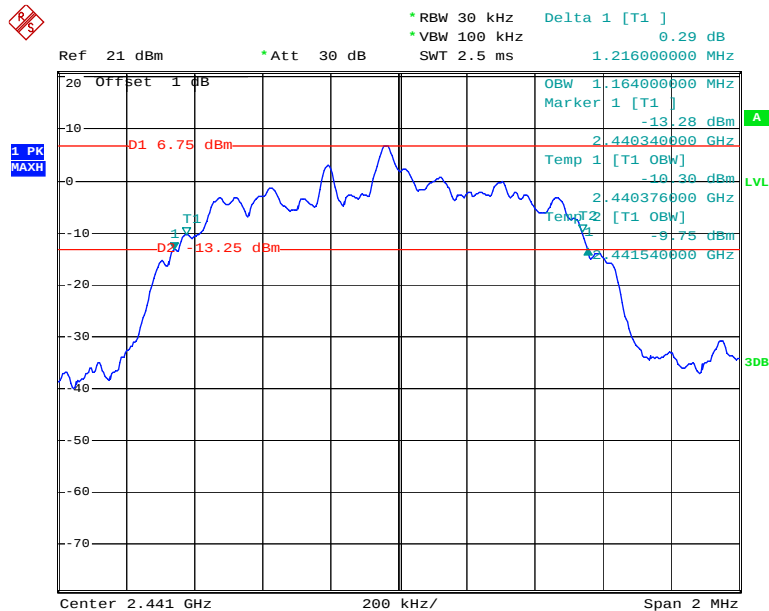
High Channel



Date: 26.JAN.2014 14:57:18

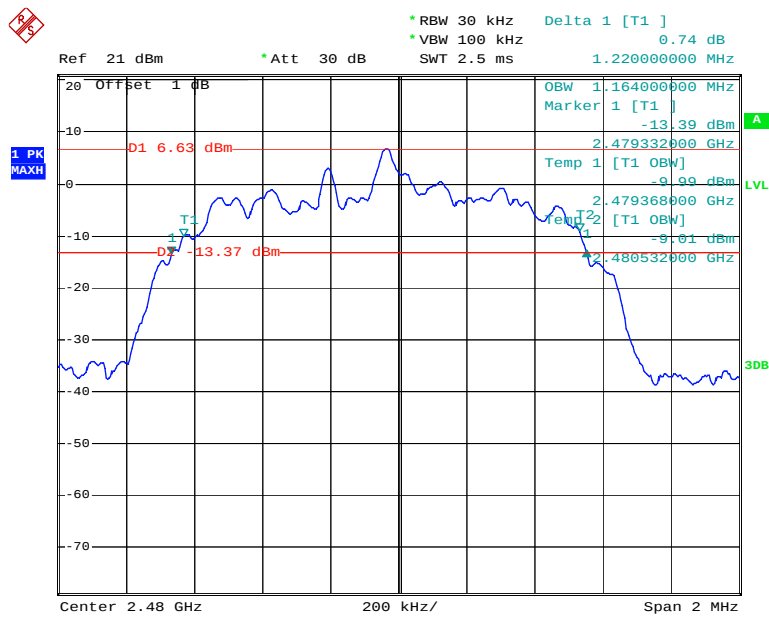
*EDR Mode ($\pi/4$ -DQPSK):***Low Channel**

Date: 26.JAN.2014 14:44:26

Middle Channel

Date: 26.JAN.2014 14:46:24

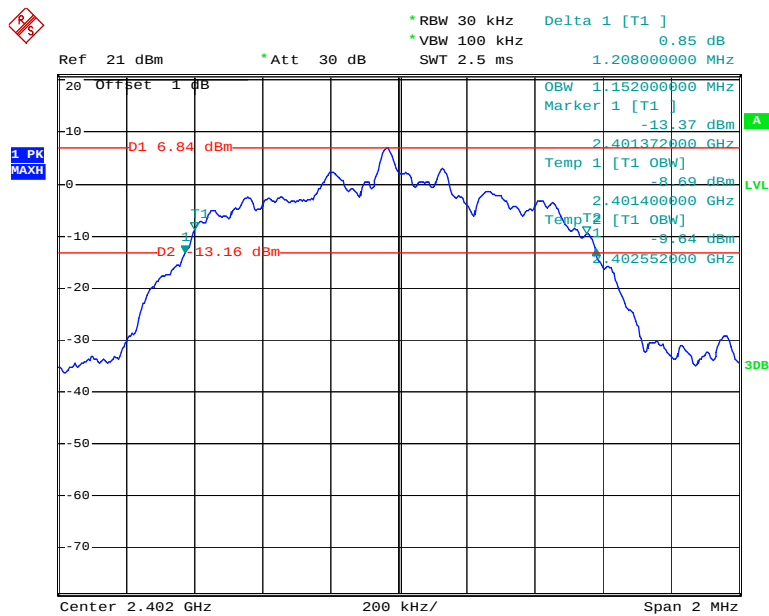
High Channel



Date: 26.JAN.2014 14:48:01

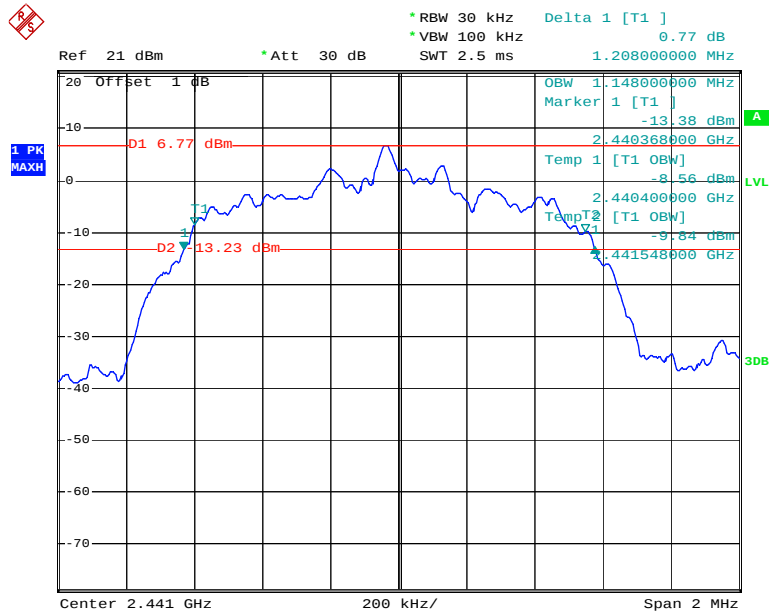
EDR Mode (8-DPSK):

Low Channel



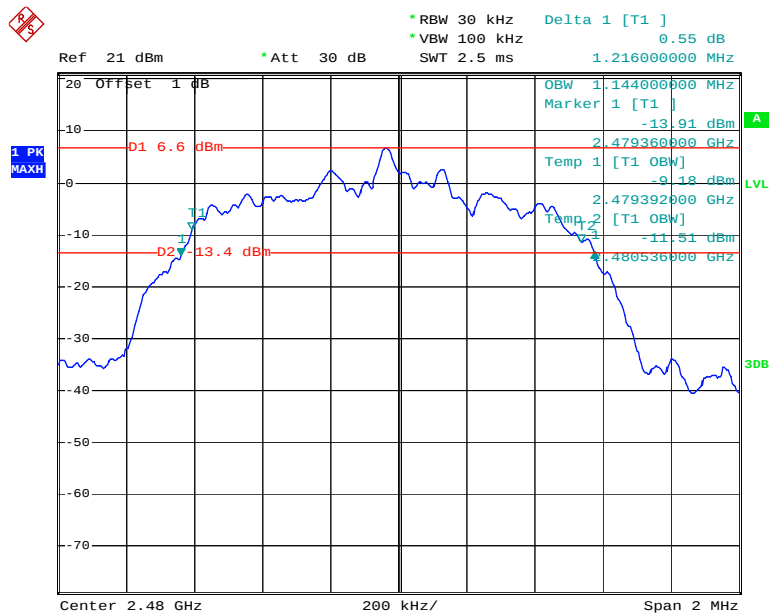
Date: 26.JAN.2014 14:42:40

Middle Channel



Date: 26.JAN.2014 14:41:12

High Channel



Date: 26.JAN.2014 14:39:45

FCC §15.247(a) (1) (iii) - QUANTITY OF HOPPING CHANNEL TEST**Applicable Standard**

Frequency hopping systems in the 2400–2483.5 MHz band shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

Test Procedure

1. Check the calibration of the measuring instrument (SA) using either an internal calibrator or a known signal from an external generator.
2. Set the EUT in hopping mode from first channel to last.
3. By using the Max-Hold function record the Quantity of the channel.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2013-06-16	2014-06-15

* Statement of Traceability: Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed in accordance to NVLAP requirements, traceable to National Primary Standards and International System of Units (SI).

Test Data**Environmental Conditions**

Temperature:	23.5 °C
Relative Humidity:	51 %
ATM Pressure:	101.4 kPa

* The testing was performed by Allen Qiao on 2014-01-26.

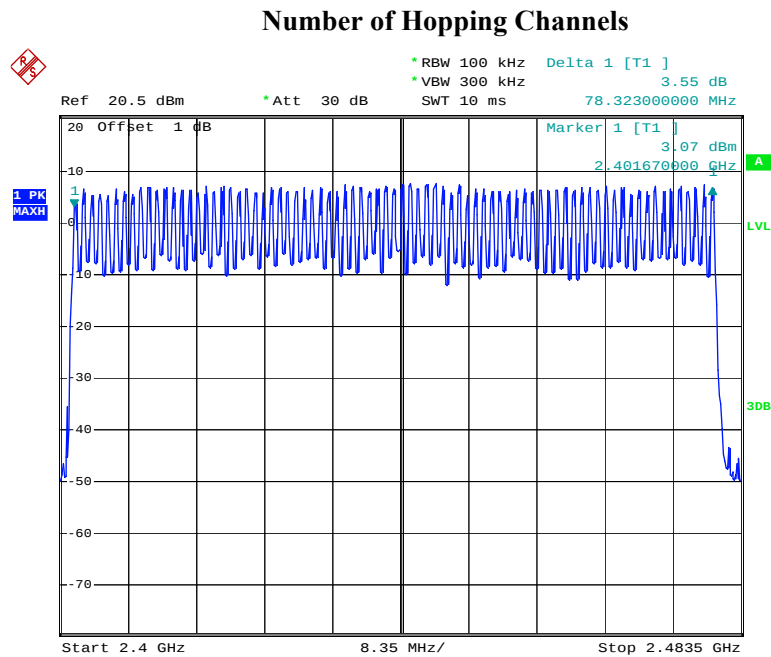
Test Result: Compliance.

Please refer to following tables and plots

Test Mode: Transmitting

BDR Mode (GFSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥ 15

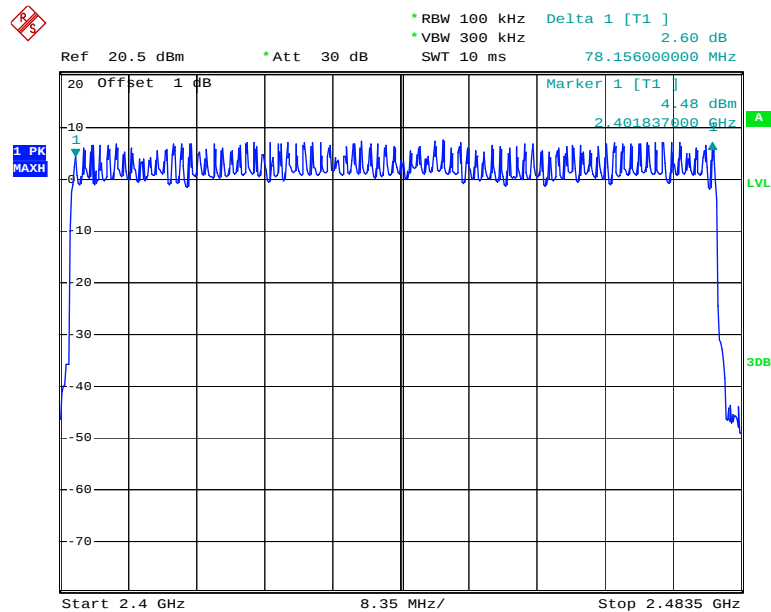


Date: 26.JAN.2014 16:04:57

EDR Mode ($\pi/4$ -DQPSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥ 15

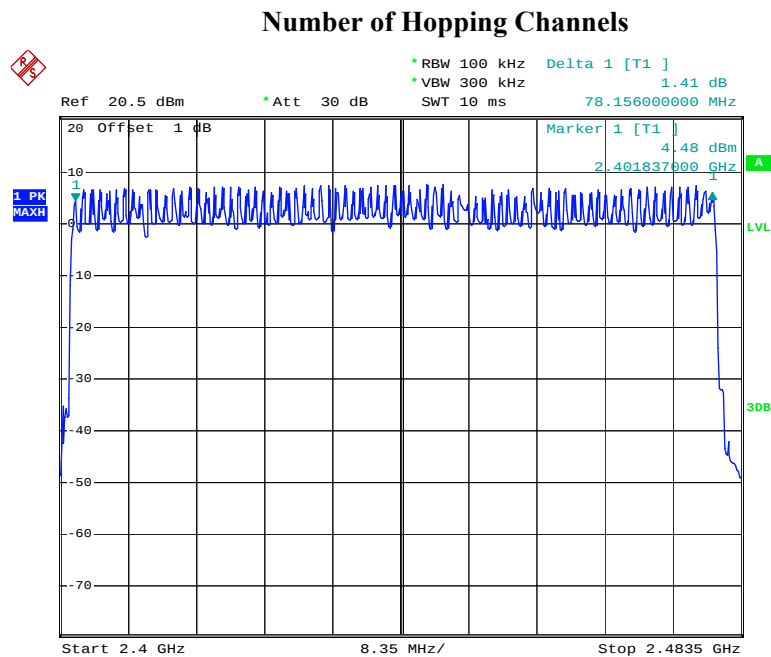
Number of Hopping Channels



Date: 26.JAN.2014 16:03:30

EDR Mode (8-DPSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥ 15



Date: 26.JAN.2014 16:01:40

FCC §15.247(a) (1) (iii) - TIME OF OCCUPANCY (DWELL TIME)**Applicable Standard**

Frequency hopping systems in the 2400-2483.5 MHz shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

Test Procedure

The EUT was worked in channel hopping; Spectrum SPAN was set as 0. Sweep was set as $0.4 \times \text{channel no. (s)}$, the quantity of pulse was get from single sweep. In addition, the time of single pulses was tested.

Dwell Time = time slot length * hop rate / number of hopping channels * 31.6s
Hop rate = 1600/s

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2013-06-16	2014-06-15

* Statement of Traceability: Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed in accordance to NVLAP requirements, traceable to National Primary Standards and International System of Units (SI).

Test Data**Environmental Conditions**

Temperature:	23.5 °C
Relative Humidity:	51 %
ATM Pressure:	101.4 kPa

* The testing was performed by Allen Qiao on 2014-01-26.

Test Result: Compliance.

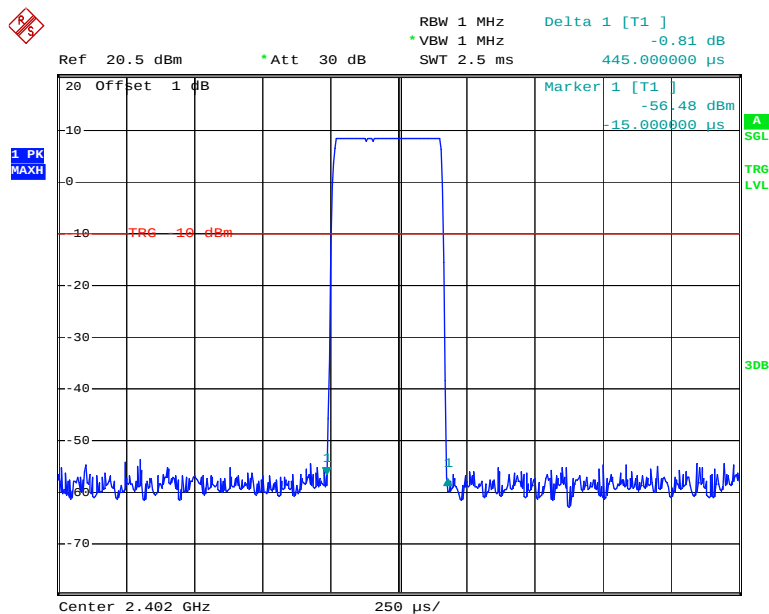
Please refer to following tables and plots

Test Mode: Transmitting

BDR Mode (GFSK):

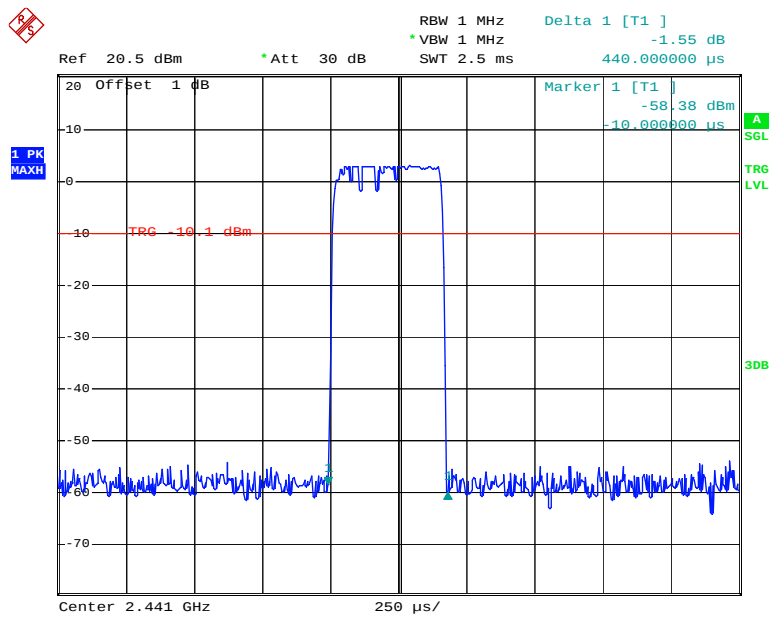
Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
DH1	Low	0.445	0.142	0.4	Pass
	Middle	0.440	0.141	0.4	Pass
	High	0.445	0.142	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/2/79) $\times$ 31.6 s				
DH3	Low	1.705	0.273	0.4	Pass
	Middle	1.705	0.273	0.4	Pass
	High	1.705	0.273	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/4/79) $\times$ 31.6 s				
DH5	Low	2.963	0.316	0.4	Pass
	Middle	2.963	0.316	0.4	Pass
	High	2.947	0.314	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/6/79) $\times$ 31.6 s				

DH1: Low Channel



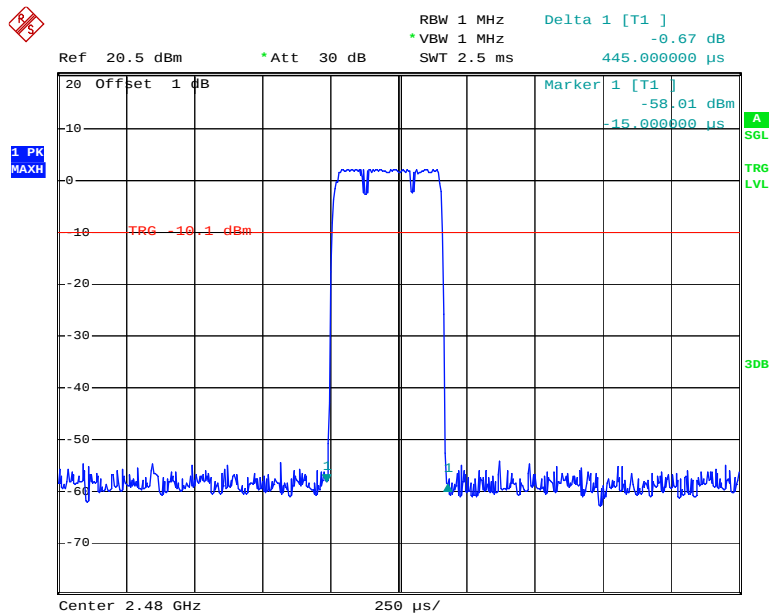
Date: 26.JAN.2014 16:43:38

DH1: Middle Channel



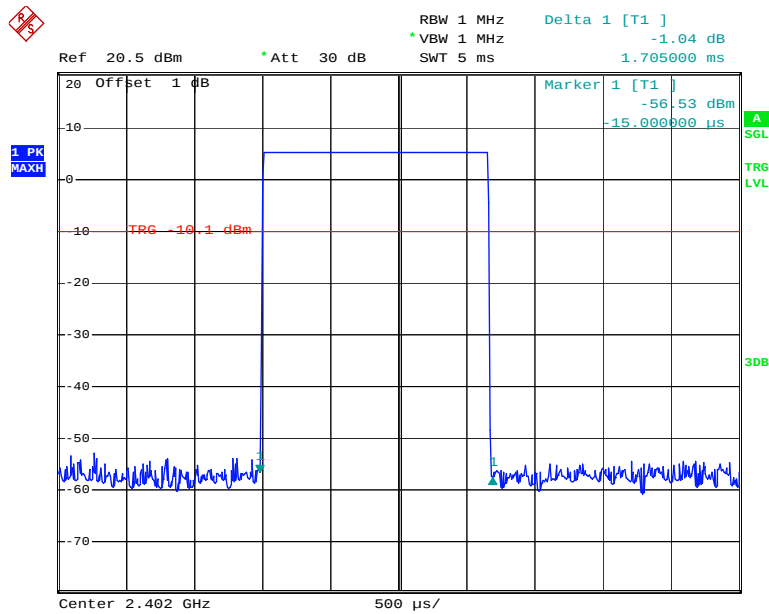
Date: 26.JAN.2014 16:44:34

DH1: High Channel



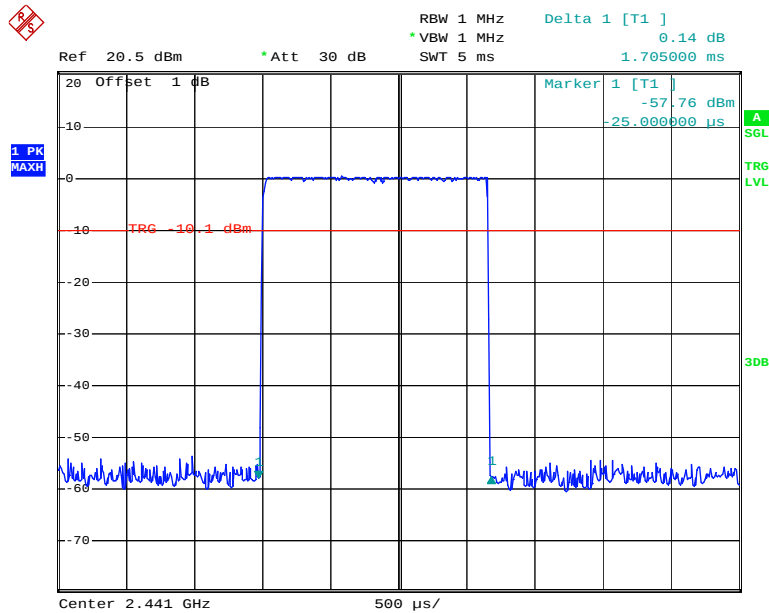
Date: 26.JAN.2014 16:44:44

DH3: Low Channel



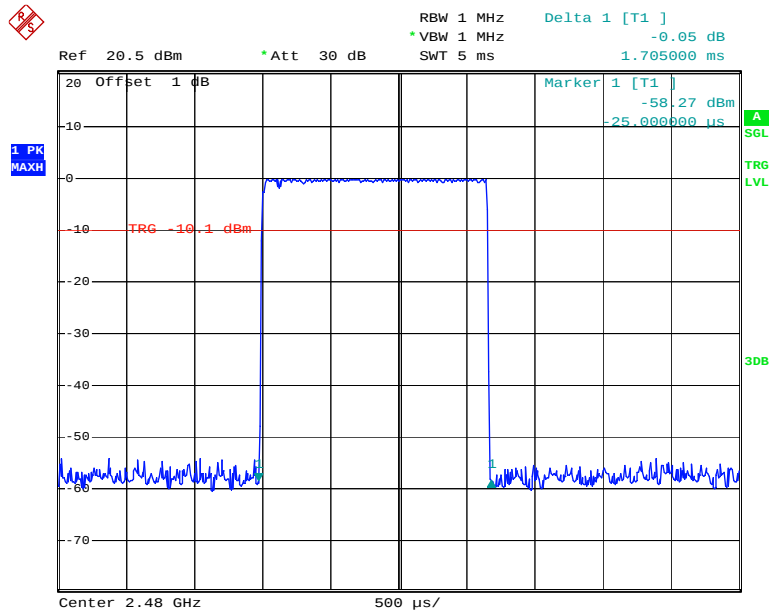
Date: 26.JAN.2014 17:18:31

DH3: Middle Channel



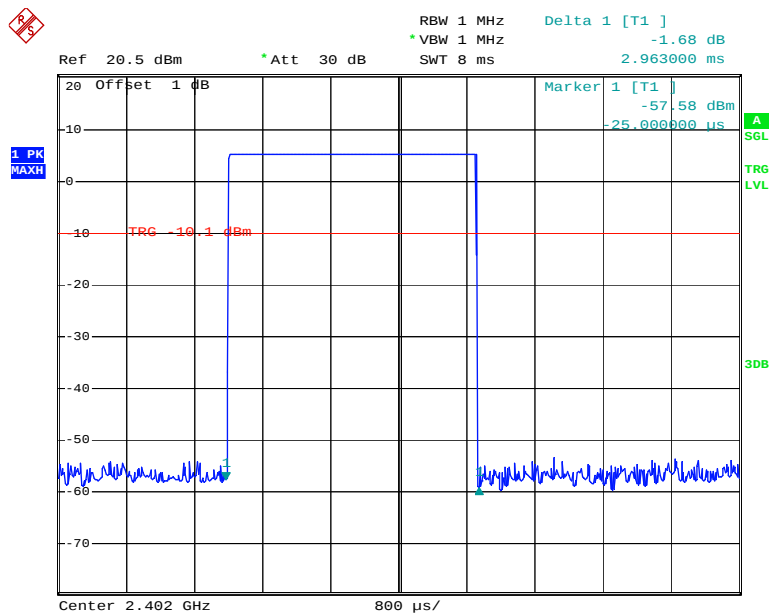
Date: 26.JAN.2014 17:18:13

DH3: High Channel



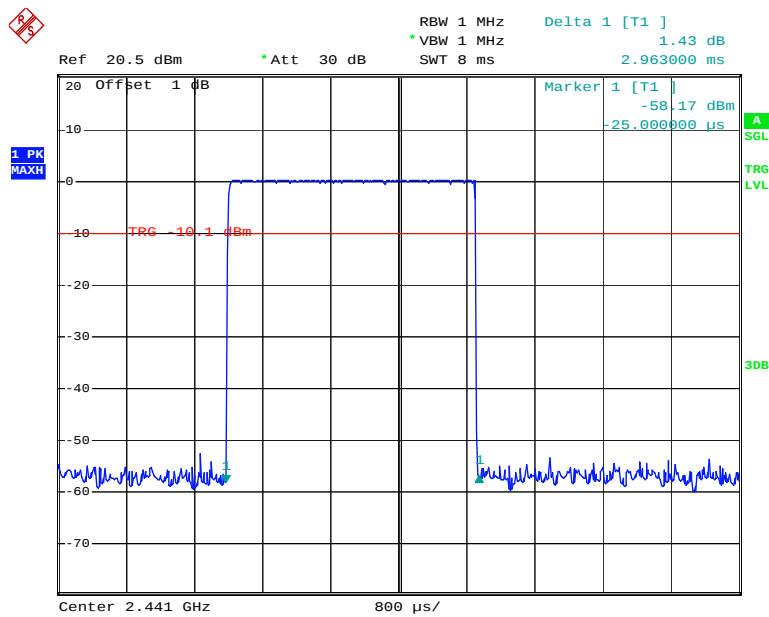
Date: 26.JAN.2014 17:18:04

DH5: Low Channel



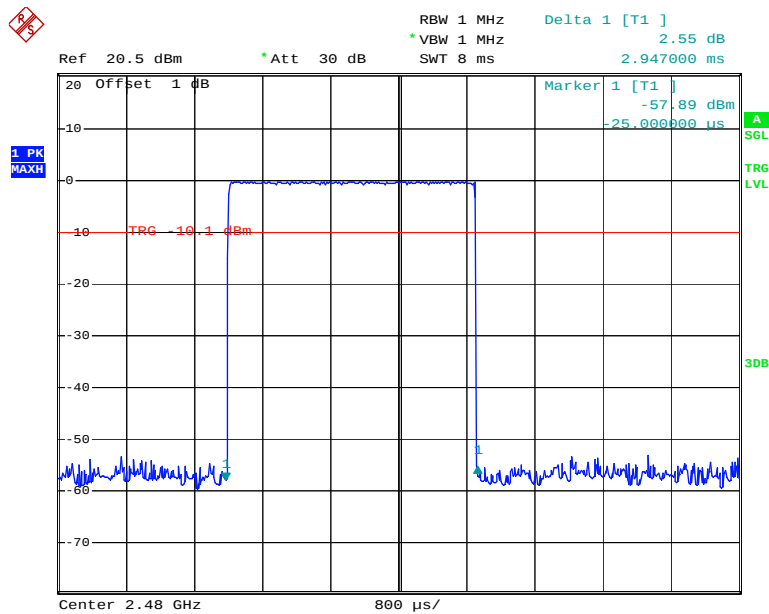
Date: 26.JAN.2014 17:30:19

DH5: Middle Channel



Date: 26.JAN.2014 17:30:34

DH5: High Channel

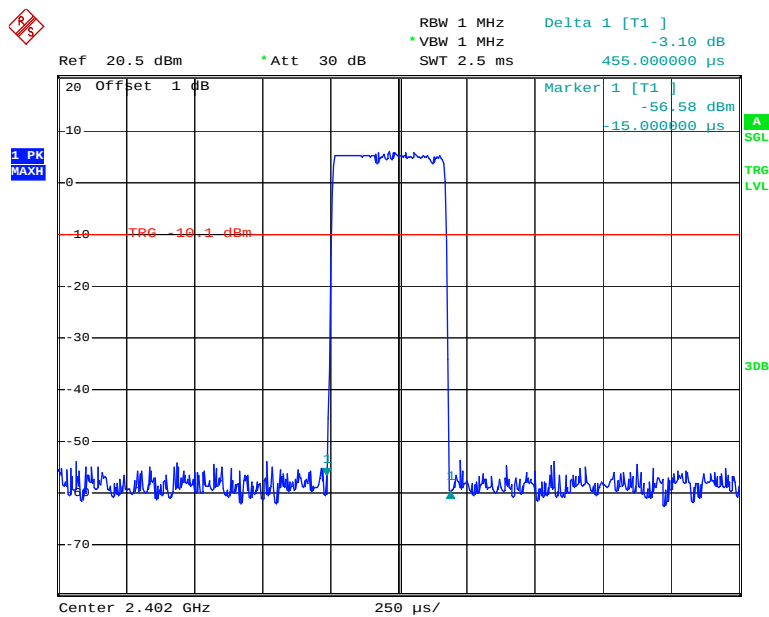


Date: 26.JAN.2014 17:30:43

EDR Mode ($\pi/4$ -DQPSK):

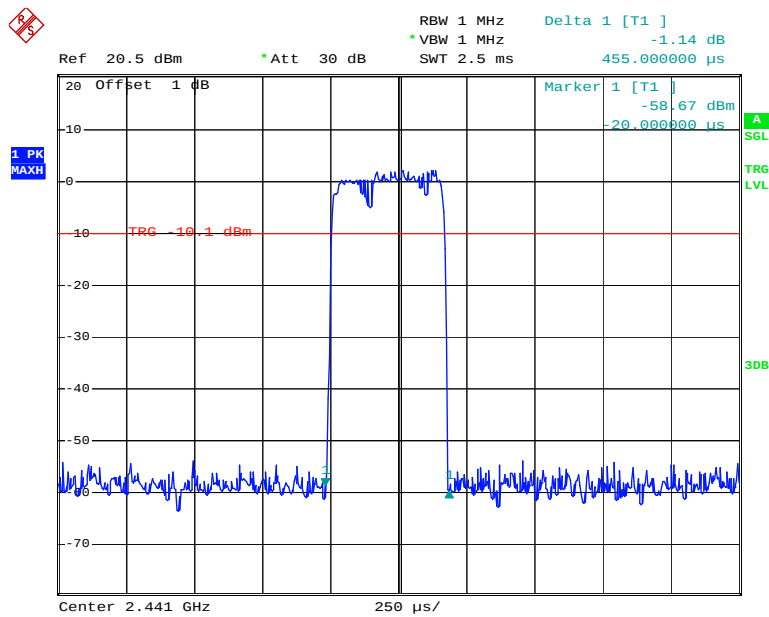
Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
DH1	Low	0.455	0.146	0.4	Pass
	Middle	0.455	0.146	0.4	Pass
	High	0.455	0.146	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/2/79) $\times$ 31.6 s				
DH3	Low	1.715	0.274	0.4	Pass
	Middle	1.725	0.276	0.4	Pass
	High	1.715	0.274	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/4/79) $\times$ 31.6 s				
DH5	Low	2.979	0.318	0.4	Pass
	Middle	2.979	0.318	0.4	Pass
	High	2.979	0.318	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/6/79) $\times$ 31.6 s				

DH1: Low Channel



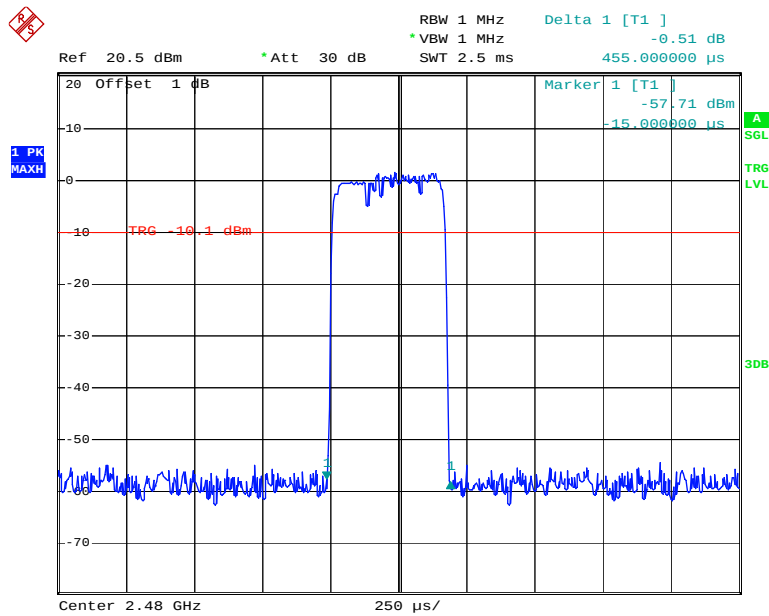
Date: 26.JAN.2014 16:45:22

DH1: Middle Channel



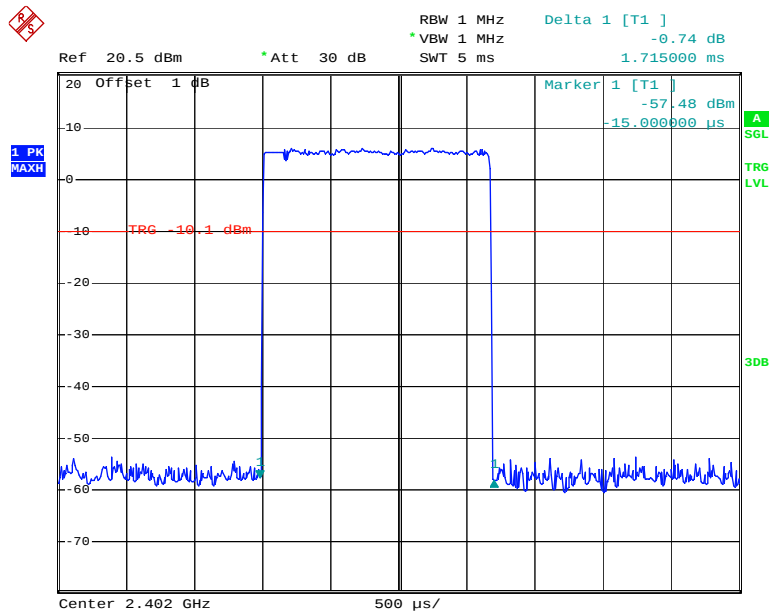
Date: 26.JAN.2014 16:45:13

DH1: High Channel



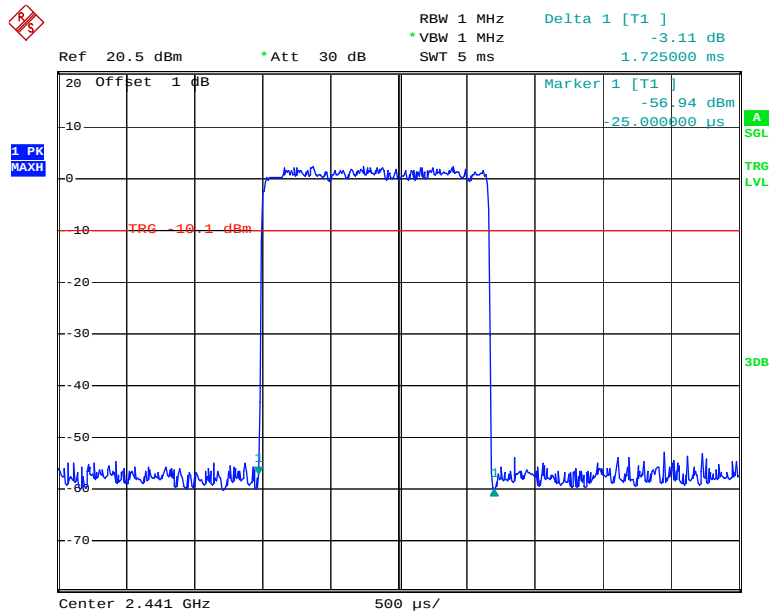
Date: 26.JAN.2014 16:45:05

DH3: Low Channel



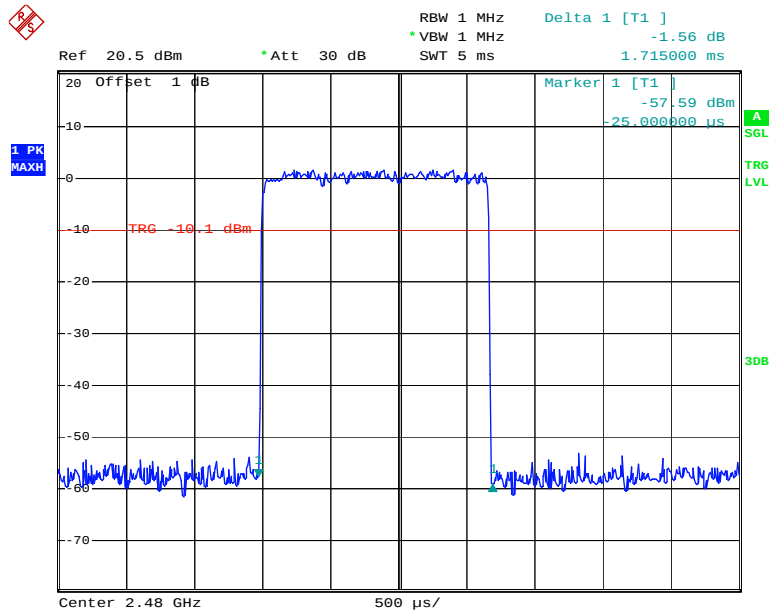
Date: 26.JAN.2014 17:19:18

DH3: Middle Channel



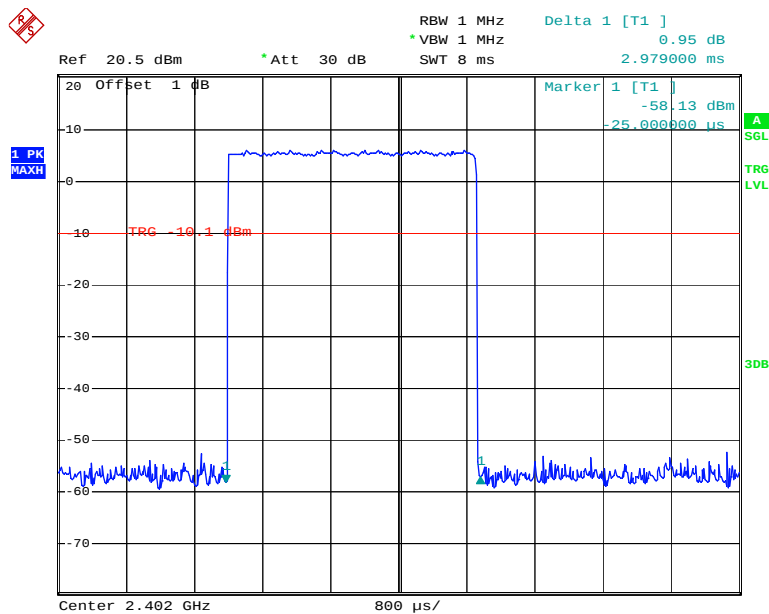
Date: 26.JAN.2014 17:19:29

DH3: High Channel



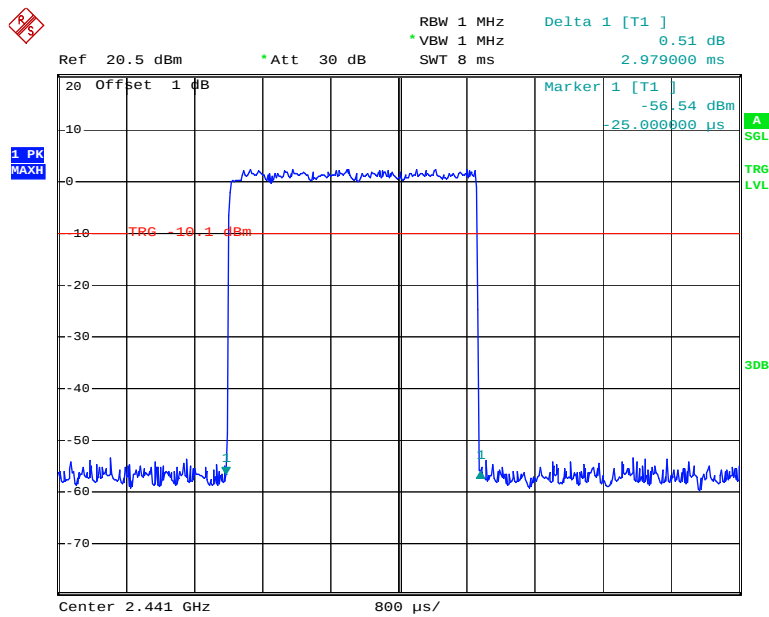
Date: 26.JAN.2014 17:21:05

DH5: Low Channel



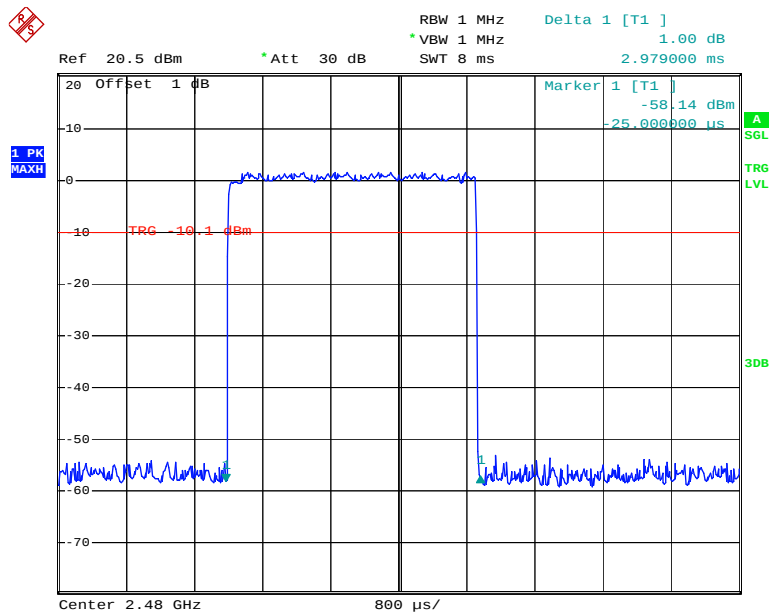
Date: 26.JAN.2014 17:31:36

DH5: Middle Channel



Date: 26.JAN.2014 17:31:25

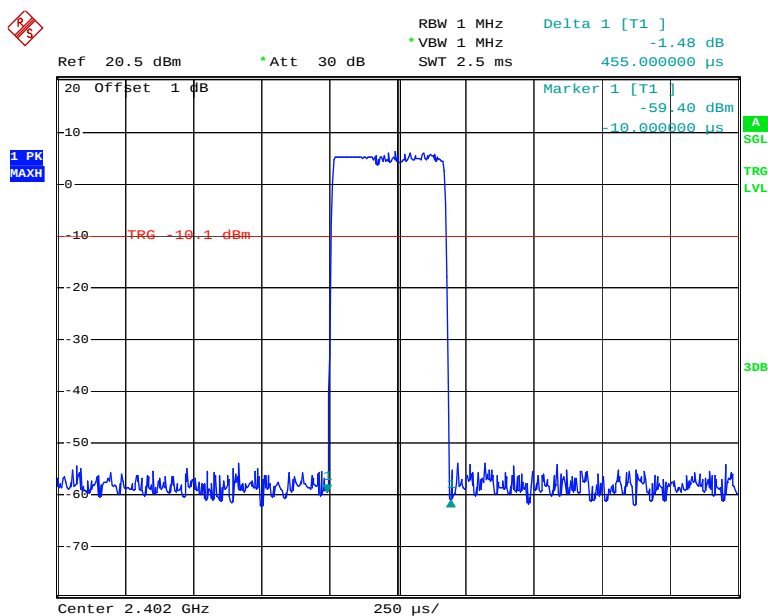
DH5: High Channel



Date: 26.JAN.2014 17:31:17

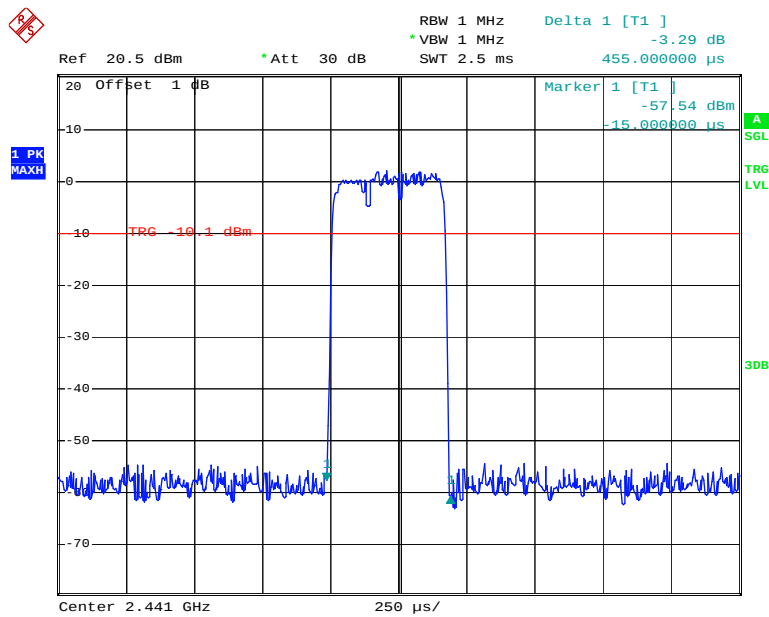
EDR Mode (8-DPSK):

Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
DH1	Low	0.455	0.146	0.4	Pass
	Middle	0.455	0.146	0.4	Pass
	High	0.455	0.146	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/2/79) $\times$ 31.6 s				
DH3	Low	1.715	0.274	0.4	Pass
	Middle	1.715	0.274	0.4	Pass
	High	1.715	0.274	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/4/79) $\times$ 31.6 s				
DH5	Low	2.979	0.318	0.4	Pass
	Middle	2.979	0.318	0.4	Pass
	High	2.963	0.316	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/6/79) $\times$ 31.6 s				

DH1: Low Channel

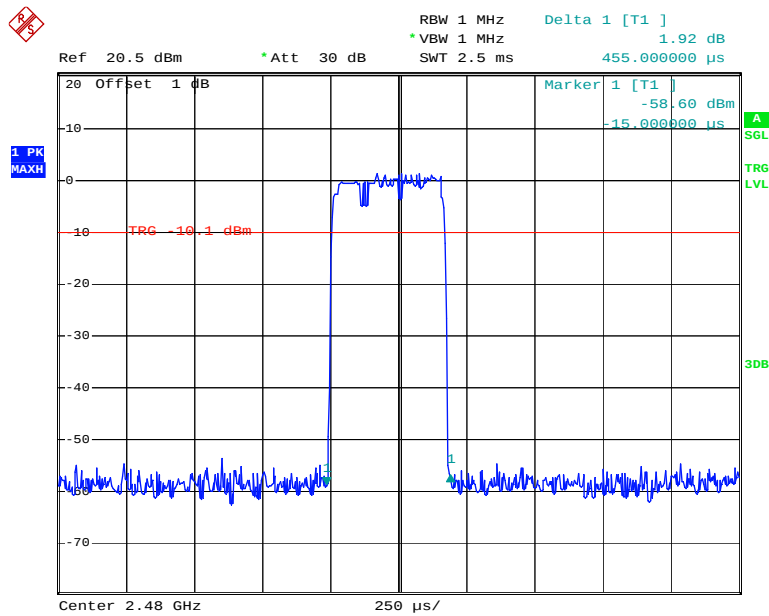
Date: 26.JAN.2014 16:45:37

DH1: Middle Channel



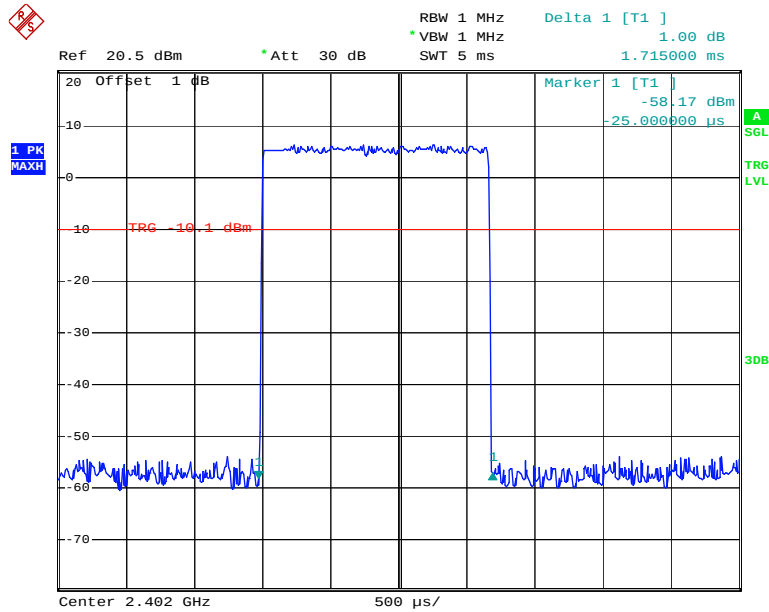
Date: 26.JAN.2014 16:45:49

DH1: High Channel



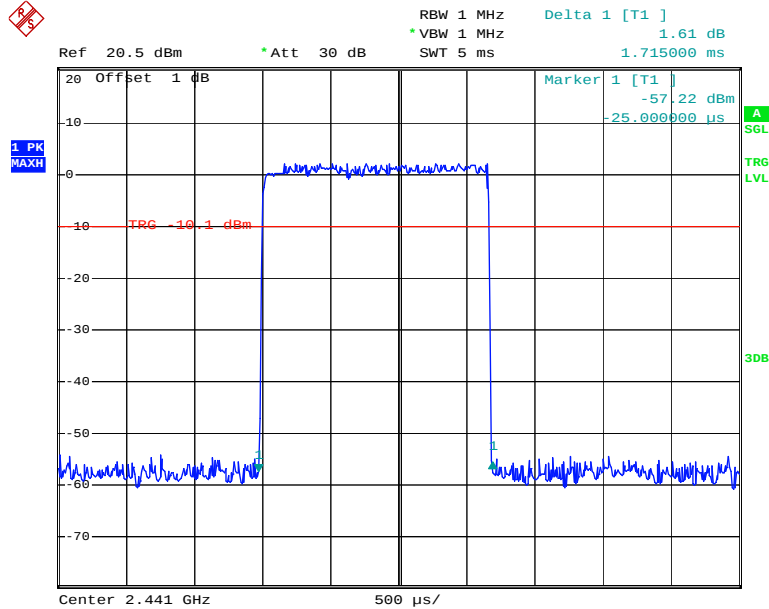
Date: 26.JAN.2014 16:45:58

DH3: Low Channel



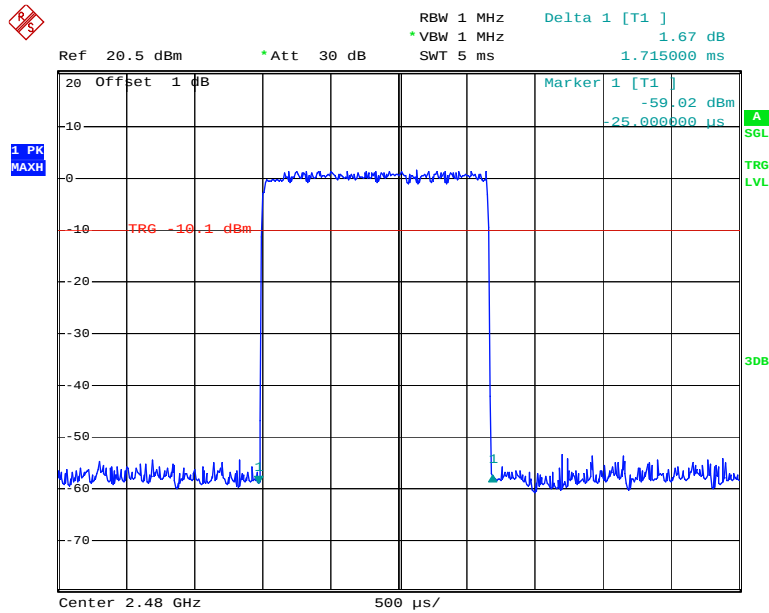
Date: 26.JAN.2014 17:22:13

DH3: Middle Channel



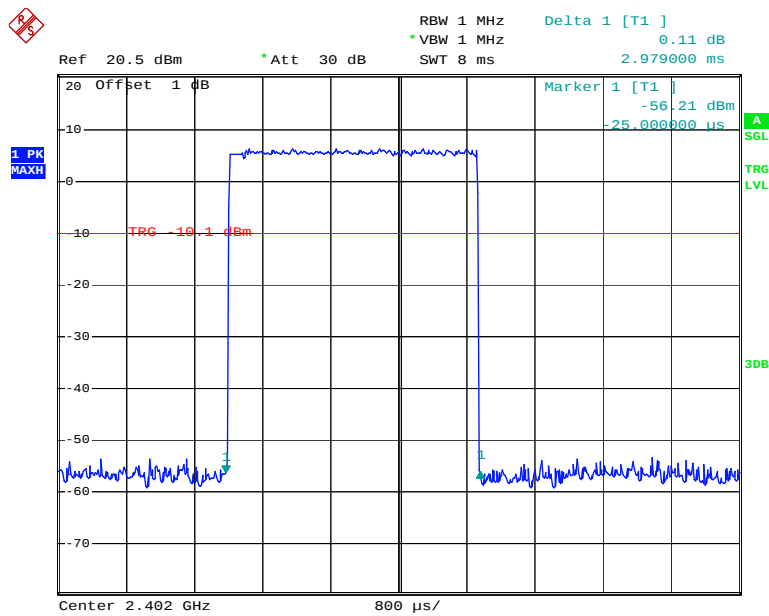
Date: 26.JAN.2014 17:21:53

DH3: High Channel



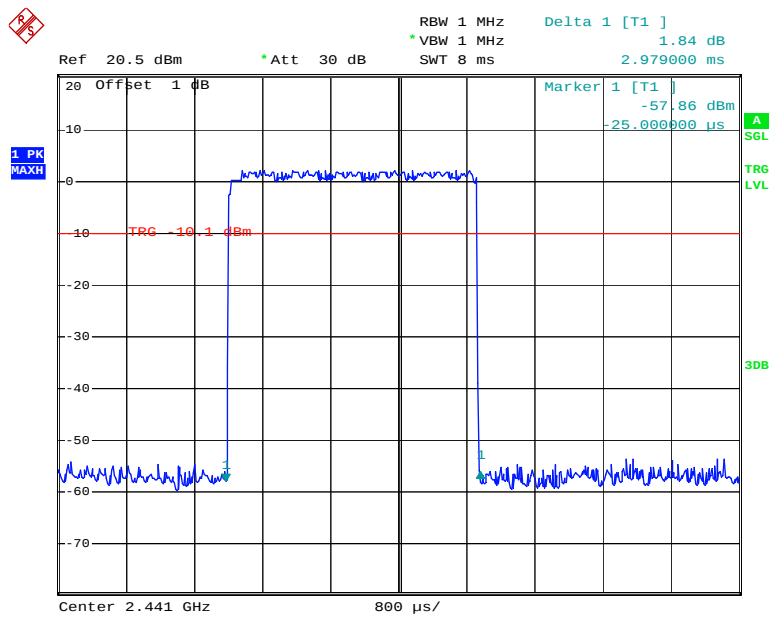
Date: 26.JAN.2014 17:21:40

DH5: Low Channel



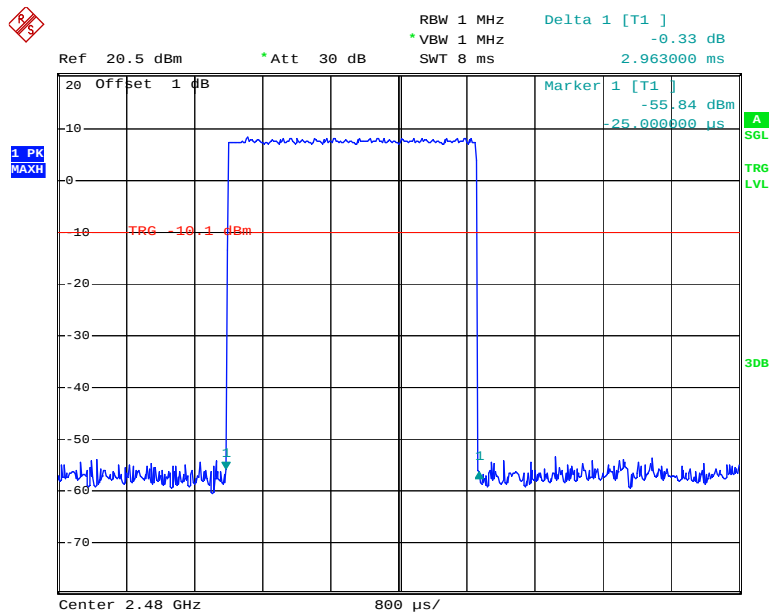
Date: 26.JAN.2014 17:32:05

DH5: Middle Channel



Date: 26.JAN.2014 17:32:14

DH5: High Channel



Date: 26.JAN.2014 17:32:24

FCC §15.247(b) (1) - PEAK OUTPUT POWER MEASUREMENT**Applicable Standard**

According to §15.247(b) (1), for frequency hopping systems operating in the 2400–2483.5 MHz band employing at least 75 non-overlapping hopping channels, and all frequency hopping systems in the 5725–5850 MHz band: 1 watt. For all other frequency hopping systems in the 2400–2483.5 MHz band: 0.125 watts

Test Procedure

1. Place the EUT on a bench and set in transmitting mode.
2. Remove the antenna from the EUT and then connect a low loss RF cable from the antenna port to an EMI test receiver.
3. Add a correction factor to the display.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2013-06-16	2014-06-15

* Statement of Traceability: Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed in accordance to NVLAP requirements, traceable to National Primary Standards and International System of Units (SI).

Test Data**Environmental Conditions**

Temperature:	23.5 ° C
Relative Humidity:	51 %
ATM Pressure:	101.4 kPa

* The testing was performed by Allen Qiao on 2014-01-26.

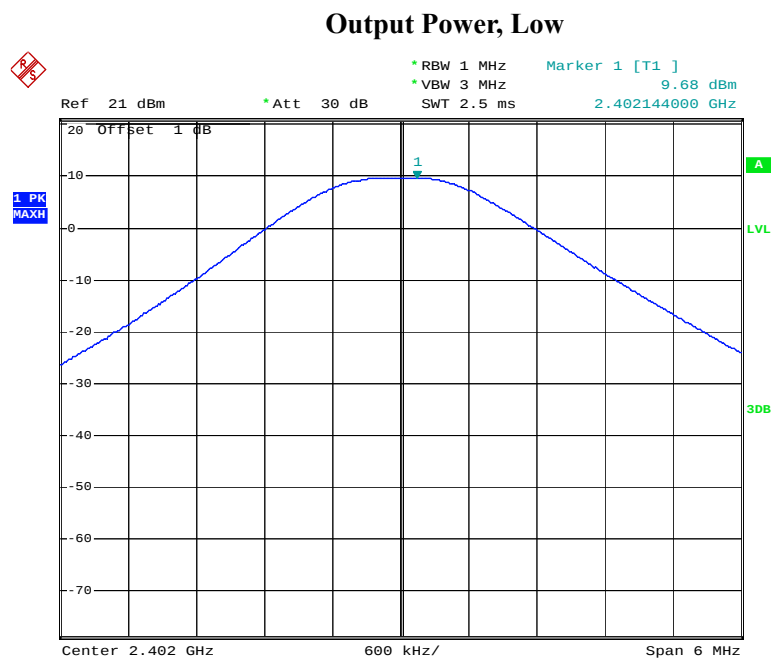
Test Result: Compliance.

Test Mode: Transmitting

Mode	Channel	Frequency (MHz)	Output power (dBm)	Limit (dBm)
BDR Mode (GFSK)	Low	2402	9.68	30
	Middle	2441	9.78	30
	High	2480	9.75	30
EDR Mode ($\pi/4$ -DQPSK)	Low	2402	8.49	30
	Middle	2441	8.58	30
	High	2480	8.33	30
EDR Mode (8-DPSK)	Low	2402	8.74	30
	Middle	2441	8.91	30
	High	2480	8.83	30

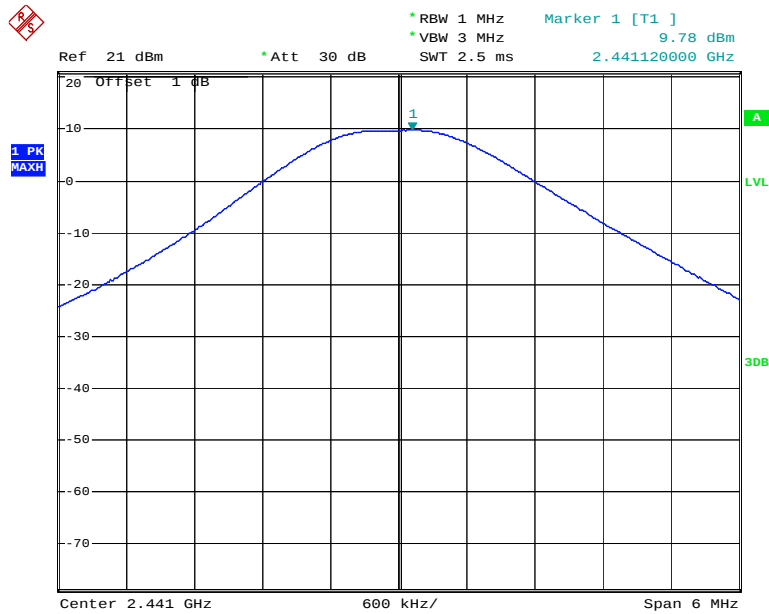
Note: The data above was tested in conducted mode.

BDR Mode (GFSK):



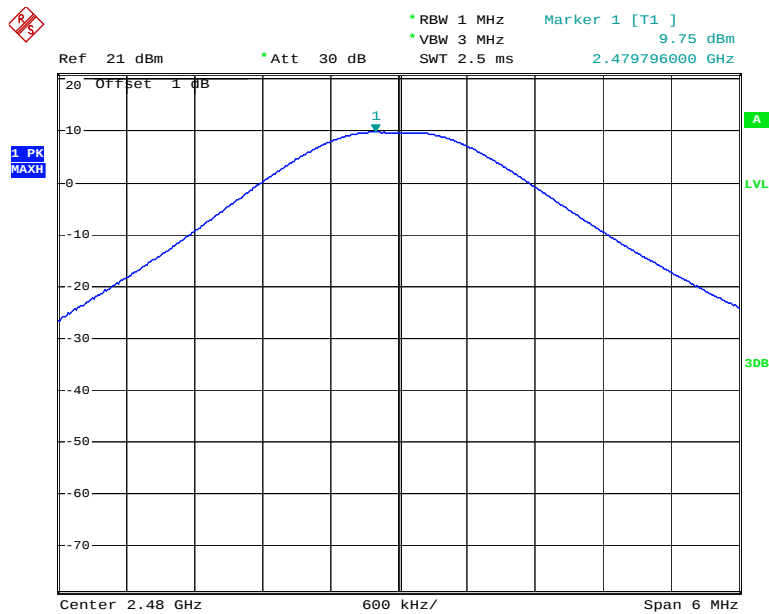
Date: 26.JAN.2014 14:31:29

Output Power, Middle



Date: 26.JAN.2014 14:31:10

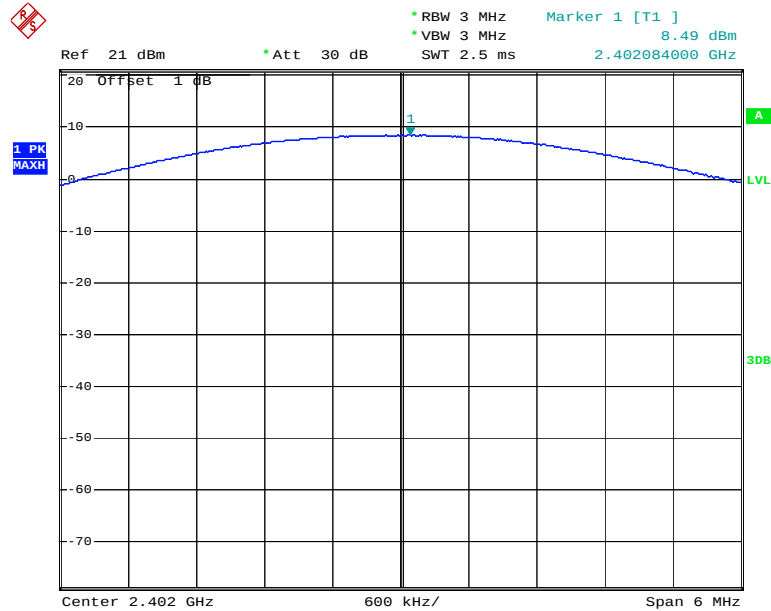
Output Power, High



Date: 26.JAN.2014 14:31:57

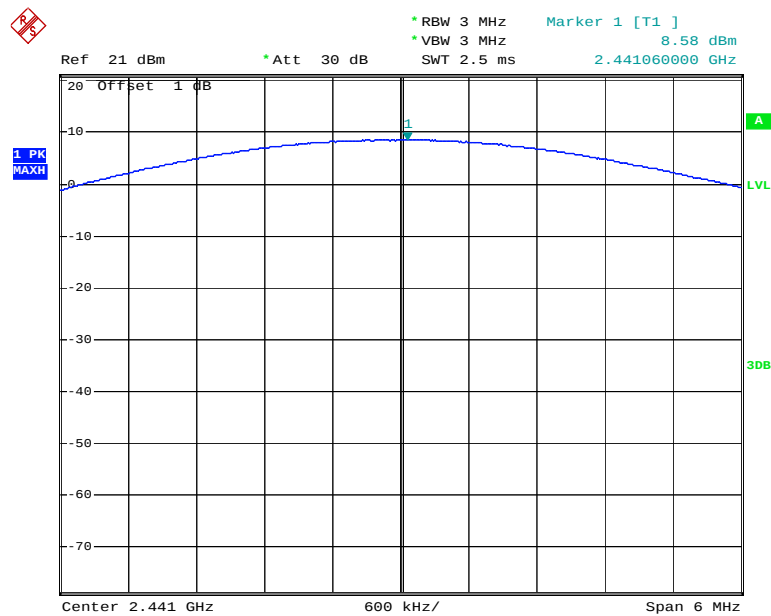
EDR Mode ($\pi/4$ -DQPSK):

Output Power, Low



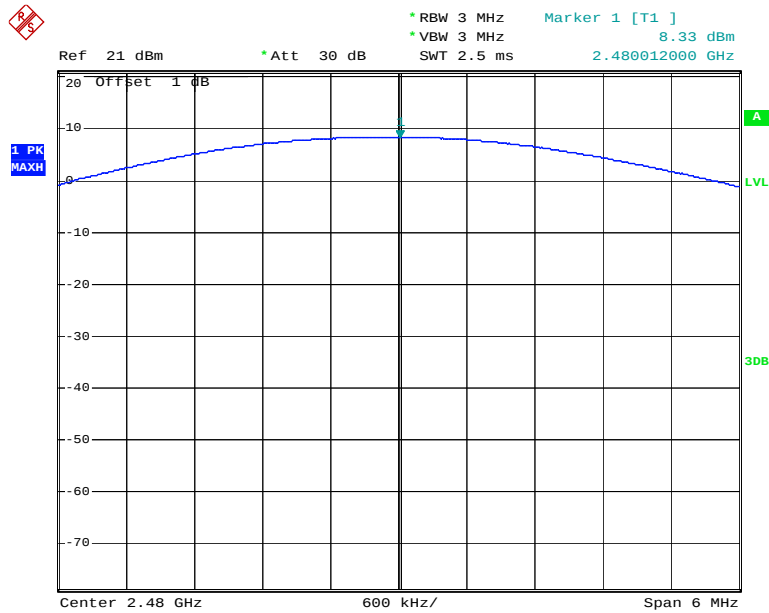
Date: 26.JAN.2014 14:34:27

Output Power, Middle



Date: 26.JAN.2014 14:34:06

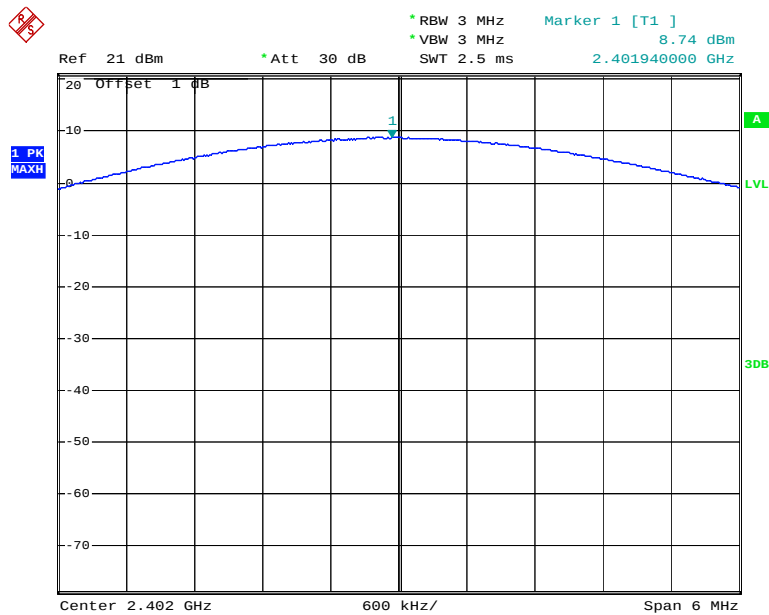
Output Power, High



Date: 26.JAN.2014 14:33:48

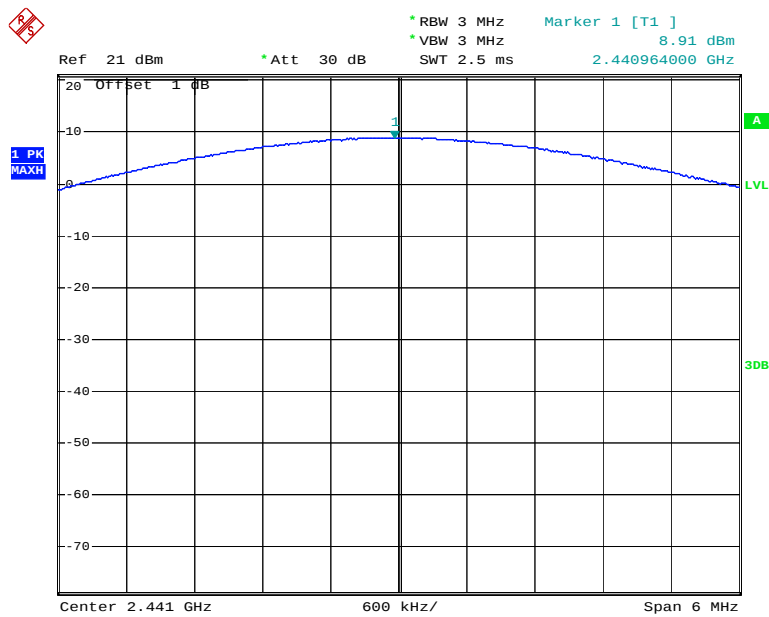
EDR Mode (8-DPSK):

Output Power, Low



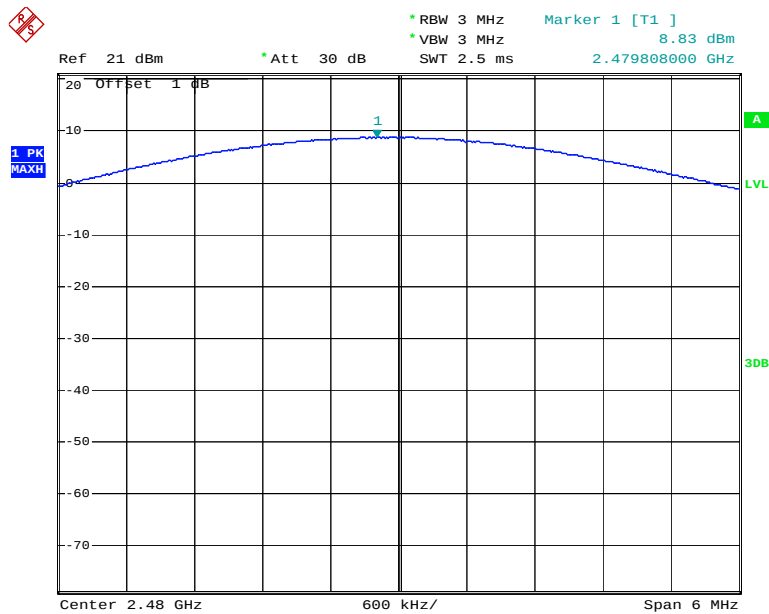
Date: 26.JAN.2014 14:36:02

Output Power, Middle



Date: 26.JAN.2014 14:36:17

Output Power, High



Date: 26.JAN.2014 14:36:35

FCC §15.247(d) - BAND EDGES TESTING

Applicable Standard

In any 100 kHz bandwidth outside the frequency band in which the spread spectrum or digitally modulated intentional radiator is operating, the radio frequency power that is produced by the intentional radiator shall be at least 20 dB below that in the 100 kHz bandwidth within the band that contains the highest level of the desired power, based on either an RF conducted or a radiated measurement, provided the transmitter demonstrates compliance with the peak conducted power limits. If the transmitter complies with the conducted power limits based on the use of RMS averaging over a time interval, as permitted under paragraph (b)(3) of this section, the attenuation required under this paragraph shall be 30 dB instead of 20 dB. Attenuation below the general limits specified in §15.209(a) is not required. In addition, radiated emissions which fall in the restricted bands, as defined in §15.205(a), must also comply with the radiated emission limits specified in §15.209(a) (see §15.205(c)).

Test Procedure

1. Check the calibration of the measuring instrument using either an internal calibrator or a known signal from an external generator.
2. Remove the antenna from the EUT and then connect to a low loss RF cable from the antenna port to a EMI test receiver, then turn on the EUT and make it operate in transmitting mode. Then set it to Low Channel and High Channel within its operating range, and make sure the instrument is operated in its linear range.
3. Set both RBW and VBW of spectrum analyzer to 100 kHz with a convenient frequency span including 100 kHz bandwidth from band edge.
4. Measure the highest amplitude appearing on spectral display and set it as a reference level. Plot the graph with marking the highest point and edge frequency.
5. Repeat above procedures until all measured frequencies were complete.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2013-06-16	2014-06-15

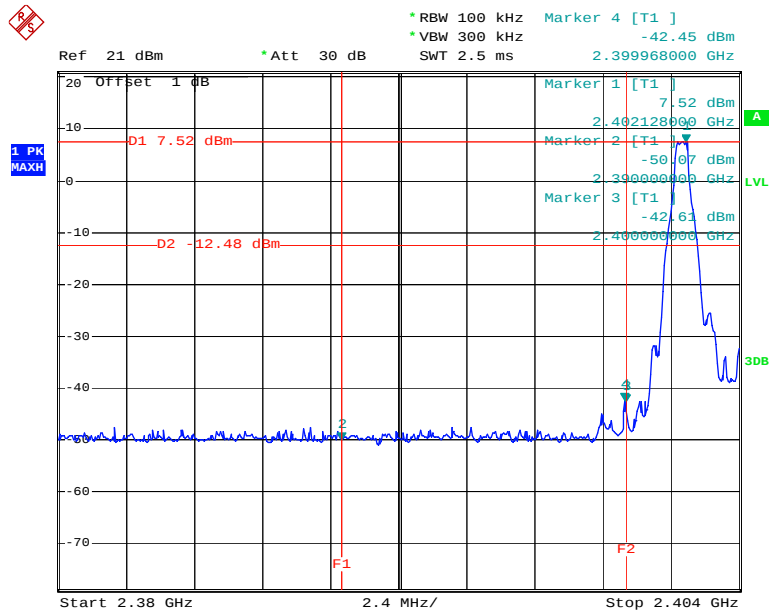
* Statement of Traceability: Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed in accordance to NVLAP requirements, traceable to National Primary Standards and International System of Units (SI).

Test Data

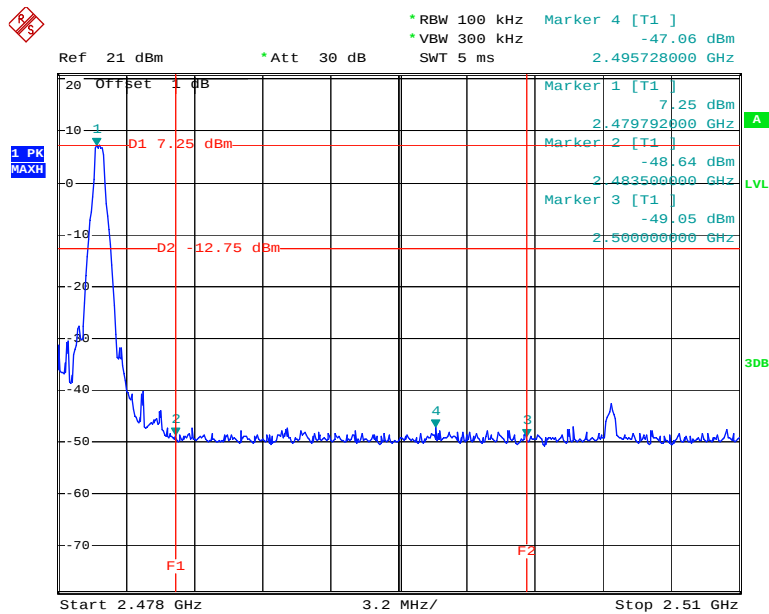
Environmental Conditions

Temperature:	23.5 ° C
Relative Humidity:	51 %
ATM Pressure:	101.4 kPa

* The testing was performed by Allen Qiao on 2014-01-26.

Test Result: Compliance*BDR Mode (GFSK):***Band Edge, Left Side**

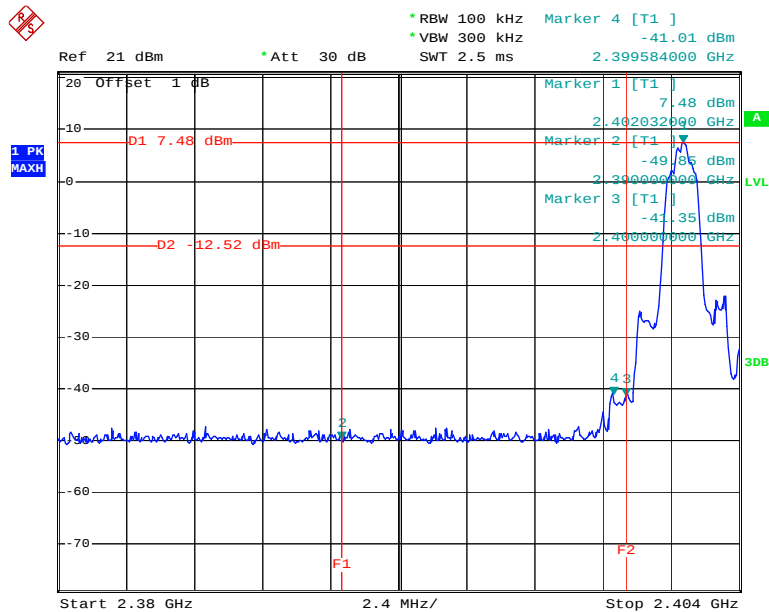
Date: 26.JAN.2014 15:17:45

Band Edge, Right Side

Date: 26.JAN.2014 15:13:20

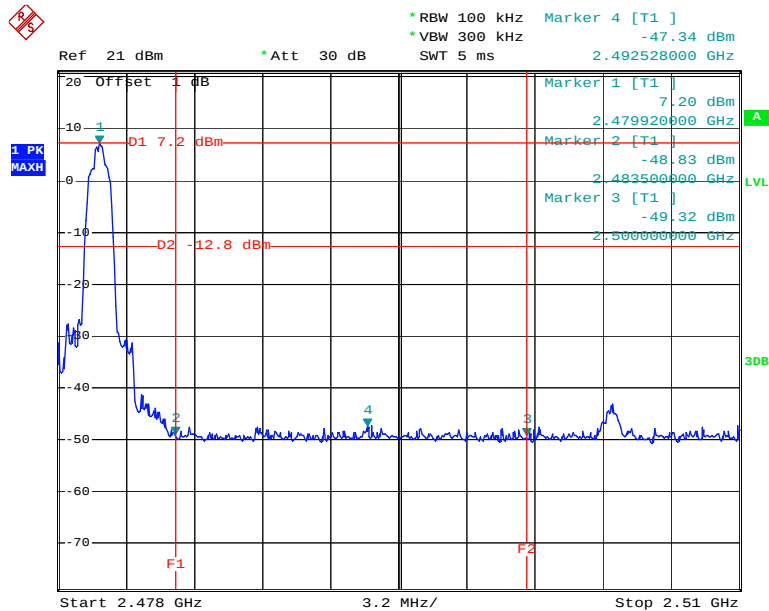
EDR Mode ($\pi/4$ -DQPSK):

Band Edge, Left Side

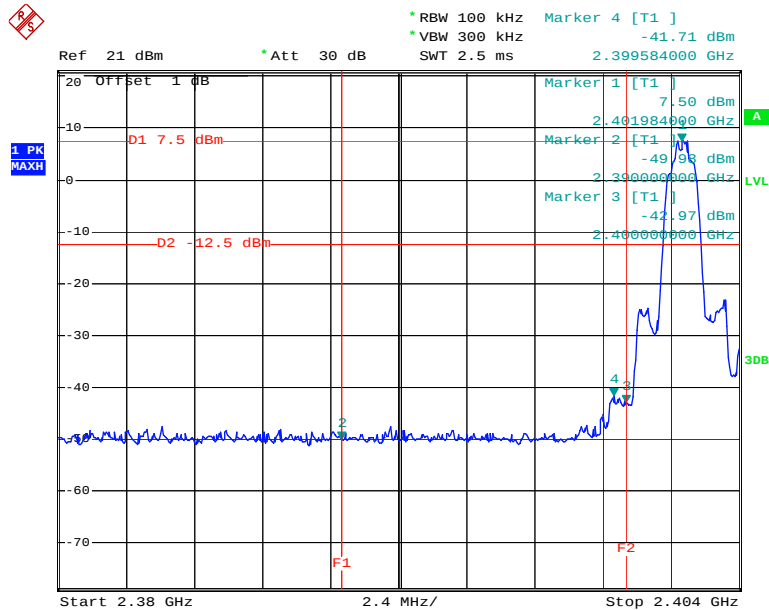


Date: 26.JAN.2014 15:15:58

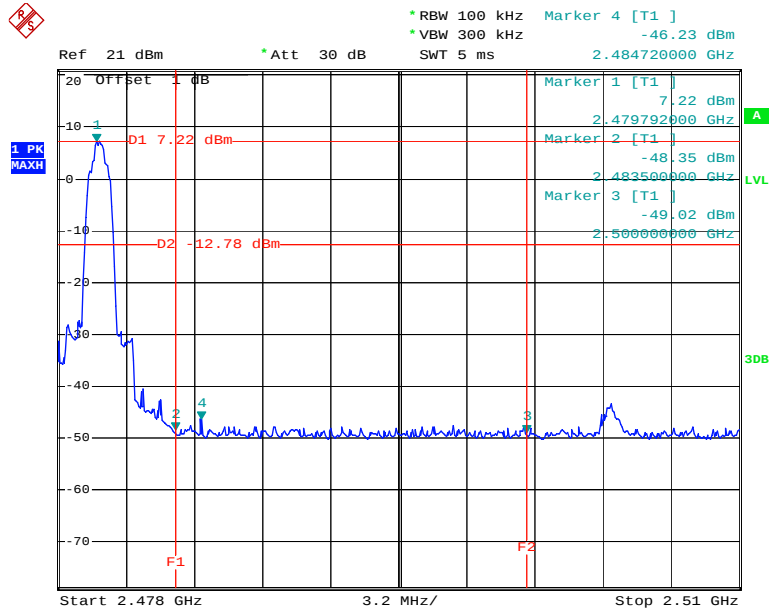
Band Edge, Right Side



Date: 26.JAN.2014 15:12:26

*EDR Mode (8-DPSK):***Band Edge, Left Side**

Date: 26.JAN.2014 15:16:37

Band Edge, Right Side

Date: 26.JAN.2014 15:11:27

******* END OF REPORT *******