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graph TD
    CPU[CPU] <--> DATABUS[DATABUS]
    CPU <--> IOBUS[IOBUS]
    CPU <--> INSIDE_UART[INSIDE UART]
    CPU <--> TEL_CPU[TEL CPU]
    
    DATABUS --> MODEM[MODEM]
    DATABUS --> DRAM[DRAM]
    DATABUS --> FLASH[FLASH EEPROM]
    DATABUS --> RTC[RTC]
    
    IOBUS --> TOUCH_CON[TOUCH CON]
    IOBUS --> KEYBOARD_CON[KEYBOARD CON]
    IOBUS --> LCD_CON[LCD CON]
    
    TOUCH_CON <--> TOUCH[TOUCH]
    KEYBOARD_CON <--> KEYBOARD[KEYBOARD]
    KEYBOARD_CON <--> KEYPAD[KEYPAD]
    LCD_CON <--> LCD[LCD]
    
    INSIDE_UART --> TEL_PORT[TEL PORT]
    INSIDE_UART --> DEBUG_PORT[DEBUG PORT]
    
    TEL_CPU <--> TEL_DSP[TEL DSP]
    TEL_DSP <--> TEL_CODEC[TEL CODEC]
    TEL_CODEC --> DAA[DAA]
    DAA <--> TEL_LINE[TEL LINE]
    
    TEL_CPU <--> TAD[TAD MEMORY]
    TEL_CPU <--> SPK[SPK CODEC]
  
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