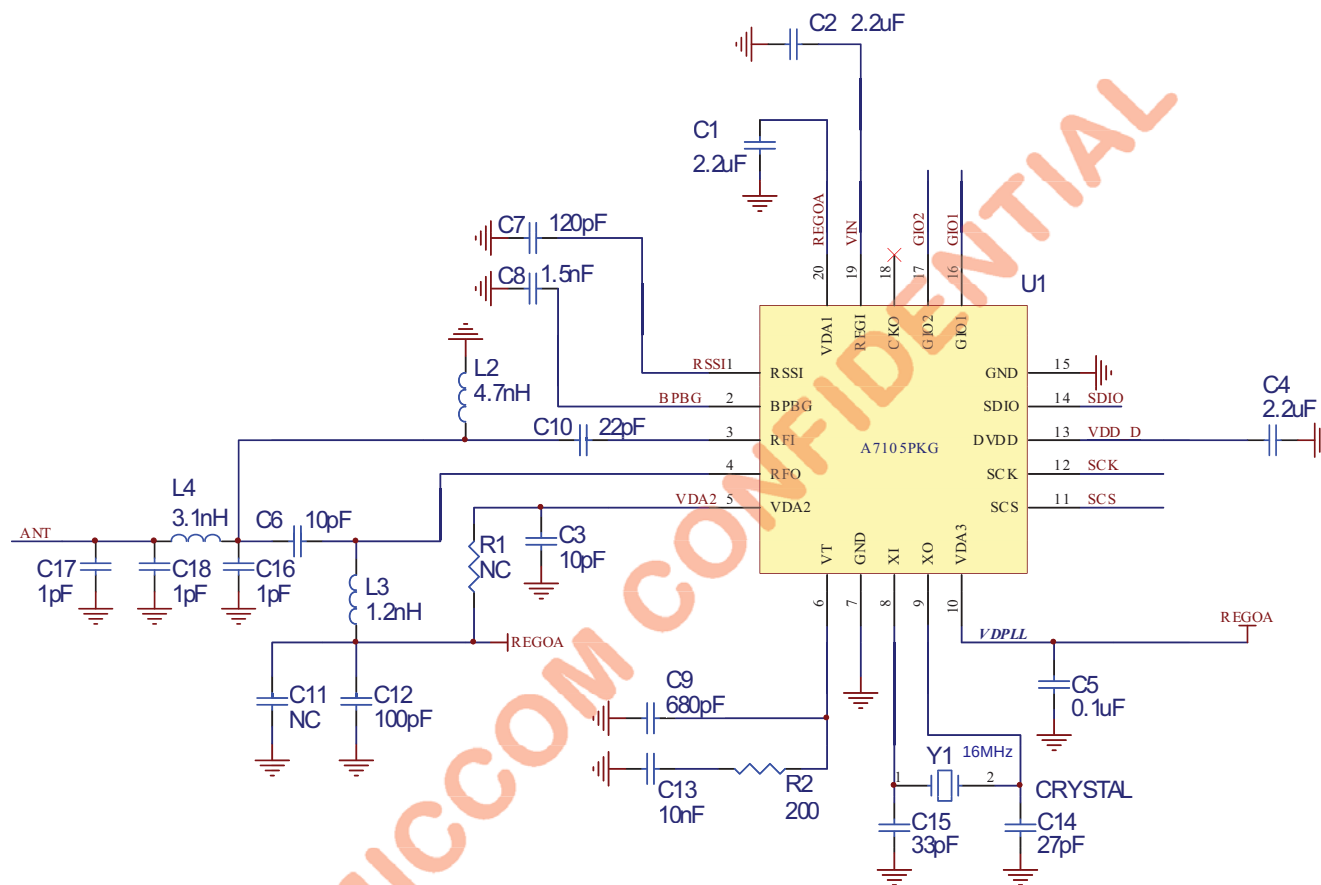




J2 2.4G IC



1. A7105 schematic for RF layouts with single ended 50Ω RF output.
2. C14 and C15 must be matched to the crystal's load capacitance (Cload). Y1 is a 16MHz crystal with 18 pF Cload, max 50ohm ESR and 20 ppm tolerance. Please see application note for detail.