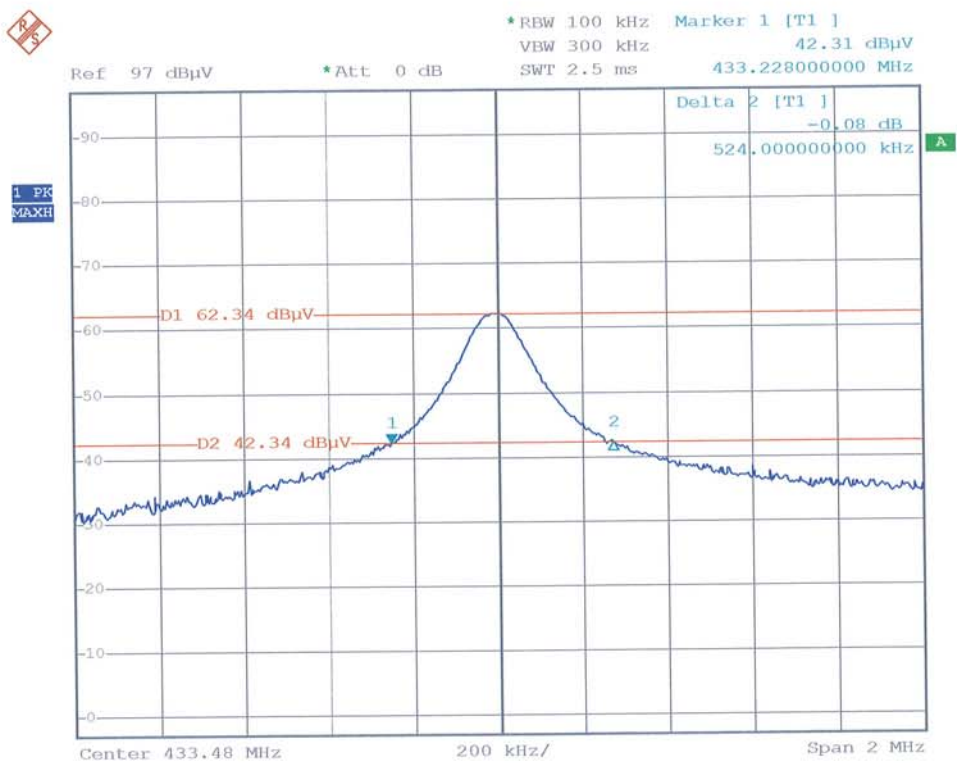
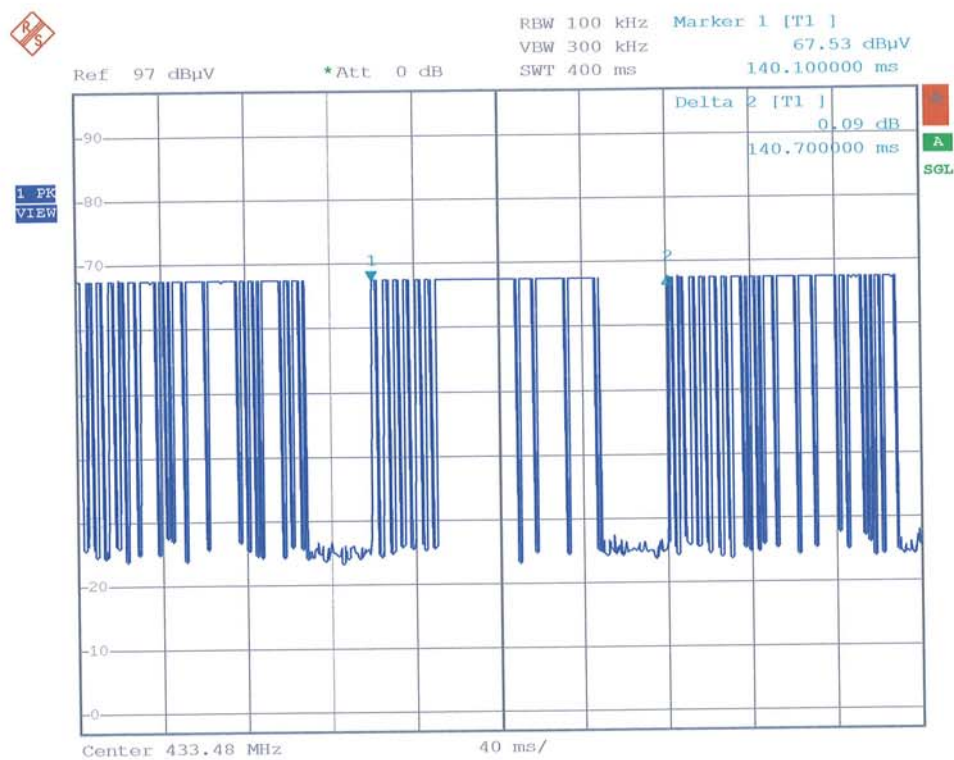


ANNEX 3: EMISSION BANDWIDTH

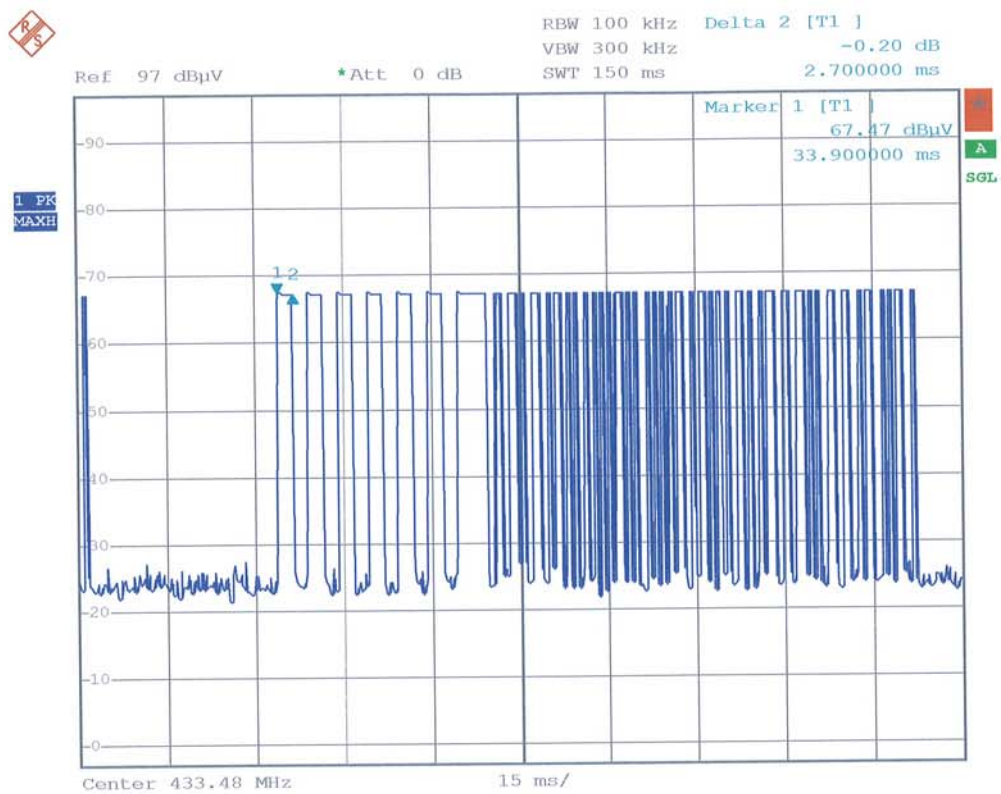


Date: 31.JAN.2006 11:54:56

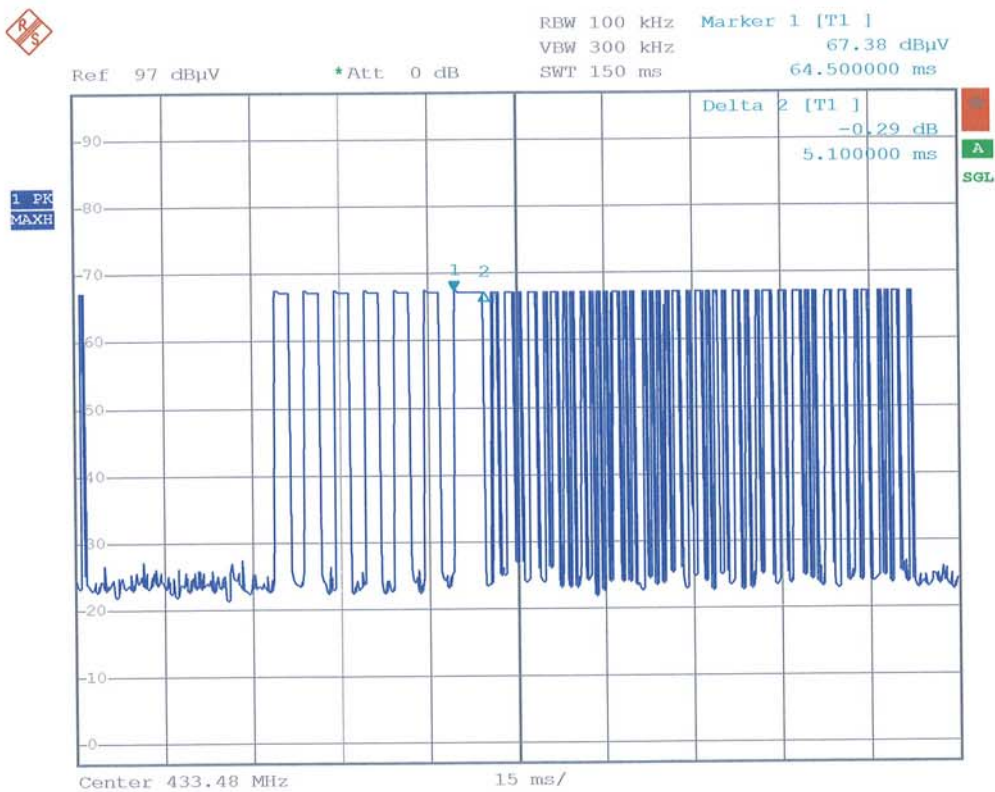
ANNEX 4: DUTY CYCLE DETERMINATION



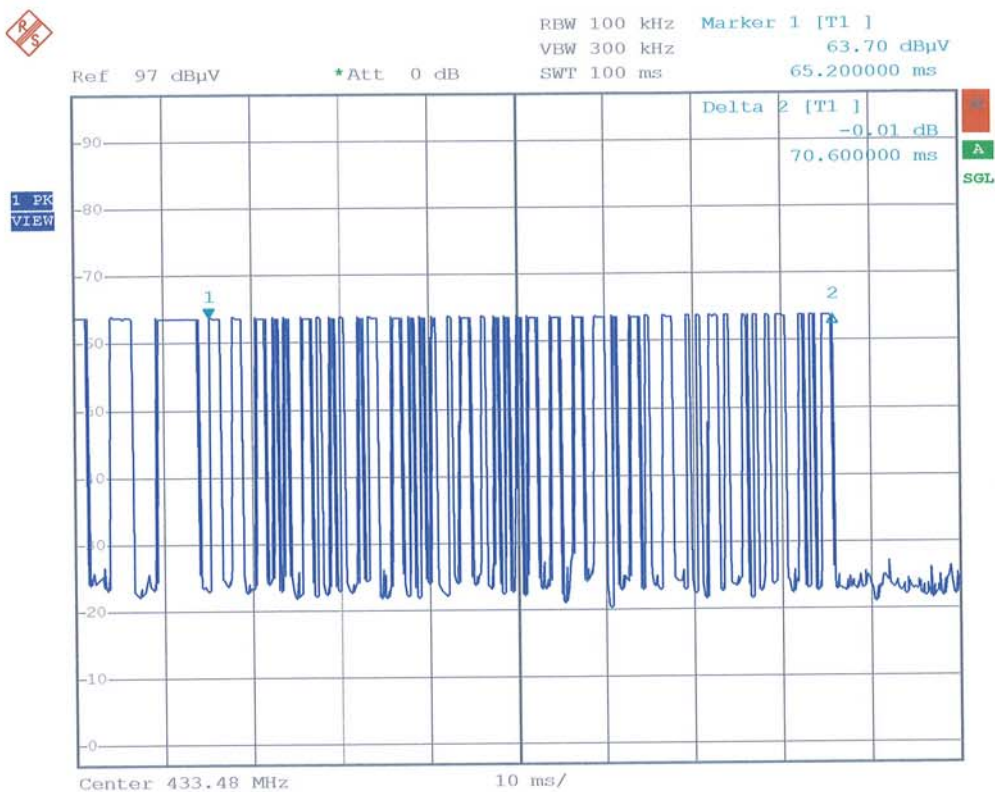
Date: 30.JAN.2006 14:35:05



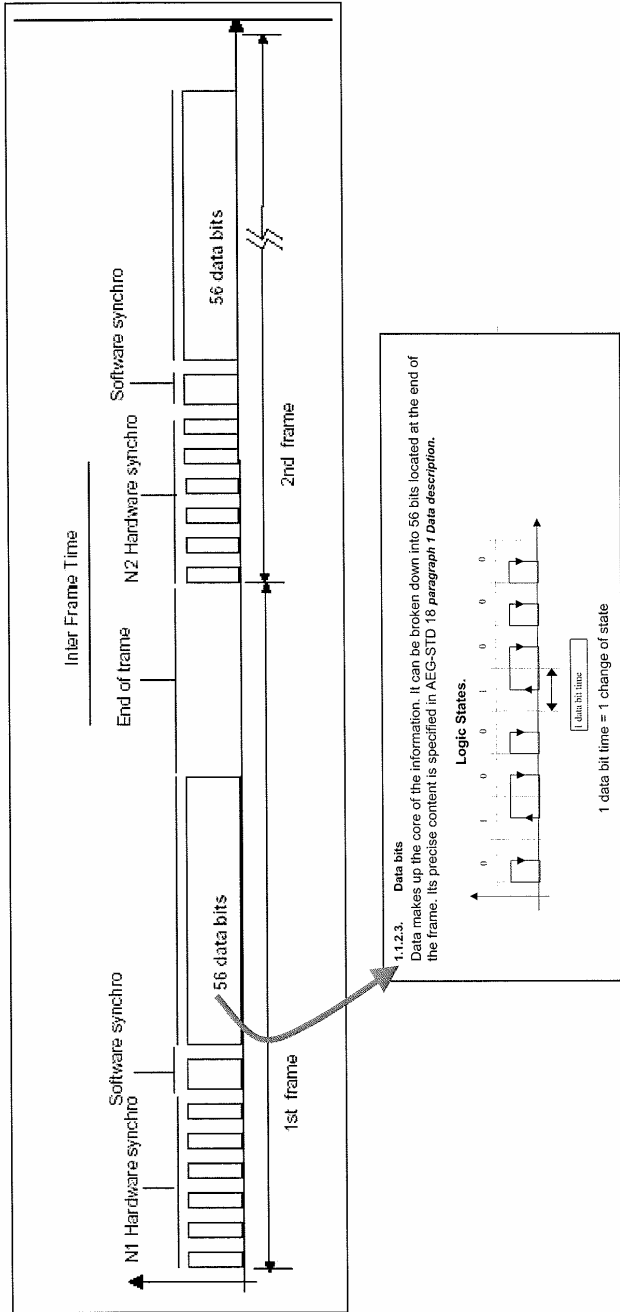
Date: 30.JAN.2006 14:44:59



Date: 30.JAN.2006 14:47:52



Date: 31.JAN.2006 12:46:08



This tram is the modulator for the Amplitude Modulation.
With the tram structure we can calculate the duty cycle of the modulator signal like that:

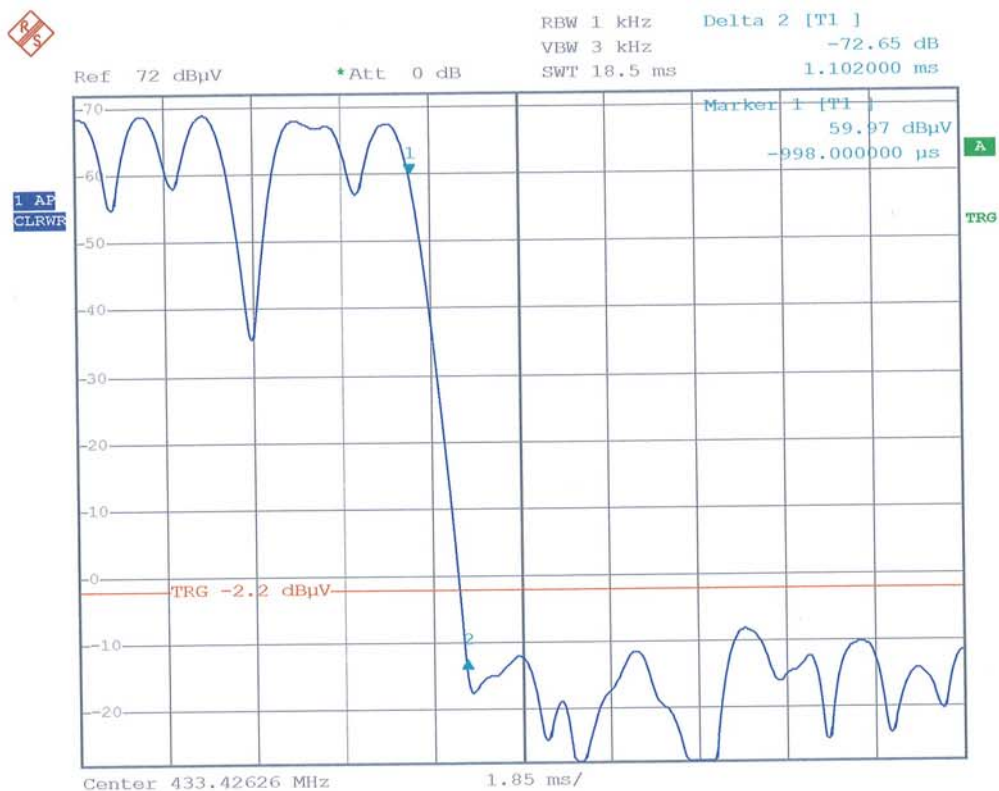
(8 Hardware Synchronization Bits + 1 Software Synchronization Bits + 56 Data bits)/ tram length
Numerical value: $((6 \times 2.5\text{ms}) + 4.8\text{ms} + (56 \times 0.64\text{ms})) / 139.62\text{ms} = 40 \%$

The provision in §15.35© of the FCC part 15, the power correction factor for the modulation is like that:

$((6 \times 2.5) + 4.8 + (56 \times 0.64)) / 100 = 55.64\% = -5.09\text{dB}$

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ANNEX 5: SHUTT-OFF TIMING



Date: 23.FEB.2006 15:12:29