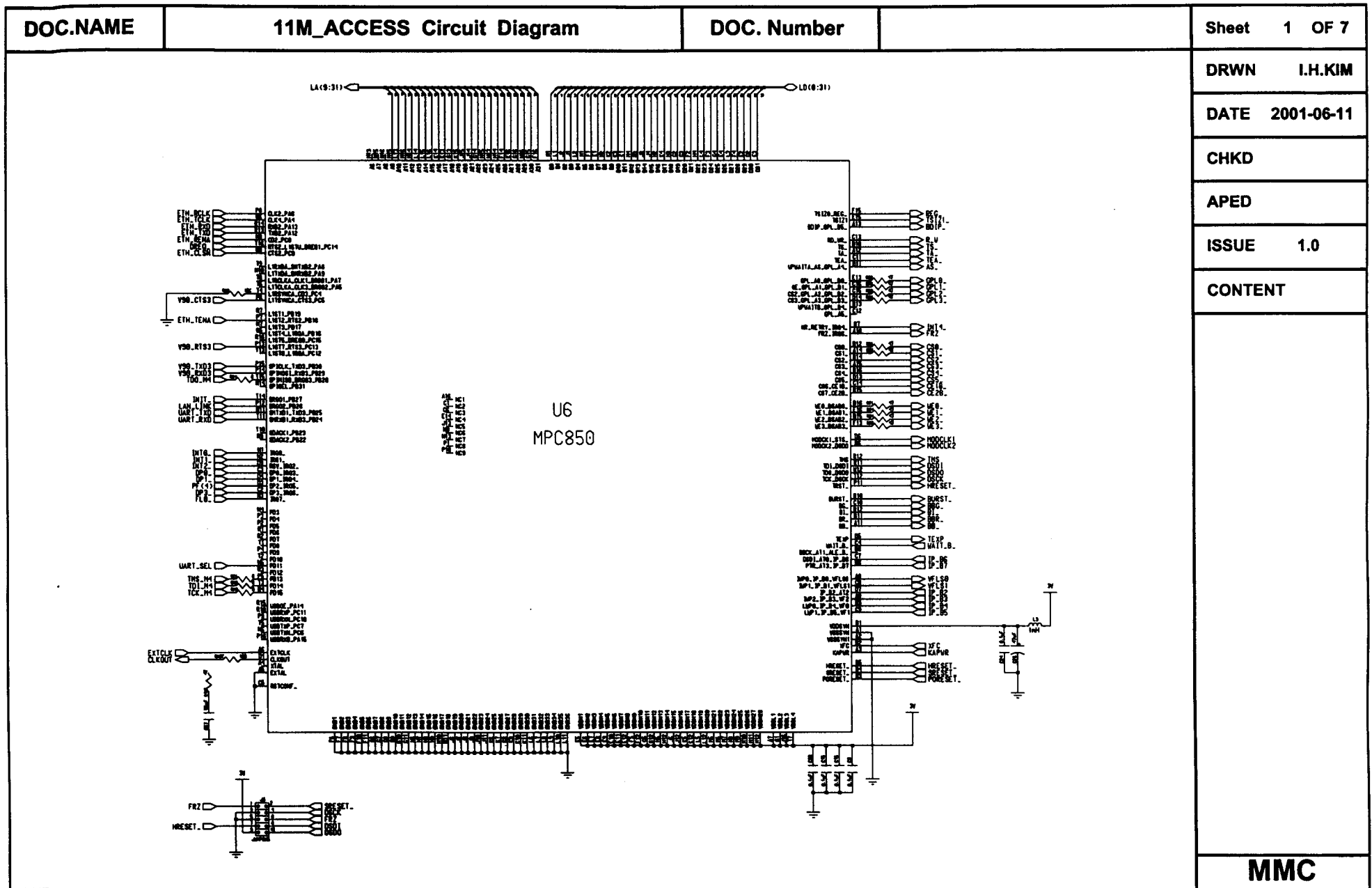


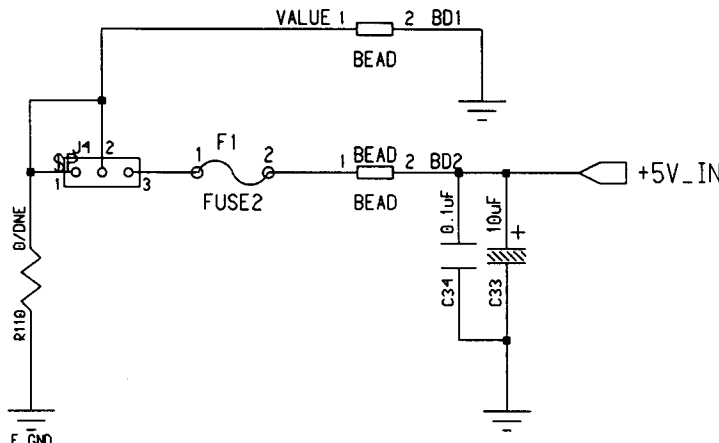
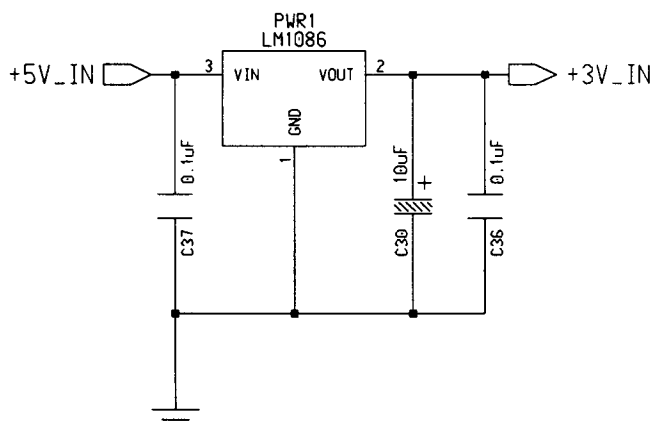




| DOC.NAME | 11M_ACCESS Circuit Diagram                                                                                                                                                                                                     | DOC. Number |  | Sheet 3 OF 7    |
|----------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------|--|-----------------|
|          |                                                                                                                                                                                                                                |             |  | DRWN I.H.KIM    |
|          |                                                                                                                                                                                                                                |             |  | DATE 2001-06-11 |
|          |                                                                                                                                                                                                                                |             |  | CHKD            |
|          |                                                                                                                                                                                                                                |             |  | APED            |
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|          |                                                                                                                                                                                                                                |             |  | CONTENT         |
|          | <p>The RS232C diagram shows a MAX232C chip connected to a 3V supply and a DB9 connector. The ETHER NET diagram shows an LXT905LC chip connected to VCC, ground, and an RJ-45 connector. A power LED circuit is also shown.</p> |             |  |                 |
|          | MMC                                                                                                                                                                                                                            |             |  |                 |



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|                                                           |                            |             | DATE 2001-06-11 |
|                                                           |                            |             | CHKD            |
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