

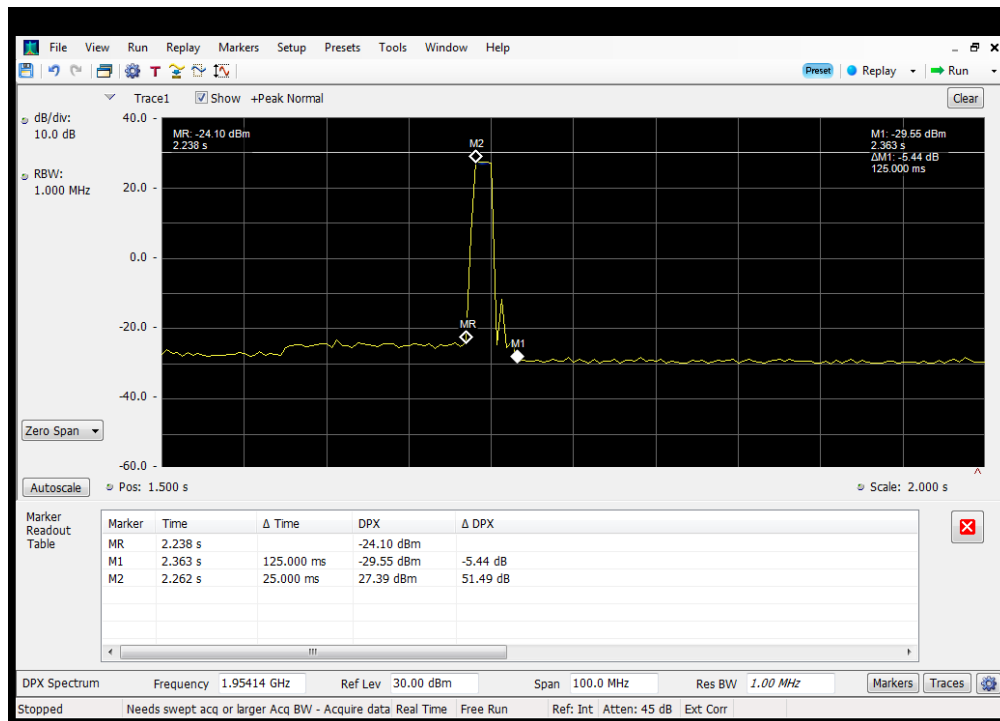


Annex I

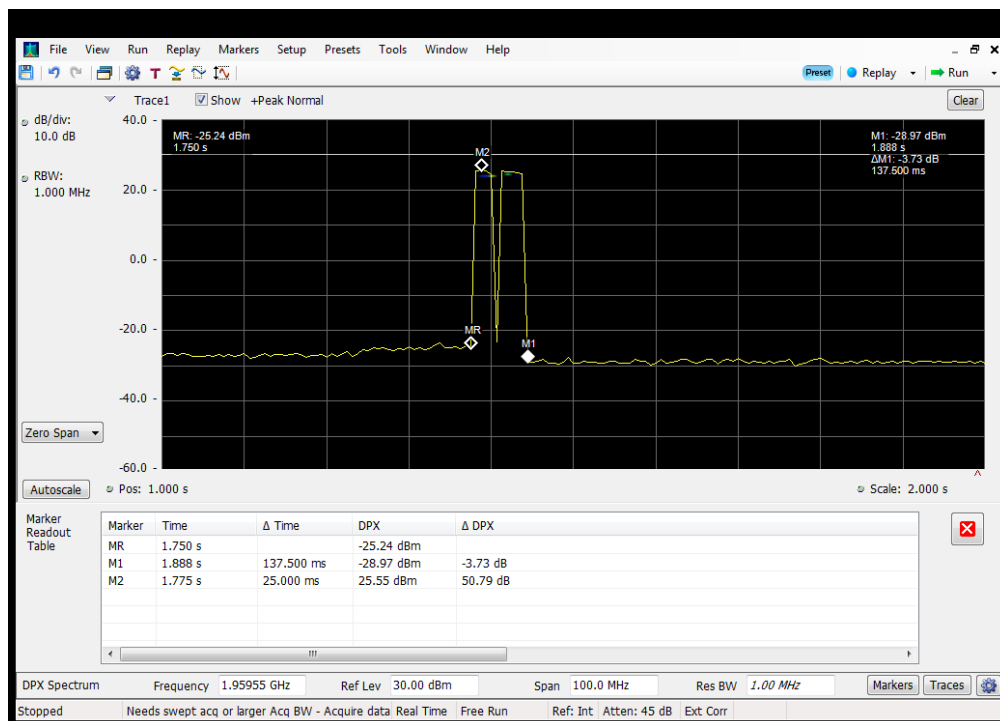
Anti - Oscillation



Detection Time_Downlink_1930 - 1995 MHz_B25_Server 2 to Dedicated Donor Port 1

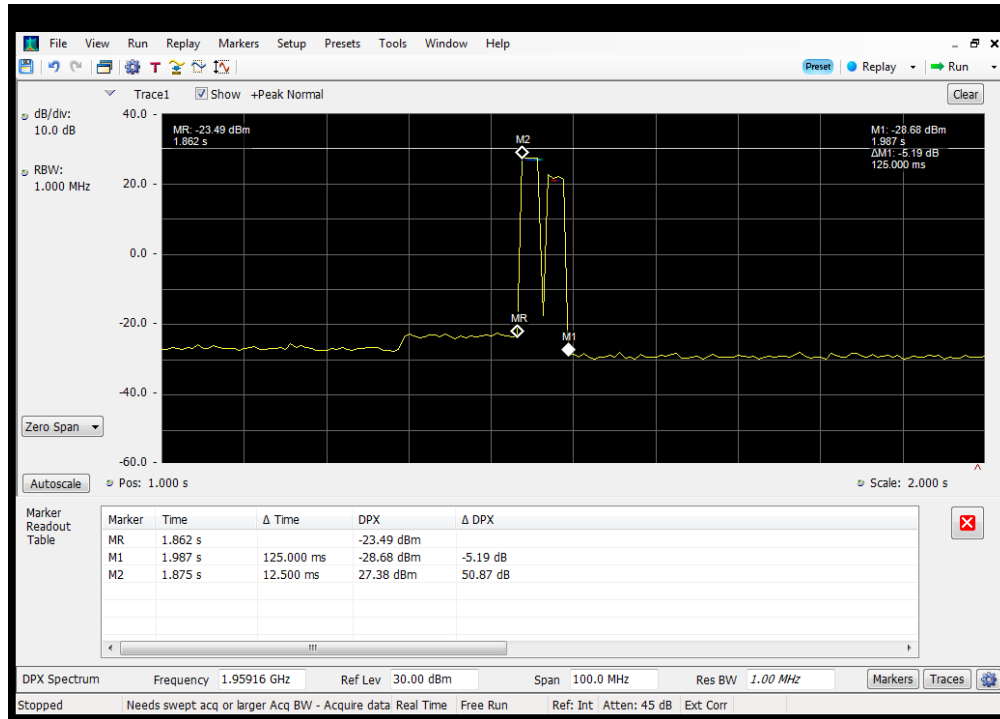


Detection Time_Downlink_1930 - 1995 MHz_B25_Server 4 to Dedicated Donor Port 1

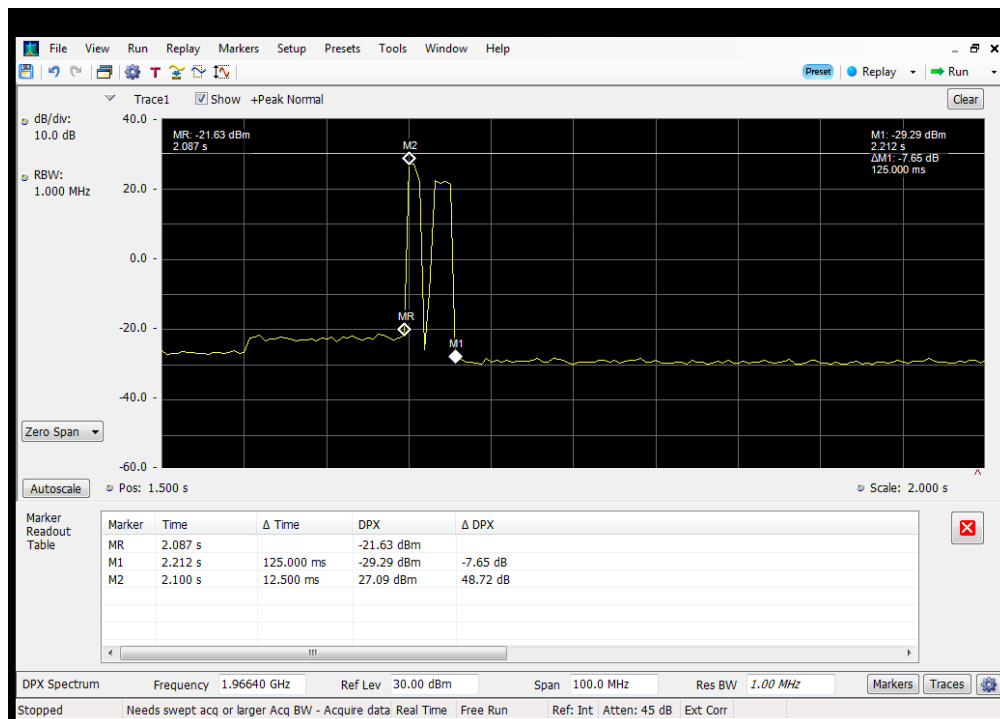




Detection Time_Downlink_1930 - 1995 MHz_B2_Server 1 to Dedicated Donor Port 1

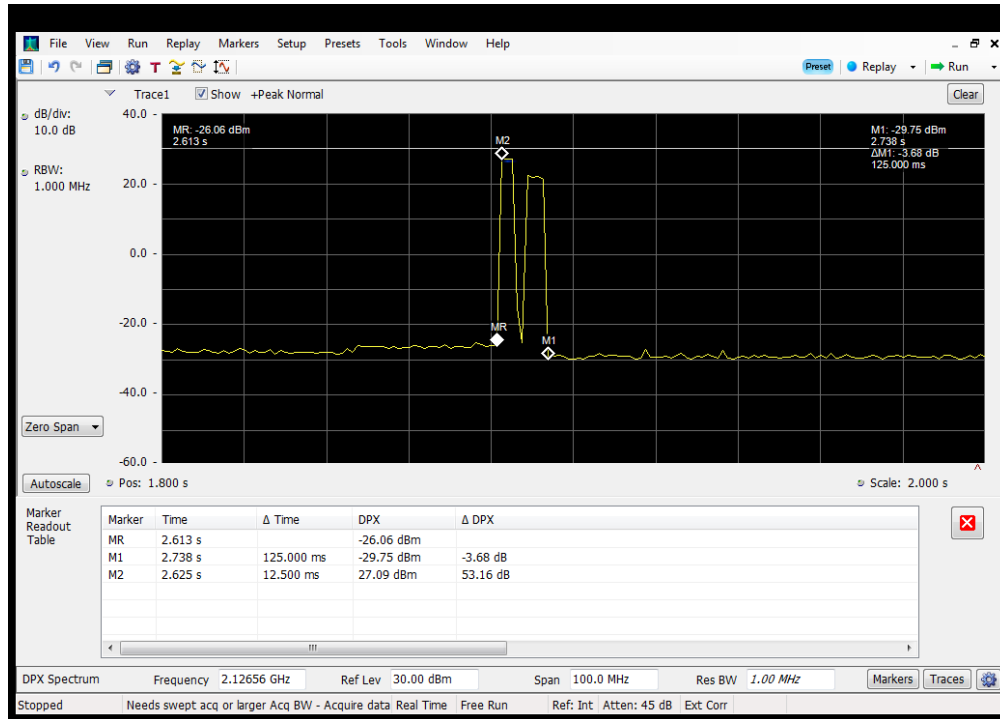


Detection Time_Downlink_1930 - 1995 MHz_B2_Server 3 to Dedicated Donor Port 1

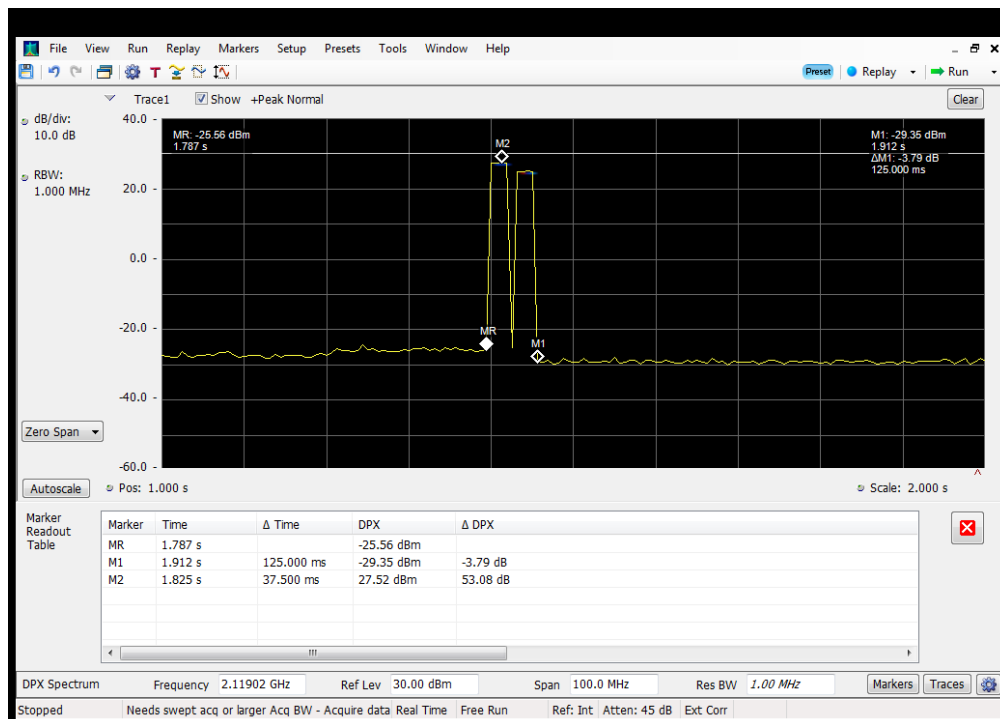




Detection Time_Downlink_2110 - 2155 MHz_B4_Server 1 to Dedicated Donor Port 1

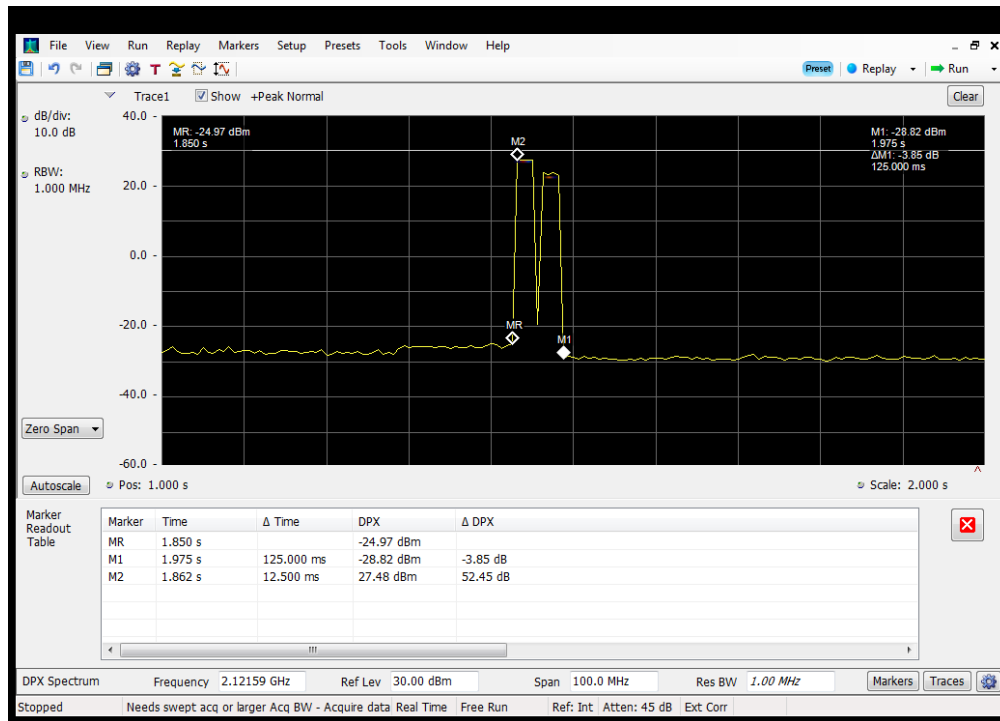


Detection Time_Downlink_2110 - 2155 MHz_B4_Server 2 to Dedicated Donor Port 1

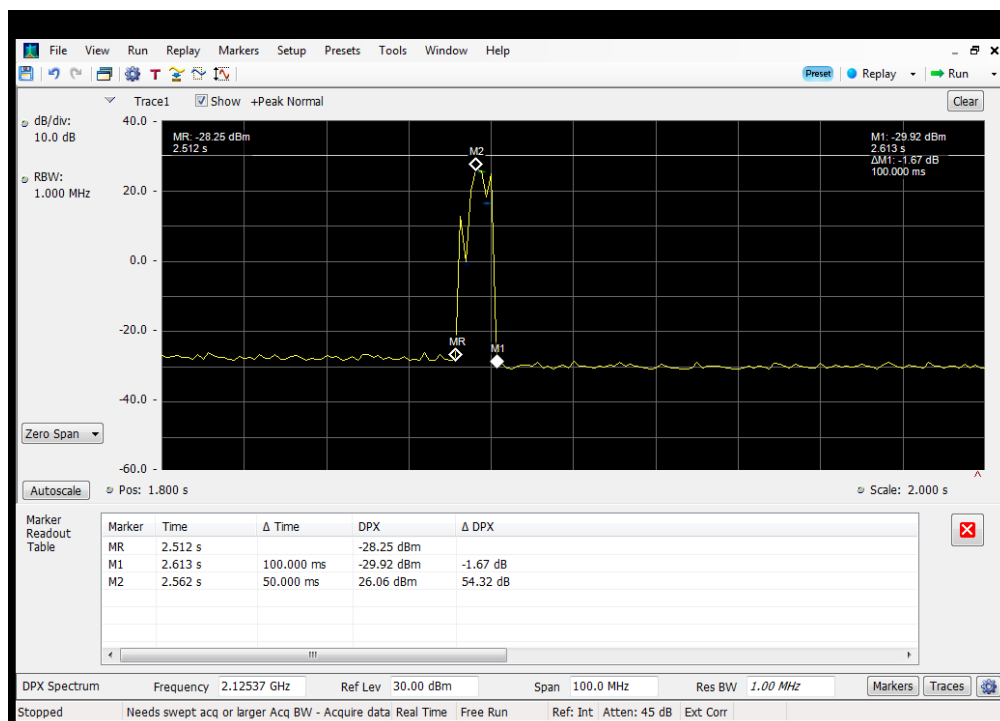




Detection Time_Downlink_2110 - 2155 MHz_B4_Server 3 to Dedicated Donor Port 1

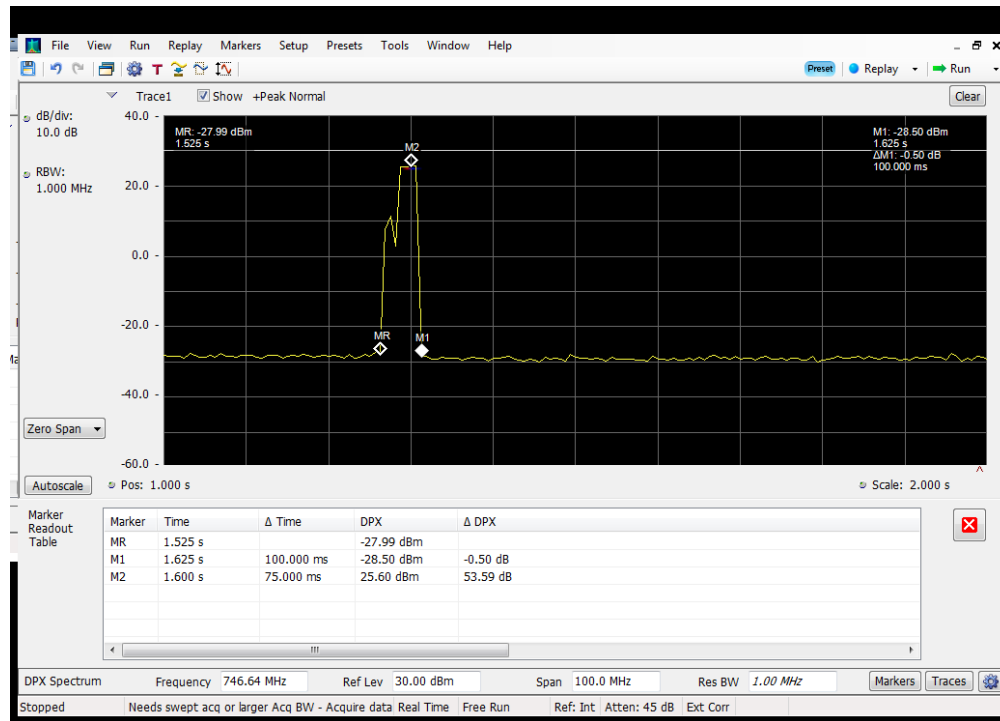


Detection Time_Downlink_2110 - 2155 MHz_B4_Server 4 to Dedicated Donor Port 1





Detection Time_Downlink_728 - 746 MHz_B12_Server 1 to Common Donor Port 1

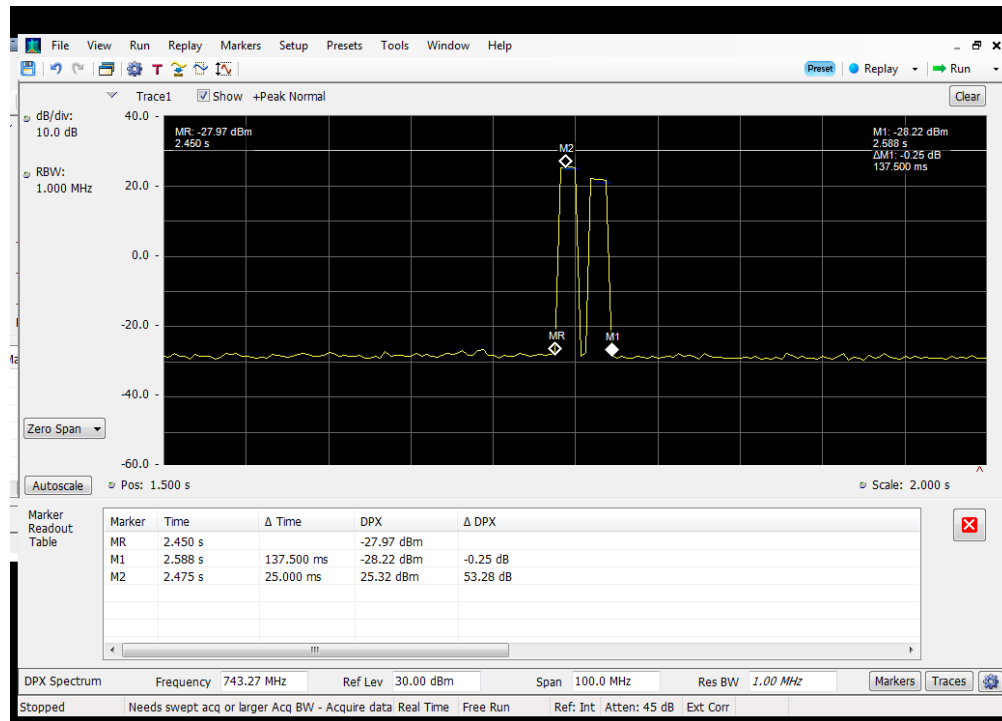


Detection Time_Downlink_728 - 746 MHz_B12_Server 1 to Dedicated Donor Port 3

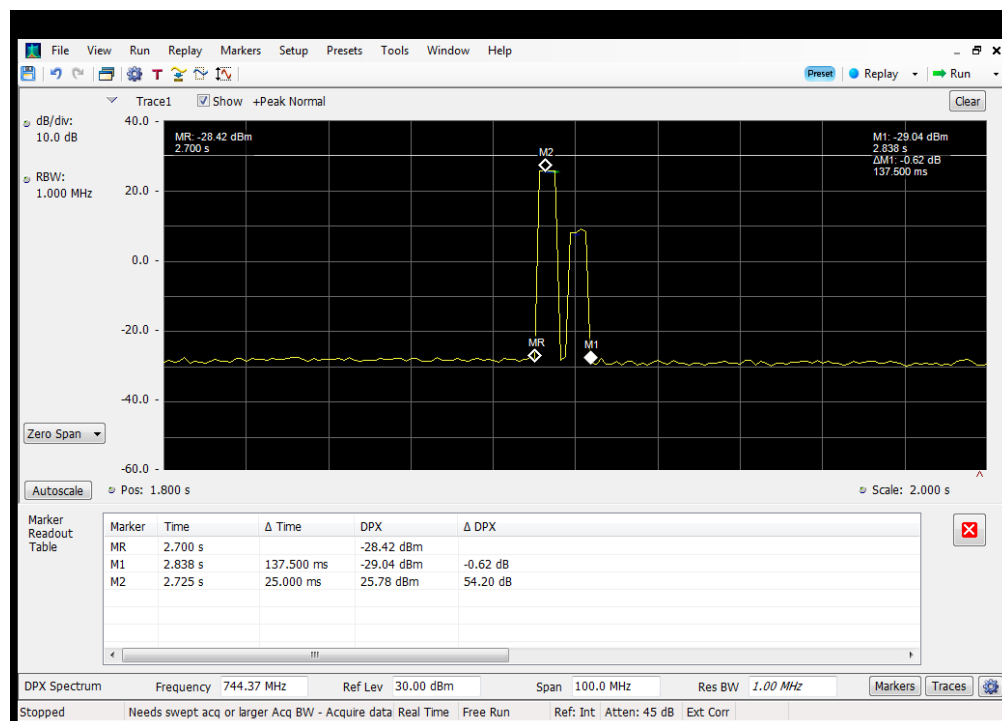




Detection Time_Downlink_728 - 746 MHz_B12_Server 2 to Common Donor Port 1

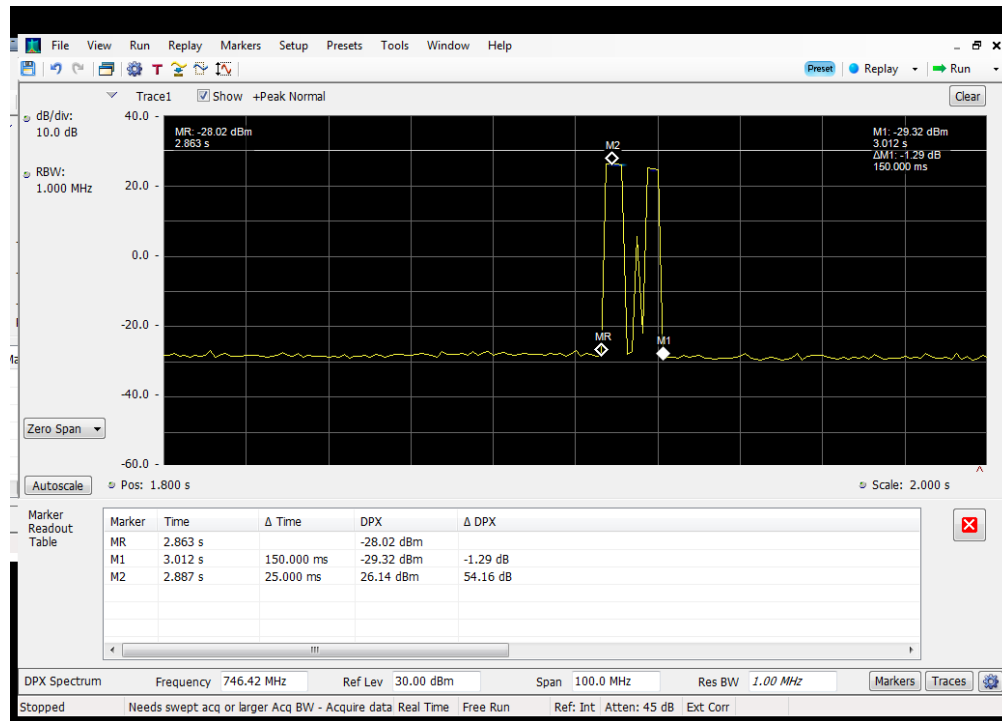


Detection Time_Downlink_728 - 746 MHz_B12_Server 2 to Dedicated Donor Port 3

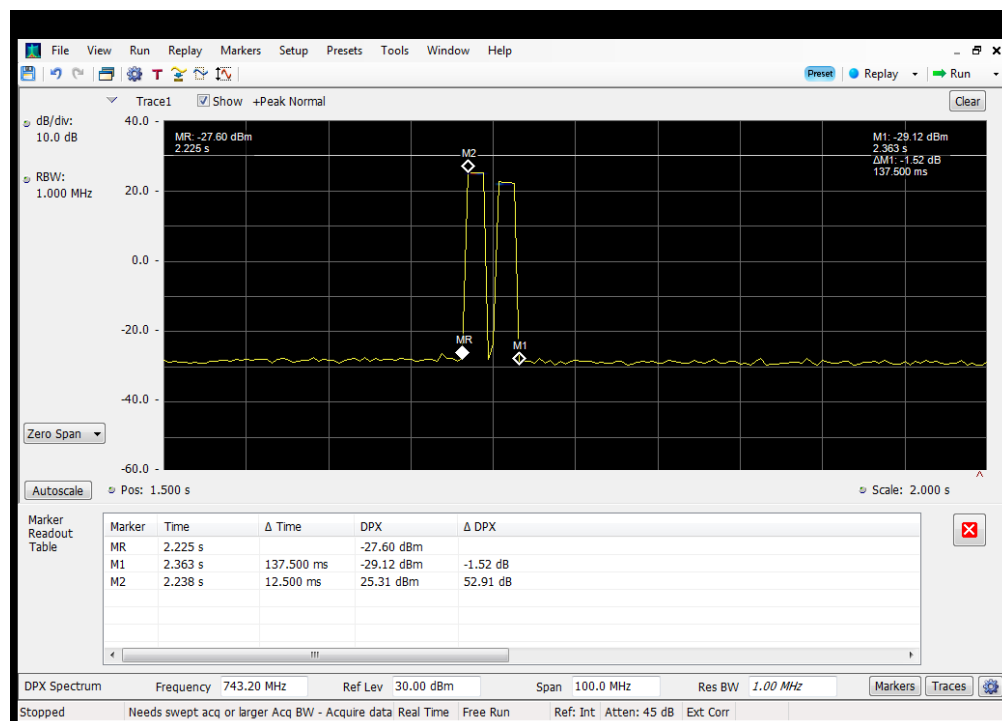




Detection Time_Downlink_728 - 746 MHz_B12_Server 3 to Common Donor Port 1

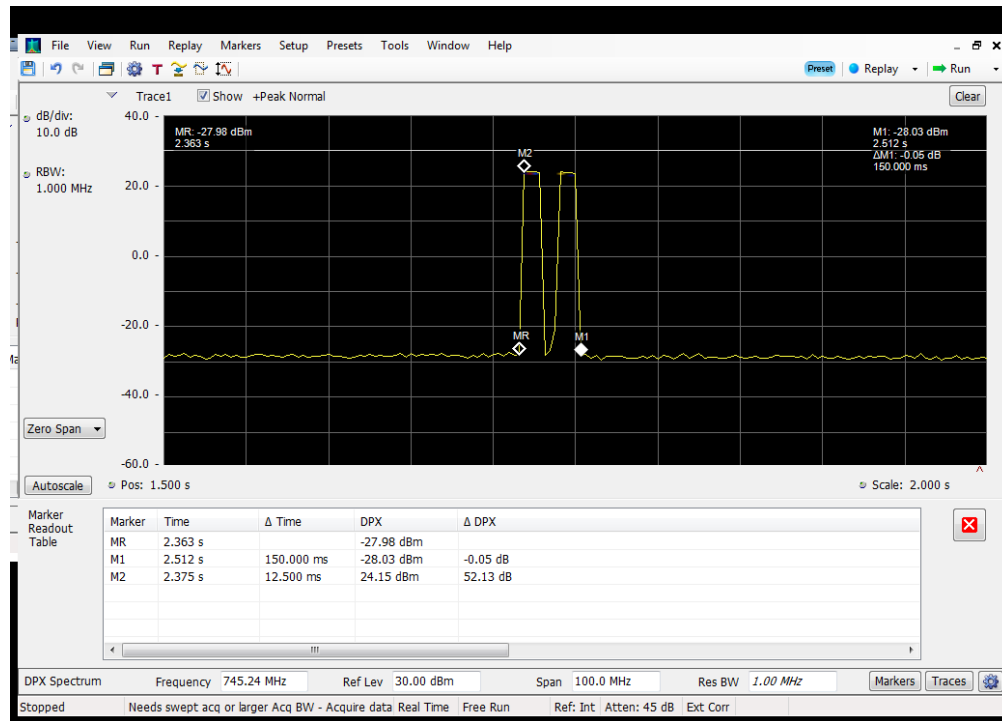


Detection Time_Downlink_728 - 746 MHz_B12_Server 3 to Dedicated Donor Port 3

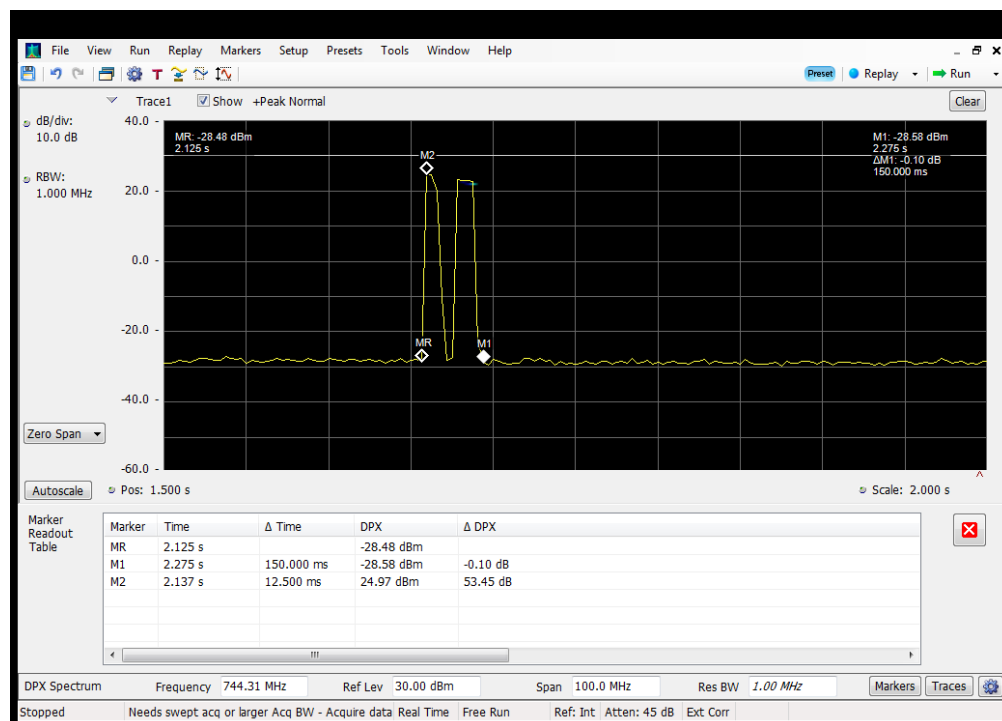




Detection Time_Downlink_728 - 746 MHz_B12_Server 4 to Common Donor Port 1

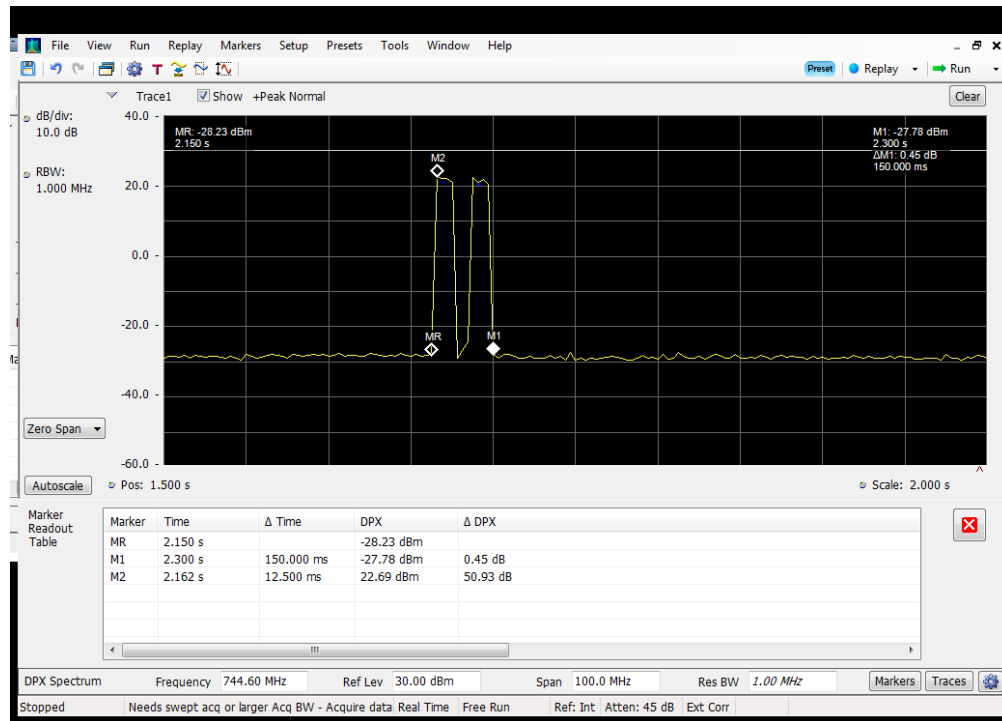


Detection Time_Downlink_728 - 746 MHz_B12_Server 4 to Dedicated Donor Port 3

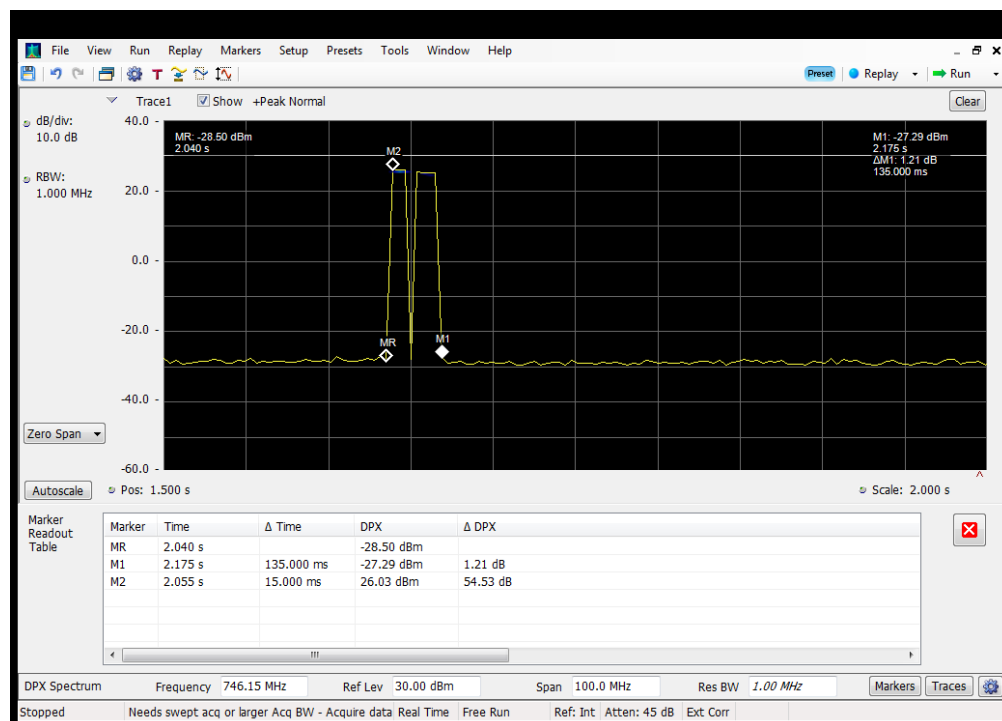




Detection Time_Downlink_746 - 757 MHz_B13_Server 1 to Common Donor Port 1

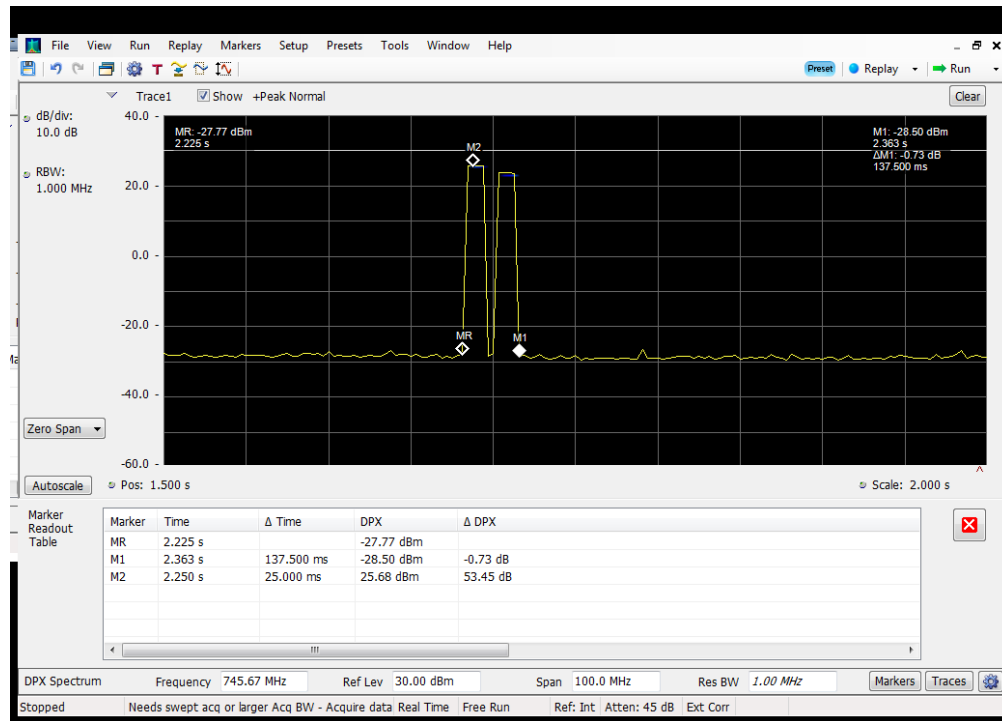


Detection Time_Downlink_746 - 757 MHz_B13_Server 1 to Dedicated Donor Port 3

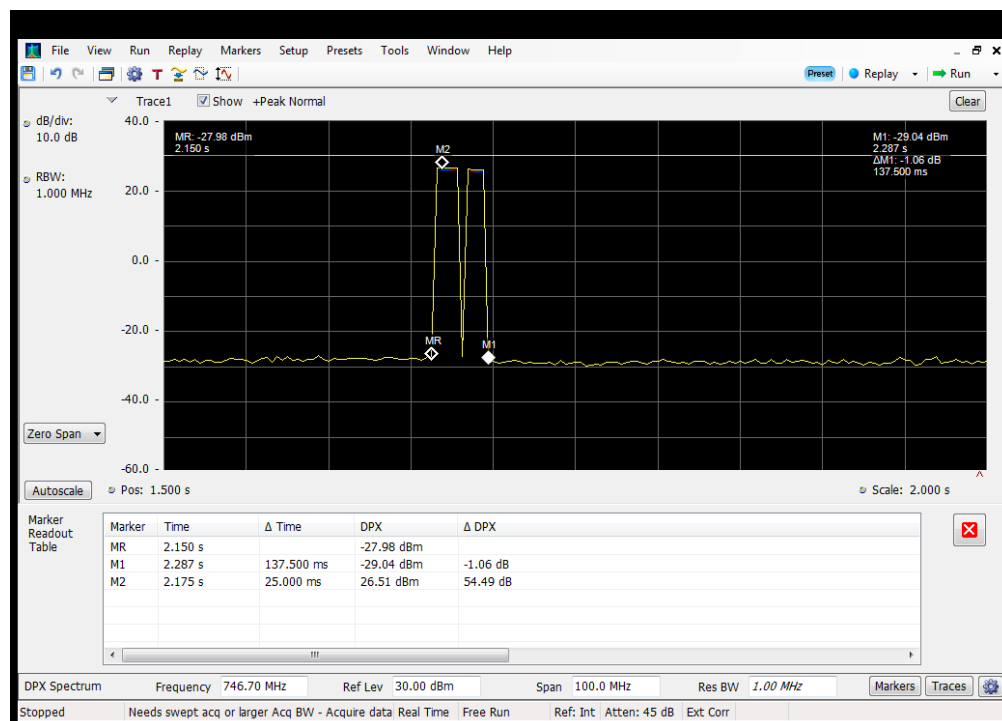




Detection Time_Downlink_746 - 757 MHz_B13_Server 2 to Common Donor Port 1

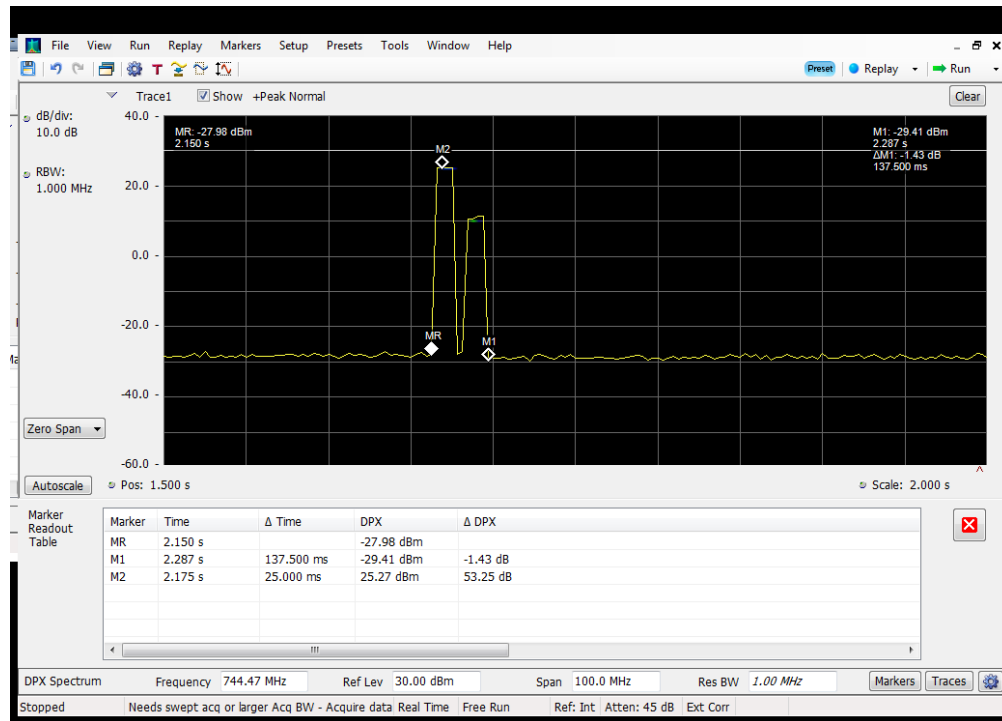


Detection Time_Downlink_746 - 757 MHz_B13_Server 2 to Dedicated Donor Port 3

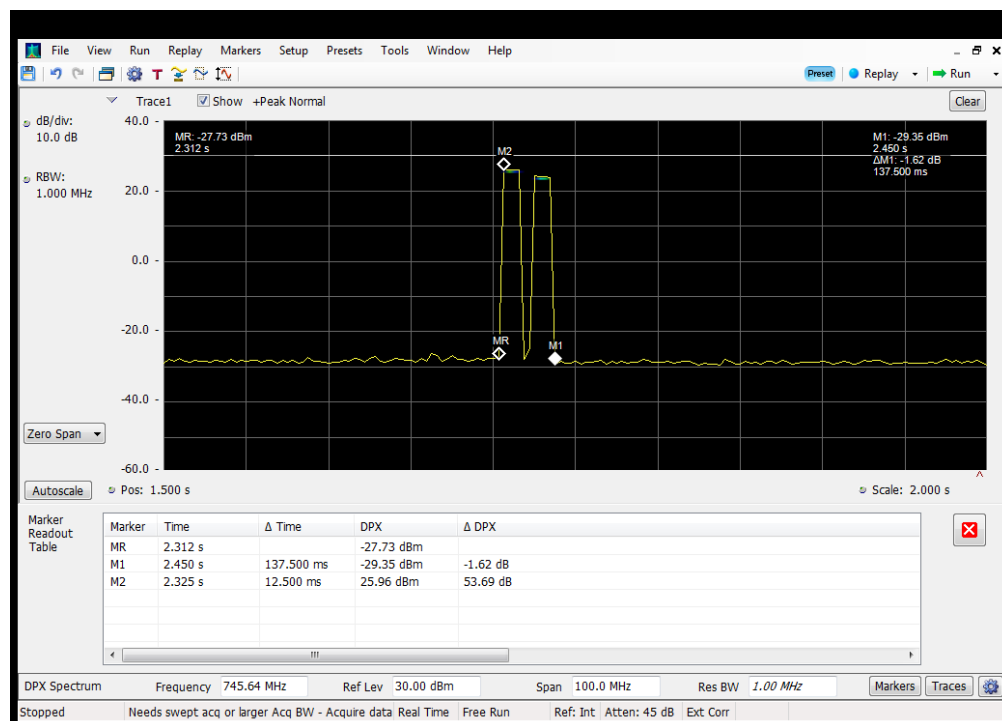




Detection Time_Downlink_746 - 757 MHz_B13_Server 3 to Common Donor Port 1

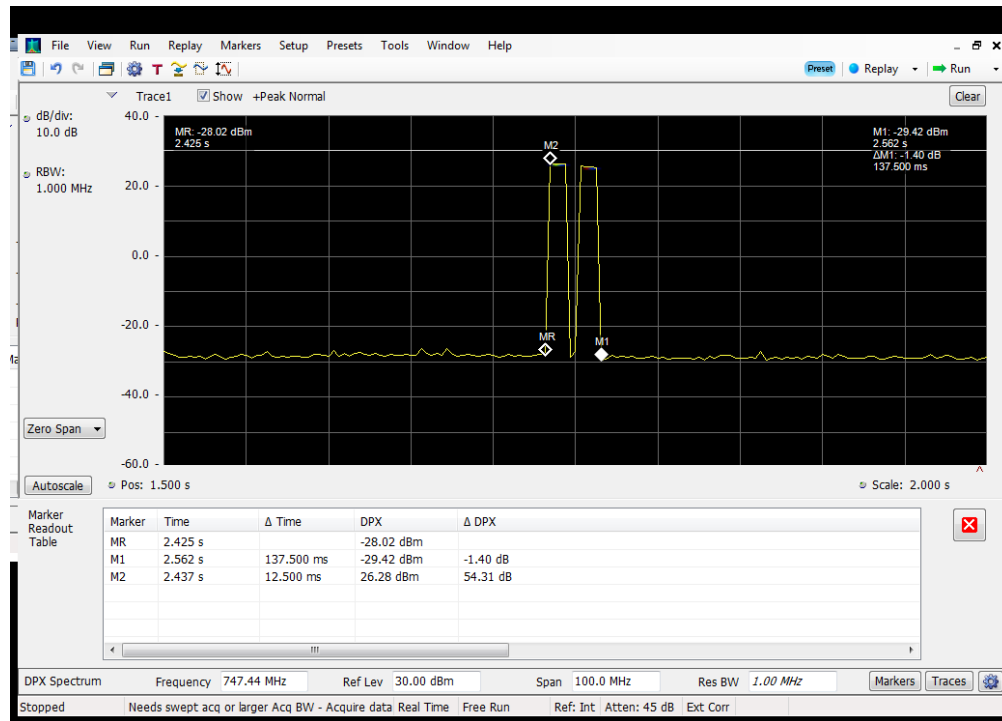


Detection Time_Downlink_746 - 757 MHz_B13_Server 3 to Dedicated Donor Port 3

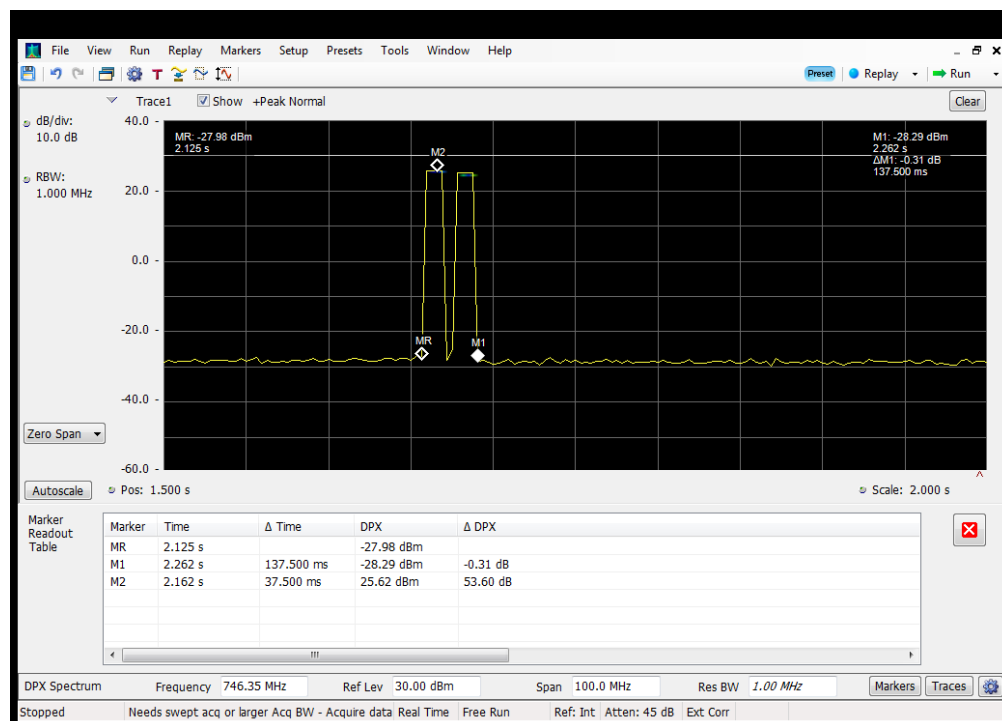




Detection Time_Downlink_746 - 757 MHz_B13_Server 4 to Common Donor Port 1

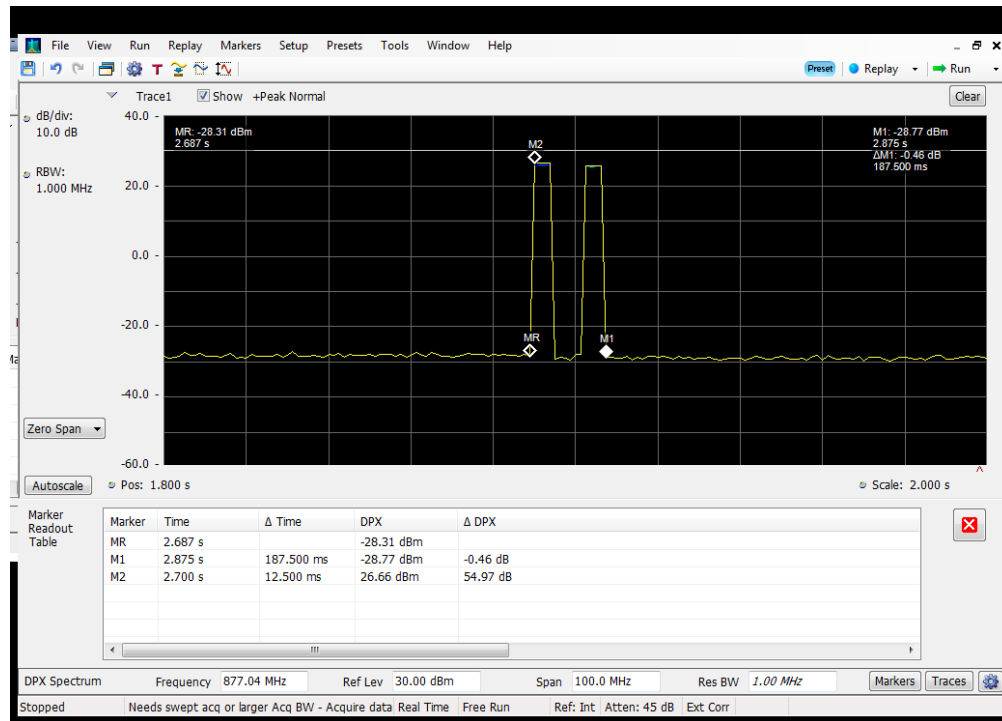


Detection Time_Downlink_746 - 757 MHz_B13_Server 4 to Dedicated Donor Port 3

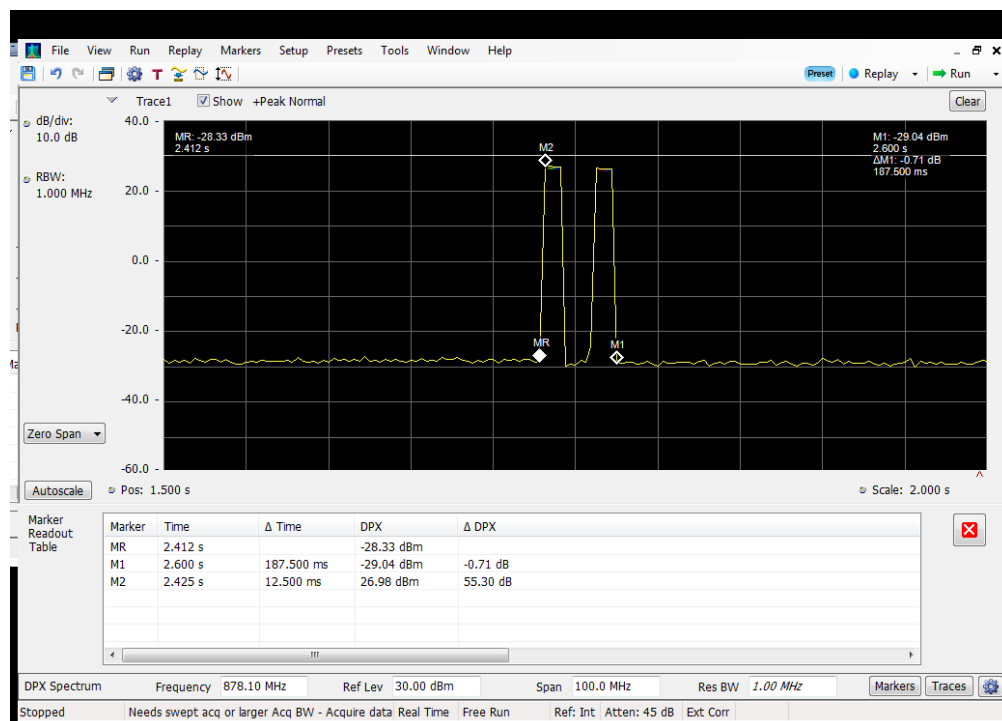




Detection Time_Downlink_869 - 894 MHz_B13_Server 4 to Common Donor Port 1

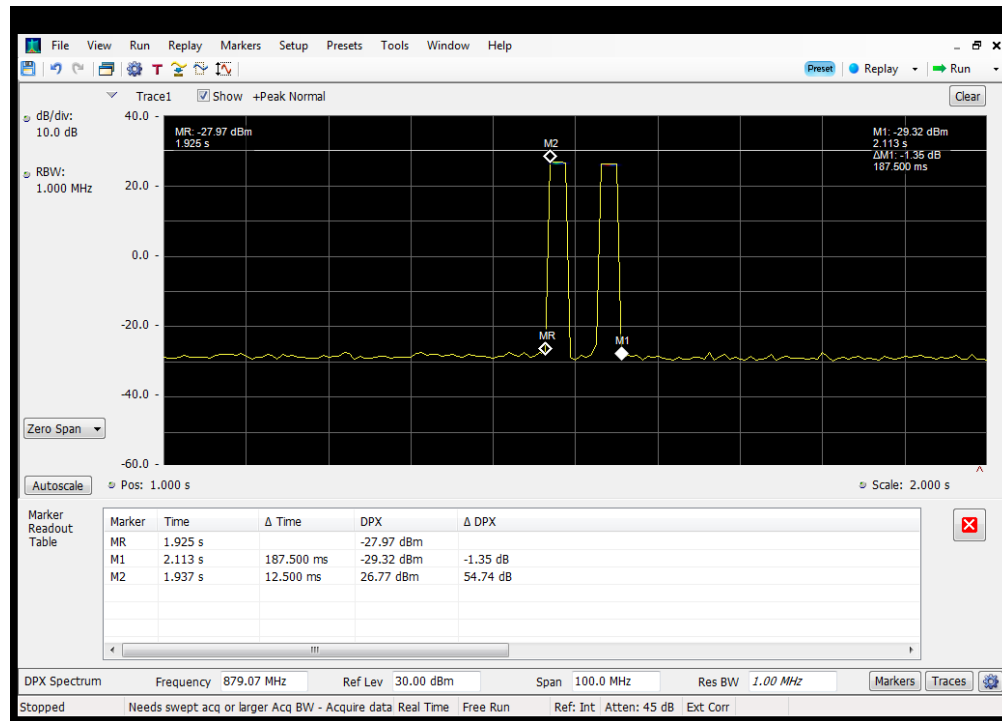


Detection Time_Downlink_869 - 894 MHz_B5_Server 1 to Common Donor Port 1

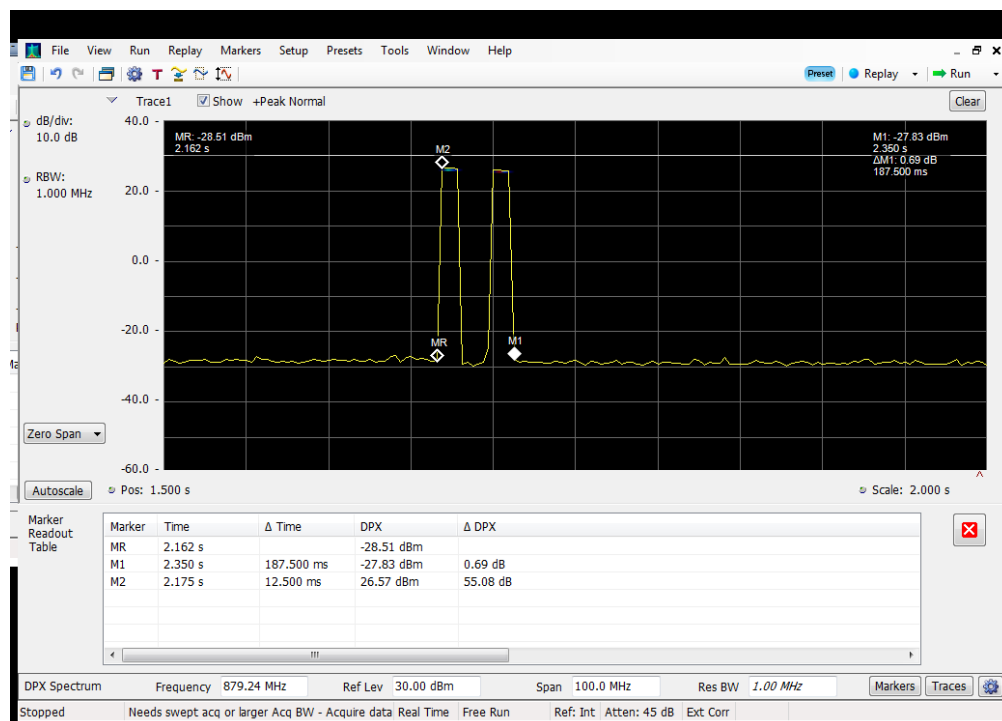




Detection Time_Downlink_869 - 894 MHz_B5_Server 1 to Dedicated Donor Port 2

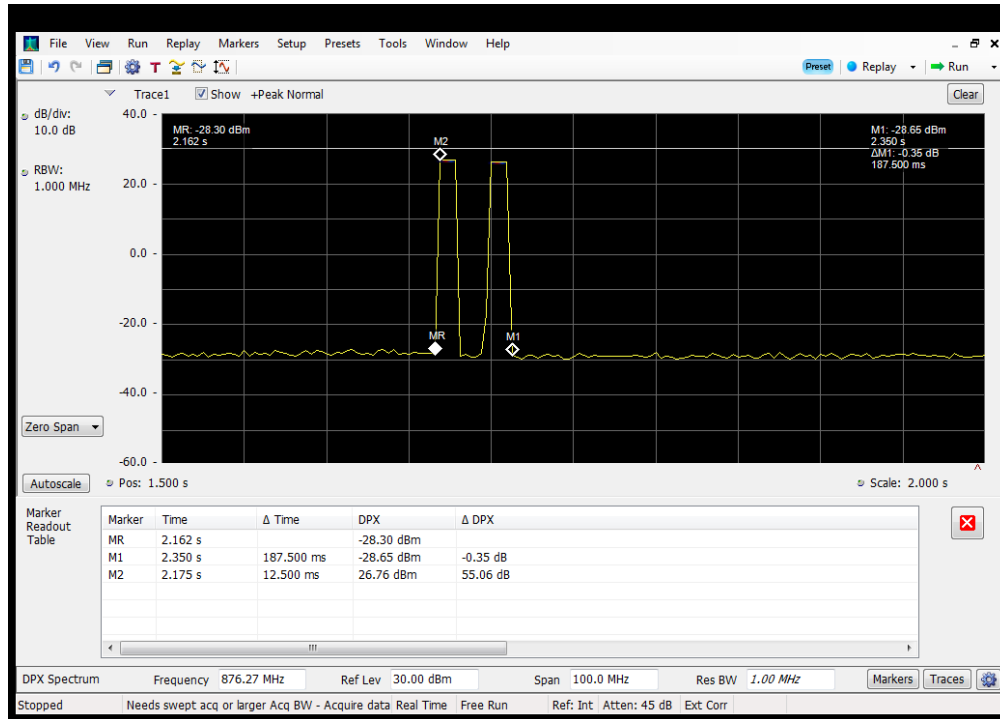


Detection Time_Downlink_869 - 894 MHz_B5_Server 2 to Common Donor Port 1

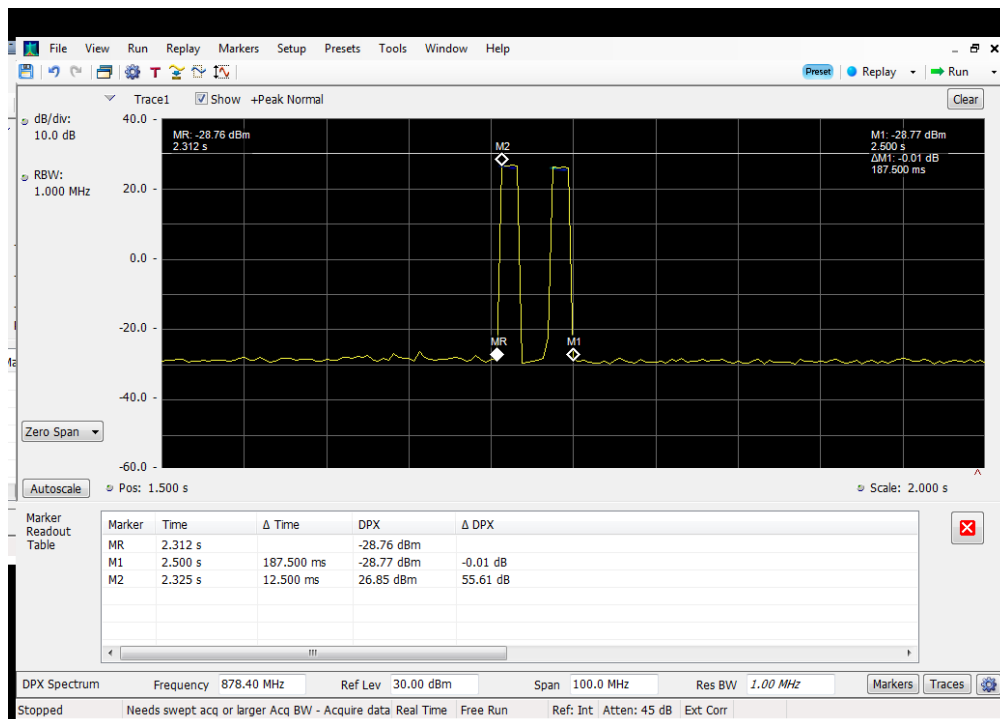




Detection Time_Downlink_869 - 894 MHz_B5_Server 2 to Dedicated Donor Port 2

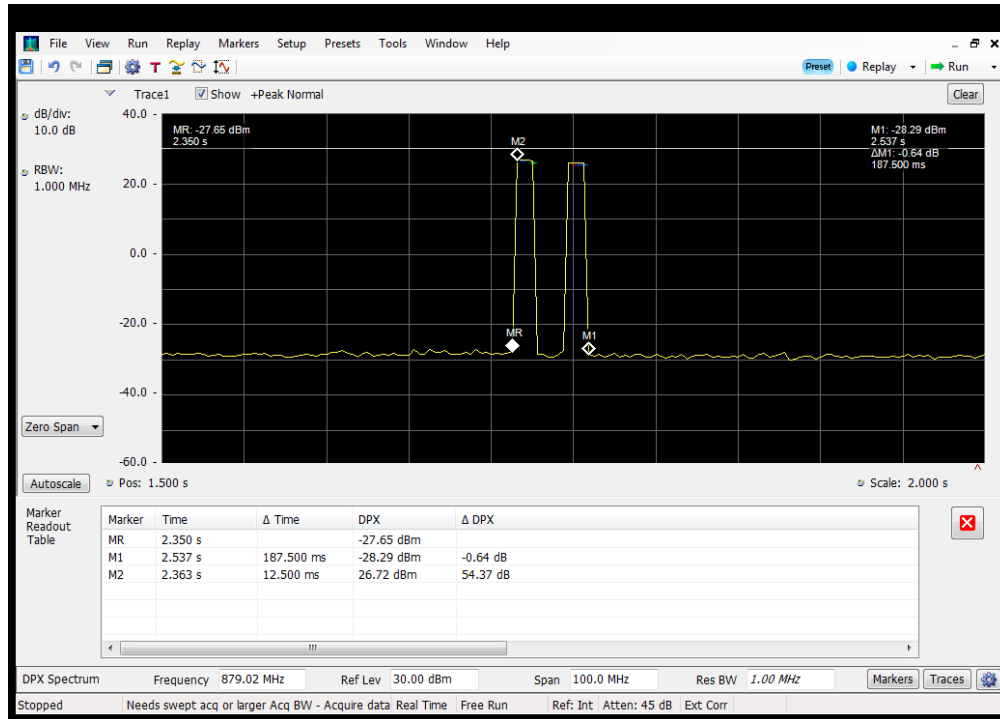


Detection Time_Downlink_869 - 894 MHz_B5_Server 3 to Common Donor Port 1

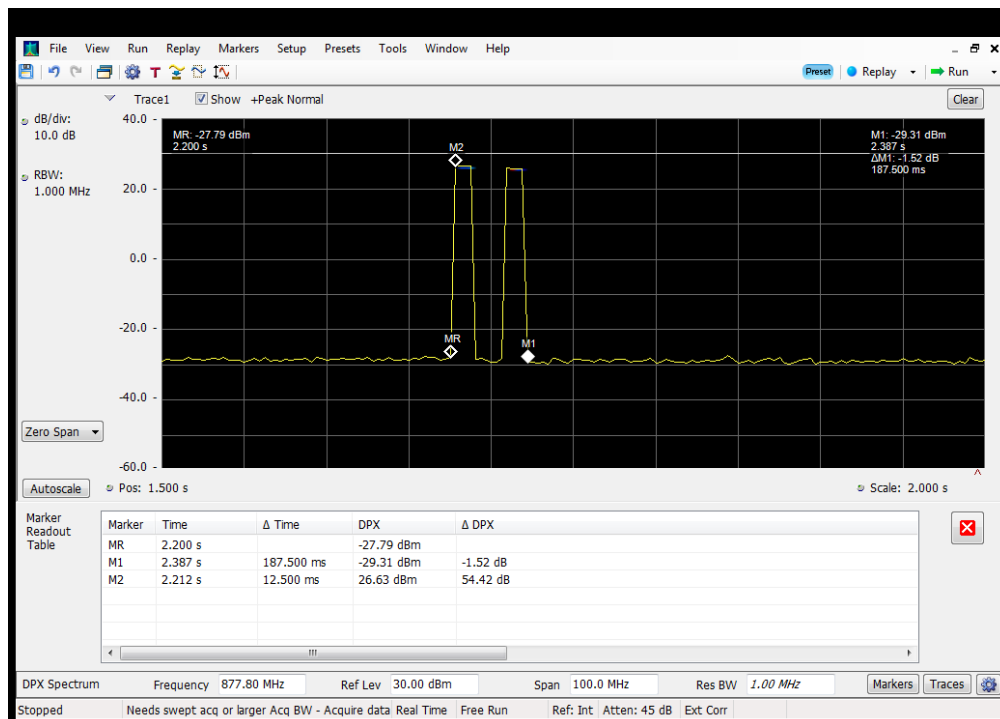




Detection Time_Downlink_869 - 894 MHz_B5_Server 3 to Dedicated Donor Port 2

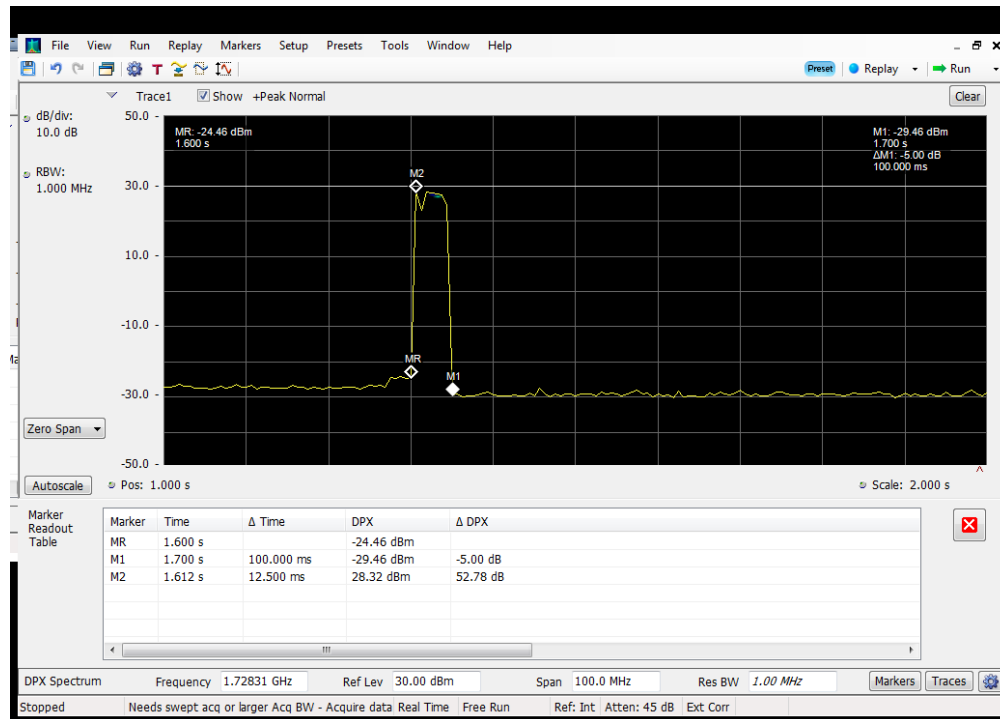


Detection Time_Downlink_869 - 894 MHz_B5_Server 4 to Dedicated Donor Port 2

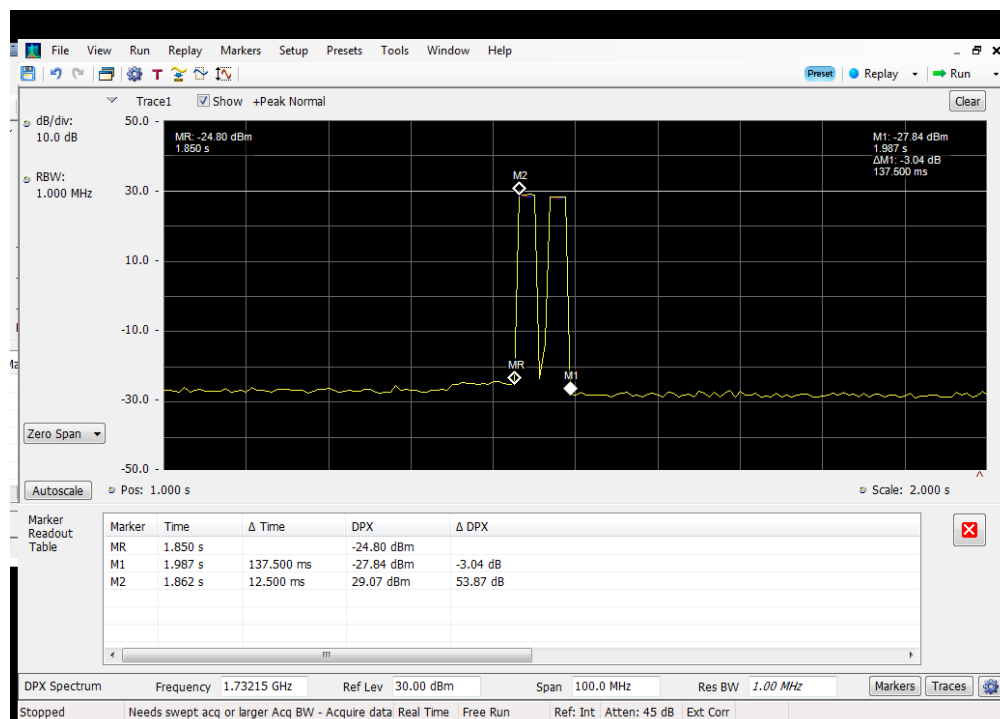




Detection Time_Uplink_1710 - 1755 MHz_B4_Server 1 to Dedicated Donor Port 1

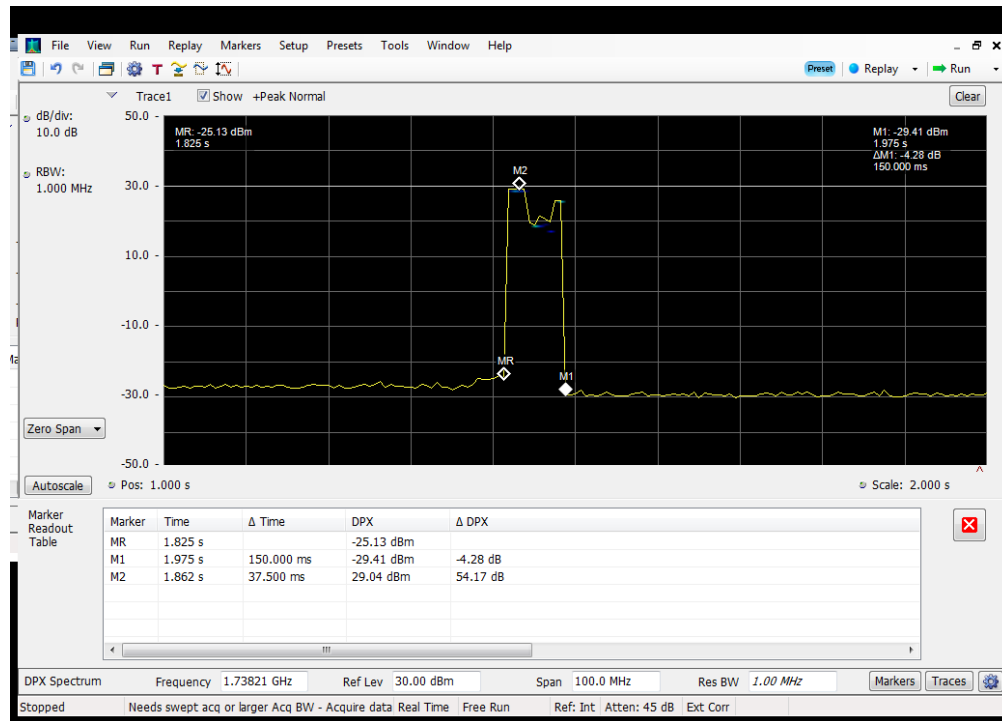


Detection Time_Uplink_1710 - 1755 MHz_B4_Server 2 to Dedicated Donor Port 1

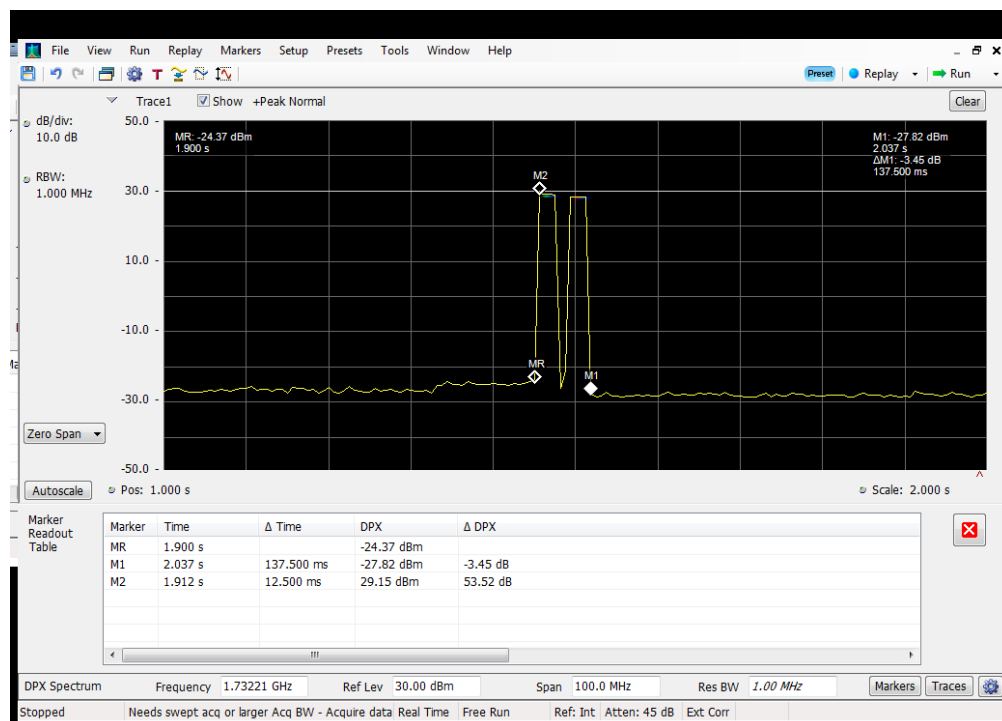




Detection Time_Uplink_1710 - 1755 MHz_B4_Server 3 to Dedicated Donor Port 1

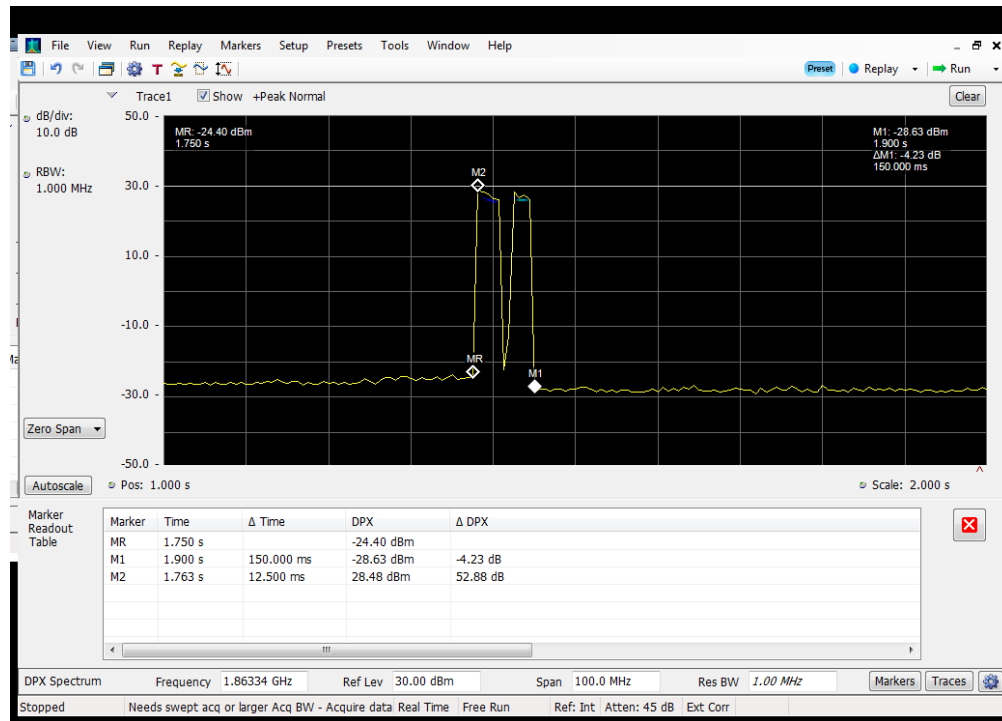


Detection Time_Uplink_1710 - 1755 MHz_B4_Server 4 to Dedicated Donor Port 1

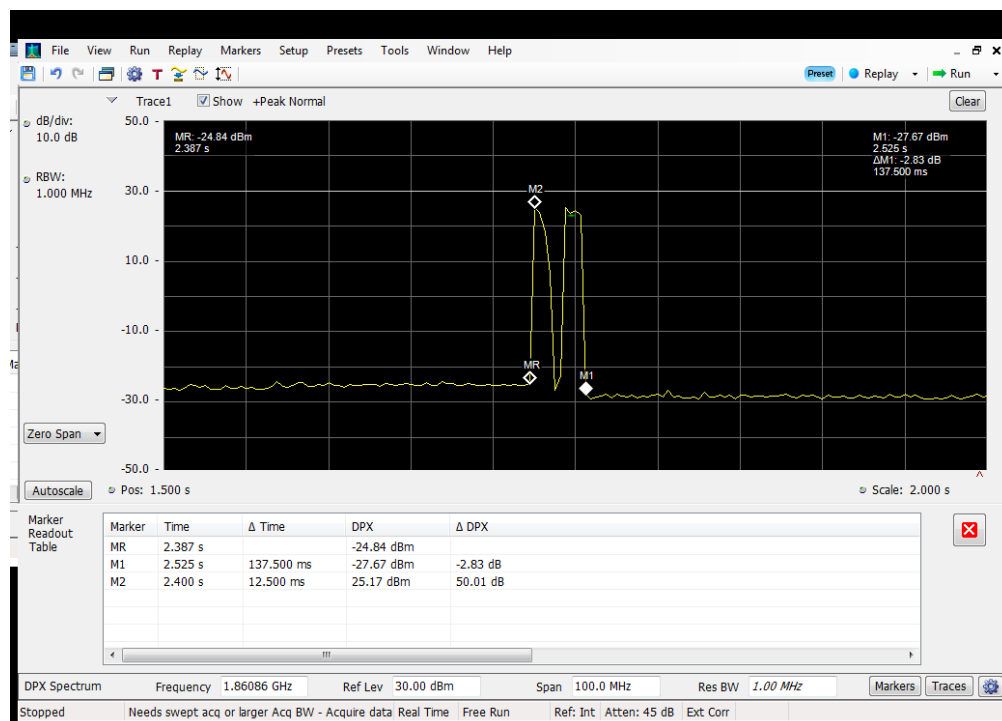




Detection Time_Uplink_1850 - 1915 MHz_B25_Server 1 to Dedicated Donor Port 1

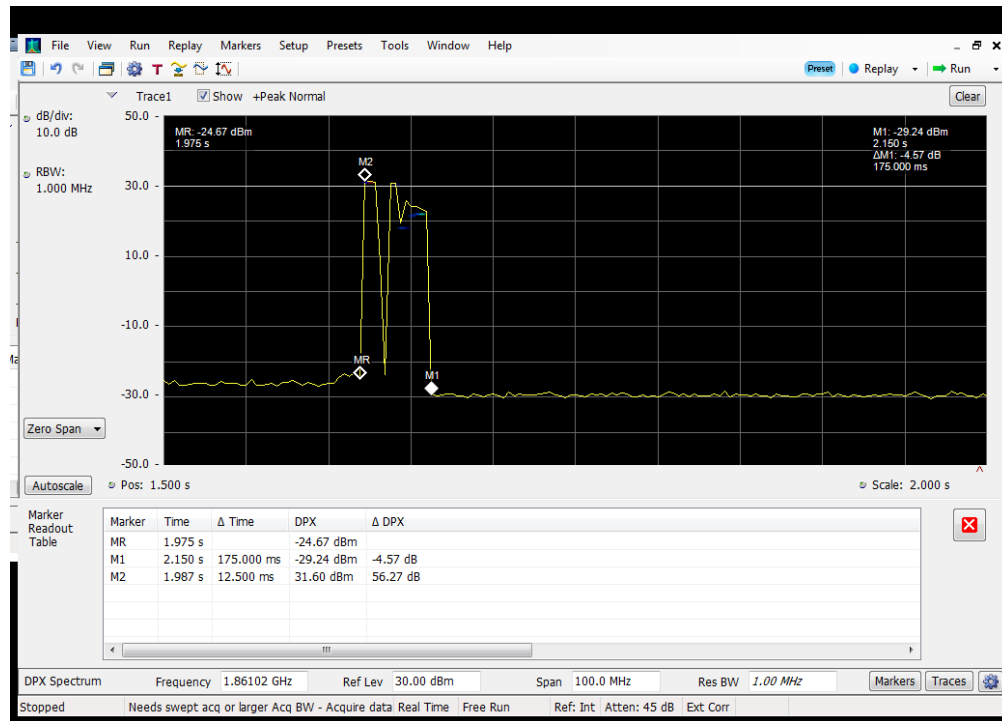


Detection Time_Uplink_1850 - 1915 MHz_B25_Server 2 to Dedicated Donor Port 1

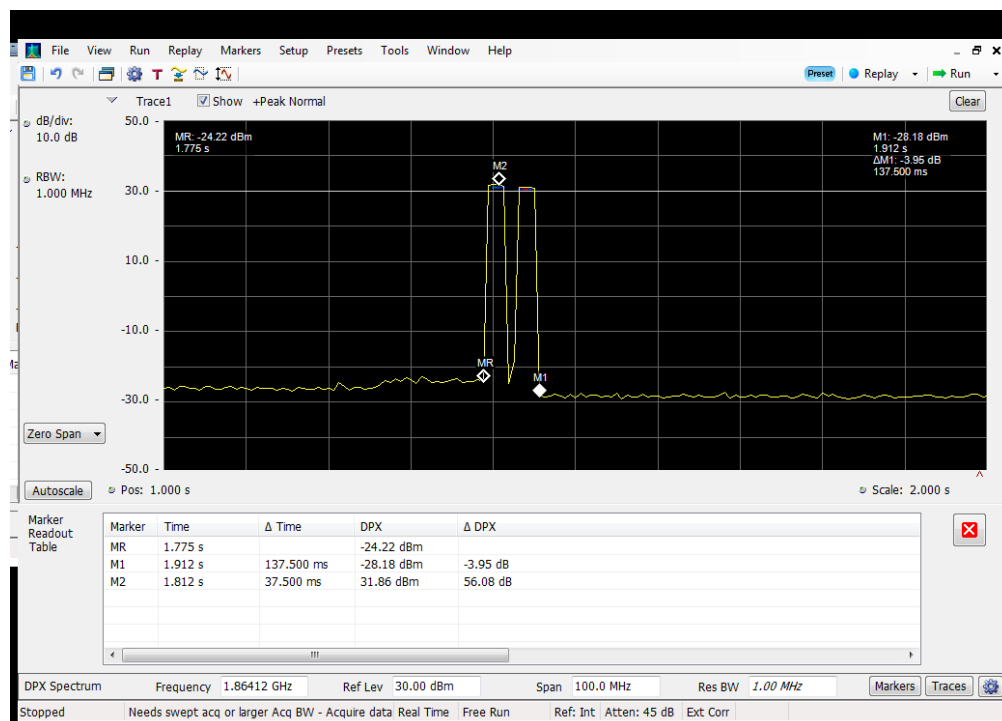




Detection Time_Uplink_1850 - 1915 MHz_B25_Server 3 to Dedicated Donor Port 1

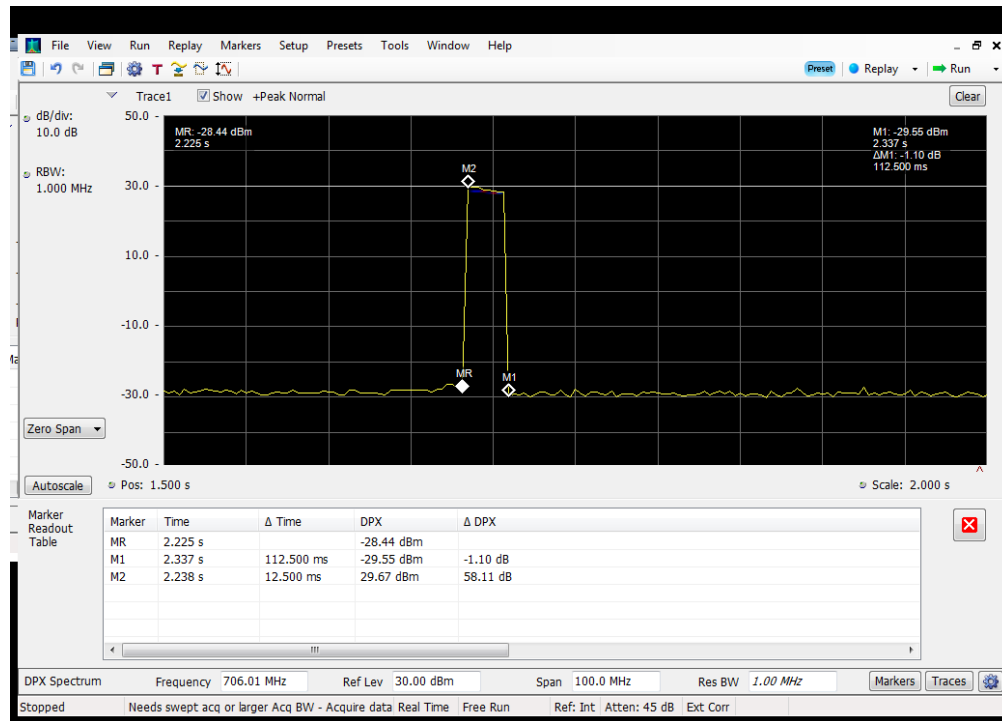


Detection Time_Uplink_1850 - 1915 MHz_B25_Server 4 to Dedicated Donor Port 1

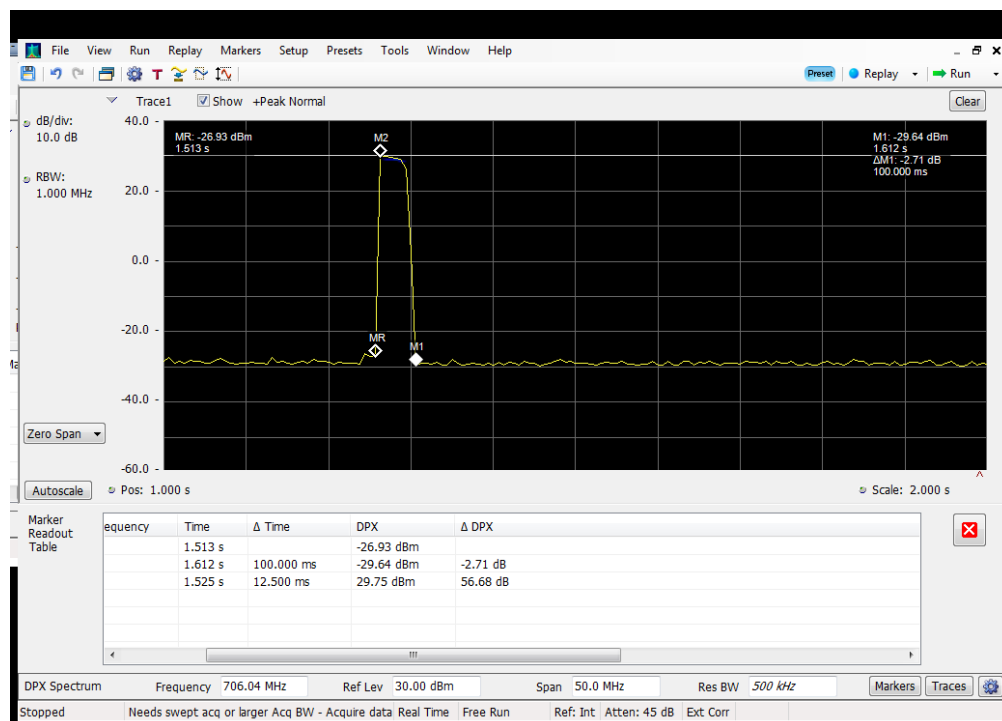




Detection Time_Uplink_698 - 716 MHz_B12_Server 1 to Common Donor Port 1

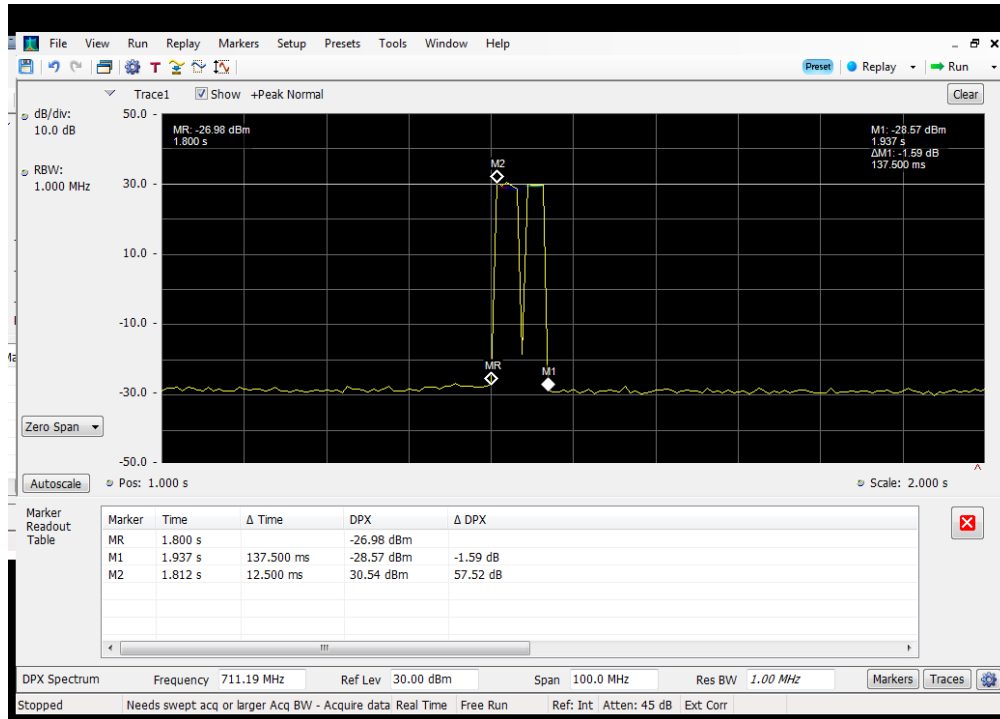


Detection Time_Uplink_698 - 716 MHz_B12_Server 1 to Dedicated Donor Port 3

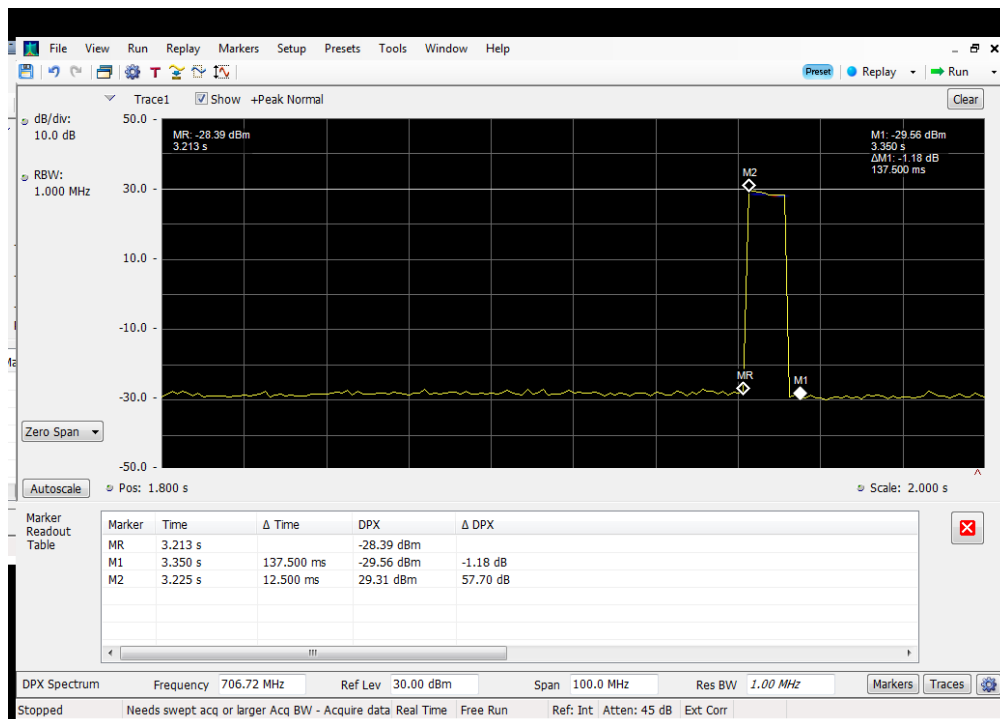




Detection Time_Uplink_698 - 716 MHz_B12_Server 2 to Dedicated Donor Port 3

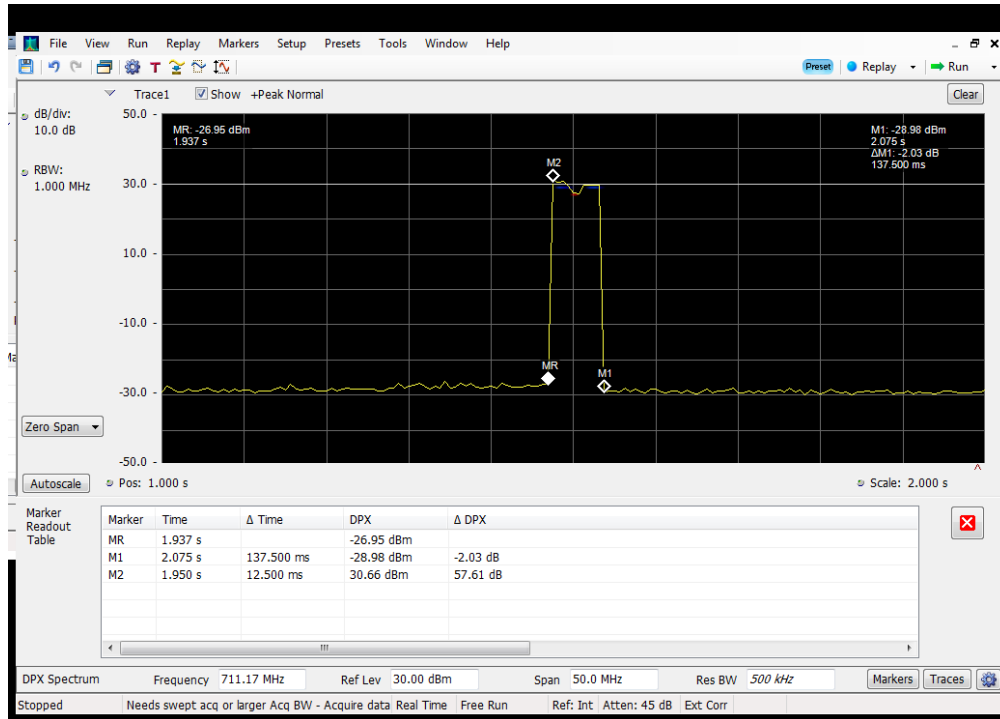


Detection Time_Uplink_698 - 716 MHz_B12_Server 3 to Common Donor Port 1

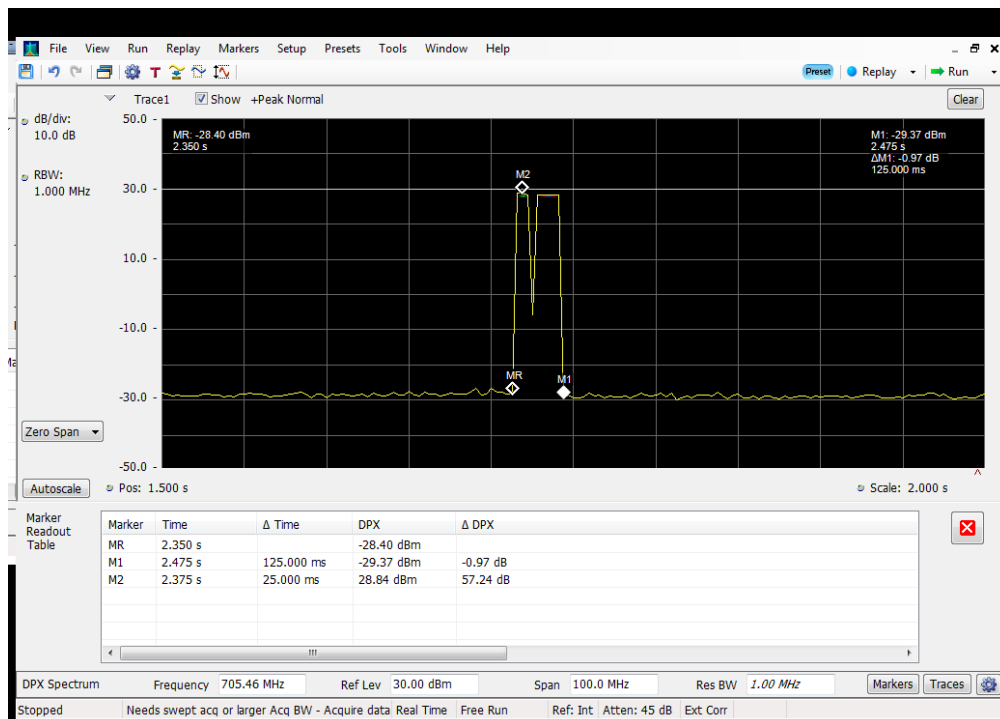




Detection Time_Uplink_698 - 716 MHz_B12_Server 3 to Dedicated Donor Port 3

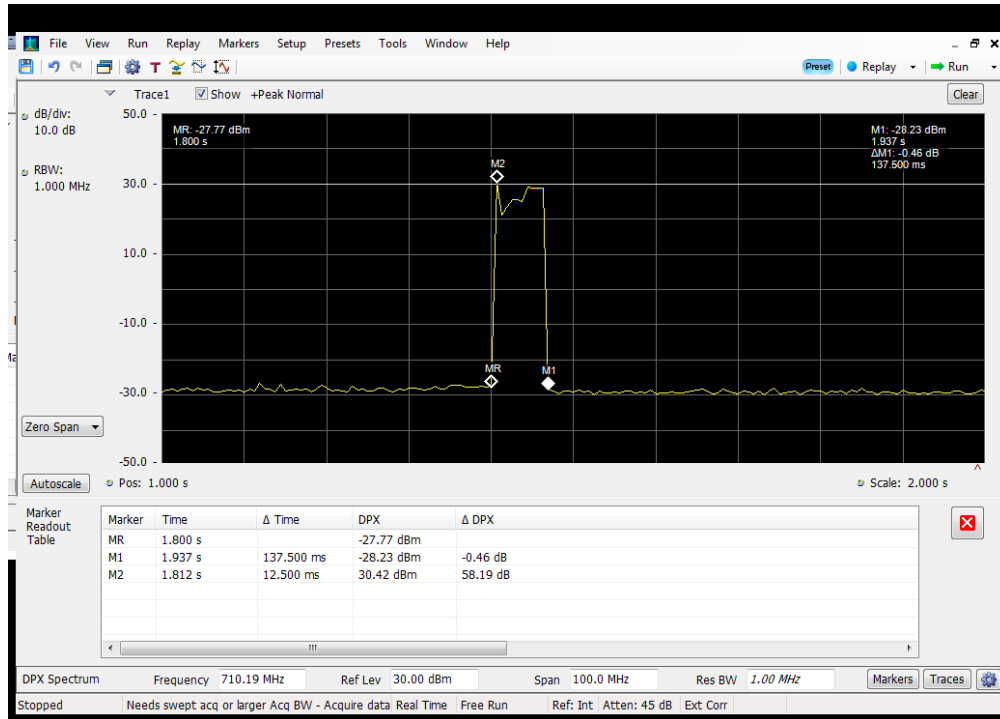


Detection Time_Uplink_698 - 716 MHz_B12_Server 4 to Common Donor Port 1

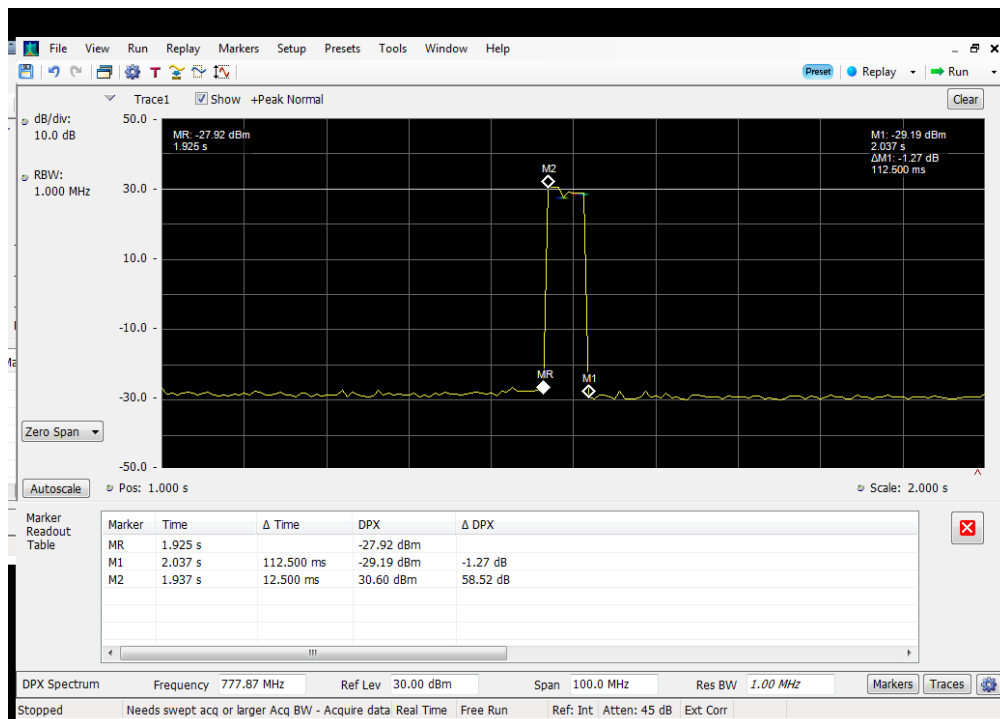




Detection Time_Uplink_698 - 716 MHz_B12_Server 4 to Dedicated Donor Port 3

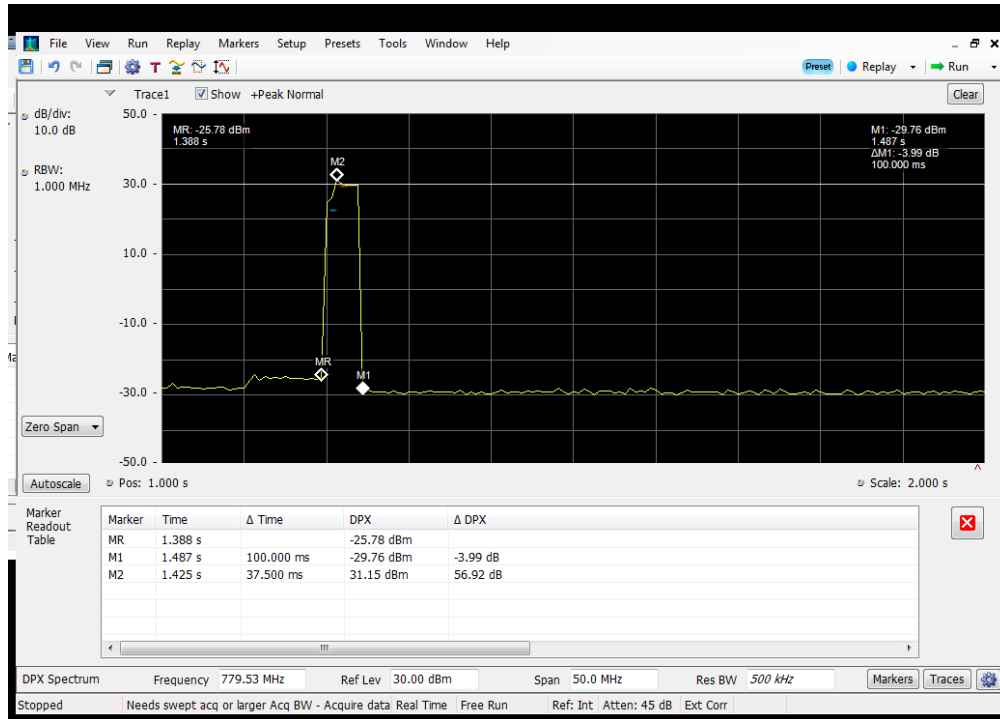


Detection Time_Uplink_776 - 787 MHz_B13_Server 1 to Common Donor Port 1

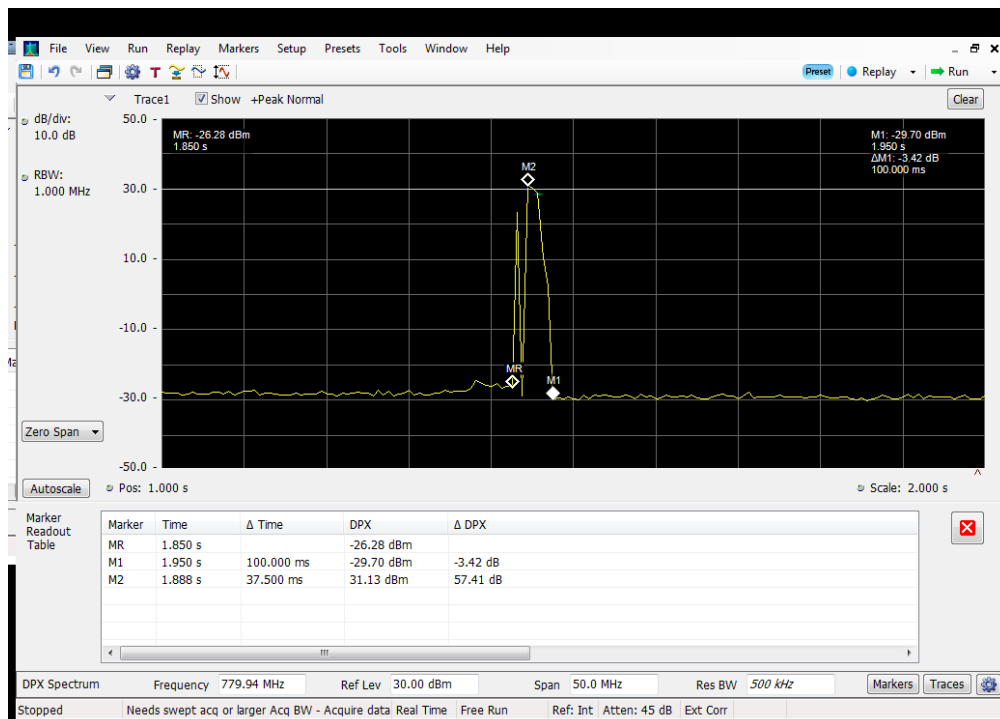




Detection Time_Uplink_776 - 787 MHz_B13_Server 1 to Dedicated Donor Port 3

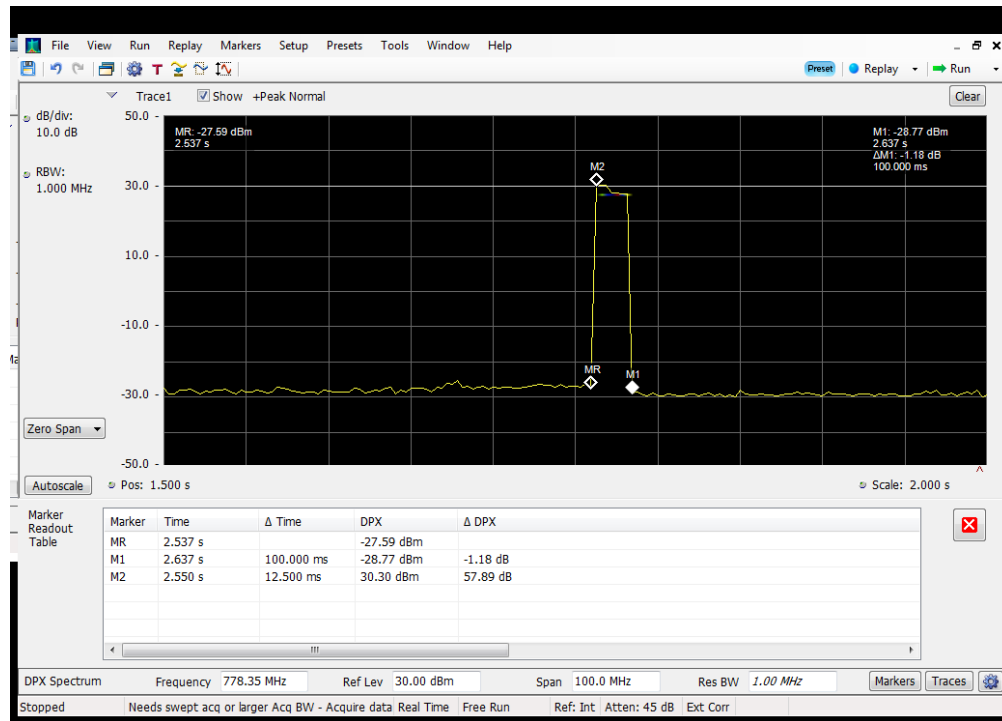


Detection Time_Uplink_776 - 787 MHz_B13_Server 2 to Dedicated Donor Port 3

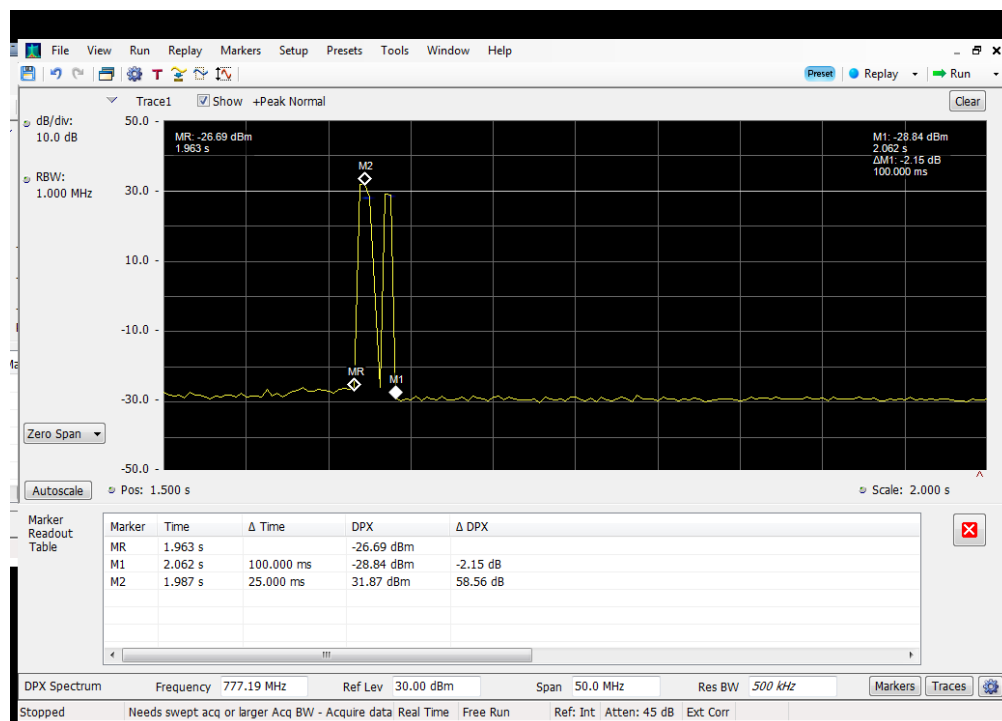




Detection Time_Uplink_776 - 787 MHz_B13_Server 3 to Common Donor Port 1

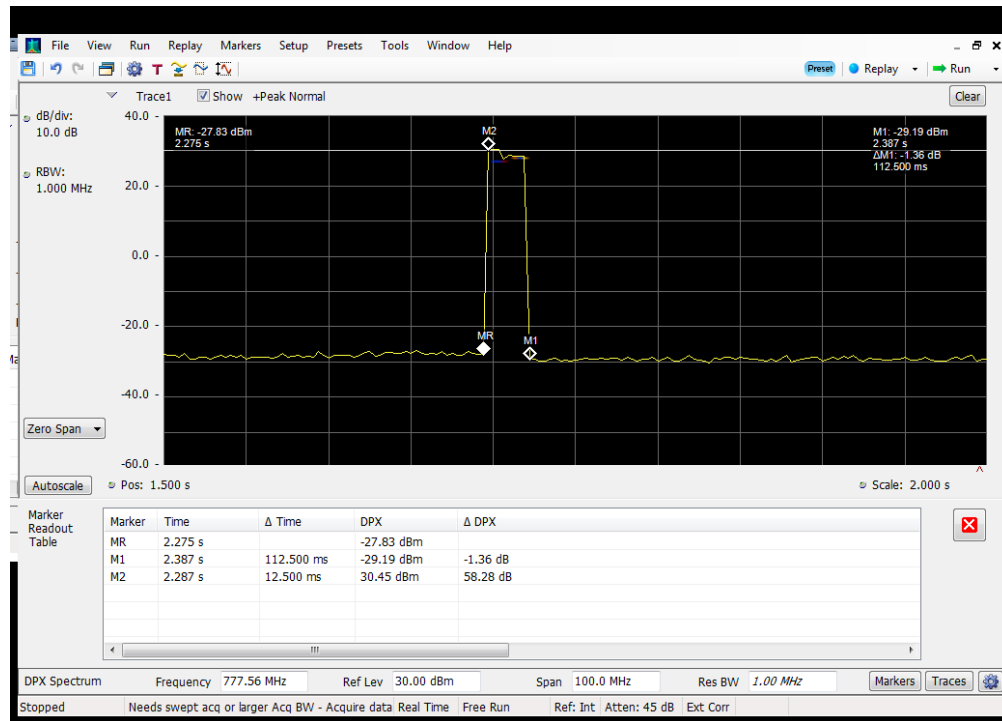


Detection Time_Uplink_776 - 787 MHz_B13_Server 3 to Dedicated Donor Port 3

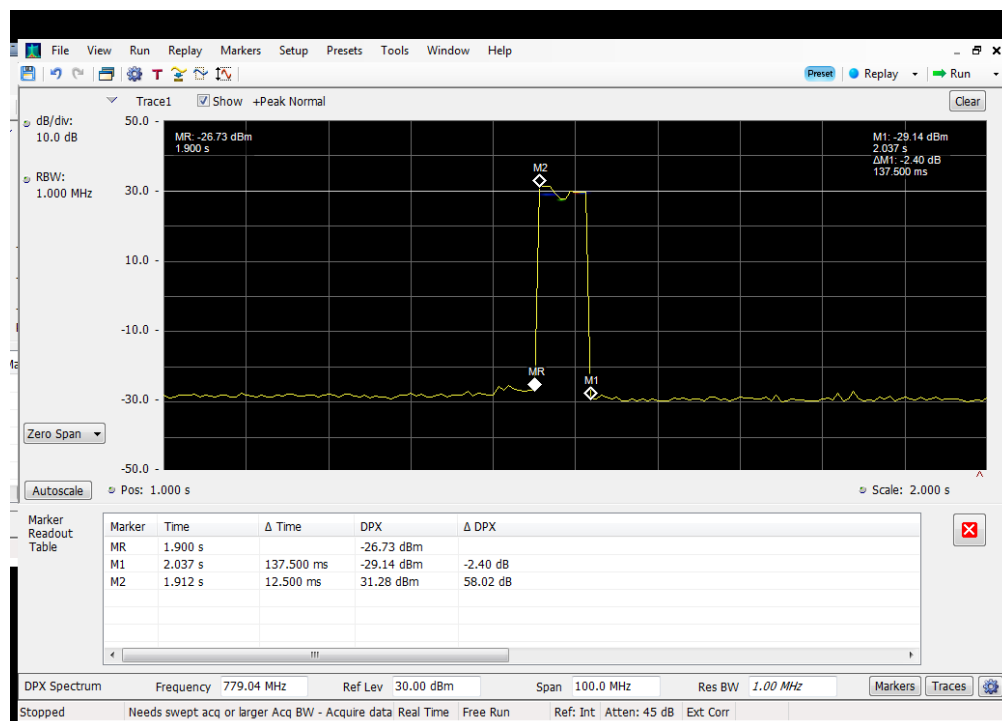




Detection Time_Uplink_776 - 787 MHz_B13_Server 4 to Common Donor Port 1

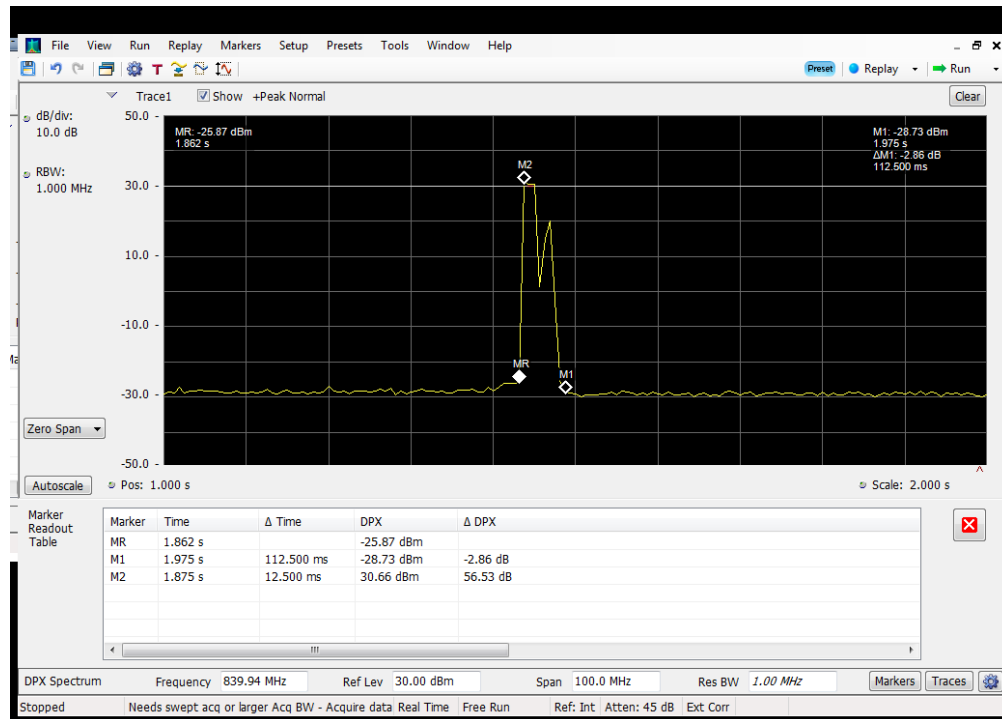


Detection Time_Uplink_776 - 787 MHz_B13_Server 4 to Dedicated Donor Port 3

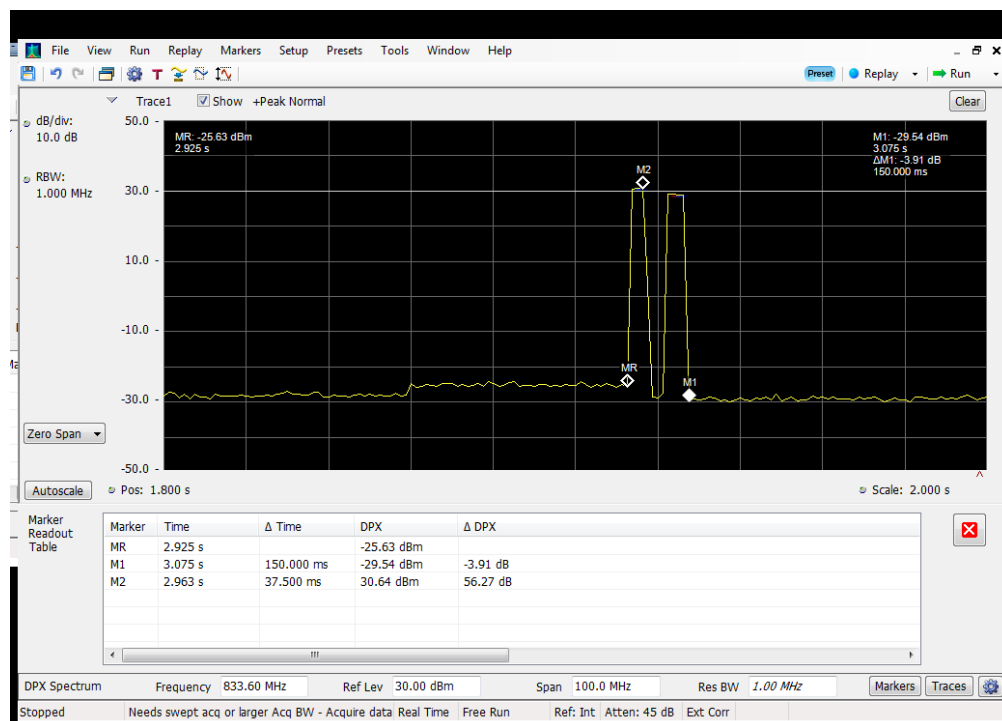




Detection Time_Uplink_824 - 849 MHz_B12_Server 3 to Common Donor Port 1

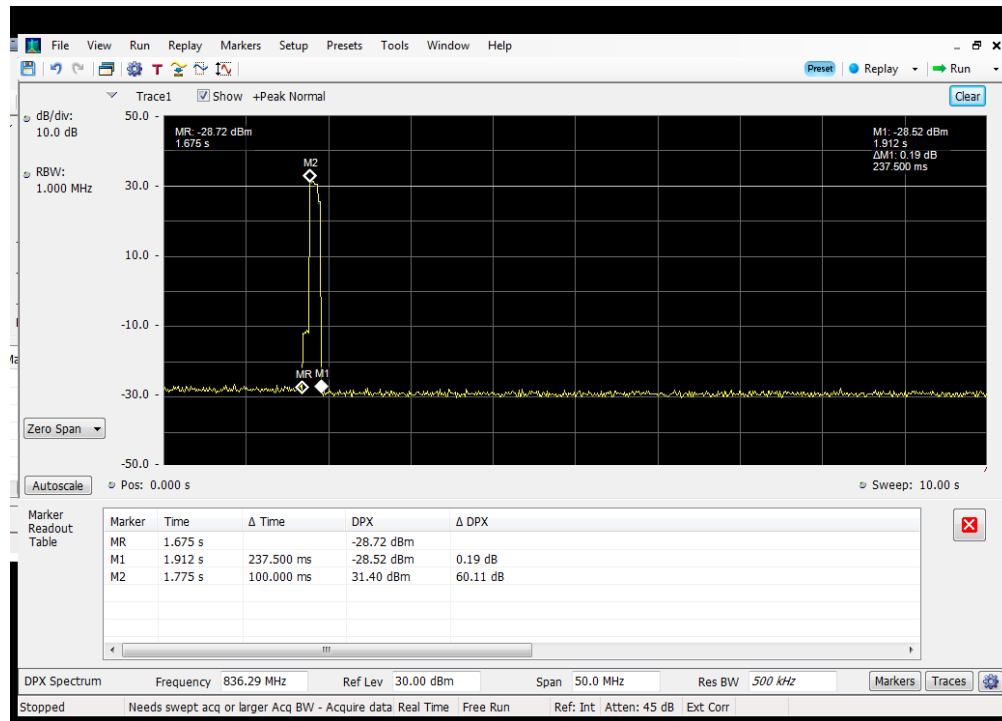


Detection Time_Uplink_824 - 849 MHz_B5_Server 1 to Common Donor Port 1

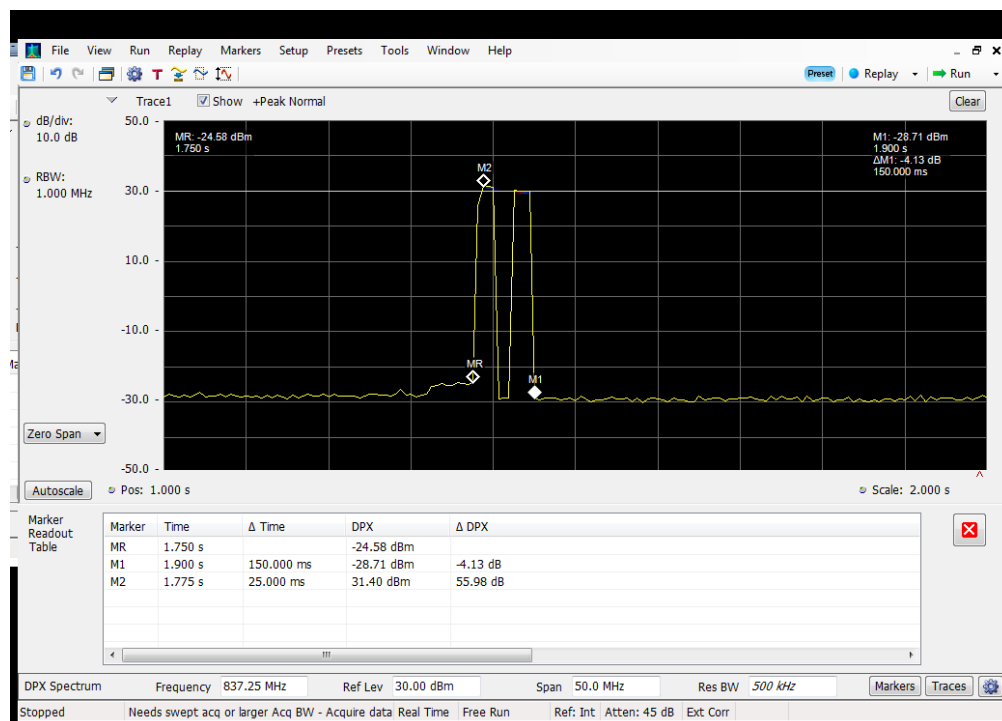




Detection Time_Uplink_824 - 849 MHz_B5_Server 1 to Dedicated Donor Port 2

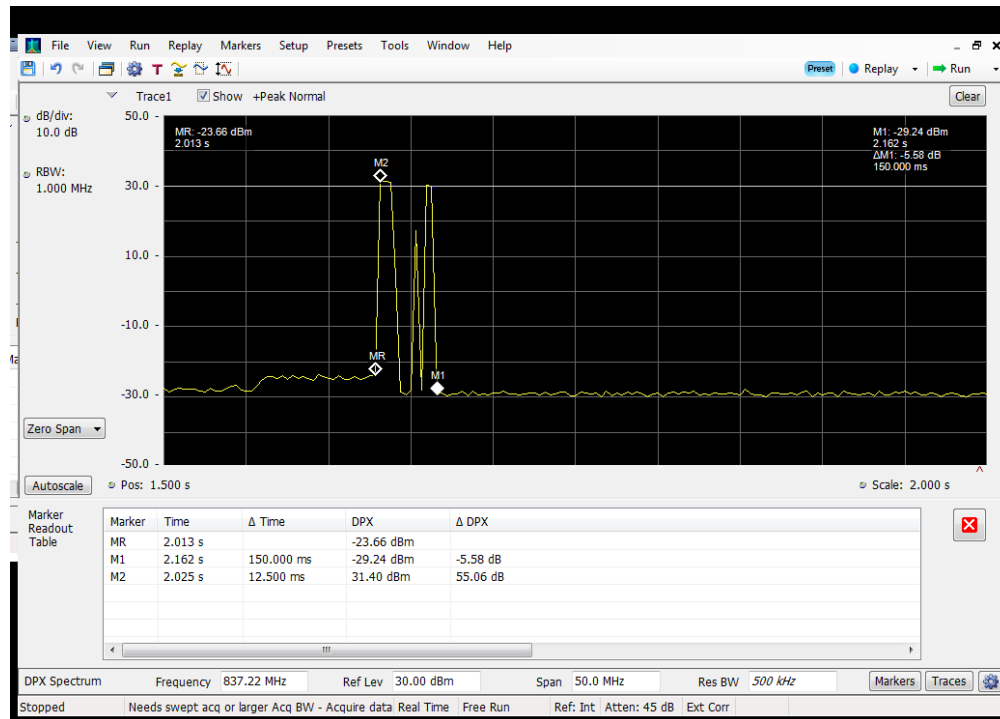


Detection Time_Uplink_824 - 849 MHz_B5_Server 2 to Dedicated Donor Port 2

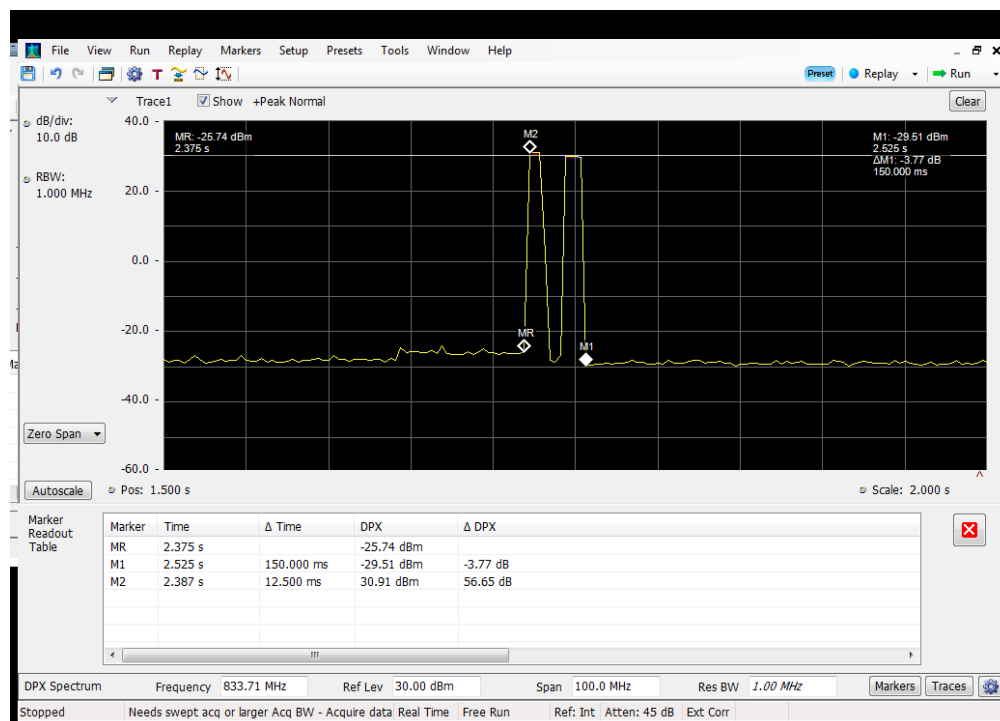




Detection Time_Uplink_824 - 849 MHz_B5_Server 3 to Dedicated Donor Port 2

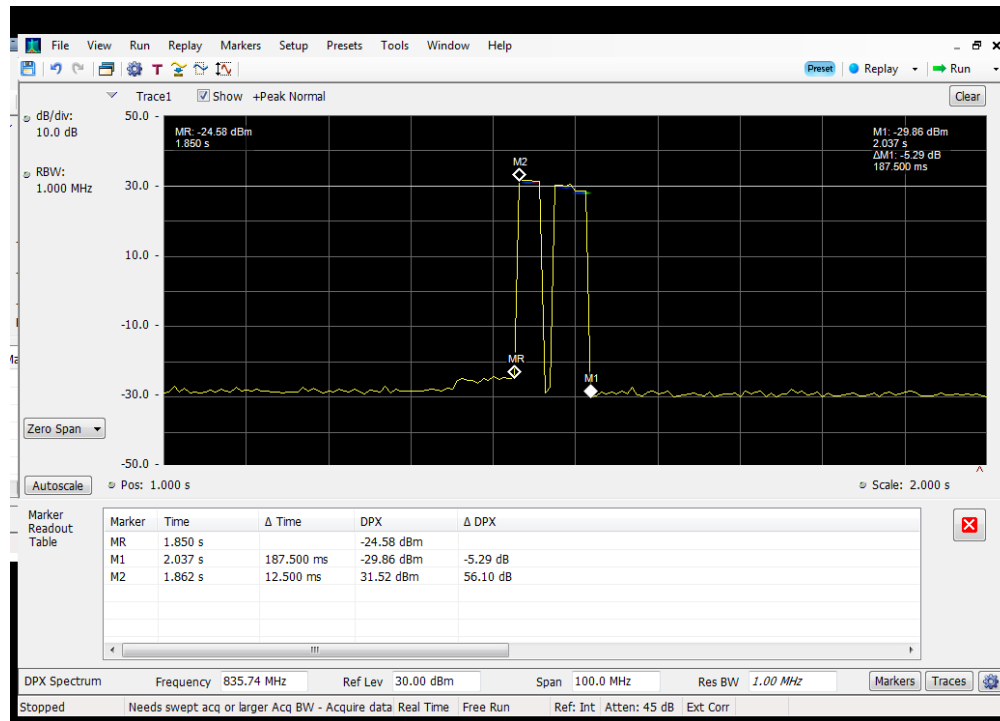


Detection Time_Uplink_824 - 849 MHz_B5_Server 4 to Common Donor Port 1

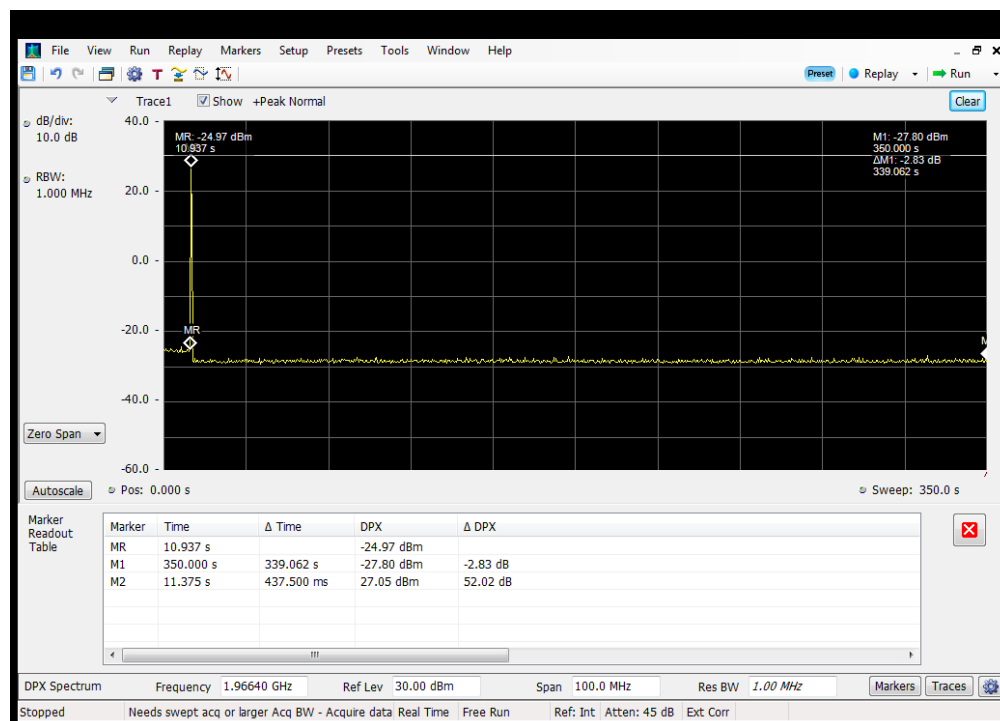




Detection Time_Uplink_824 - 849 MHz_B5_Server 4 to Dedicated Donor Port 2

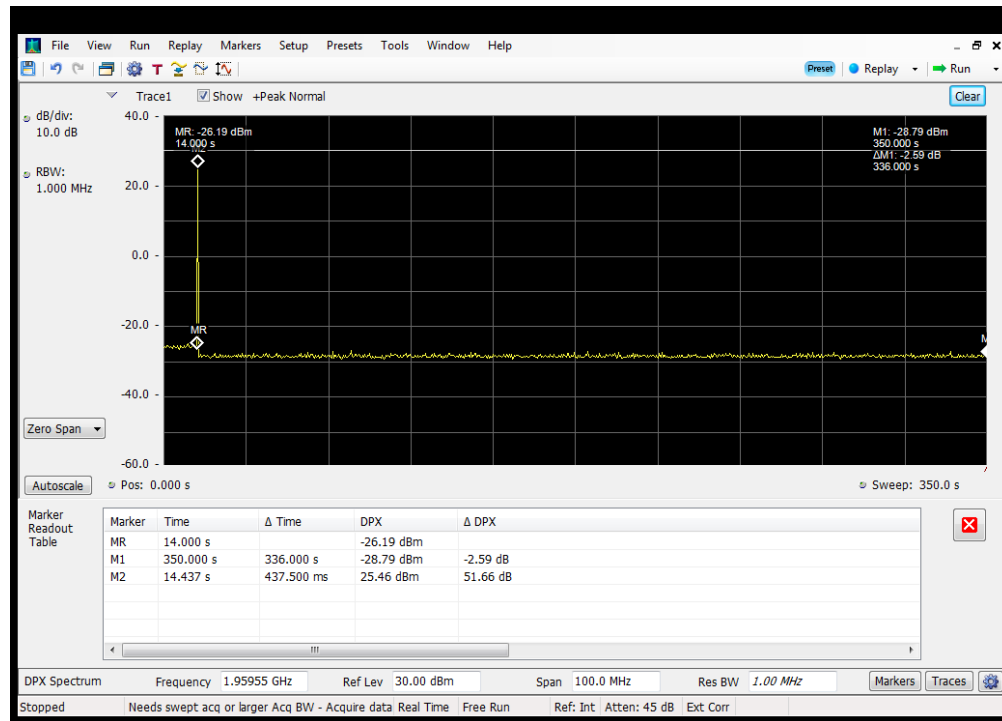


Restart Time_Downlink_1930 - 1995 MHz_B25_Server 3 to Dedicated Donor Port 1

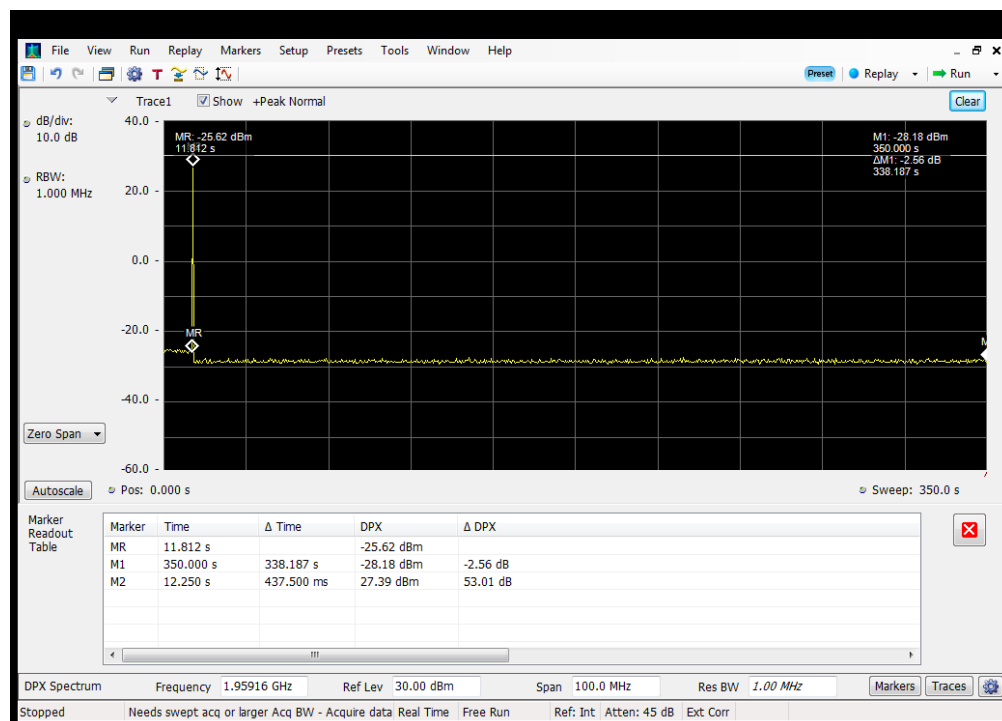




Restart Time_Downlink_1930 - 1995 MHz_B25_Server 4 to Dedicated Donor Port 1

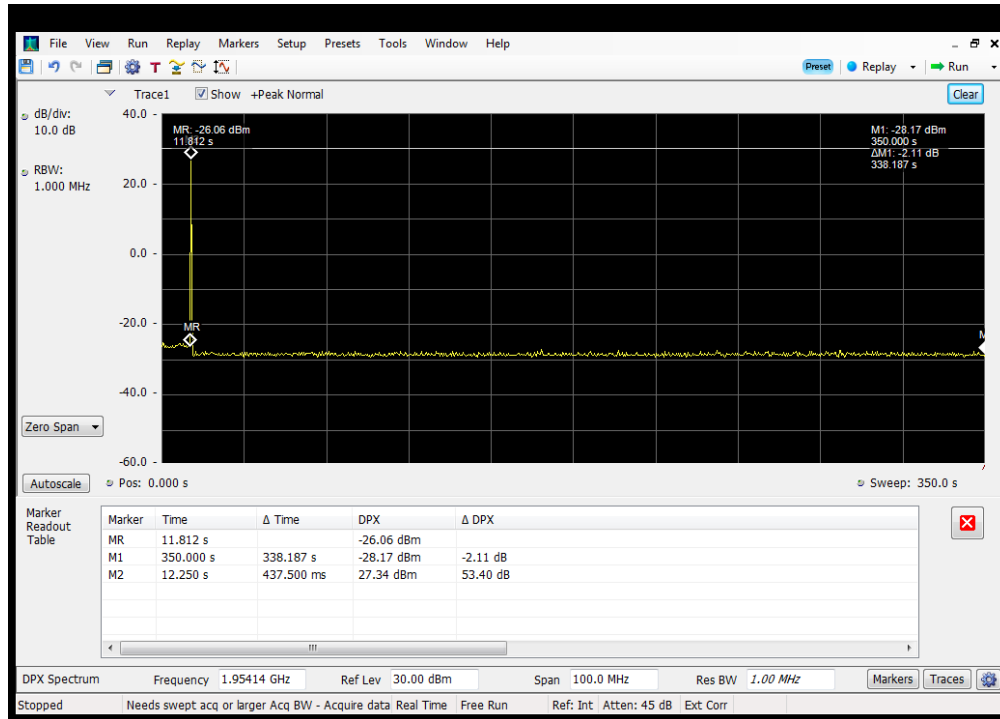


Restart Time_Downlink_1930 - 1995 MHz_B2_Server 1 to Dedicated Donor Port 1

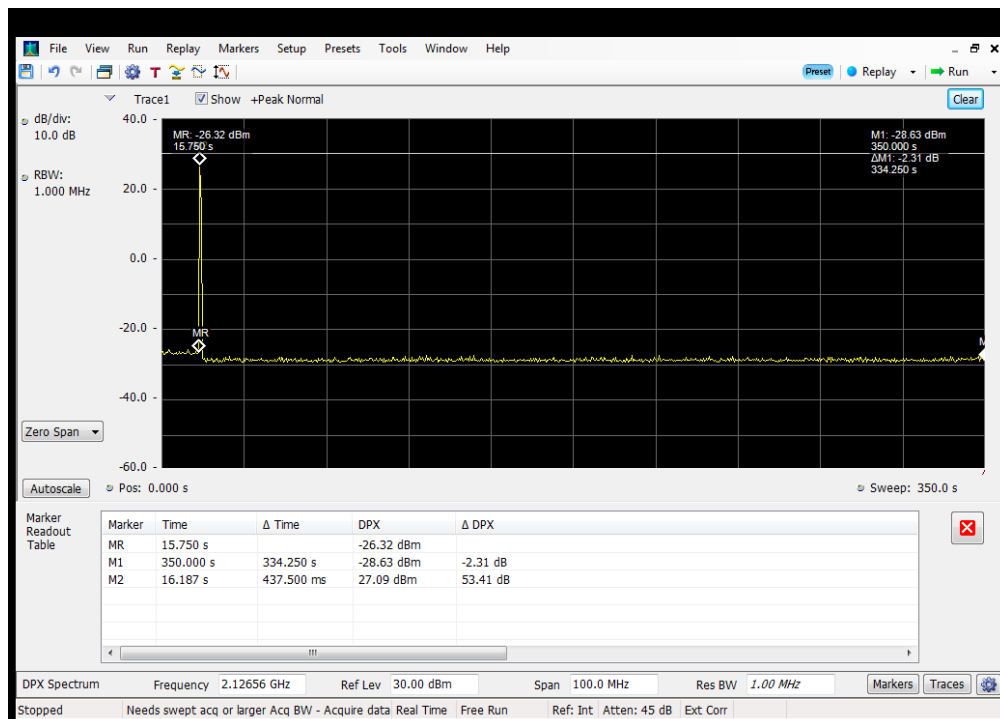




Restart Time_Downlink_1930 - 1995 MHz_B2_Server 2 to Dedicated Donor Port 1

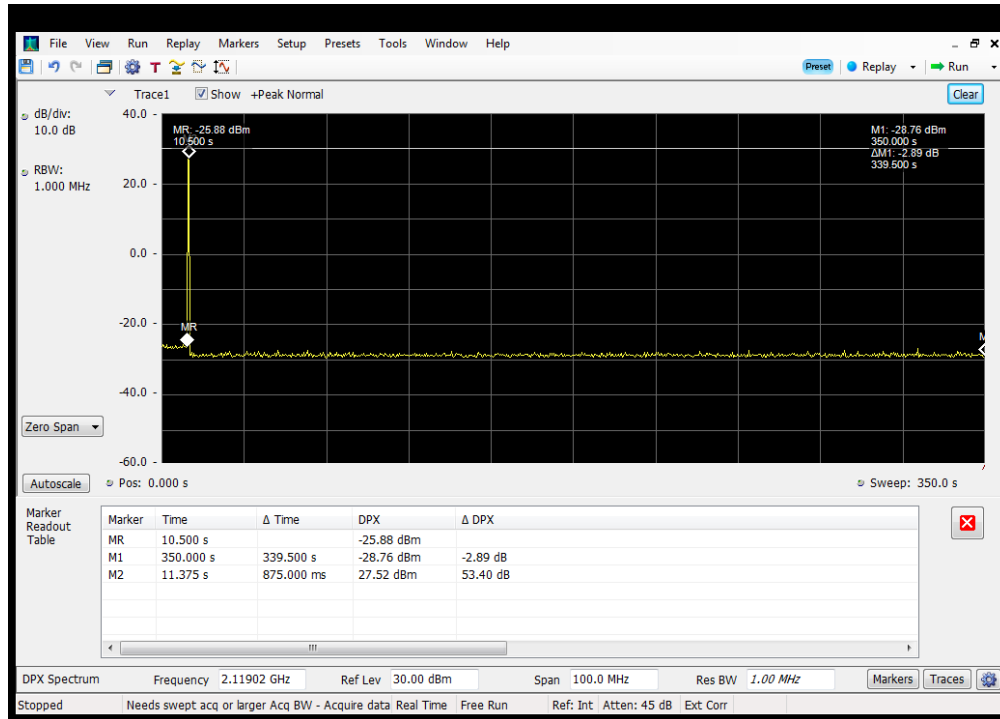


Restart Time_Downlink_2110 - 2155 MHz_B4_Server 1 to Dedicated Donor Port 1

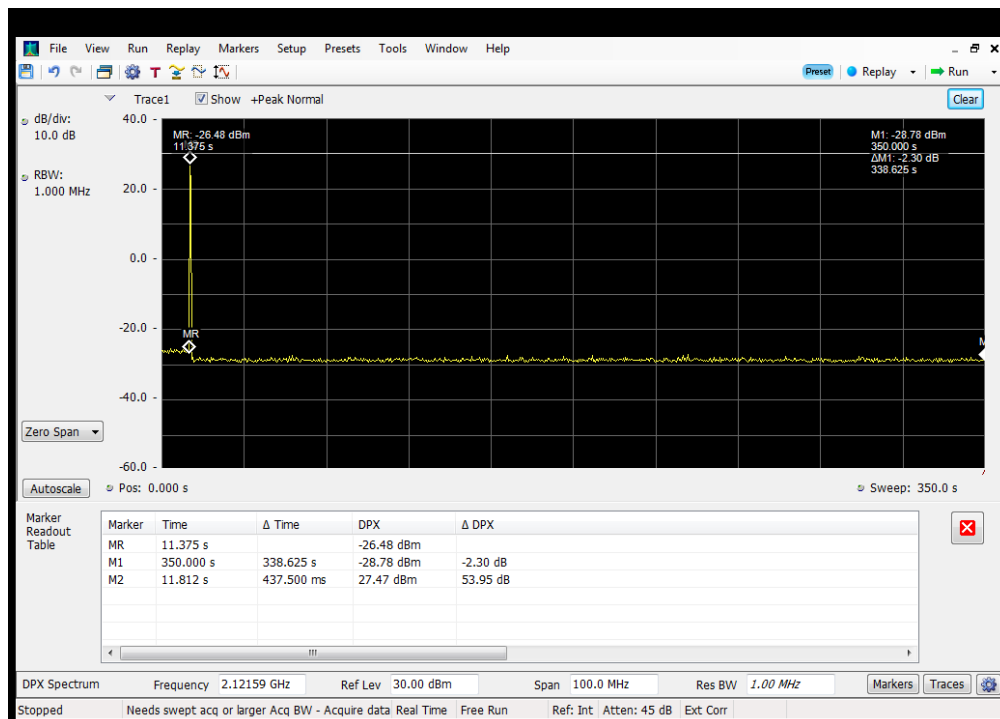




Restart Time_Downlink_2110 - 2155 MHz_B4_Server 2 to Dedicated Donor Port 1

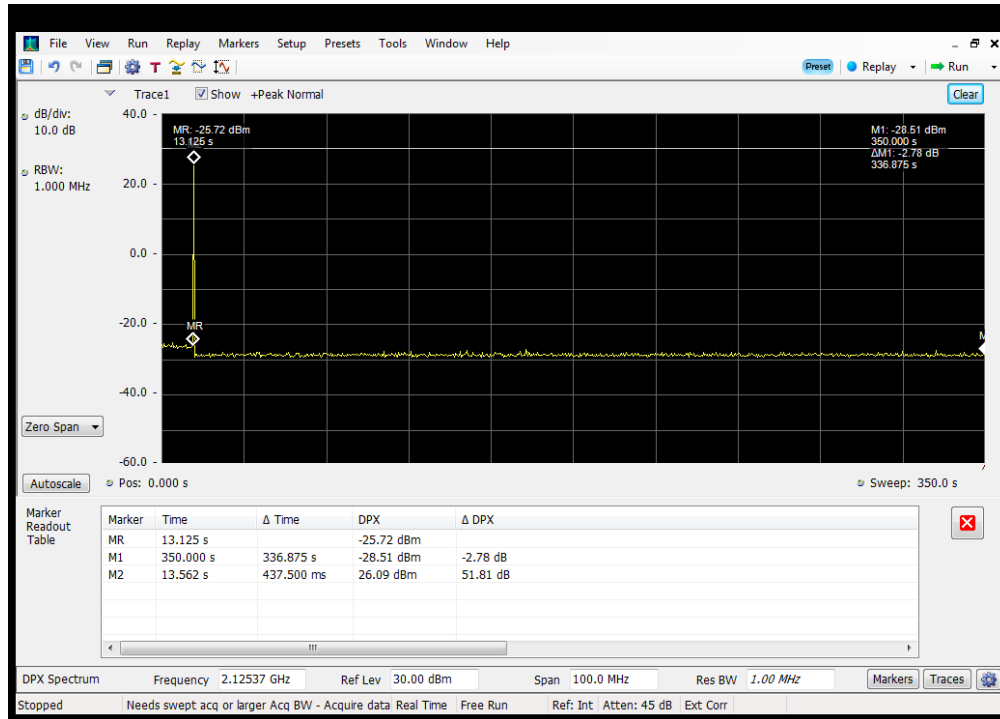


Restart Time_Downlink_2110 - 2155 MHz_B4_Server 3 to Dedicated Donor Port 1

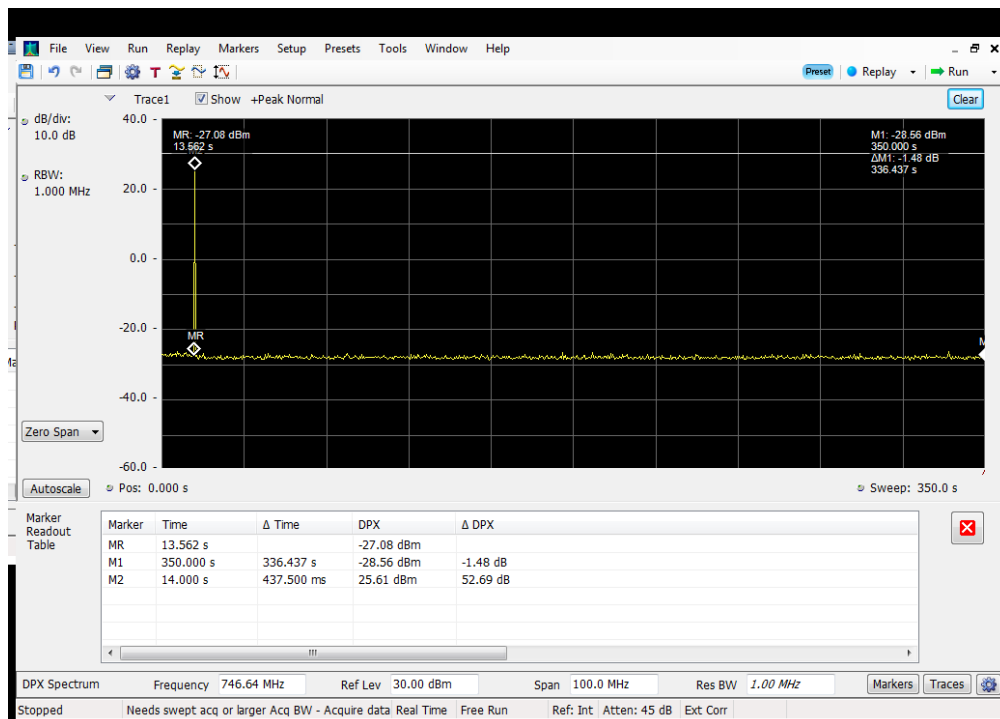




Restart Time_Downlink_2110 - 2155 MHz_B4_Server 4 to Dedicated Donor Port 1

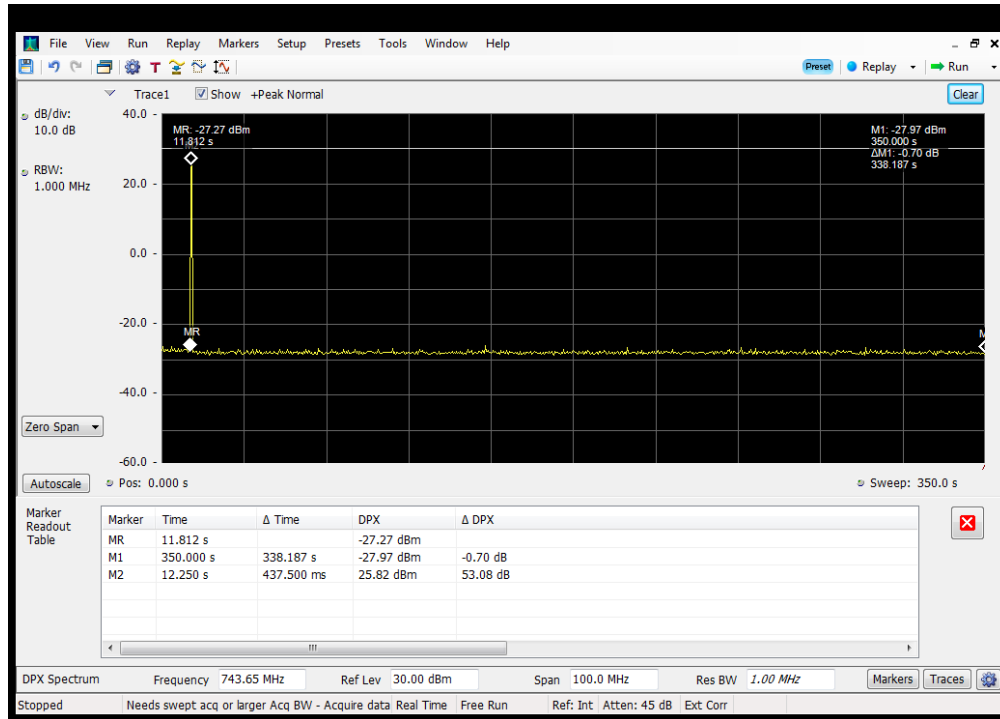


Restart Time_Downlink_728 - 746 MHz_B12_Server 1 to Common Donor Port 1

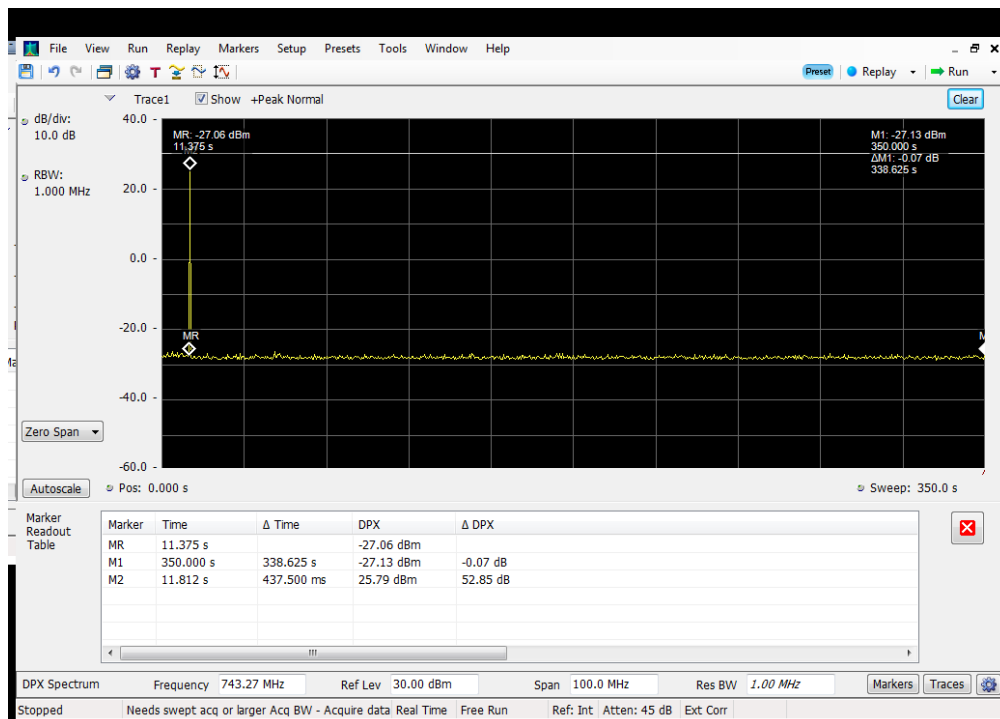




Restart Time_Downlink_728 - 746 MHz_B12_Server 1 to Dedicated Donor Port 3

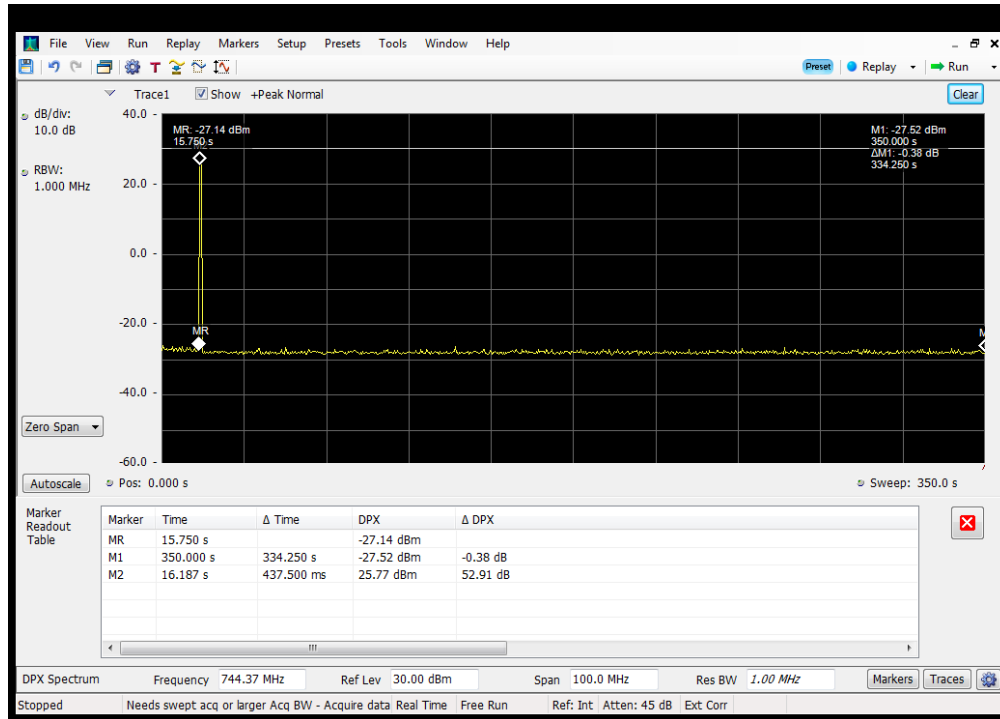


Restart Time_Downlink_728 - 746 MHz_B12_Server 2 to Common Donor Port 1

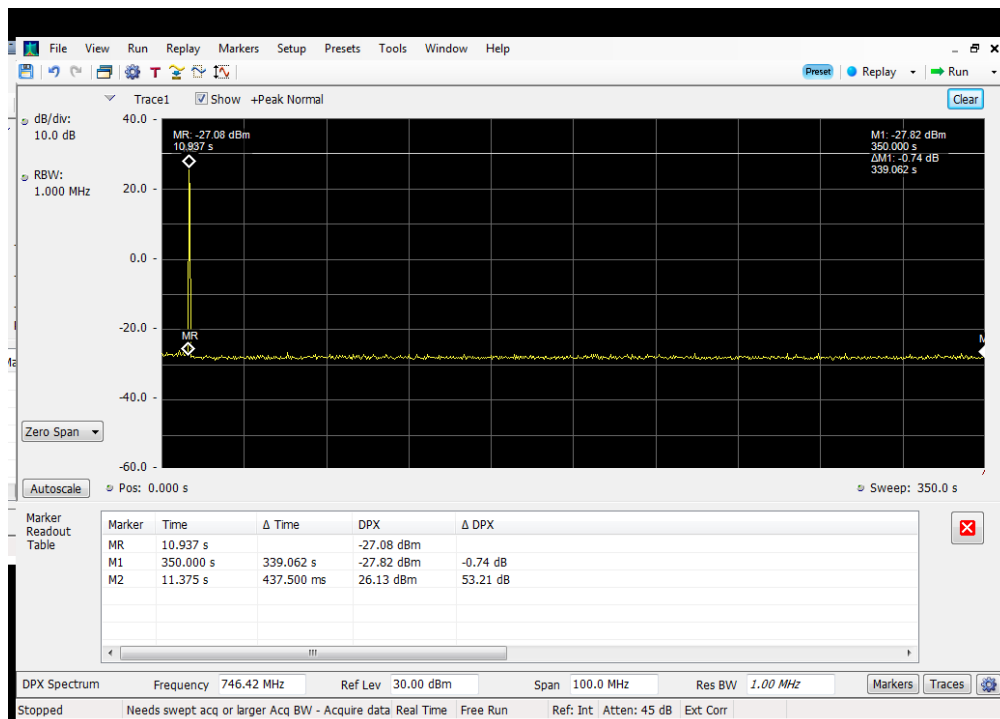




Restart Time_Downlink_728 - 746 MHz_B12_Server 2 to Dedicated Donor Port 3

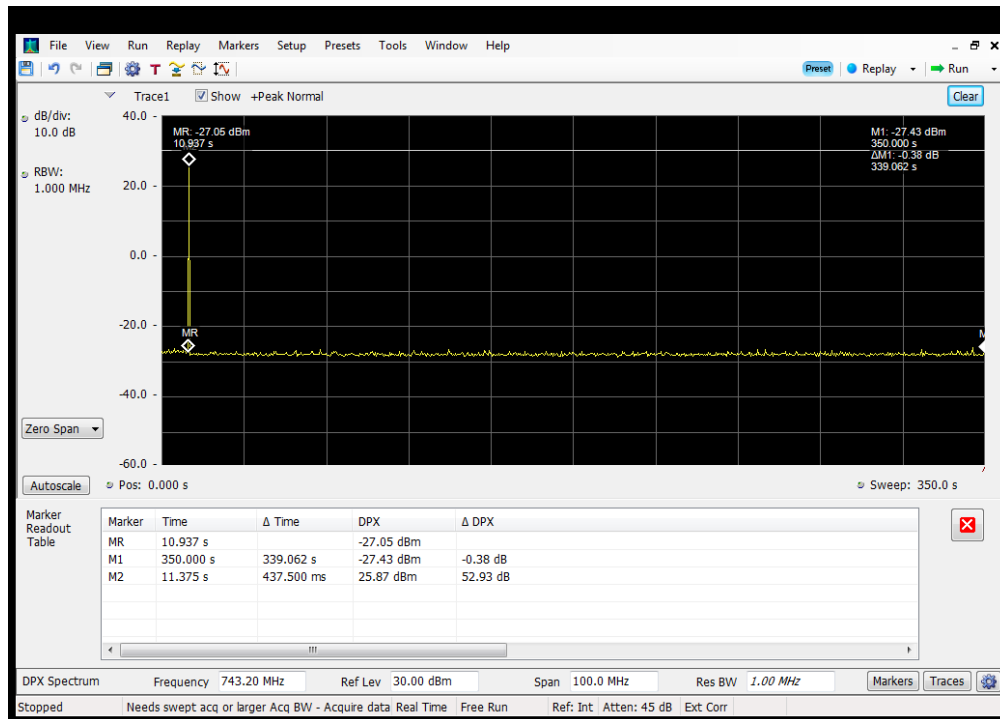


Restart Time_Downlink_728 - 746 MHz_B12_Server 3 to Common Donor Port 1

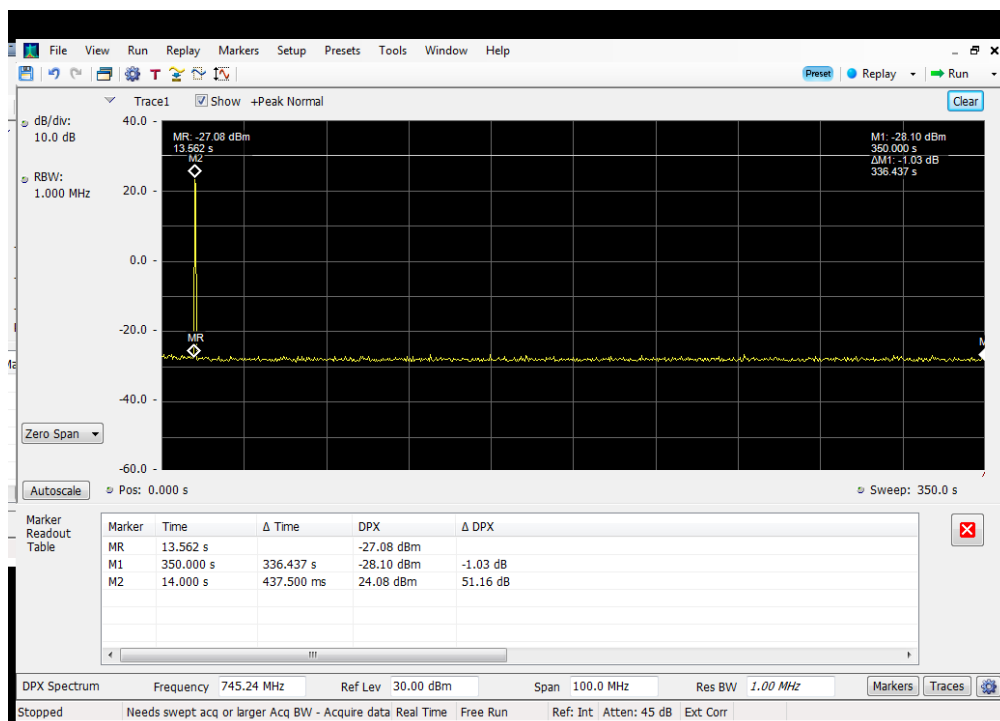




Restart Time_Downlink_728 - 746 MHz_B12_Server 3 to Dedicated Donor Port 3

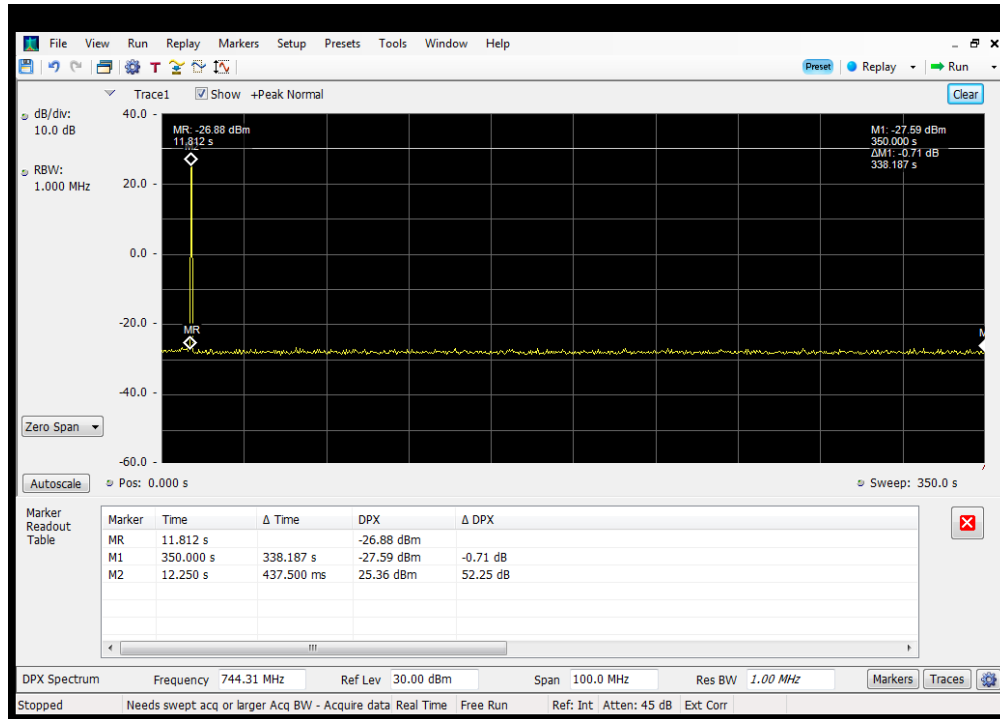


Restart Time_Downlink_728 - 746 MHz_B12_Server 4 to Common Donor Port 1

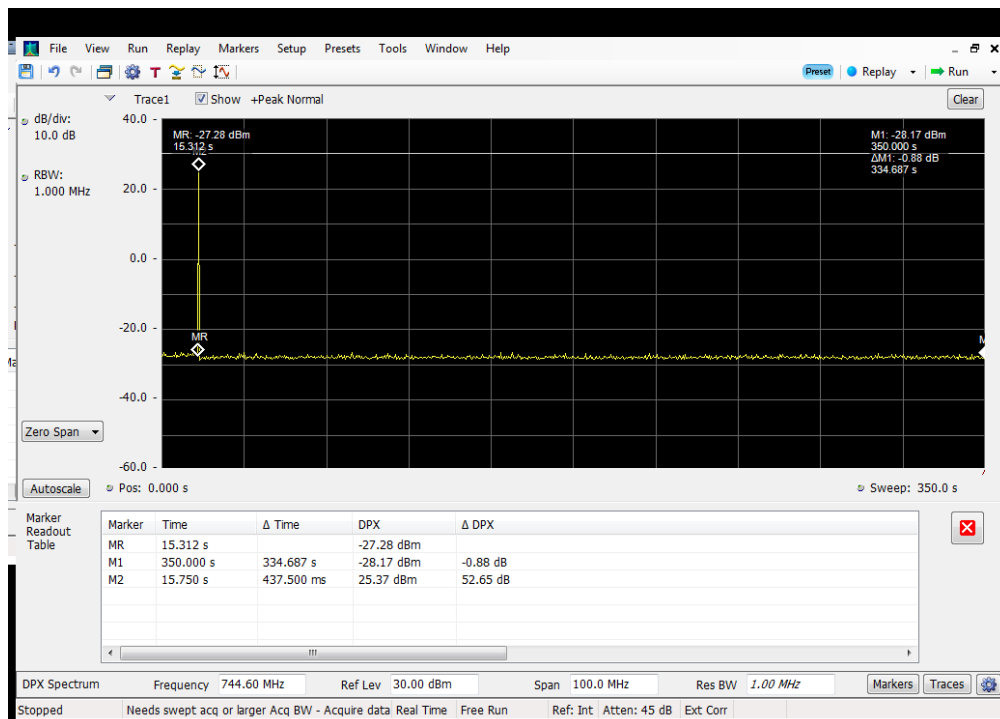




Restart Time_Downlink_728 - 746 MHz_B12_Server 4 to Dedicated Donor Port 3

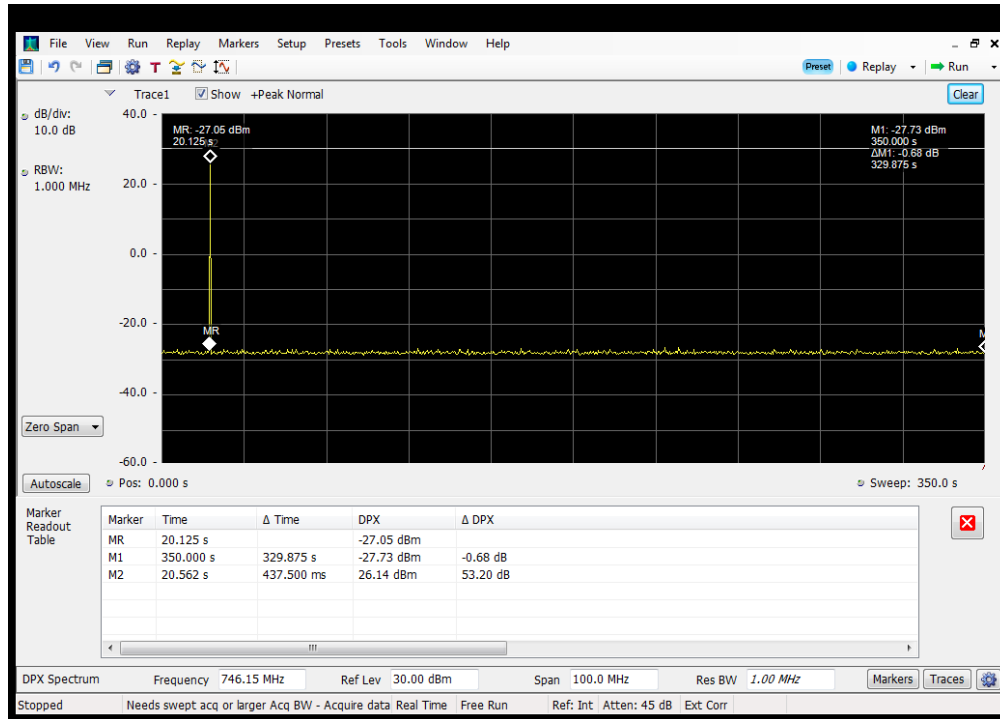


Restart Time_Downlink_746 - 757 MHz_B13_Server 1 to Common Donor Port 1

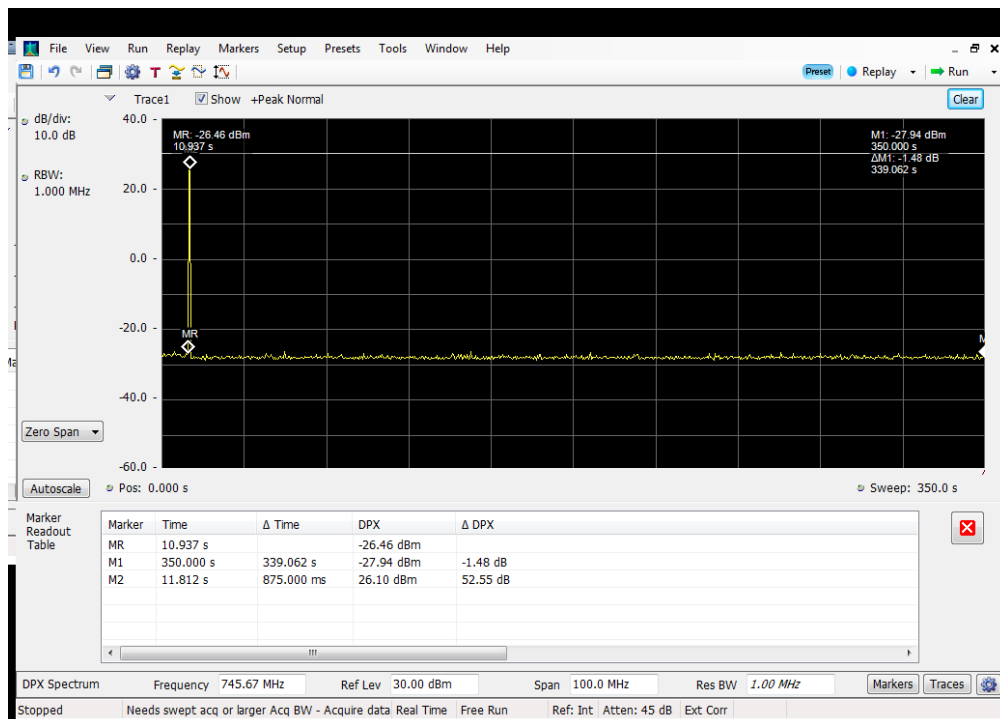




Restart Time_Downlink_746 - 757 MHz_B13_Server 1 to Dedicated Donor Port 3

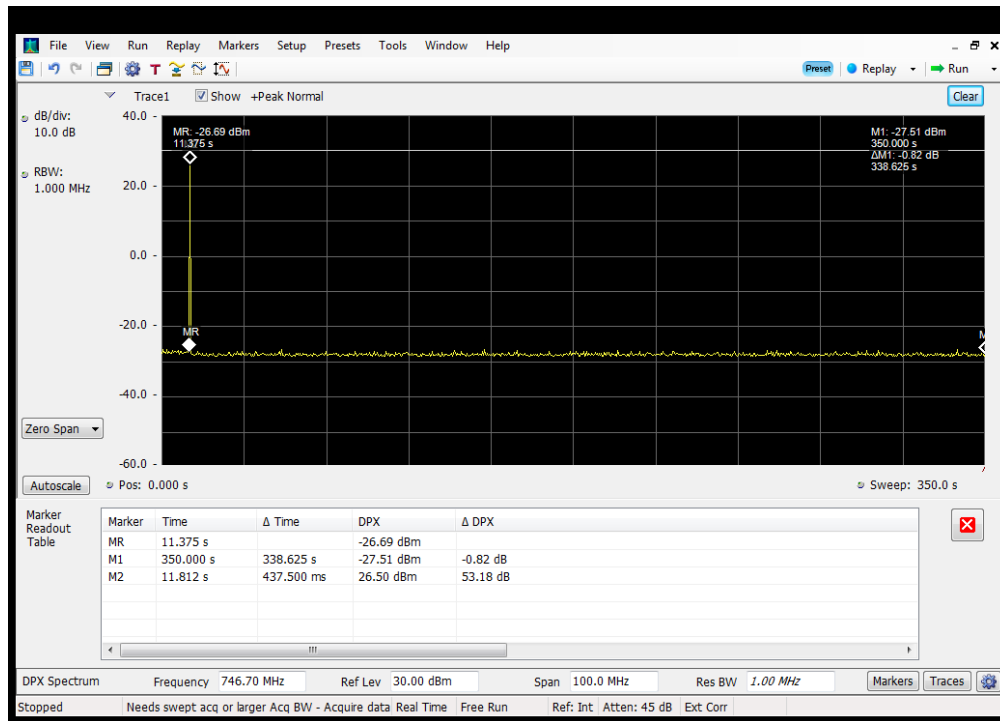


Restart Time_Downlink_746 - 757 MHz_B13_Server 2 to Common Donor Port 1

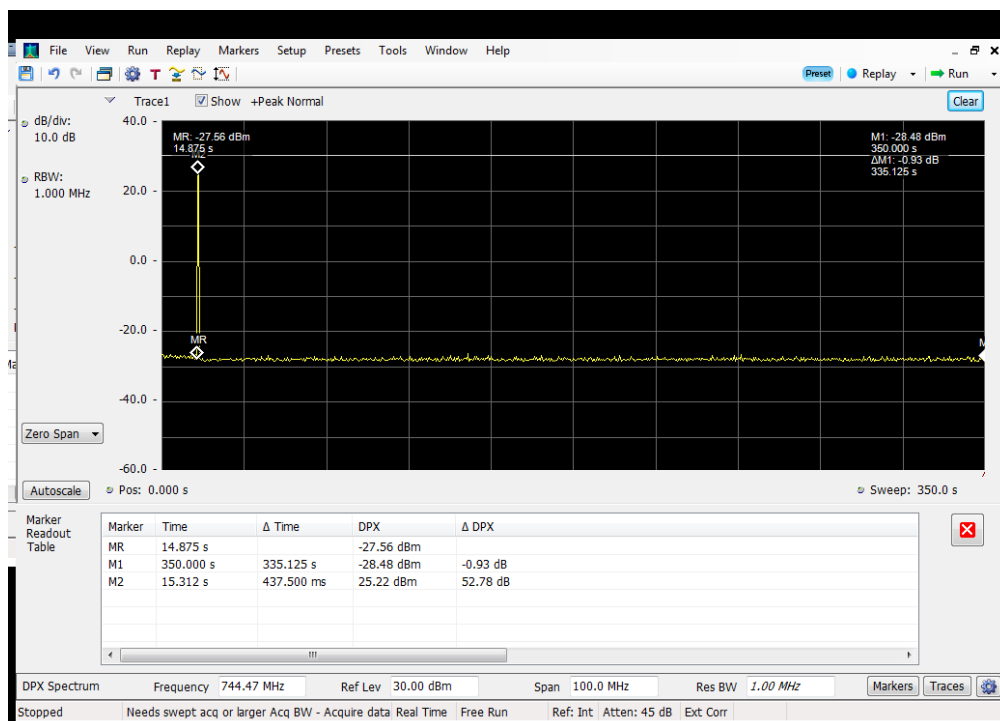




Restart Time_Downlink_746 - 757 MHz_B13_Server 2 to Dedicated Donor Port 3

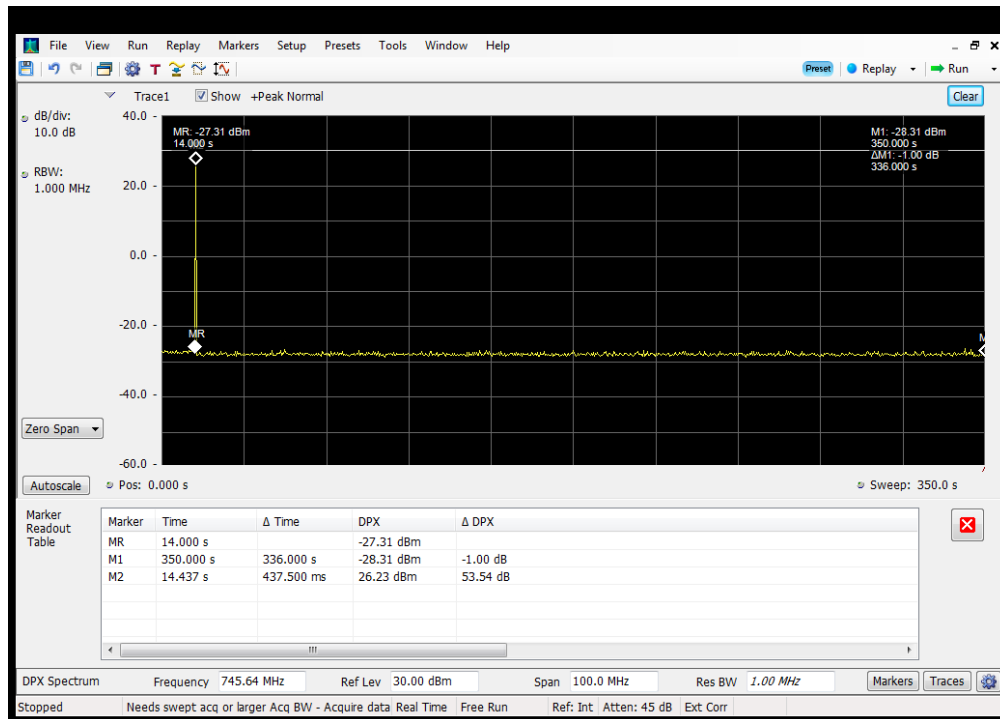


Restart Time_Downlink_746 - 757 MHz_B13_Server 3 to Common Donor Port 1

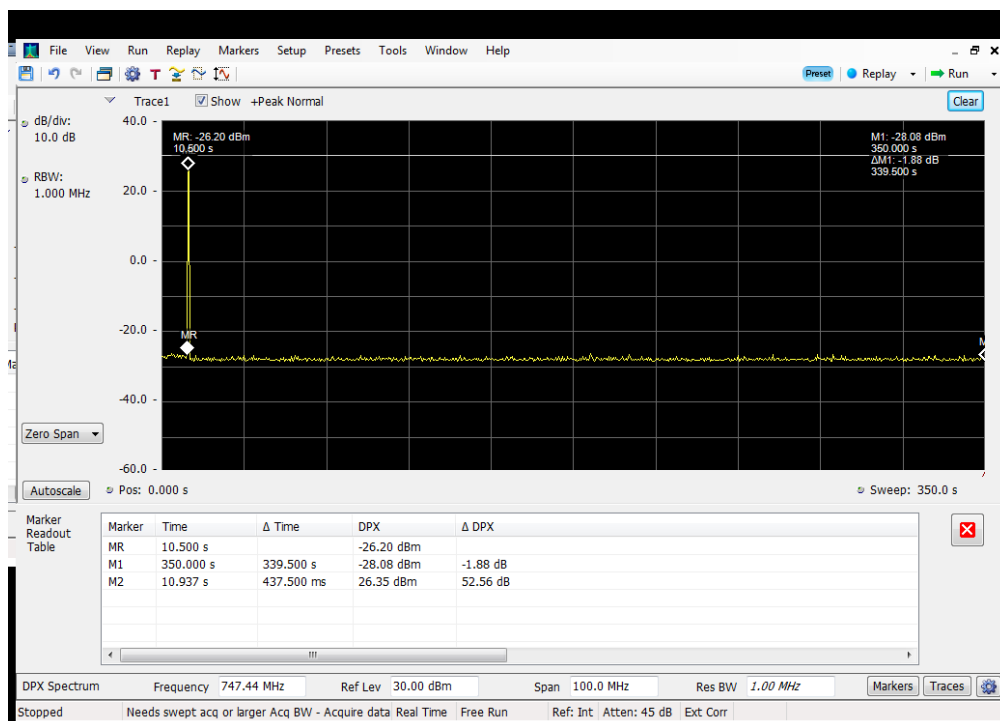




Restart Time_Downlink_746 - 757 MHz_B13_Server 3 to Dedicated Donor Port 3

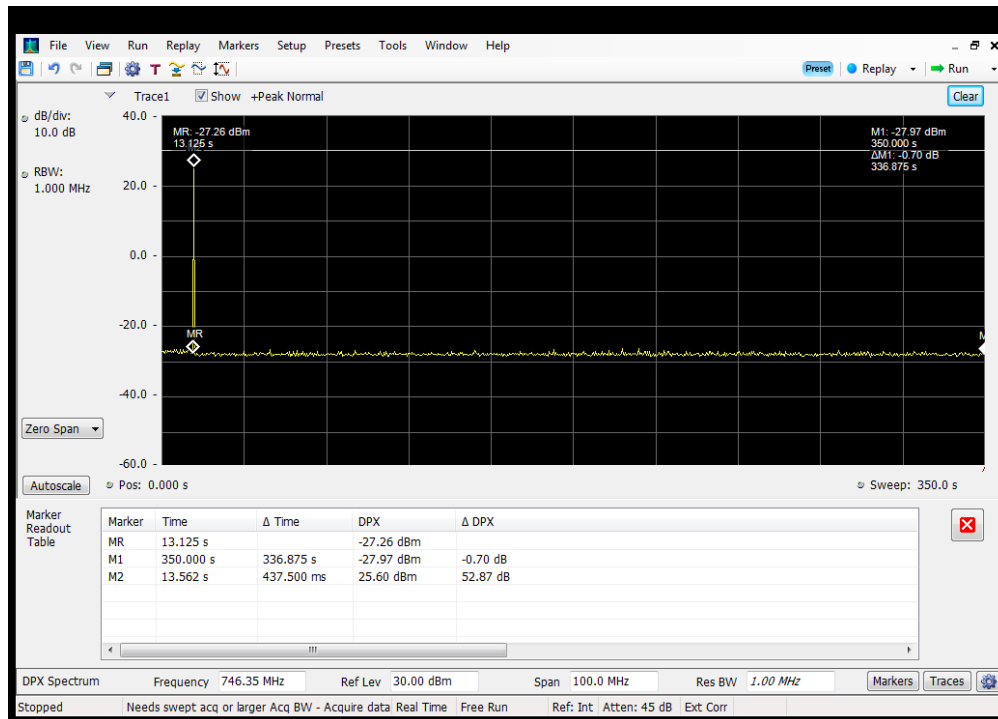


Restart Time_Downlink_746 - 757 MHz_B13_Server 4 to Common Donor Port 1

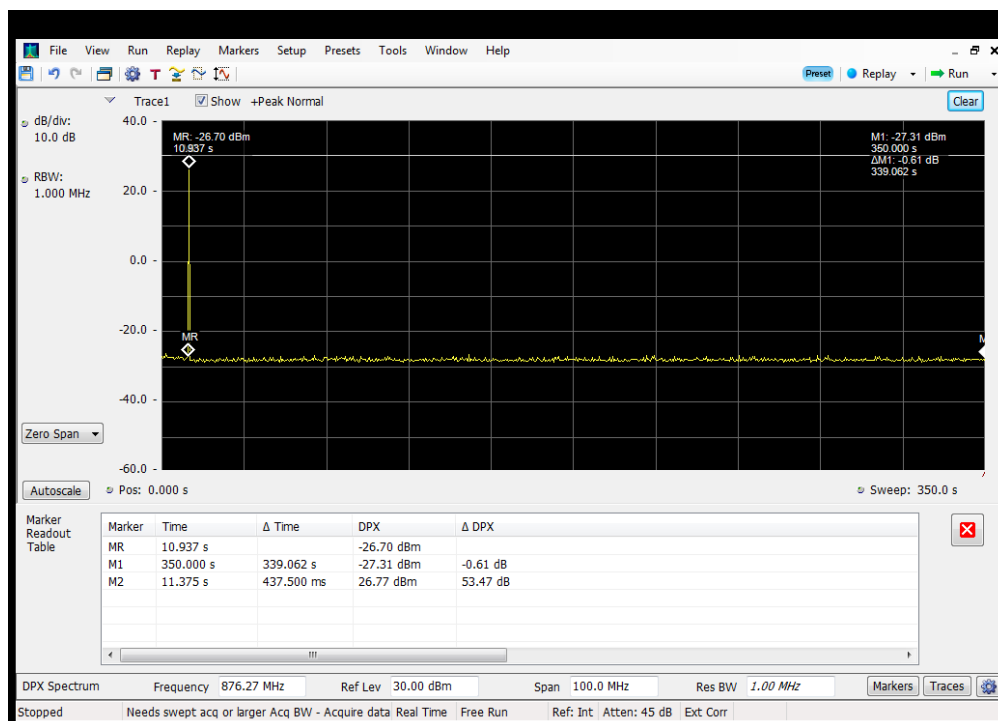




Restart Time_Downlink_746 - 757 MHz_B13_Server 4 to Dedicated Donor Port 3

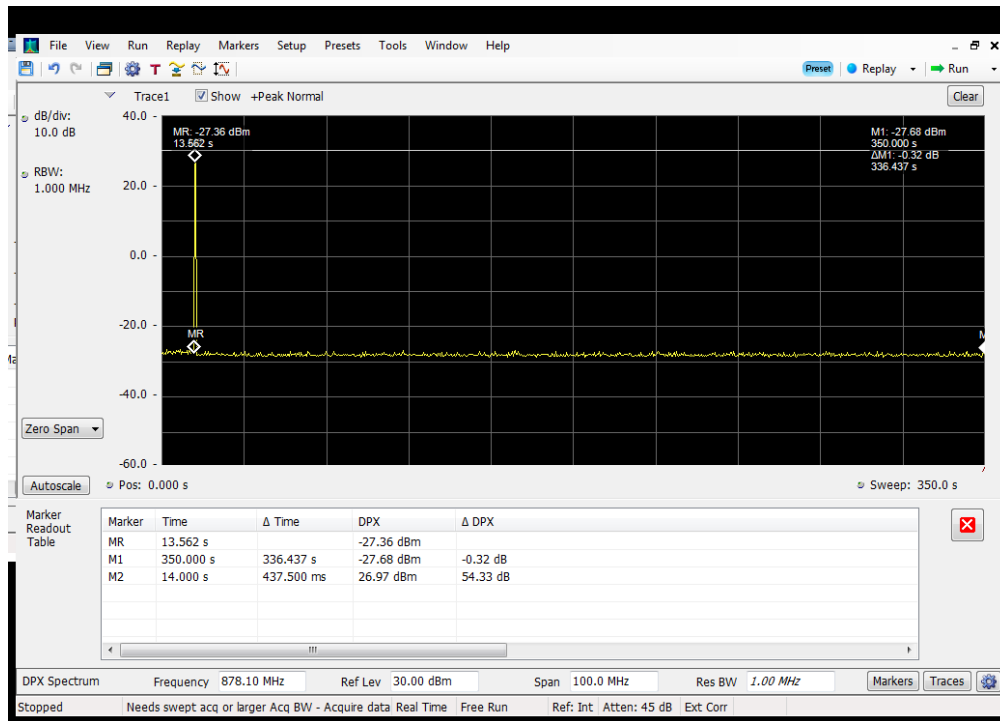


Restart Time_Downlink_869 - 894 MHz_B4_Server 2 to Dedicated Donor Port 3

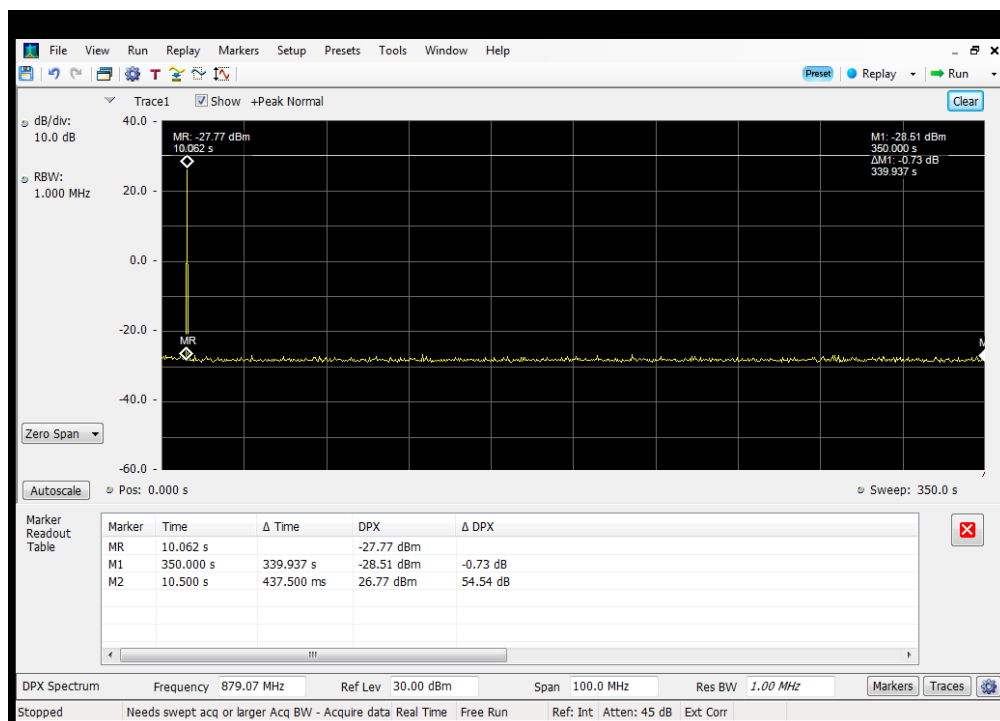


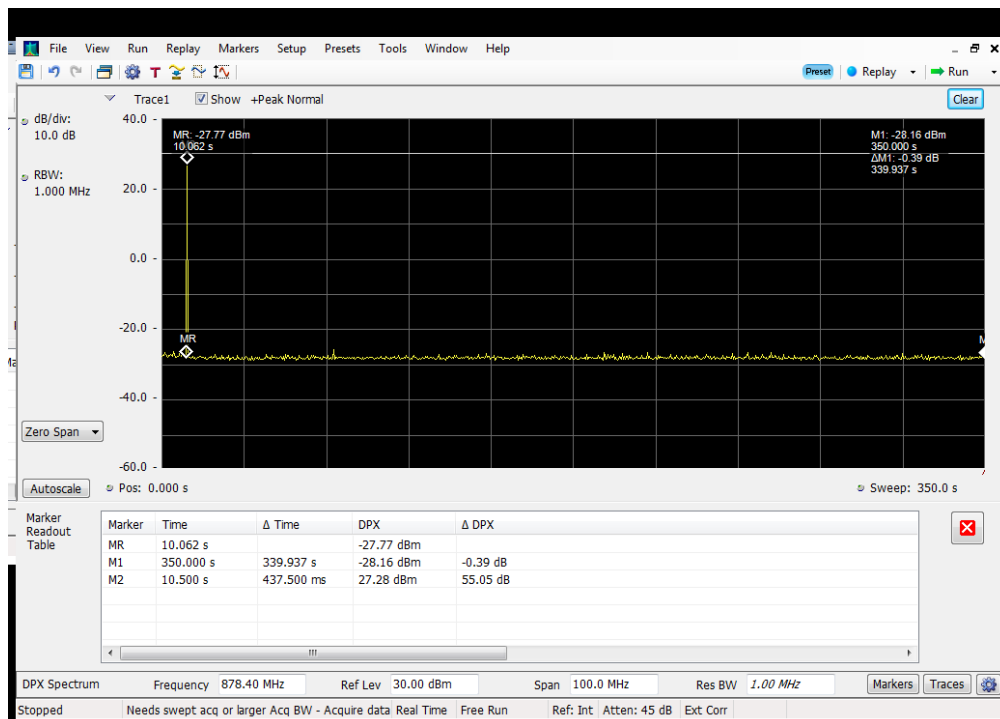
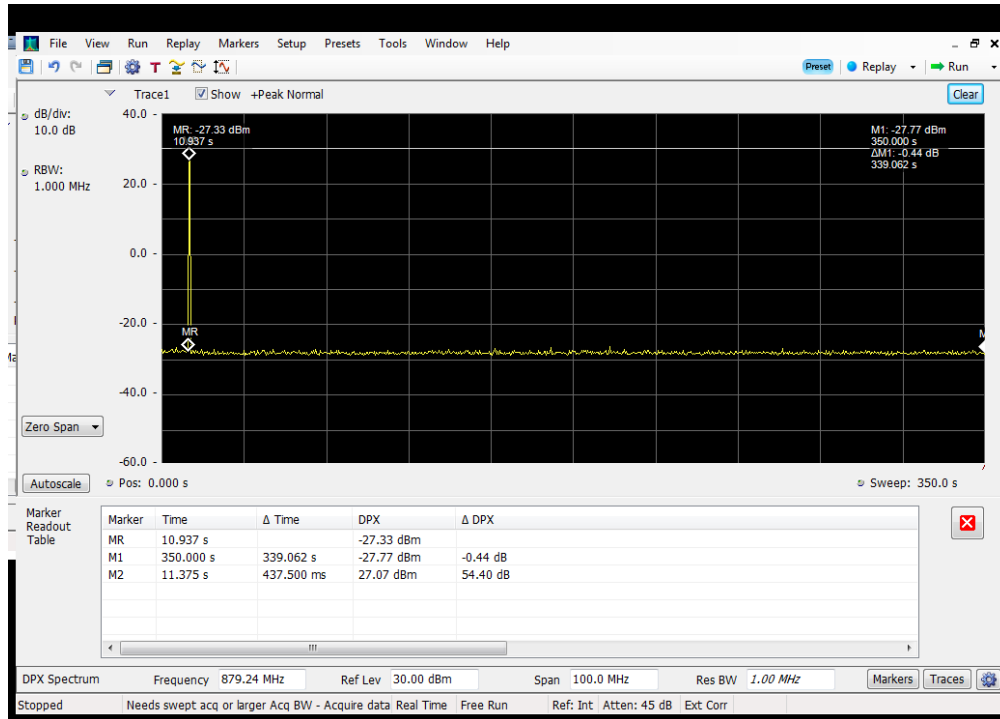


Restart Time_Downlink_869 - 894 MHz_B5_Server 1 to Common Donor Port 1



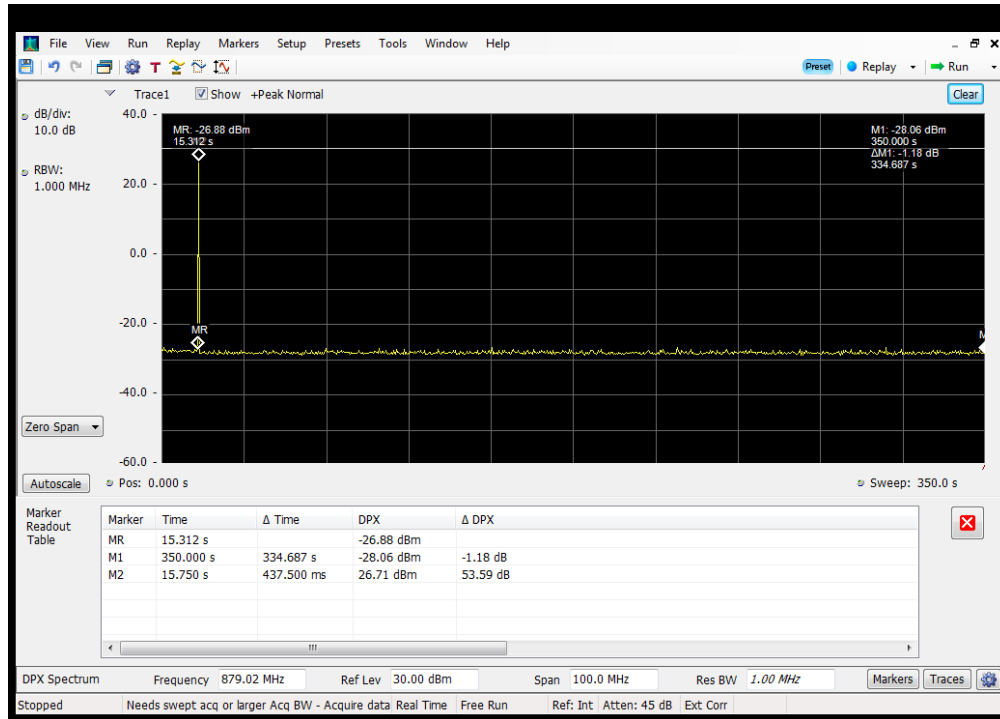
Restart Time_Downlink_869 - 894 MHz_B5_Server 1 to Dedicated Donor Port 2



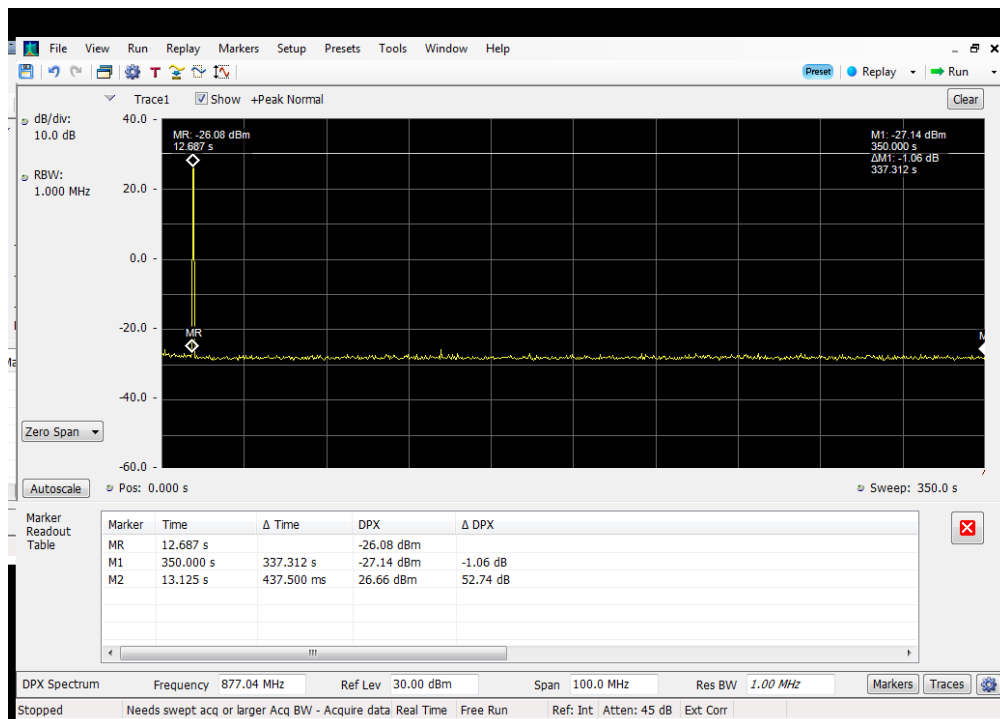




Restart Time_Downlink_869 - 894 MHz_B5_Server 3 to Dedicated Donor Port 2

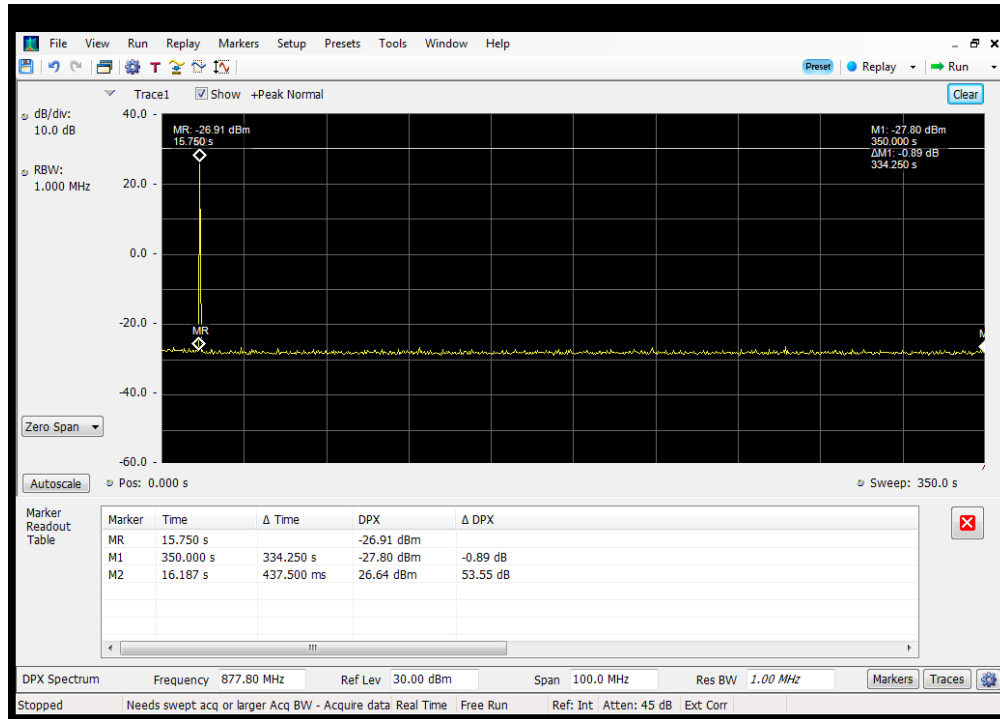


Restart Time_Downlink_869 - 894 MHz_B5_Server 4 to Common Donor Port 1

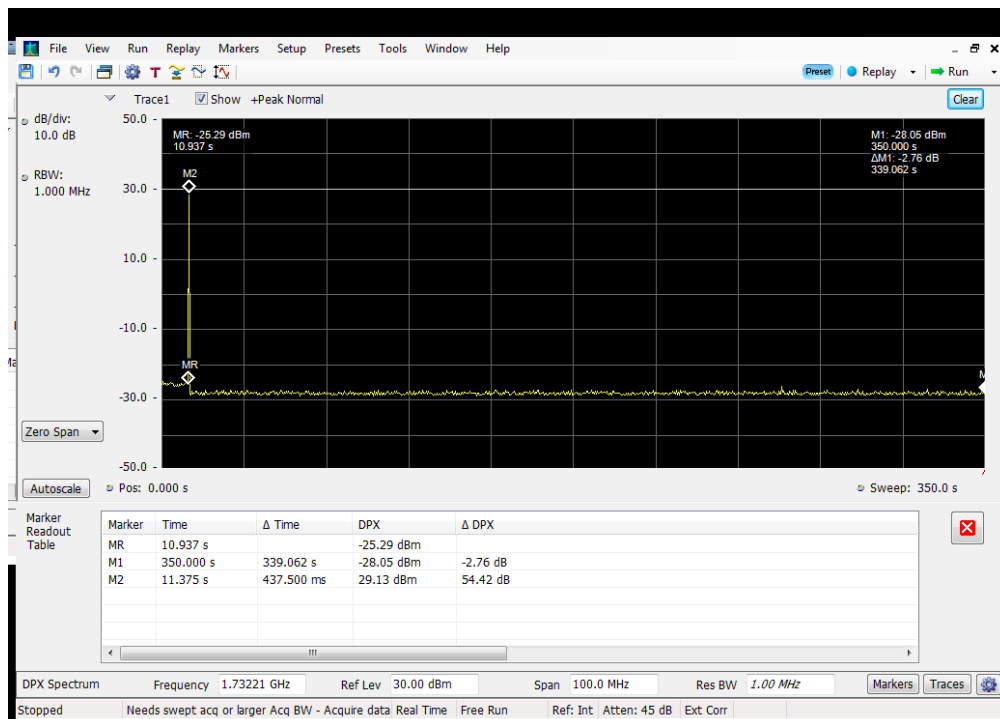




Restart Time_Downlink_869 - 894 MHz_B5_Server 4 to Dedicated Donor Port 2

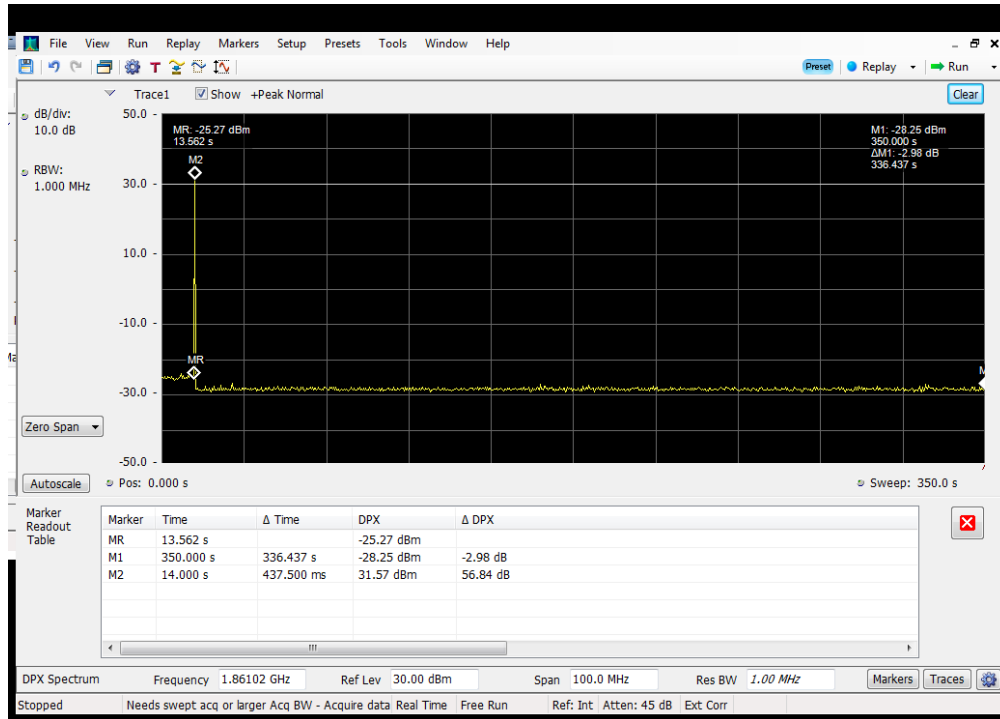


Restart Time_Uplink_1710 - 1755 MHz_B4_Server 4 to Dedicated Donor Port 1

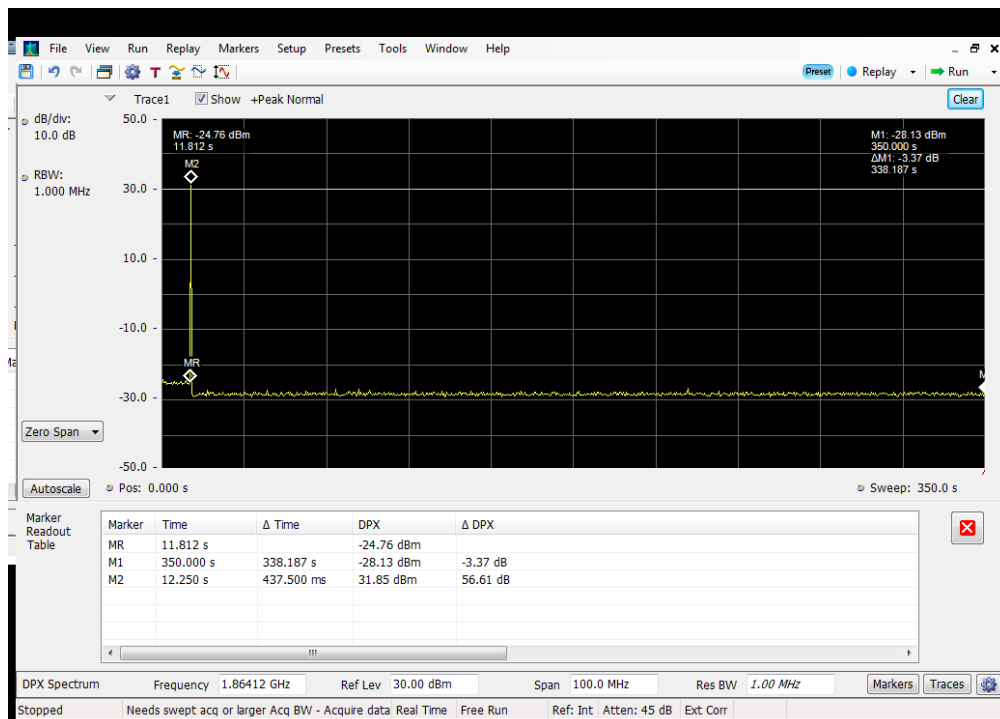




Restart Time_Uplink_1850 - 1915 MHz_B25_Server 3 to Dedicated Donor Port 1

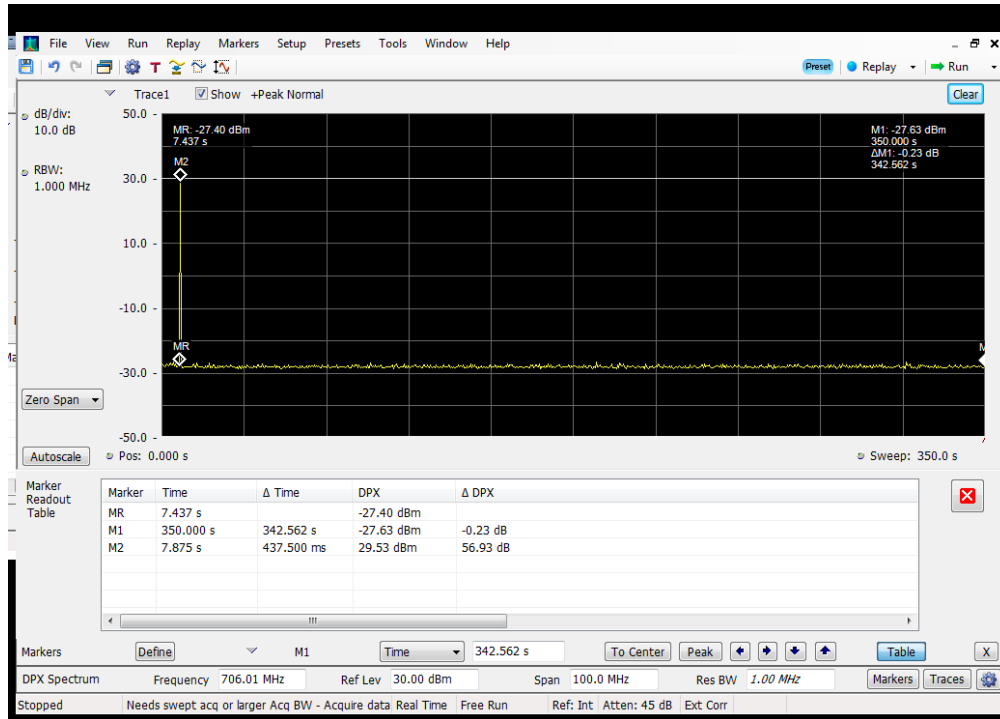


Restart Time_Uplink_1850 - 1915 MHz_B25_Server 4 to Dedicated Donor Port 1

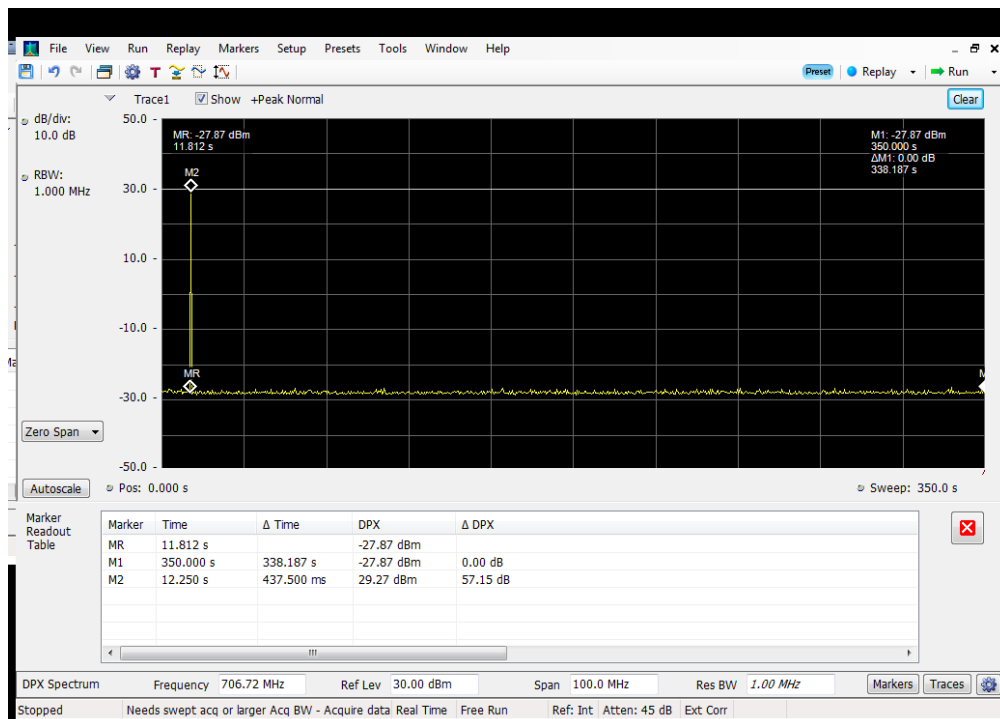




Restart Time_Uplink_698 - 716 MHz_B12_Server 1 to Common Donor Port 1

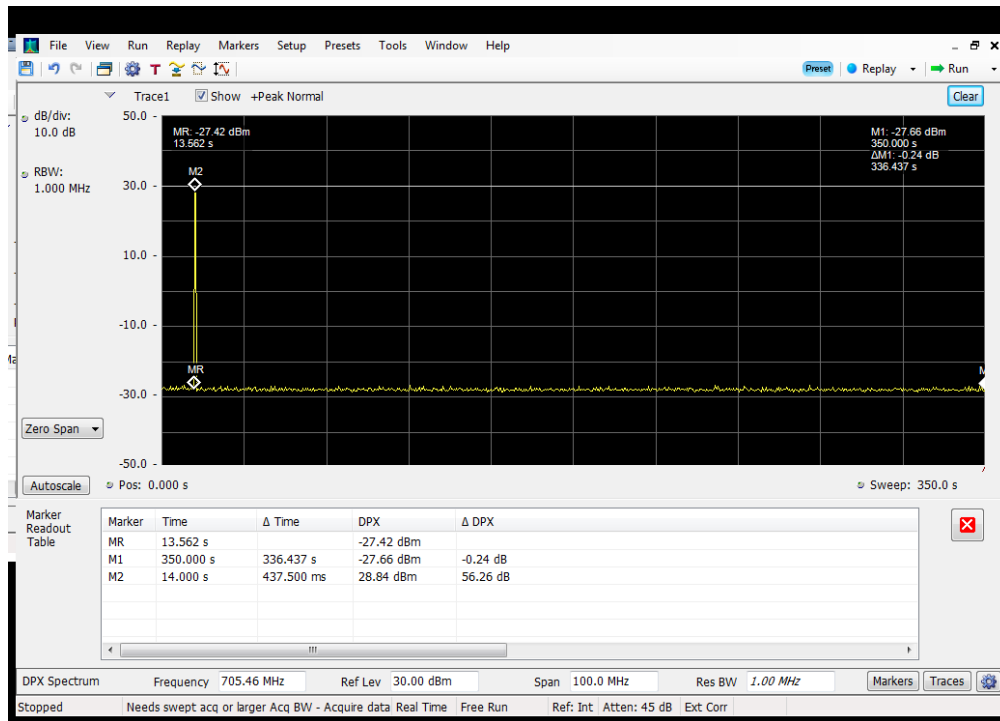


Restart Time_Uplink_698 - 716 MHz_B12_Server 3 to Common Donor Port 1





Restart Time_Uplink_698 - 716 MHz_B12_Server 4 to Common Donor Port 1

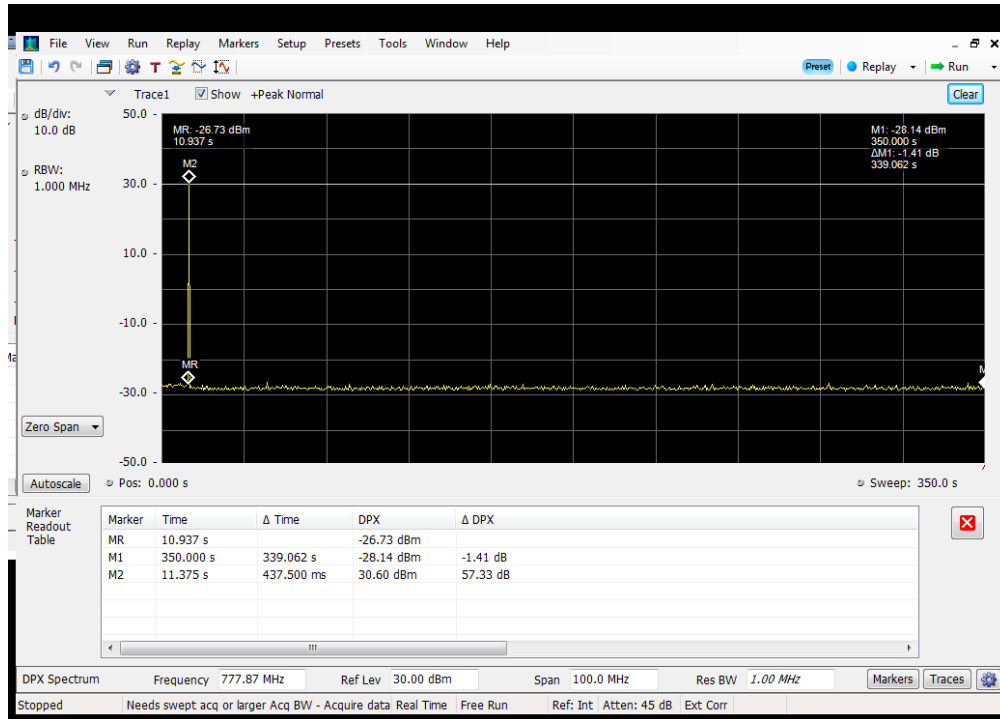


Restart Time_Uplink_698 - 716 MHz_B12_Server 4 to Dedicated Donor Port 3

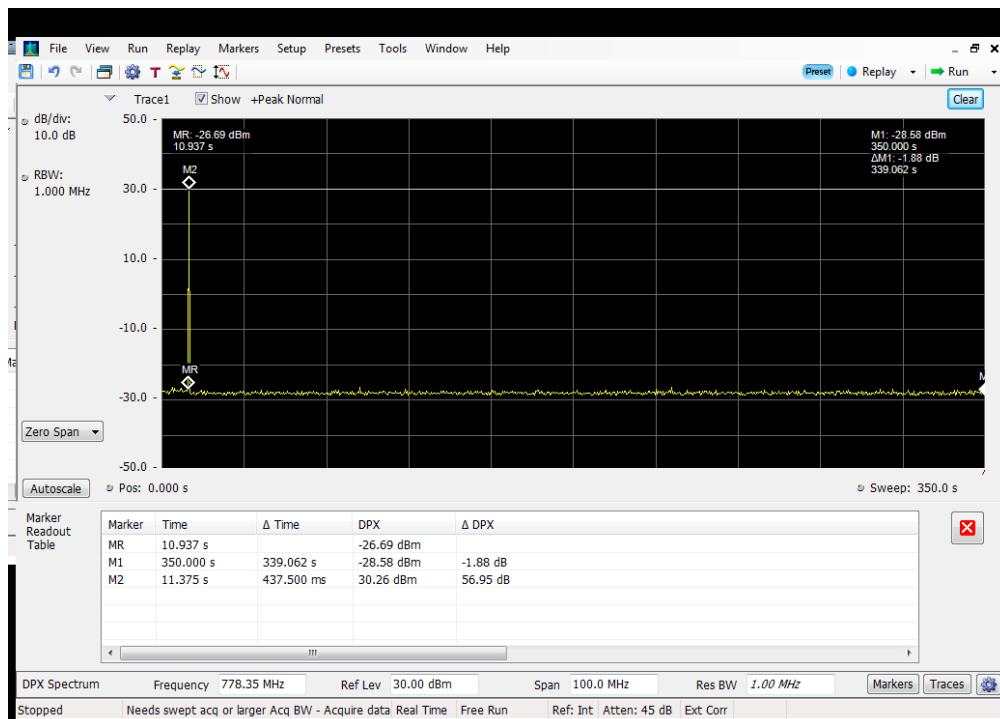




Restart Time_Uplink_776 - 787 MHz_B13_Server 1 to Common Donor Port 1

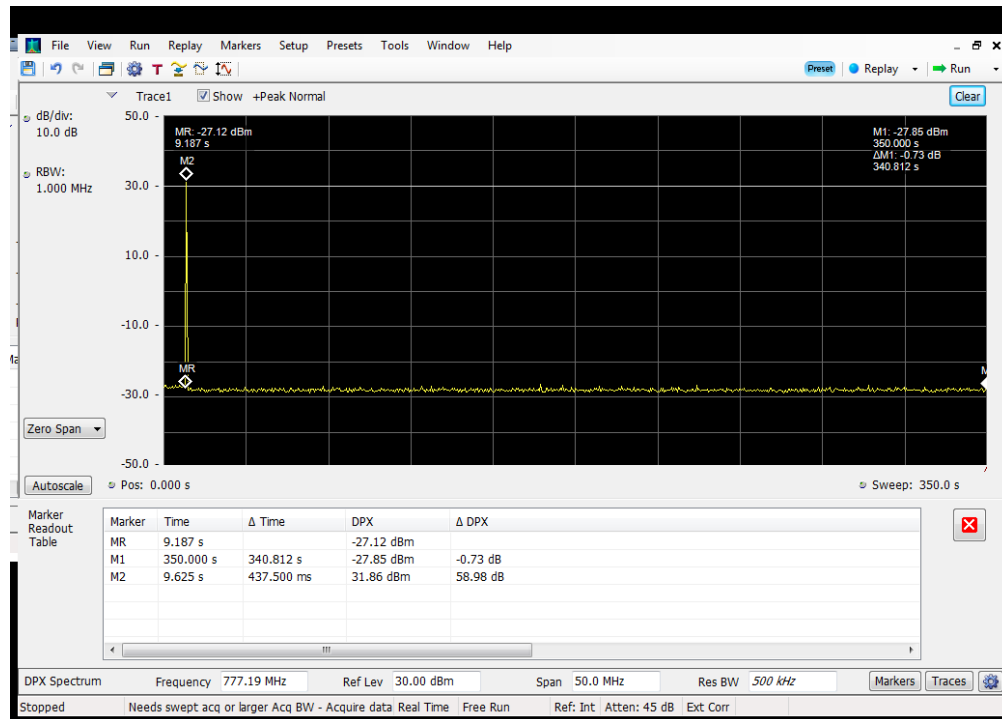


Restart Time_Uplink_776 - 787 MHz_B13_Server 3 to Common Donor Port 1

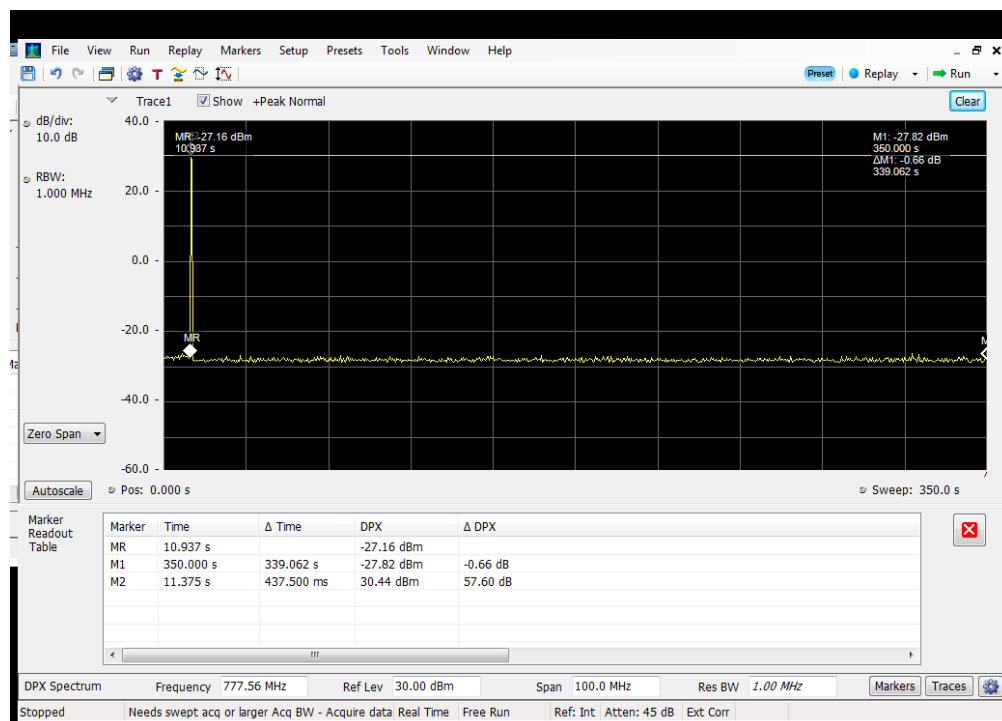




Restart Time_Uplink_776 - 787 MHz_B13_Server 3 to Dedicated Donor Port 3

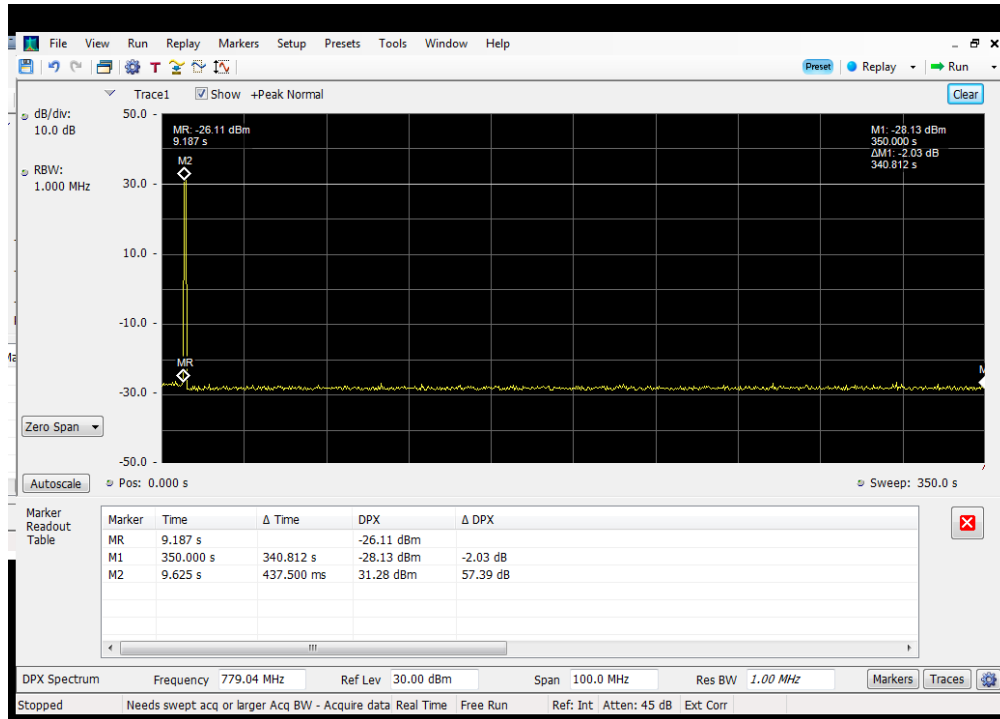


Restart Time_Uplink_776 - 787 MHz_B13_Server 4 to Common Donor Port 1

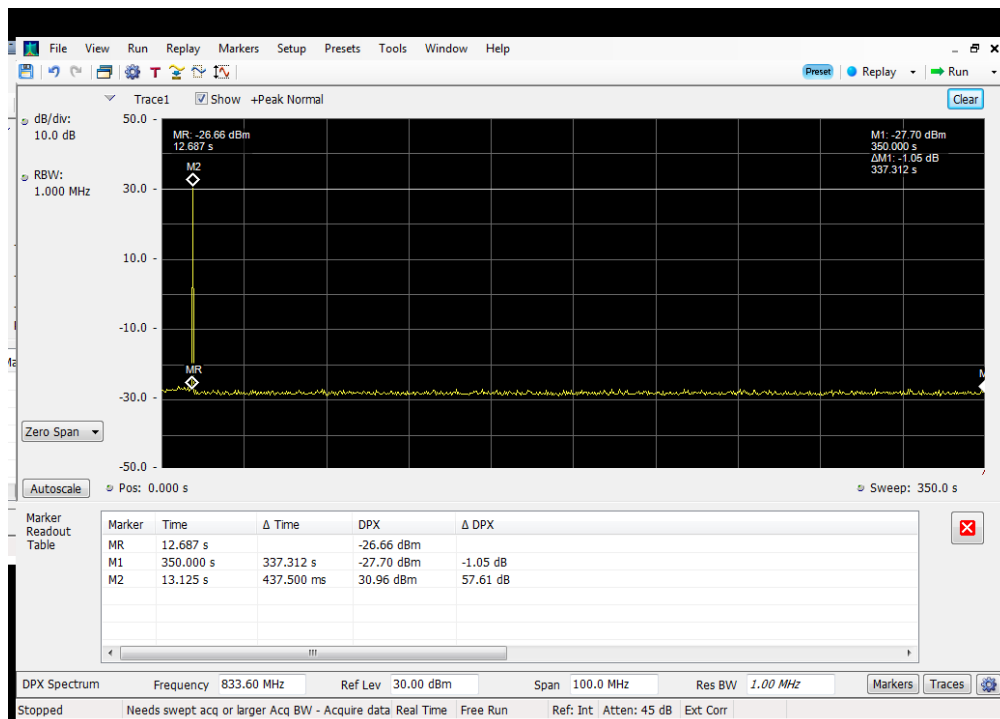




Restart Time_Uplink_776 - 787 MHz_B13_Server 4 to Dedicated Donor Port 3

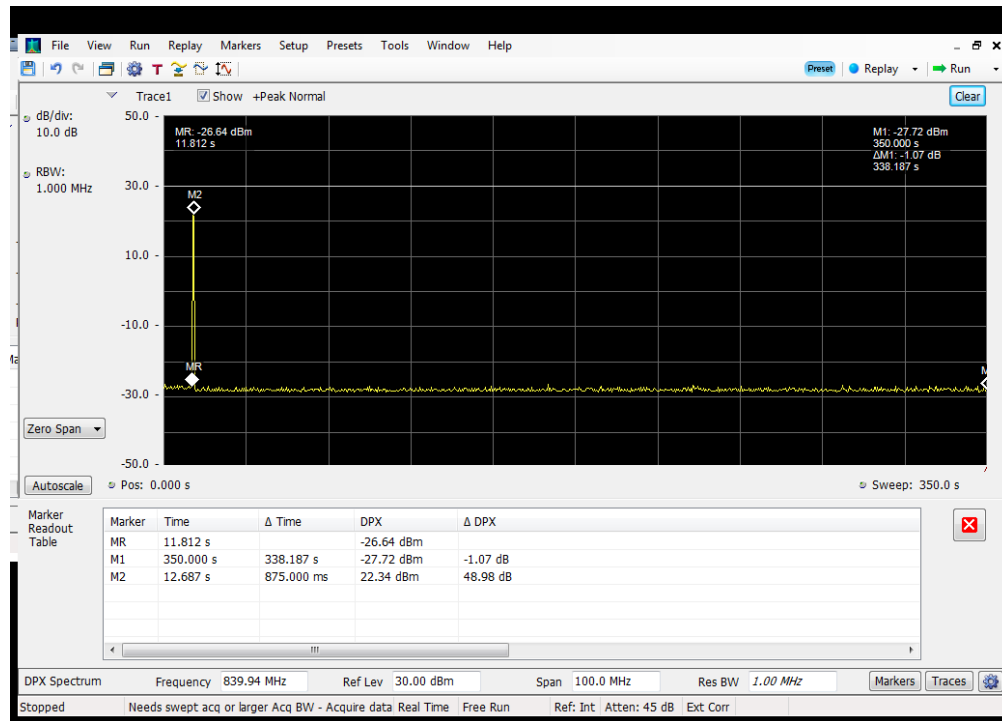


Restart Time_Uplink_824 - 849 MHz_B5_Server 1 to Common Donor Port 1

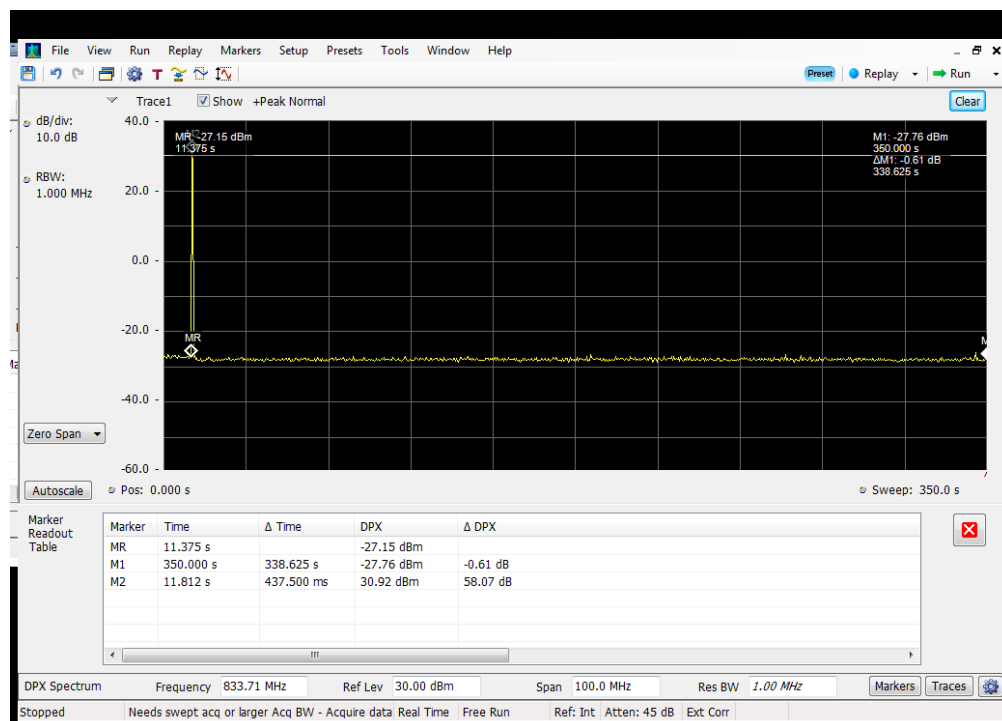




Restart Time_Uplink_824 - 849 MHz_B5_Server 3 to Common Donor Port 1

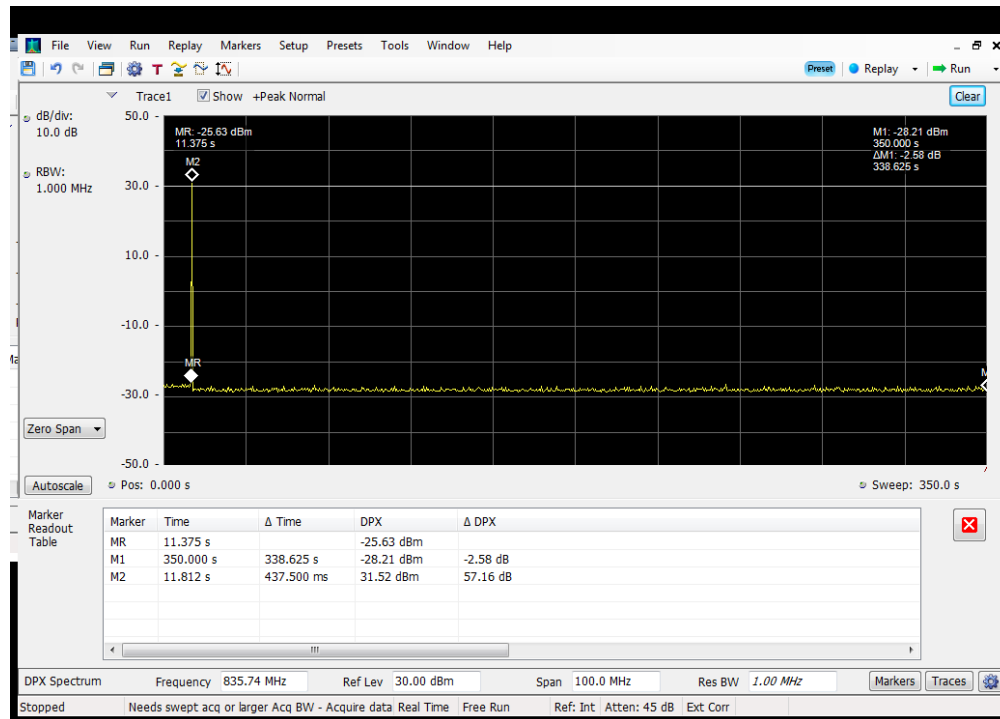


Restart Time_Uplink_824 - 849 MHz_B5_Server 4 to Common Donor Port 1

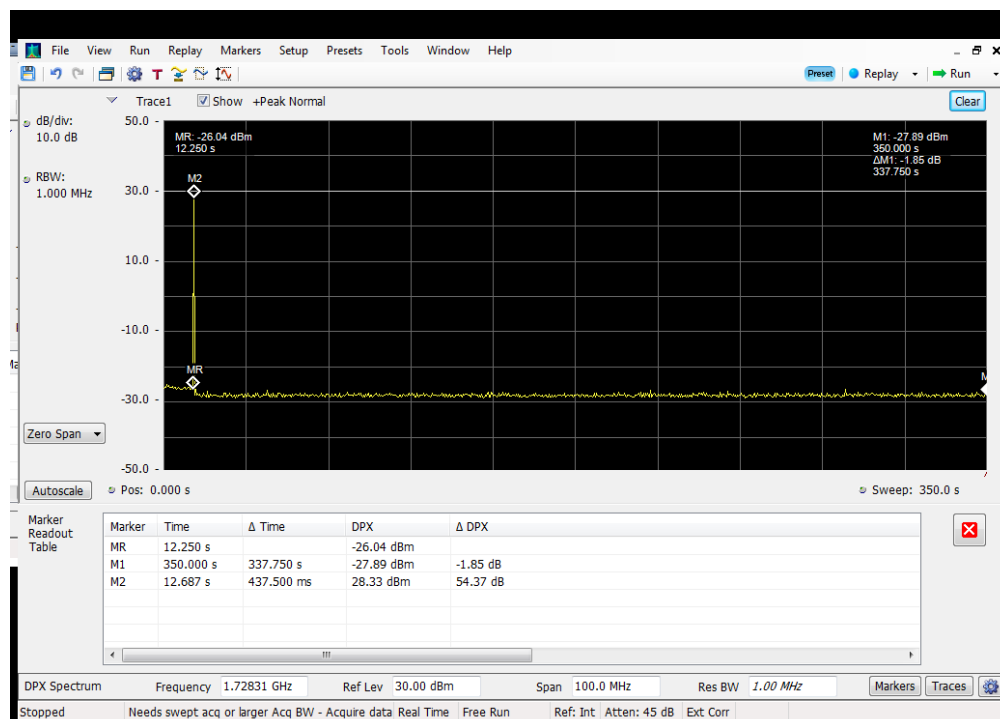




Restart Time_Uplink_824 - 849 MHz_B5_Server 4 to Dedicated Donor Port 2

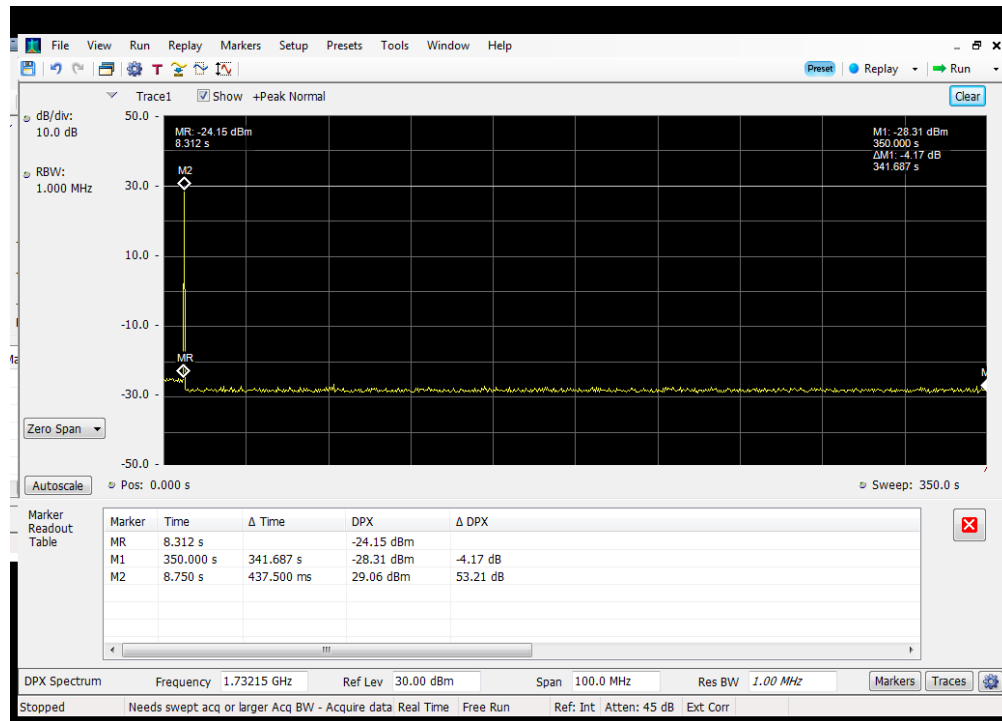


Restart Timing_Uplink_1710 - 1755 MHz_B4_Server 1 to Dedicated Donor Port 1





Restart Timing_Uplink_1710 - 1755 MHz_B4_Server 2 to Dedicated Donor Port 1

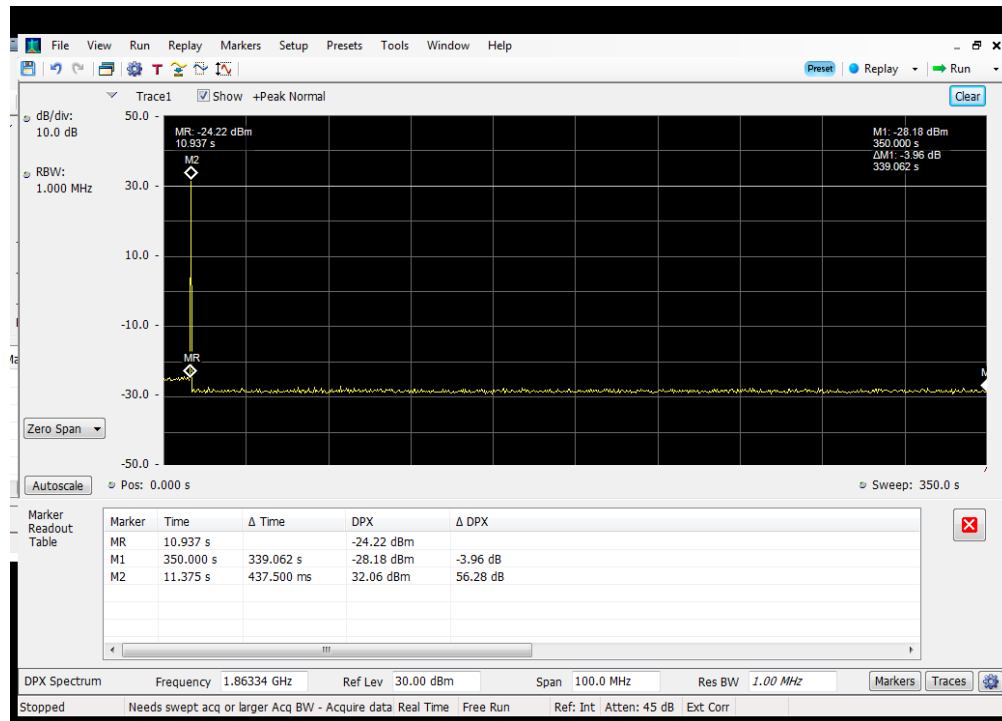


Restart Timing_Uplink_1710 - 1755 MHz_B4_Server 3 to Dedicated Donor Port 1

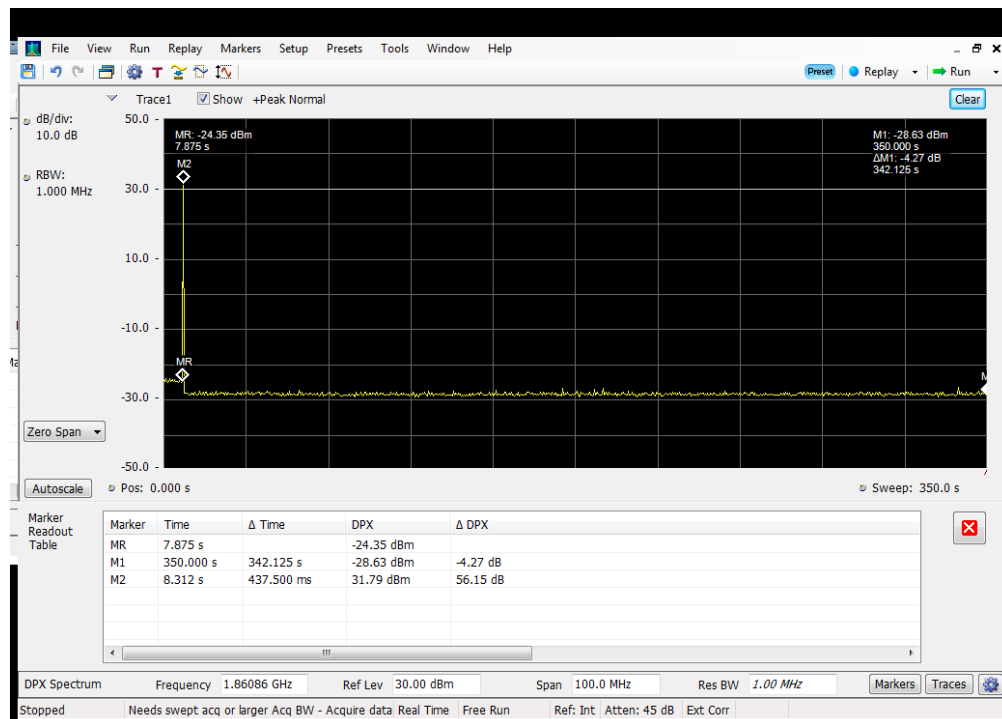




Restart Timing_Uplink_1850 - 1915 MHz_B25_Server 1 to Dedicated Donor Port 1



Restart Timing_Uplink_1850 - 1915 MHz_B25_Server 2 to Dedicated Donor Port 1



The screenshot shows the Keysight Spectrum Analyzer software interface. The main plot area displays a DPX Spectrum plot with a grid. The y-axis is labeled 'dB/div: 10.0 dB' and ranges from -50.0 to 50.0. The x-axis is labeled 'Frequency' and ranges from 700.0 to 710.0 MHz. A signal is visible at 706.04 MHz with a peak at -27.05 dBm. The plot is titled 'Trace1' and has a 'Show +Peak Normal' checkbox. The 'Zero Span' dropdown is set to 'Zero Span'. The 'Autoscale' button is active. The 'Pos: 0.000 s' and 'Sweep: 350.0 s' are displayed. The bottom panel shows a 'Marker Readout Table' with the following data:

Marker	Time	Δ Time	DPX	Δ DPX
MR	9.625 s		-27.05 dBm	
M1	350.000 s	340.375 s	-27.39 dBm	-0.34 dB
M2	10.062 s	437.500 ms	29.72 dBm	56.76 dB

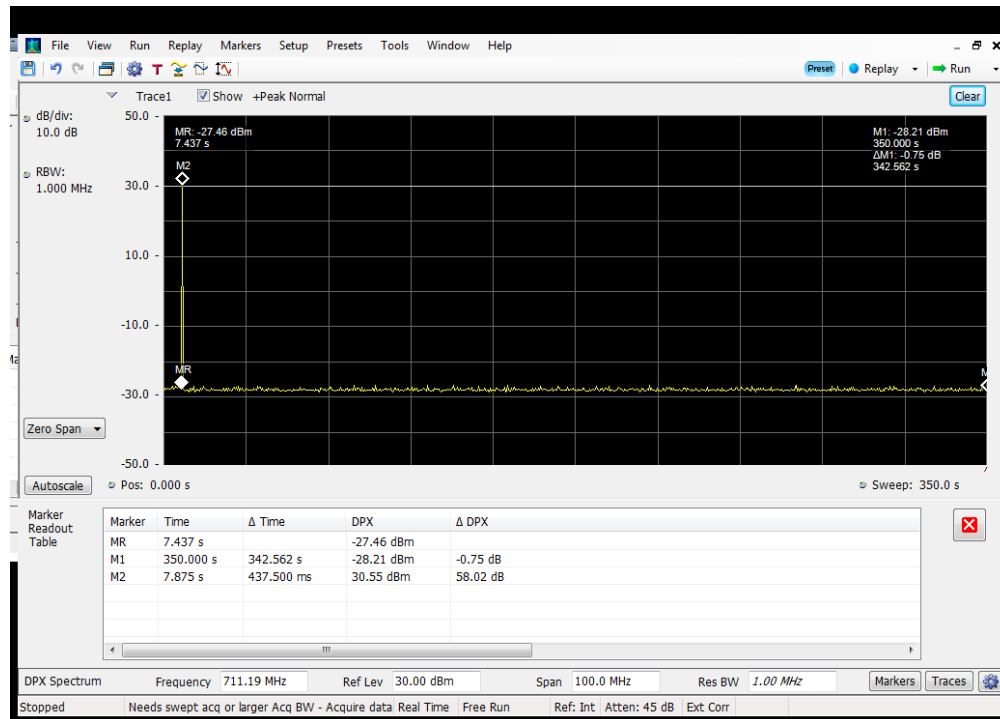
The bottom status bar shows the following information: DPX Spectrum, Frequency 706.04 MHz, Ref Lev 30.00 dBm, Span 50.0 MHz, Res BW 500 kHz, Markers, Traces, Stopped, Needs swept acq or larger Acq BW - Acquire data Real Time, Free Run, Ref: Int, Atten: 45 dB, Ext Corr.

The screenshot shows the Keysight Spectrum Analyzer software interface. The main plot area displays a DPX Spectrum plot with a signal at 12.250 s and a power level of -27.88 dBm. The Y-axis is labeled 'dB/div: 10.0 dB' and 'RBW: 1.000 MHz'. The X-axis is labeled 'Pos: 0.000 s' and 'Sweep: 350.0 s'. A table at the bottom shows marker data for MR, M1, and M2.

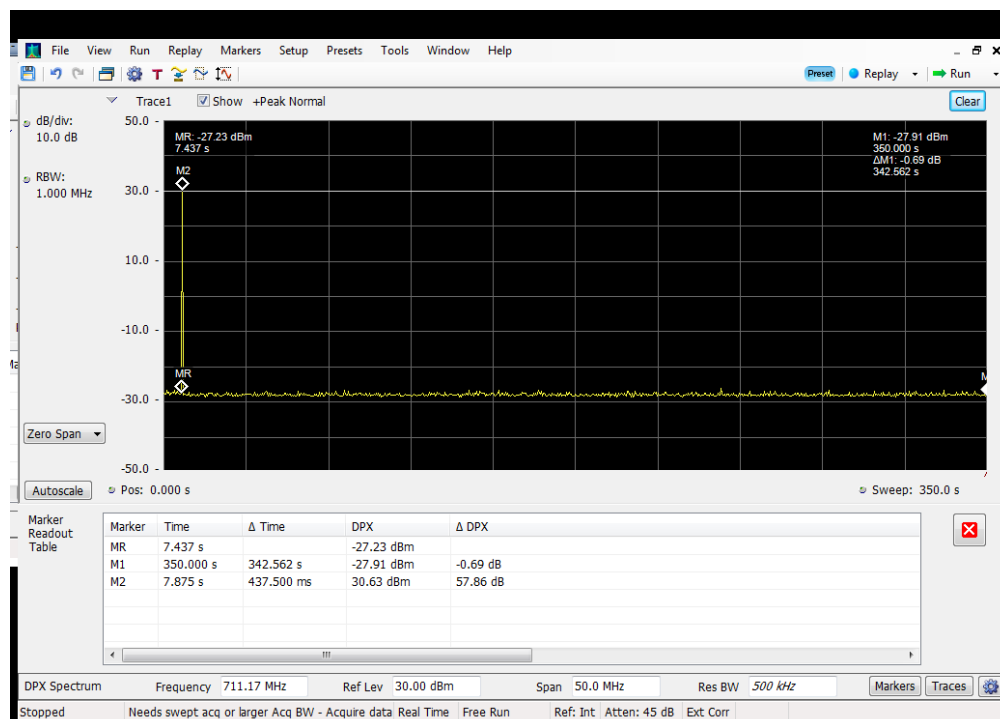
Marker	Time	Δ Time	DPX	Δ DPX
MR	12.250 s		-27.88 dBm	
M1	350.000 s	337.750 s	-27.72 dBm	0.16 dB
M2	12.687 s	437.500 ms	29.39 dBm	57.27 dB



Restart Timing_Uplink_698 - 716 MHz_B12_Server 2 to Dedicated Donor Port 3

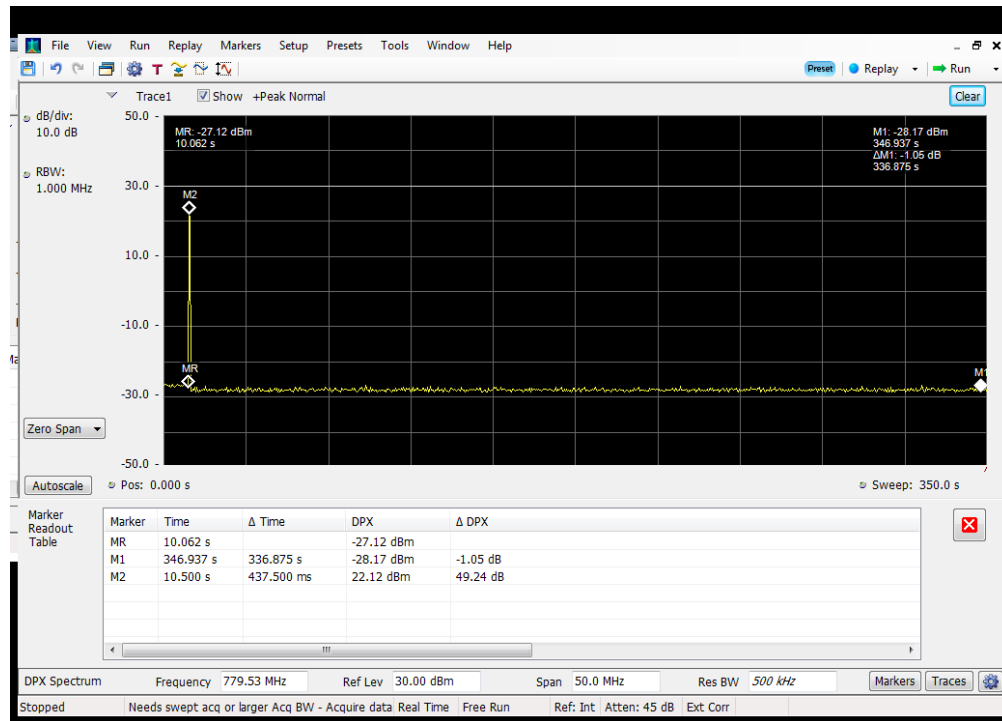


Restart Timing_Uplink_698 - 716 MHz_B12_Server 3 to Dedicated Donor Port 3

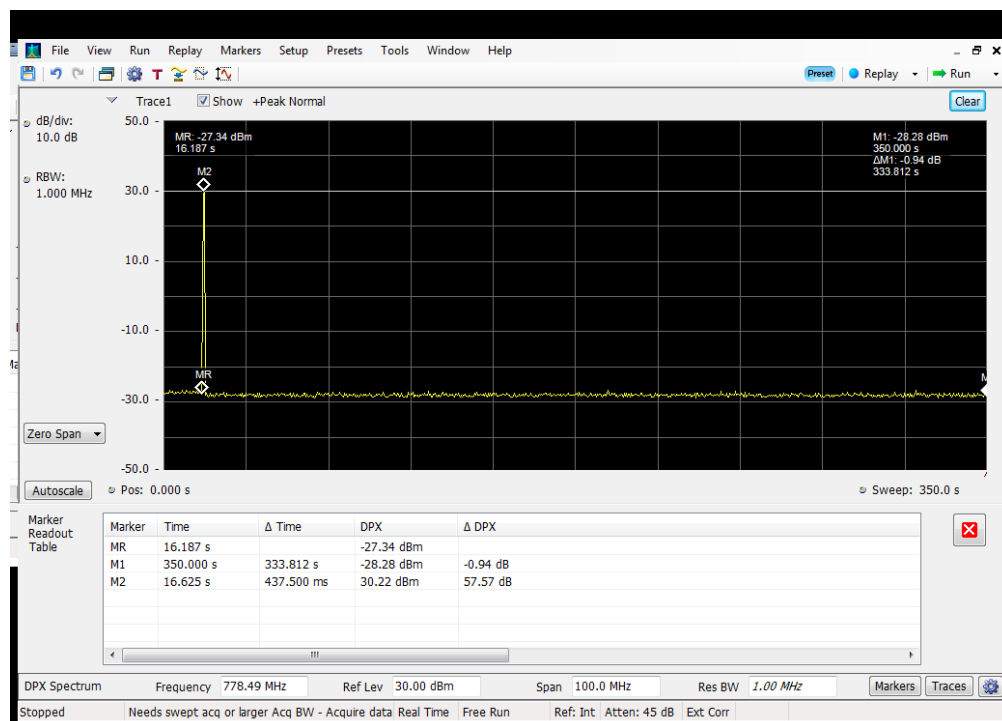




Restart Timing_Uplink_776 - 787 MHz_B13_Server 1 to Dedicated Donor Port 3

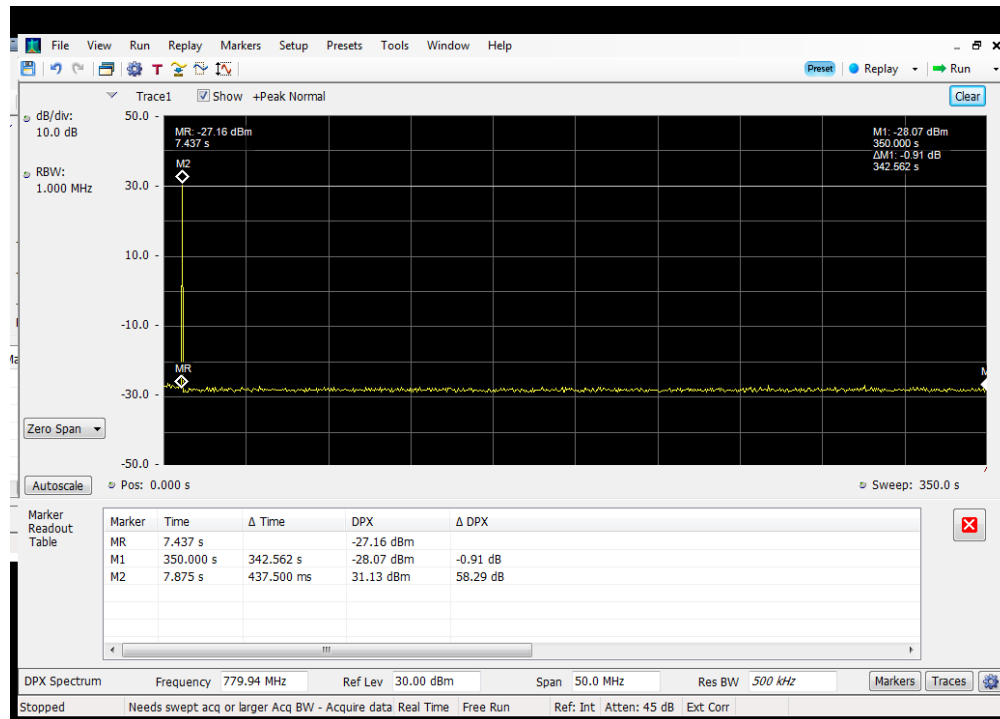


Restart Timing_Uplink_776 - 787 MHz_B13_Server 2 to Common Donor Port 1

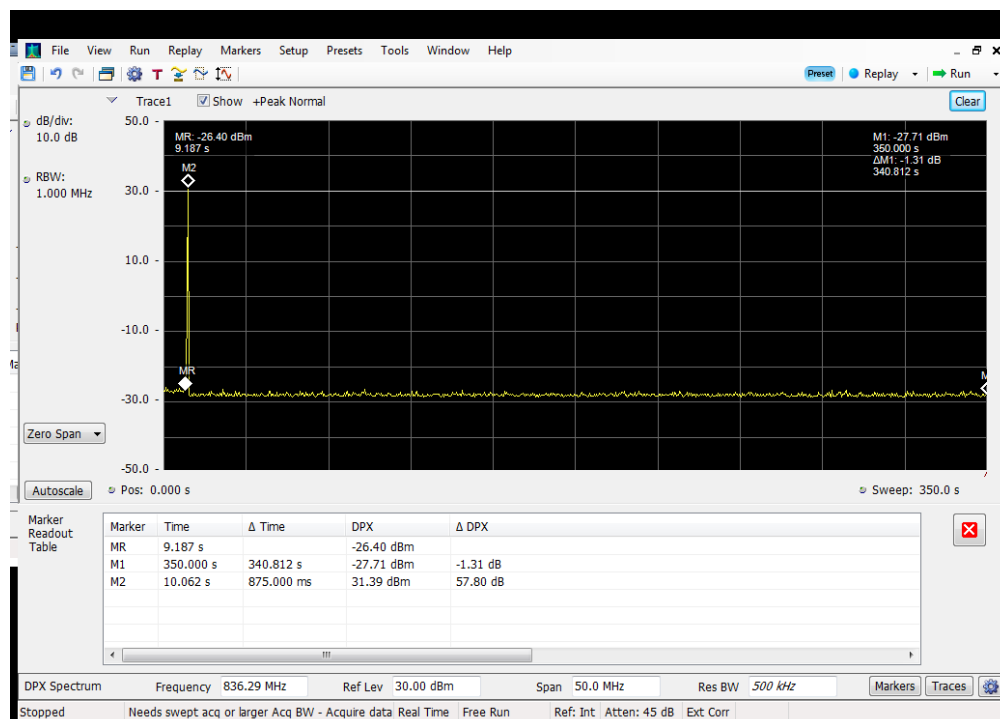




Restart Timing_Uplink_776 - 787 MHz_B13_Server 2 to Dedicated Donor Port 3

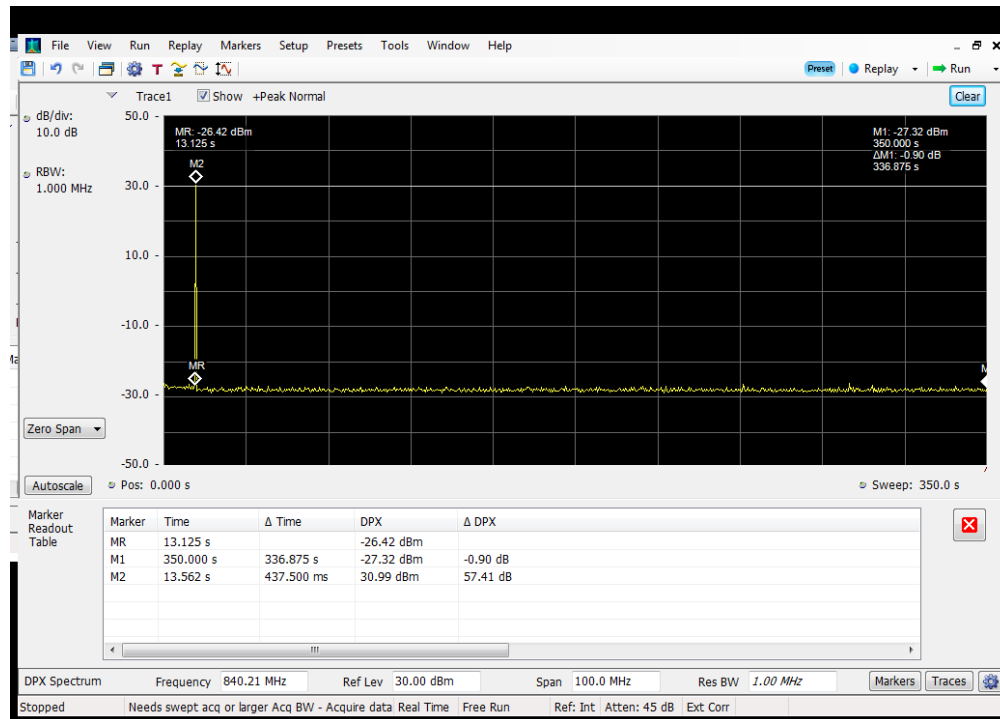


Restart Timing_Uplink_824 - 849 MHz_B5_Server 1 to Dedicated Donor Port 2





Restart Timing_Uplink_824 - 849 MHz_B5_Server 2 to Common Donor Port 1



Restart Timing_Uplink_824 - 849 MHz_B5_Server 2 to Dedicated Donor Port 2





Restart Timing_Uplink_824 - 849 MHz_B5_Server 3 to Dedicated Donor Port 2

