



Annex I

Anti - Oscillation



Osc Det_DL_1930 - 1995 MHz

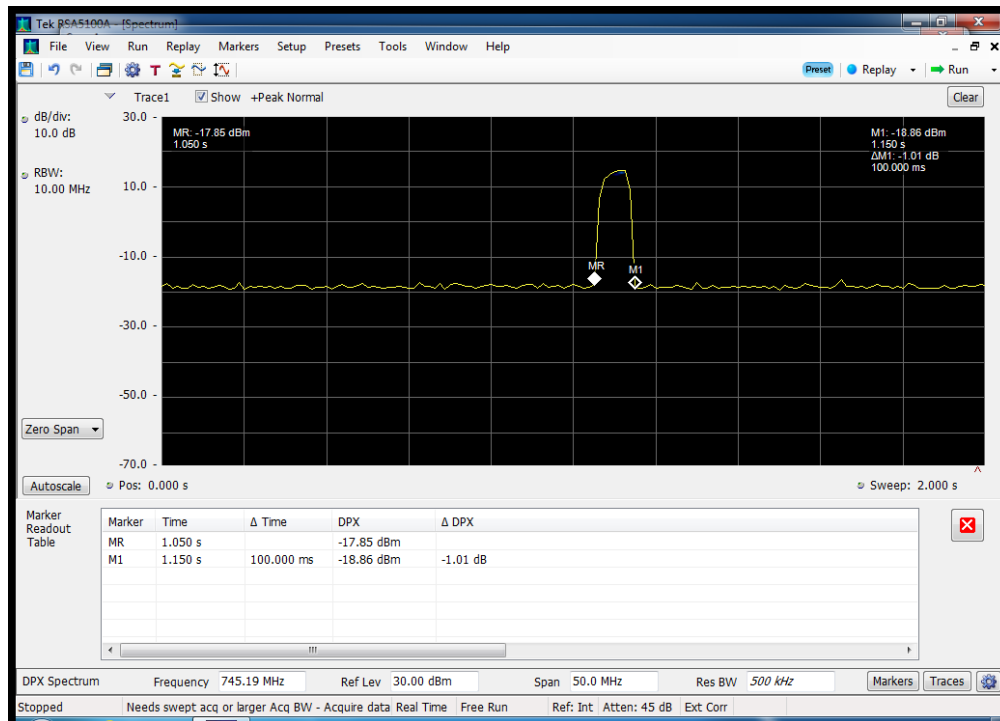


Osc Det_DL_2110 - 2155 MHz

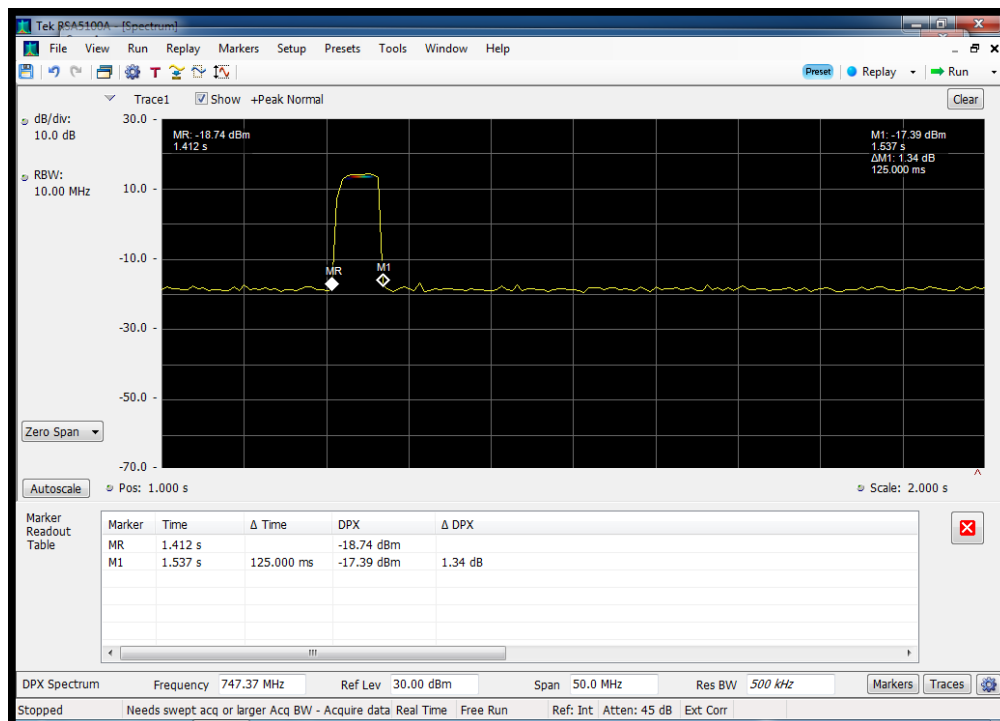




Osc Det_DL_728 - 746 MHz



Osc Det_DL_746 - 757 MHz

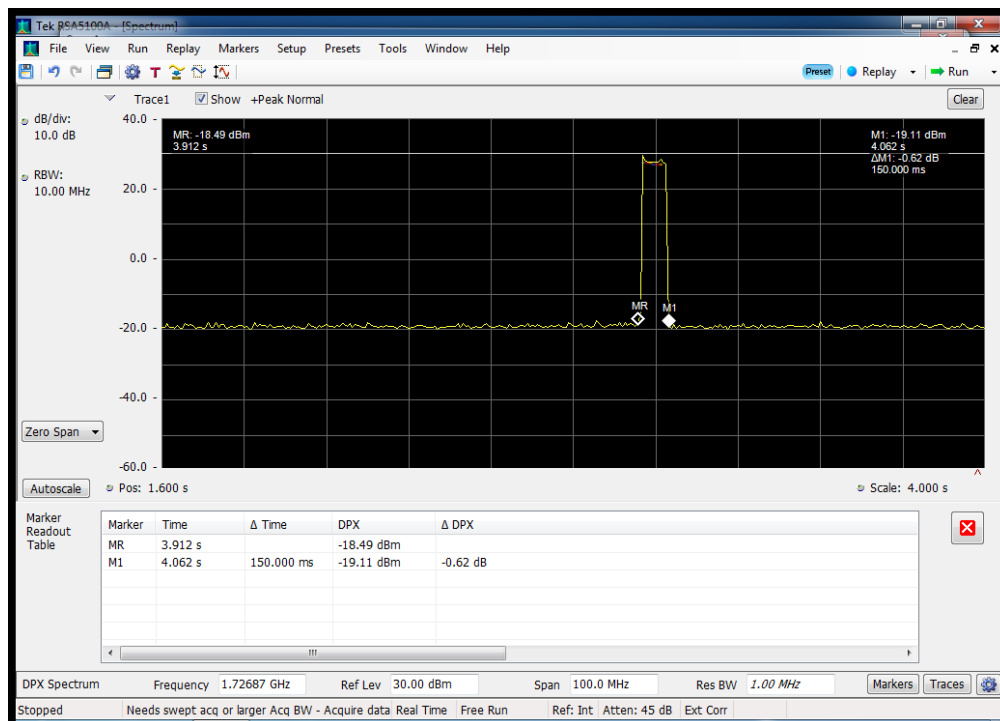




Osc Det_DL_869 - 894 MHz

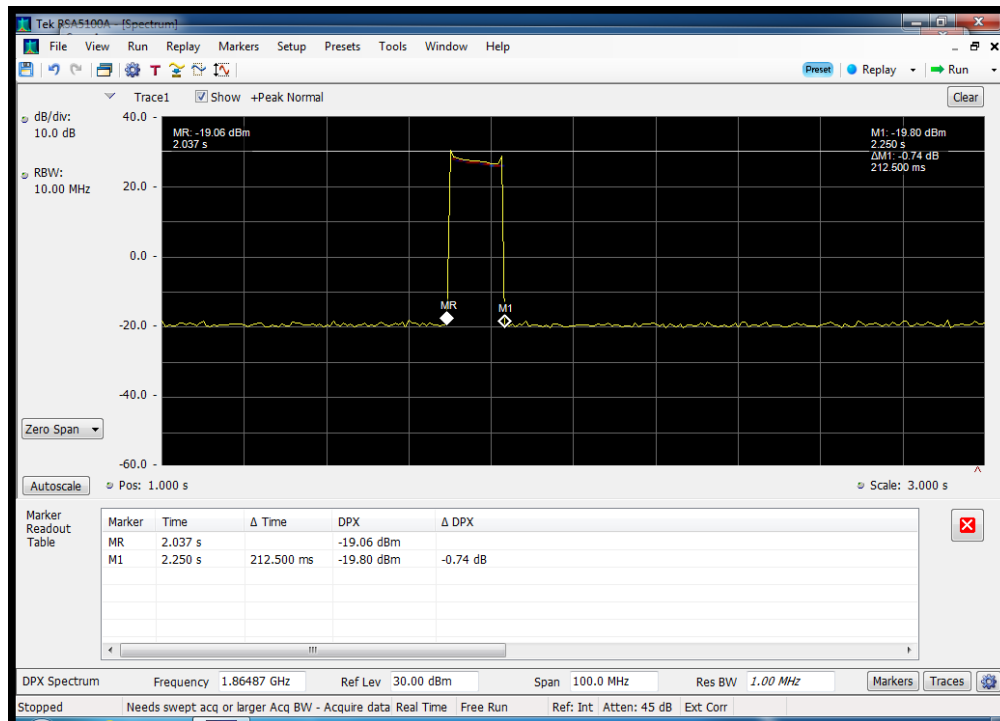


Osc Det_UL_1710 - 1755 MHz





Osc Det_UL_1850 - 1915 MHz

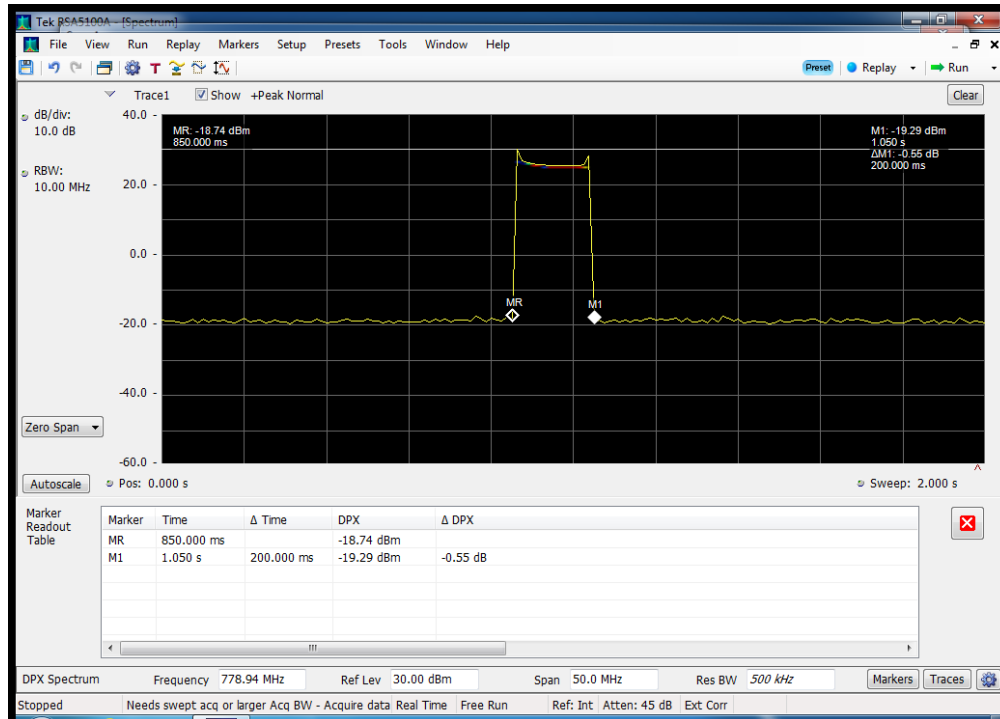


Osc Det_UL_698 - 716 MHz





Osc Det_UL_776 - 787 MHz

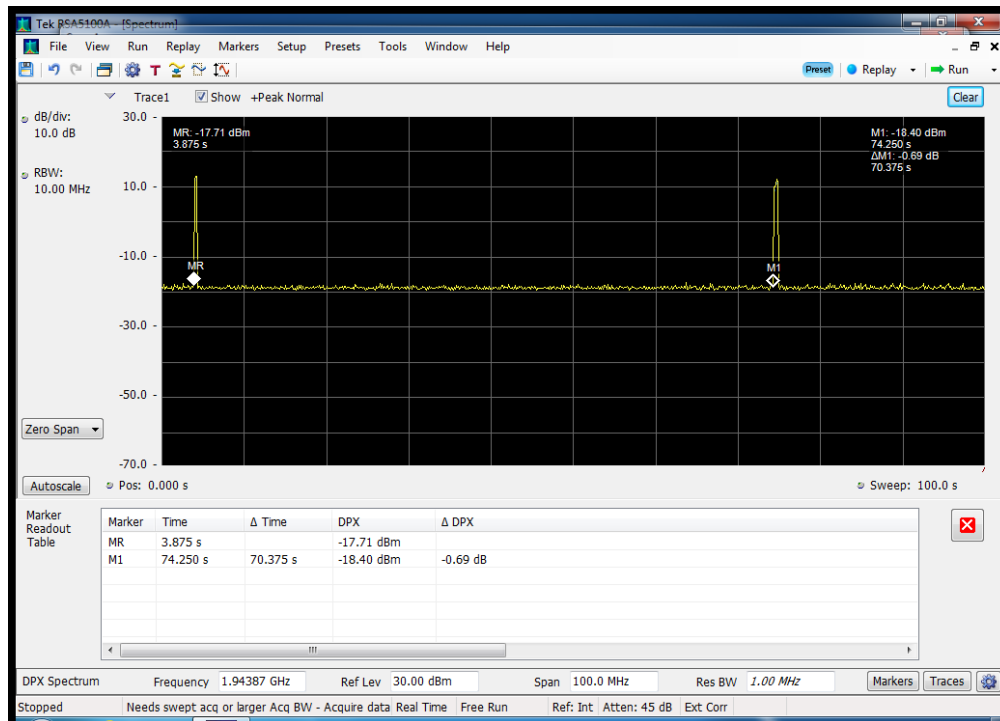


Osc Det_UL_824 - 849 MHz

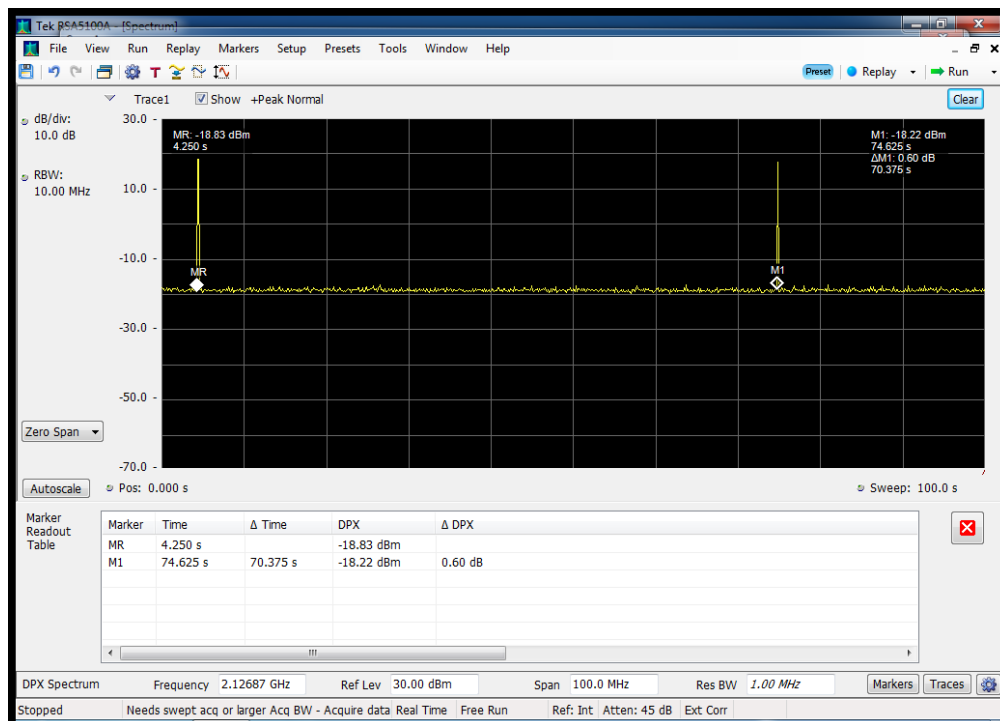




Restart Time_DL_1930 - 1995 MHz

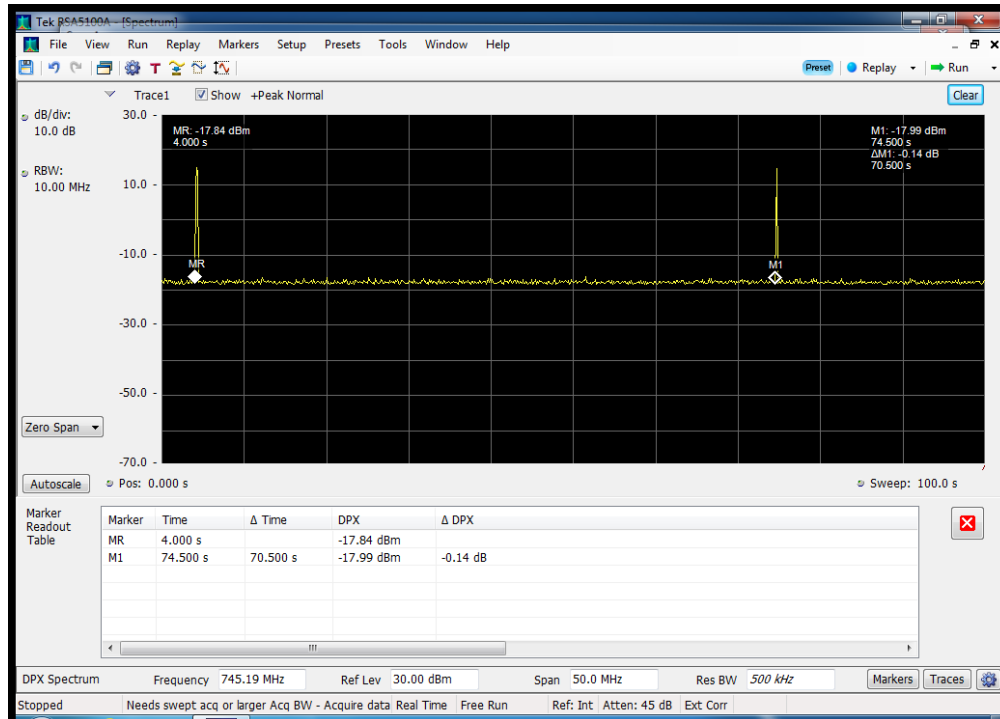


Restart Time_DL_2110 - 2155 MHz

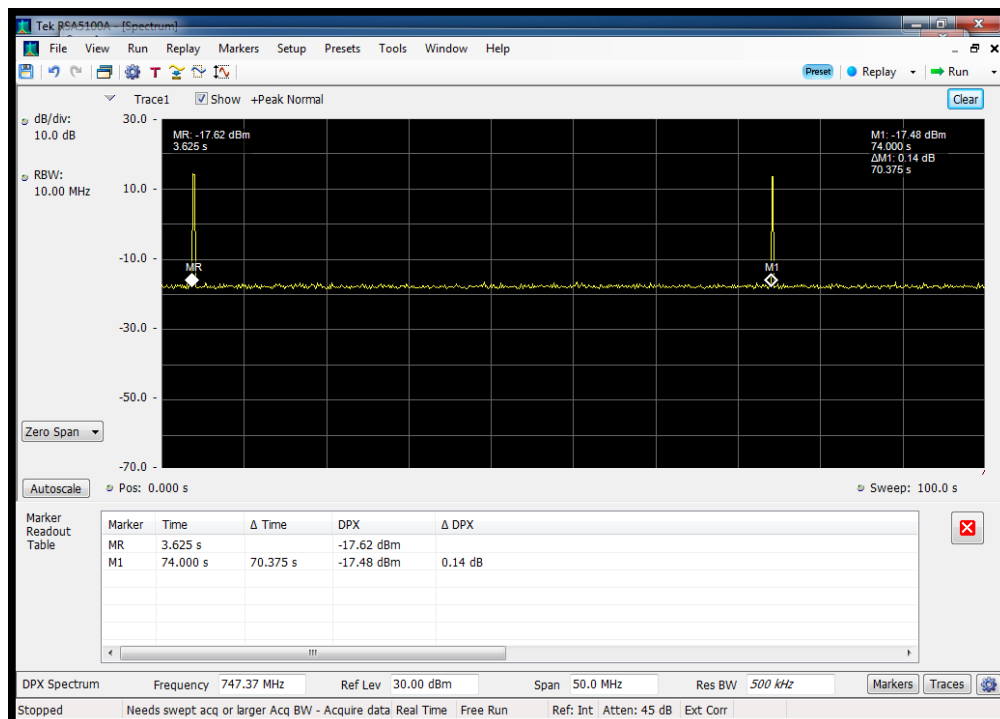




Restart Time_DL_728 - 746 MHz

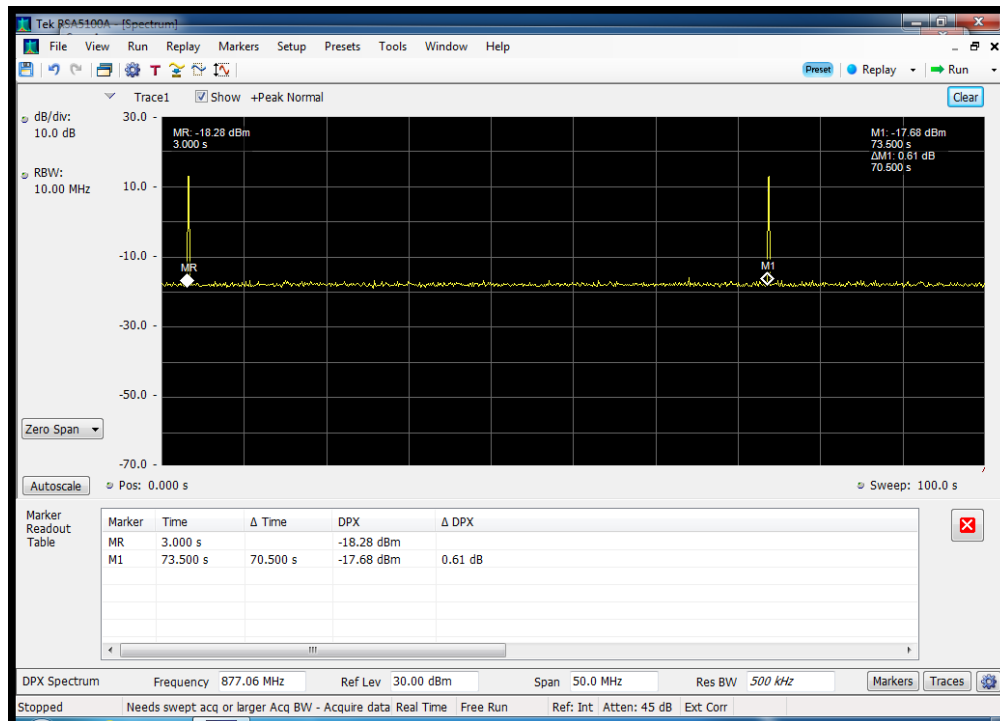


Restart Time_DL_746 - 757 MHz





Restart Time_DL_869 - 894 MHz

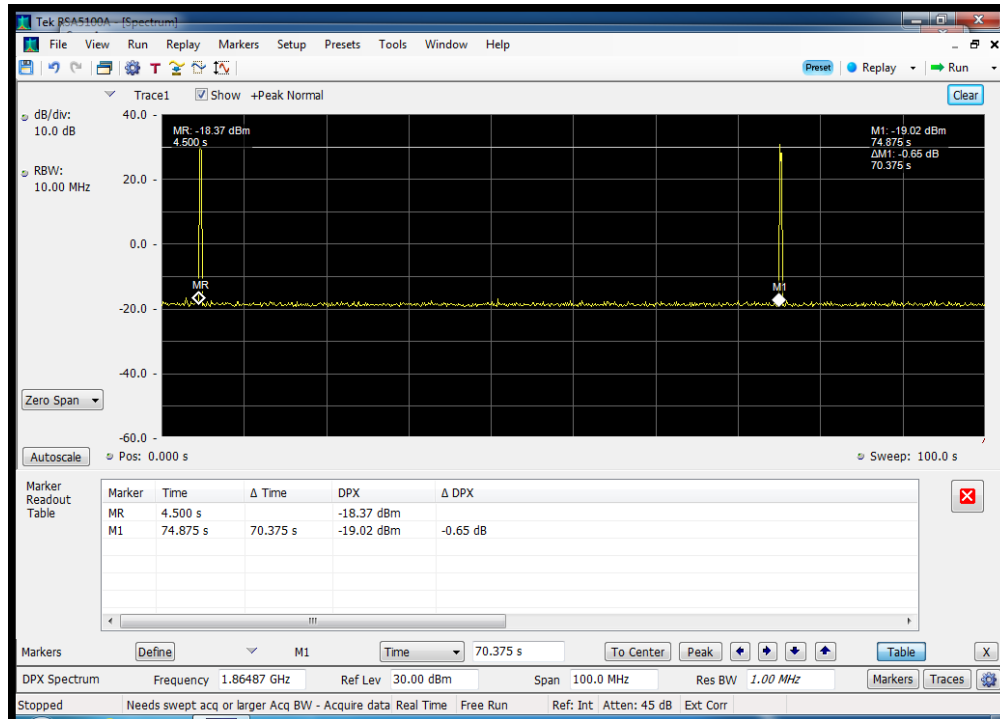


Restart Time_UL_1710 - 1755 MHz

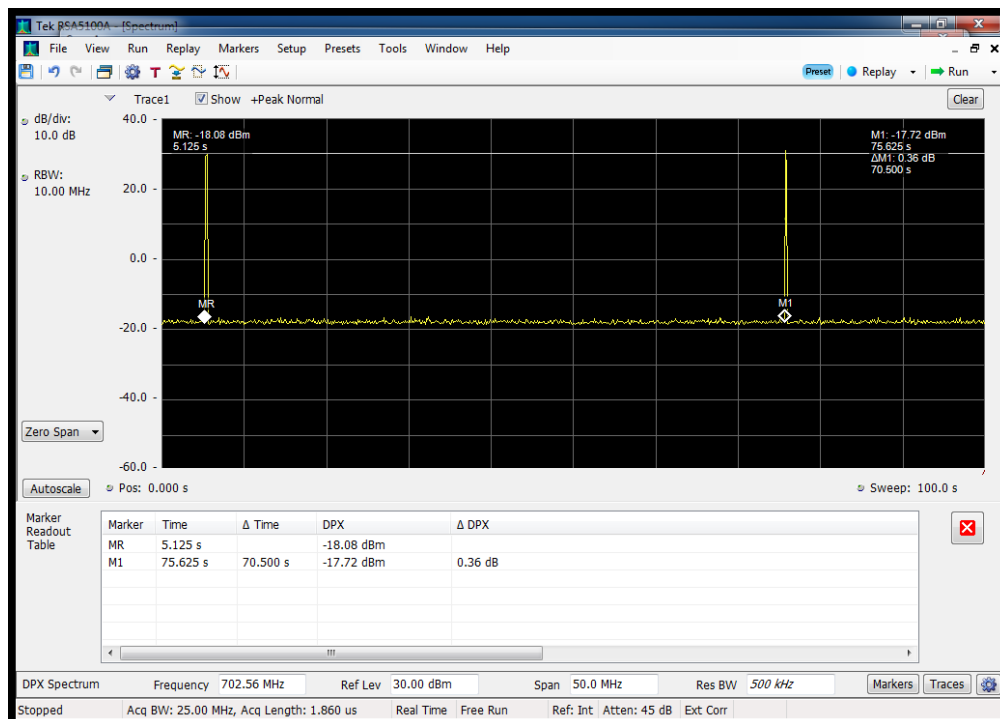




Restart Time_UL_1850 - 1915 MHz

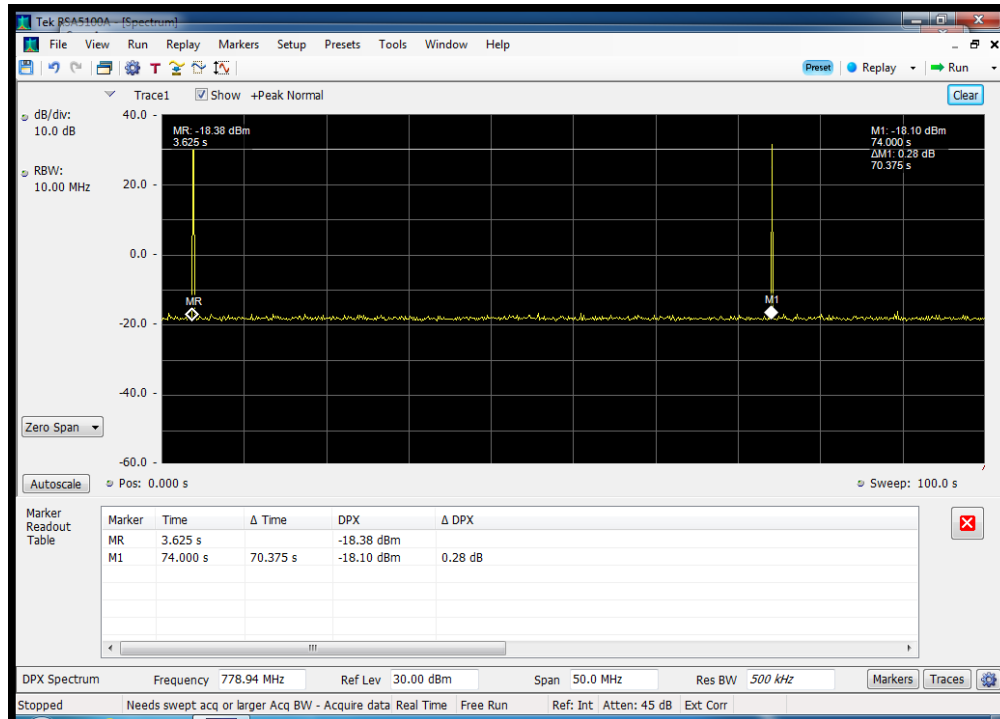


Restart Time_UL_698 - 716 MHz

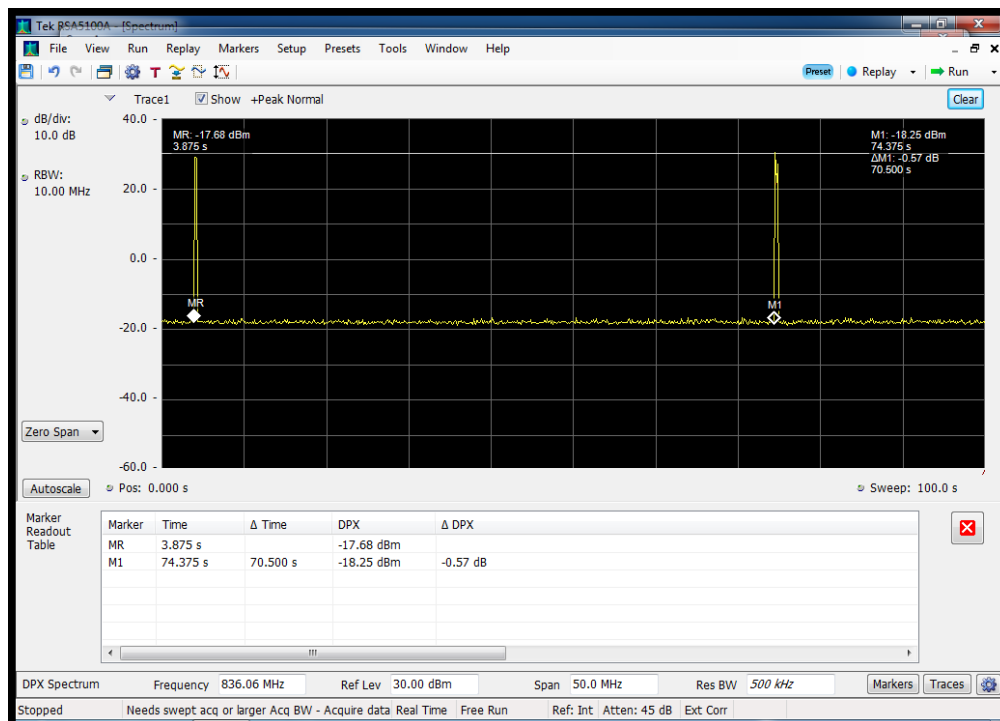




Restart Time_UL_776 - 787 MHz

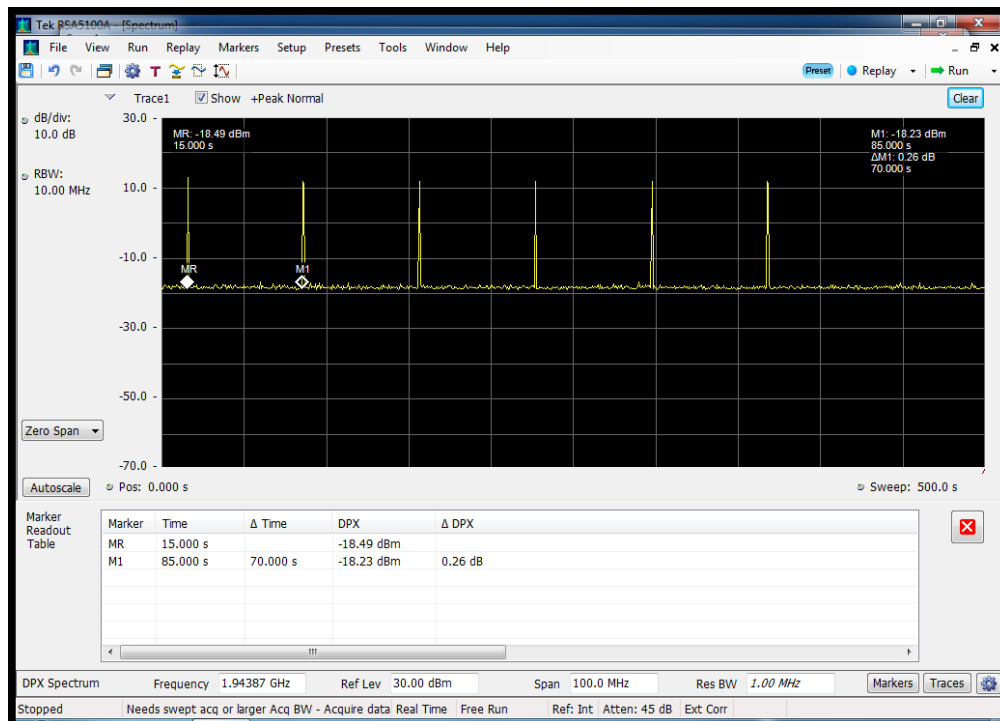


Restart Time_UL_824 - 849 MHz

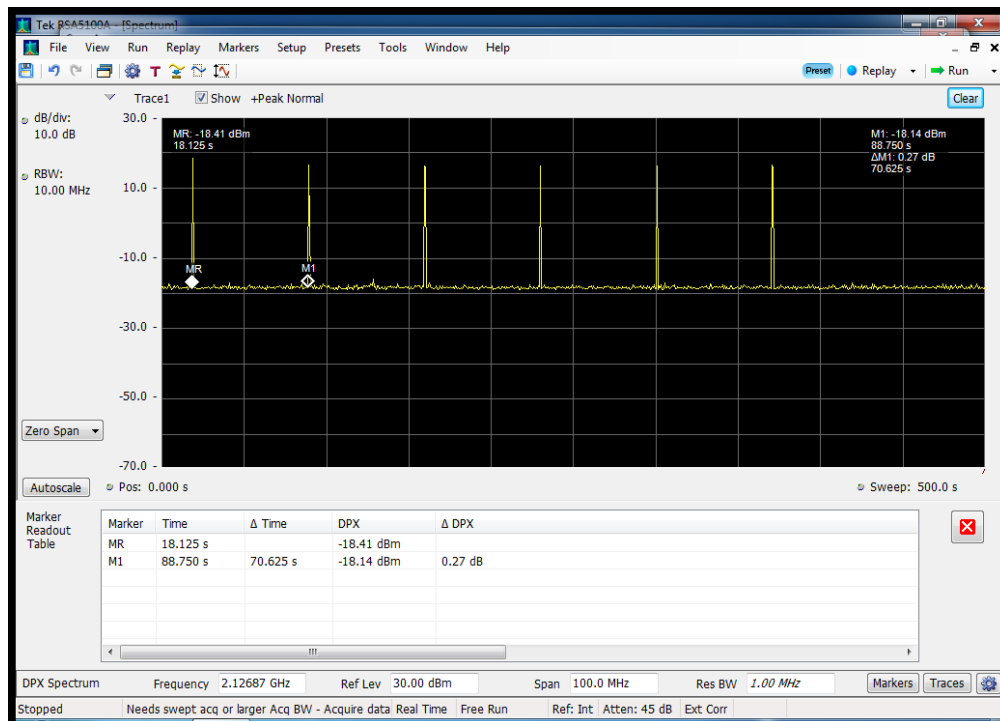




of Restarts_DL_1930 - 1995 MHz

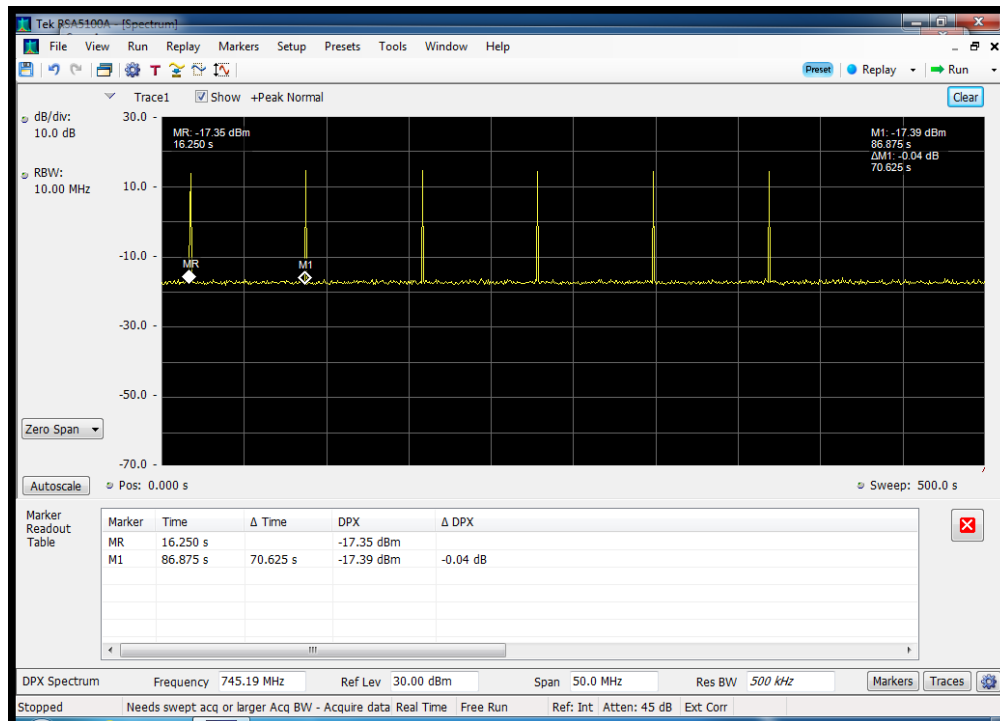


of Restarts_DL_2110 - 2155 MHz

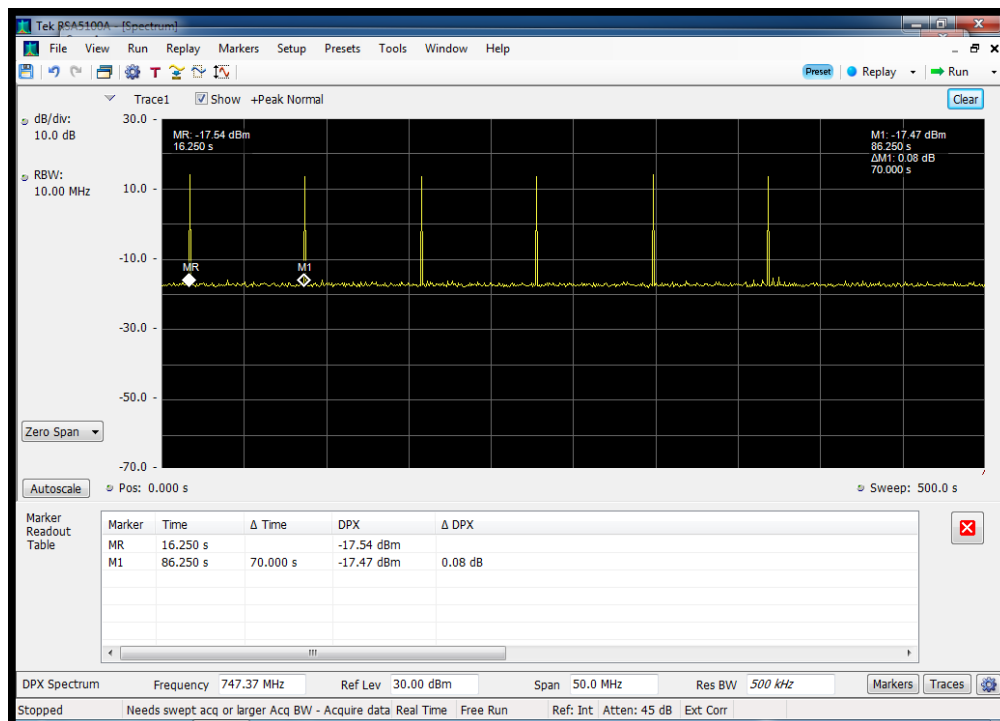




of Restarts_DL_728 - 746 MHz

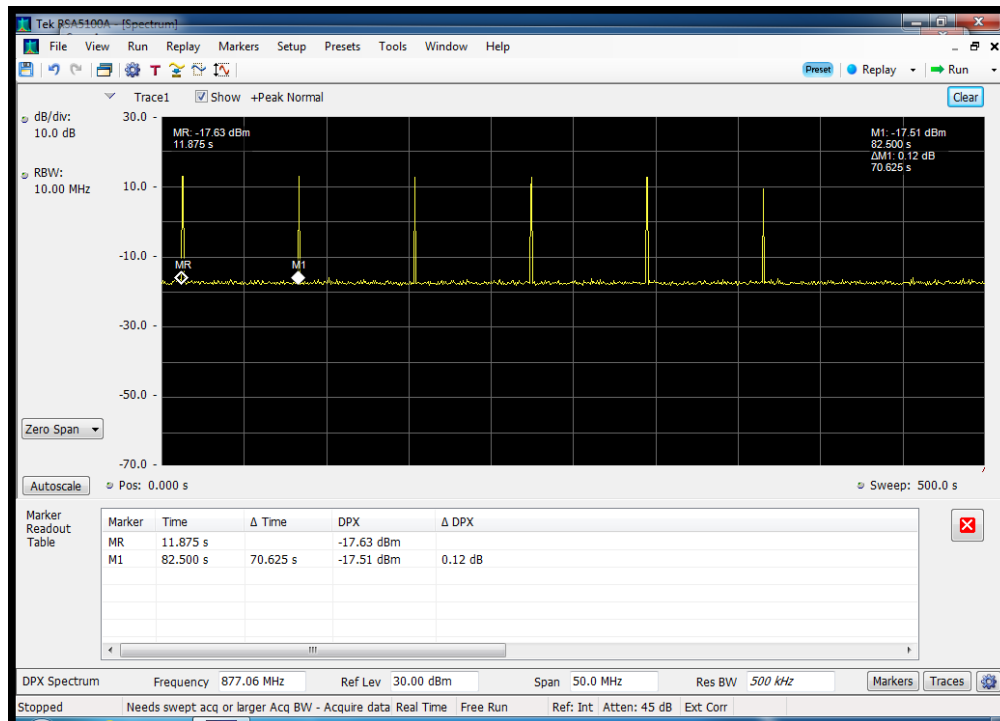


of Restarts_DL_746 - 757 MHz





of Restarts_DL_869 - 894 MHz

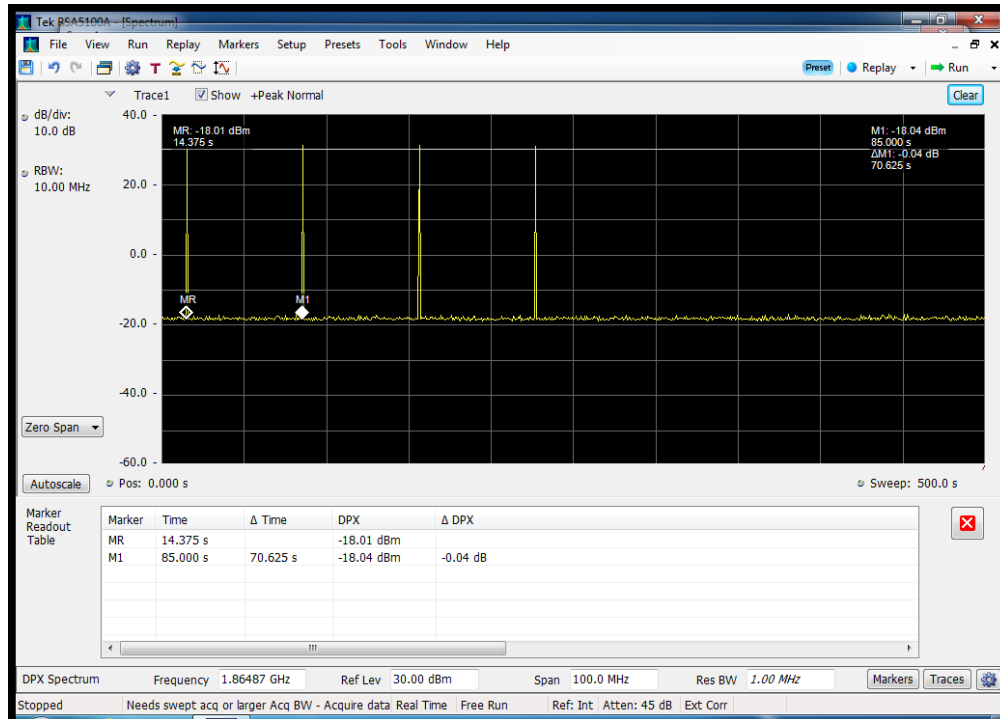


of Restarts_UL_1710 - 1755 MHz





of Restarts_UL_1850 - 1915 MHz

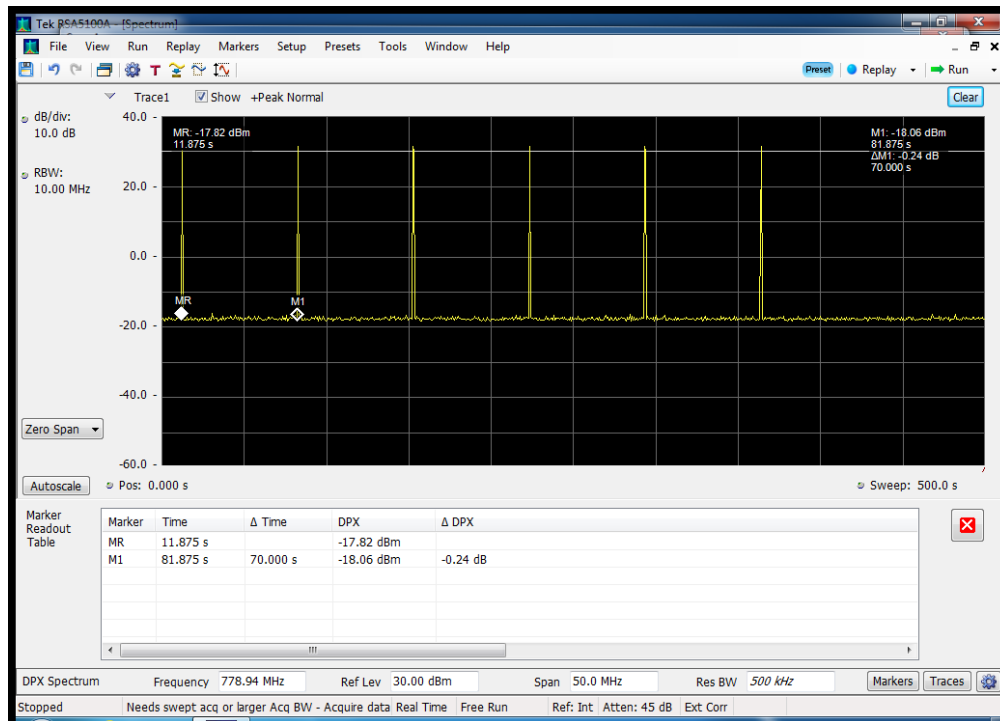


of Restarts_UL_698 - 716 MHz





of Restarts_UL_776 - 787 MHz



of Restarts_UL_824 - 849 MHz

