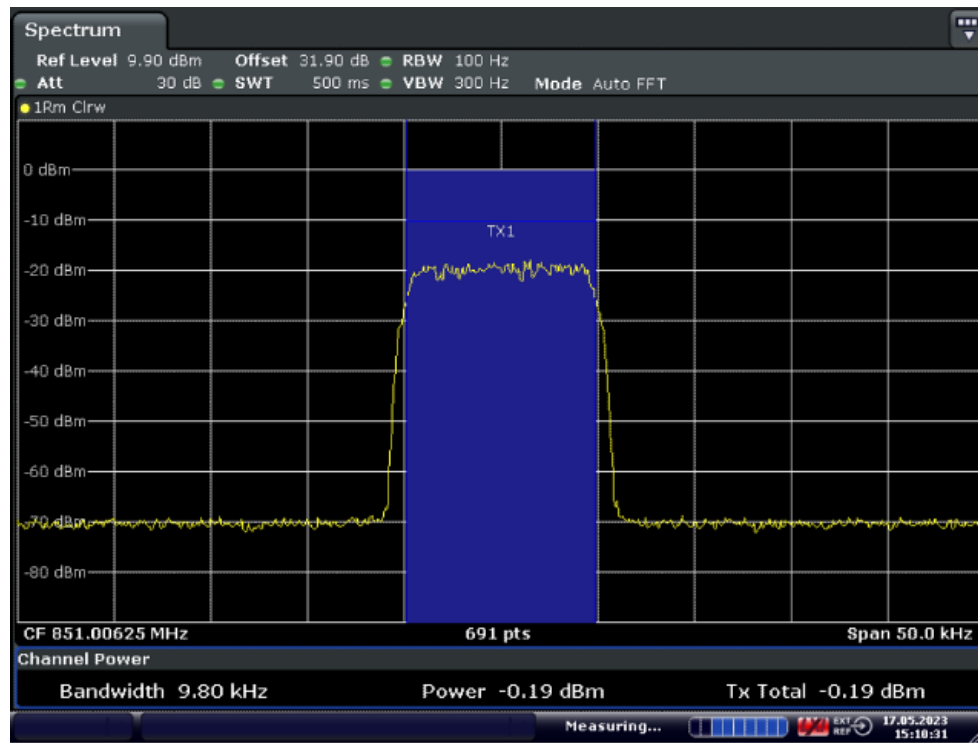
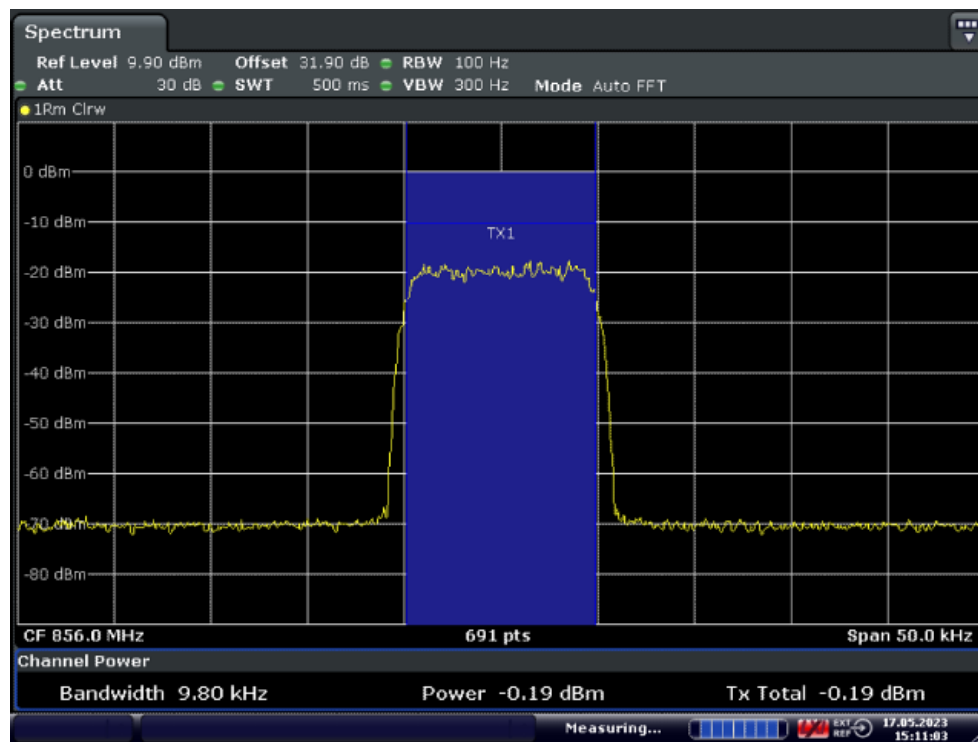


11.2.3.2.2. P25 phase II (H-DQPSK)

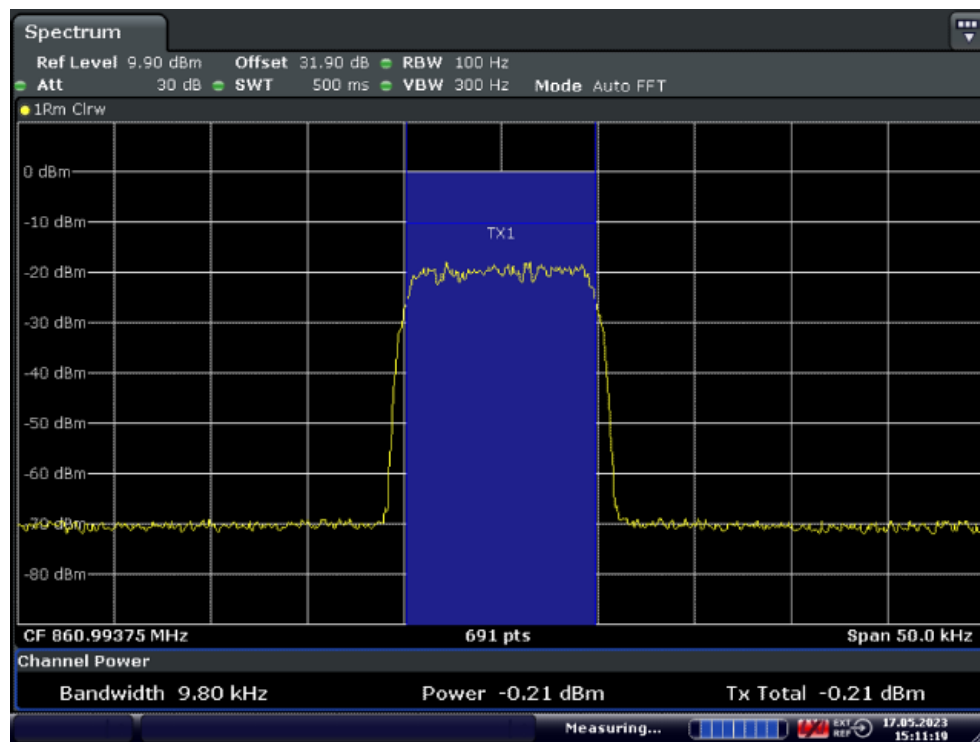
11.2.3.2.2.1. Downlink



Low Frequency: 851.00625MHz



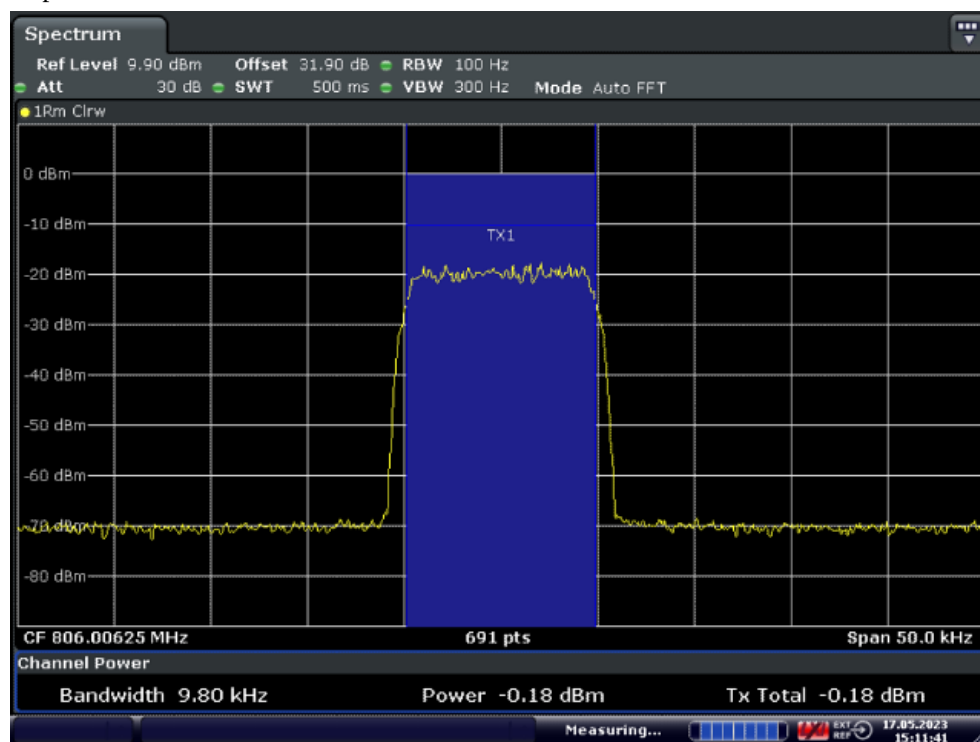
Middle Frequency: 856.0MHz



Date: 17.MAY.2023 15:11:19

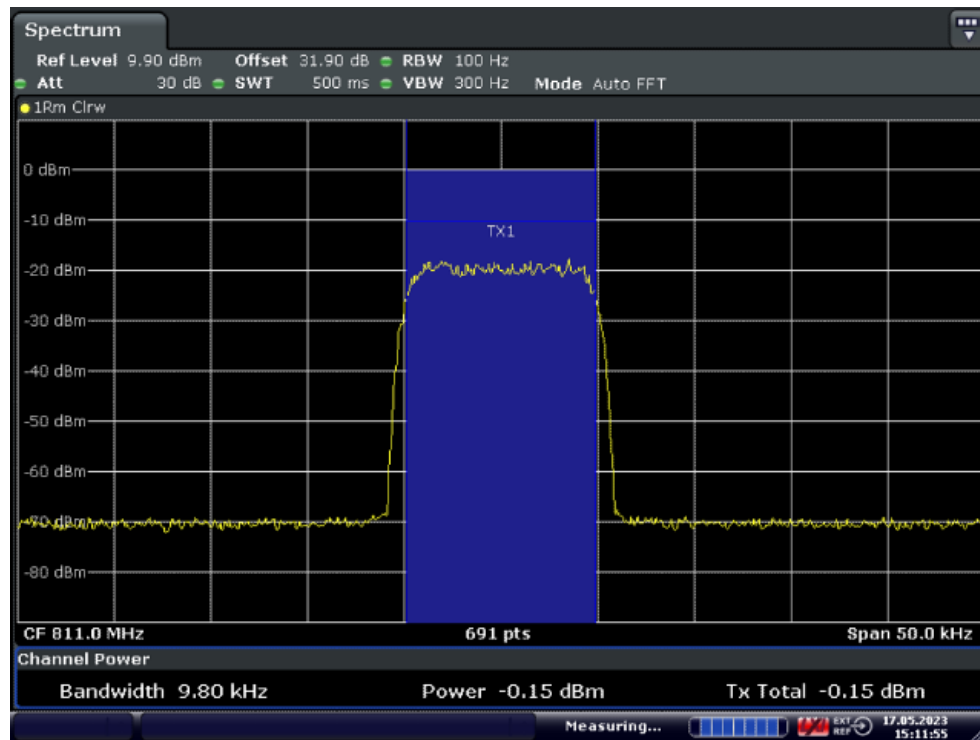
High Frequency: 860.99375MHz

11.2.3.2.2. Uplink



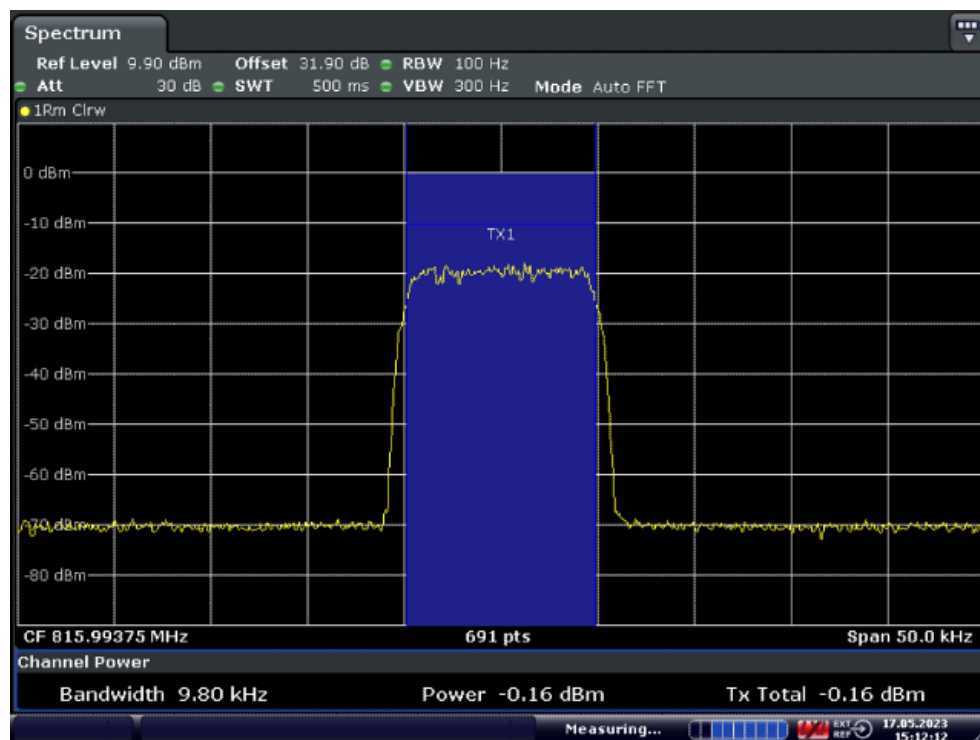
Date: 17.MAY.2023 15:11:41

Low Frequency: 806.00625MHz



Date: 17.MAY.2023 15:11:55

Middle Frequency: 811.0MHz

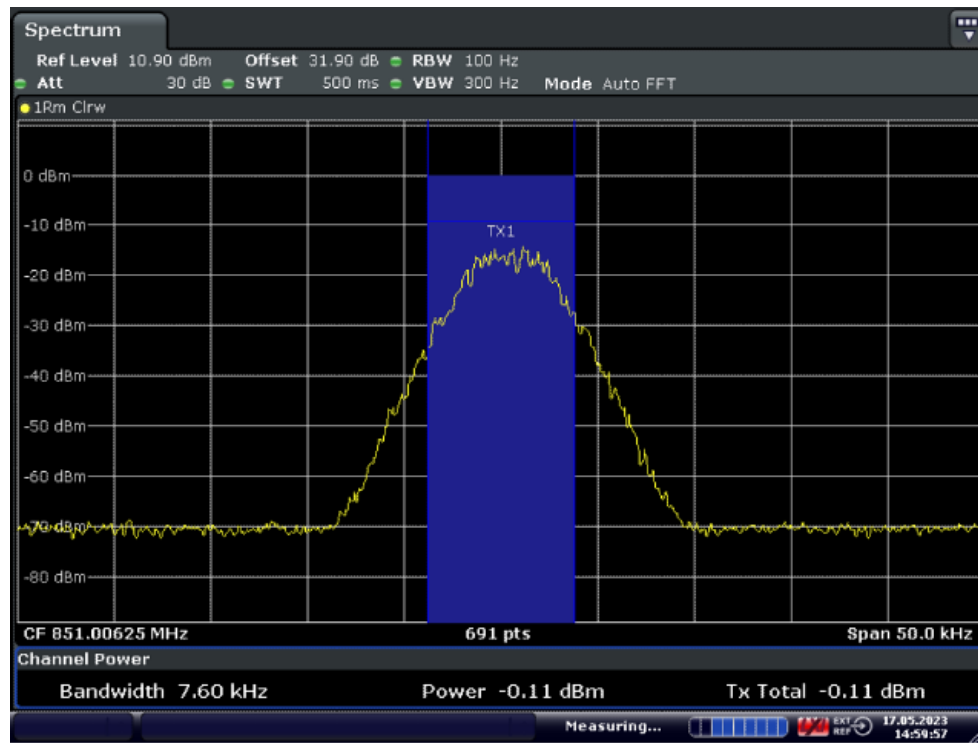


Date: 17.MAY.2023 15:12:12

High Frequency: 815.99375MHz

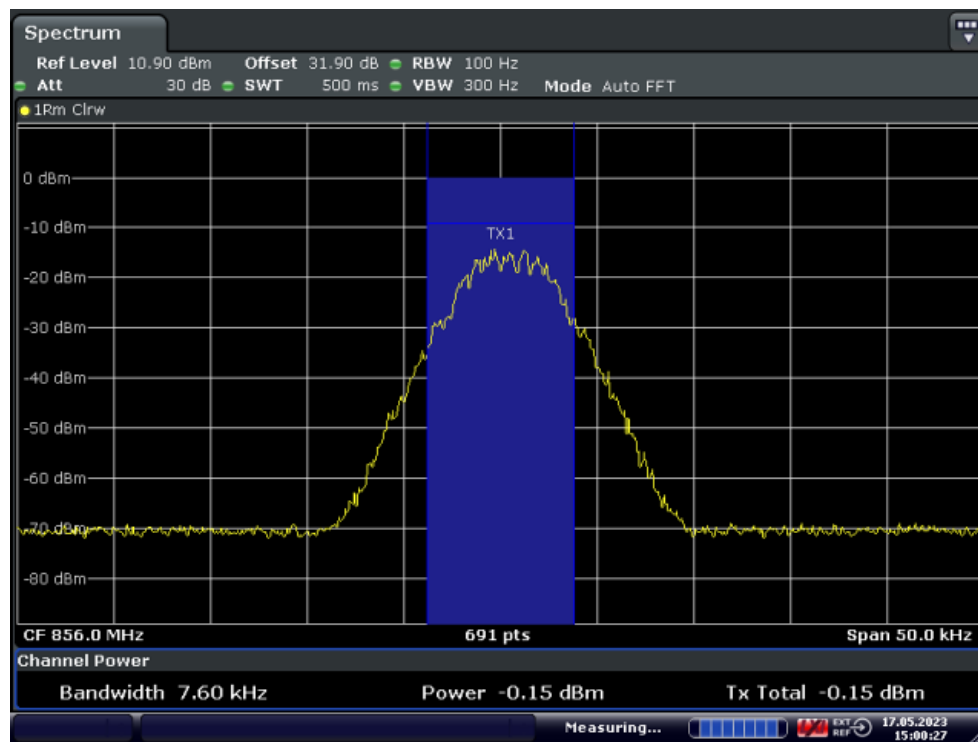
11.2.3.2.3. DMR

11.2.3.2.3.1. Downlink



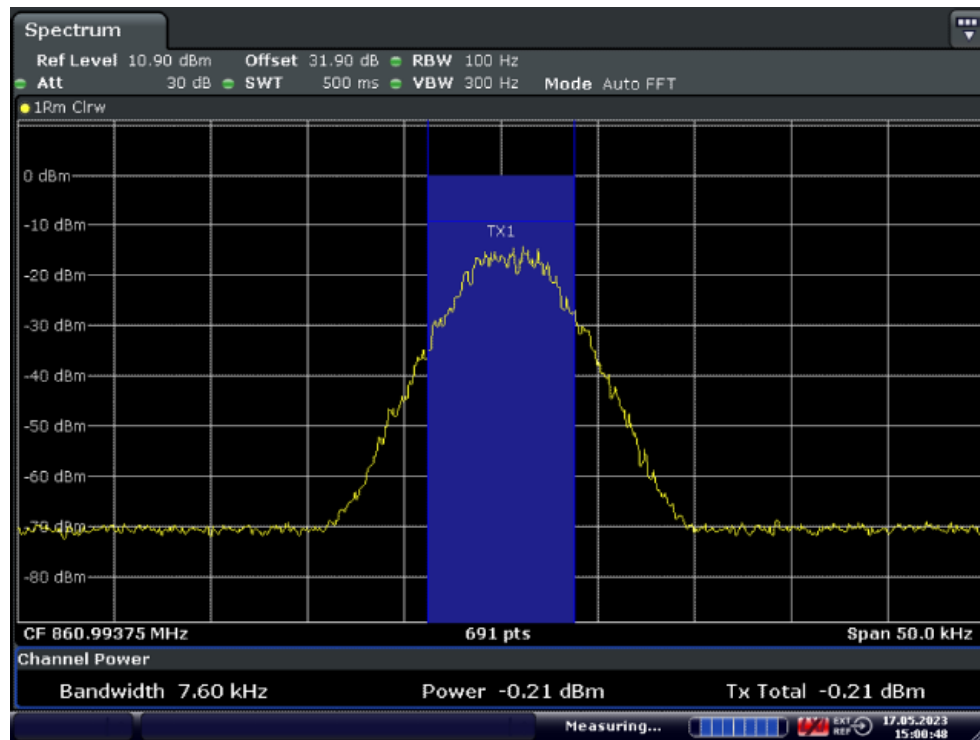
Date: 17.MAY.2023 14:59:58

Low Frequency: 851.00625MHz



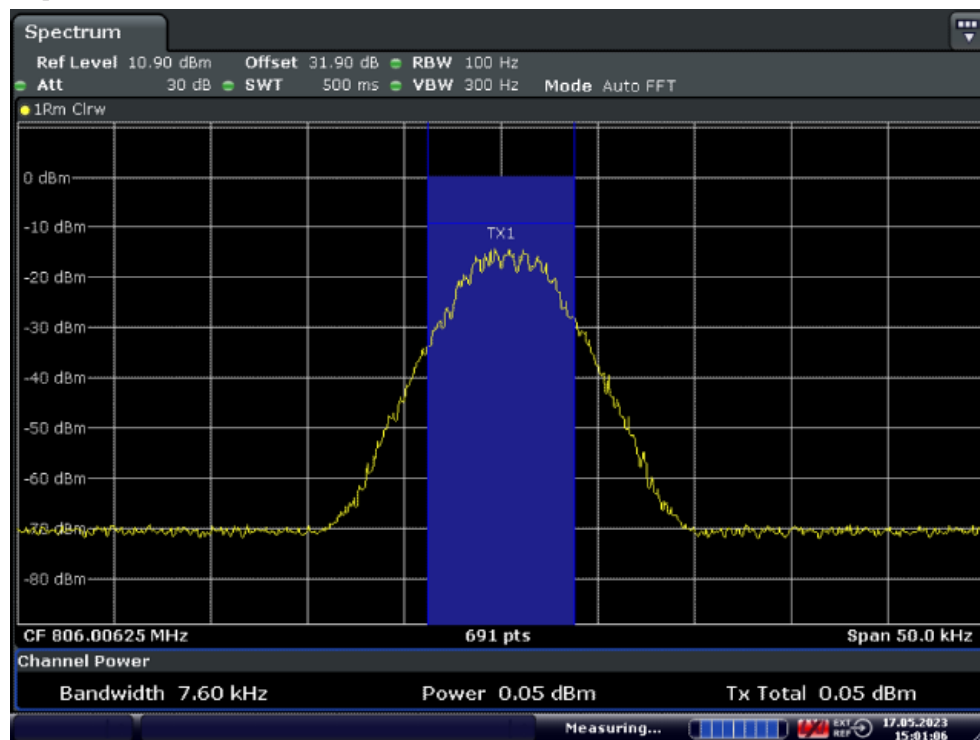
Date: 17.MAY.2023 15:00:28

Middle Frequency: 856.0MHz

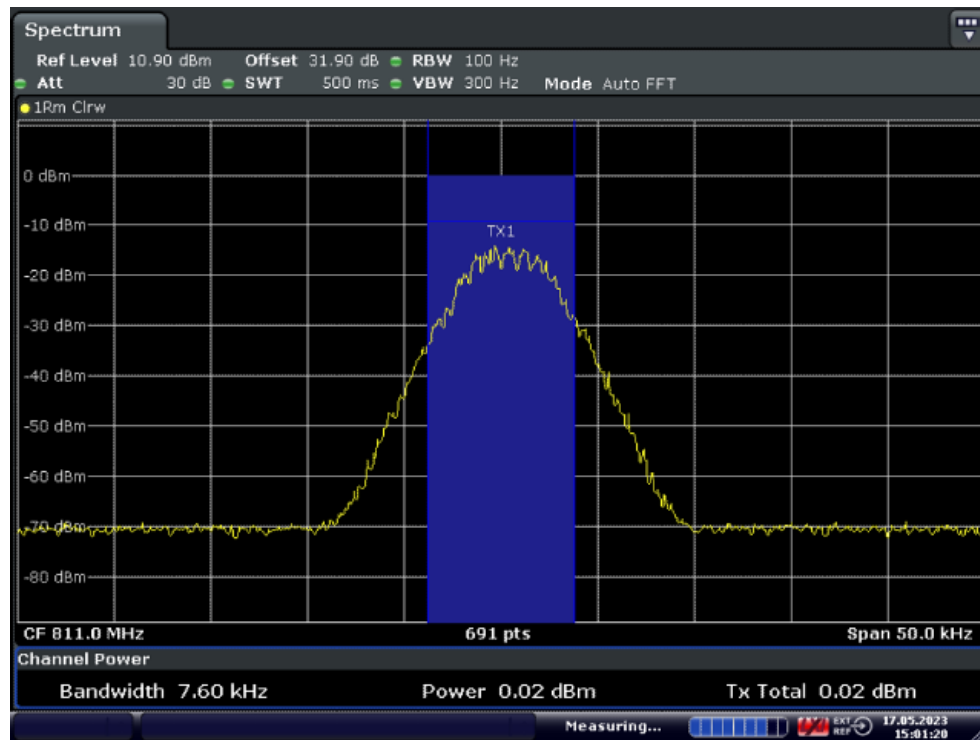


High Frequency: 860.99375MHz

11.2.3.2.3.2. Uplink

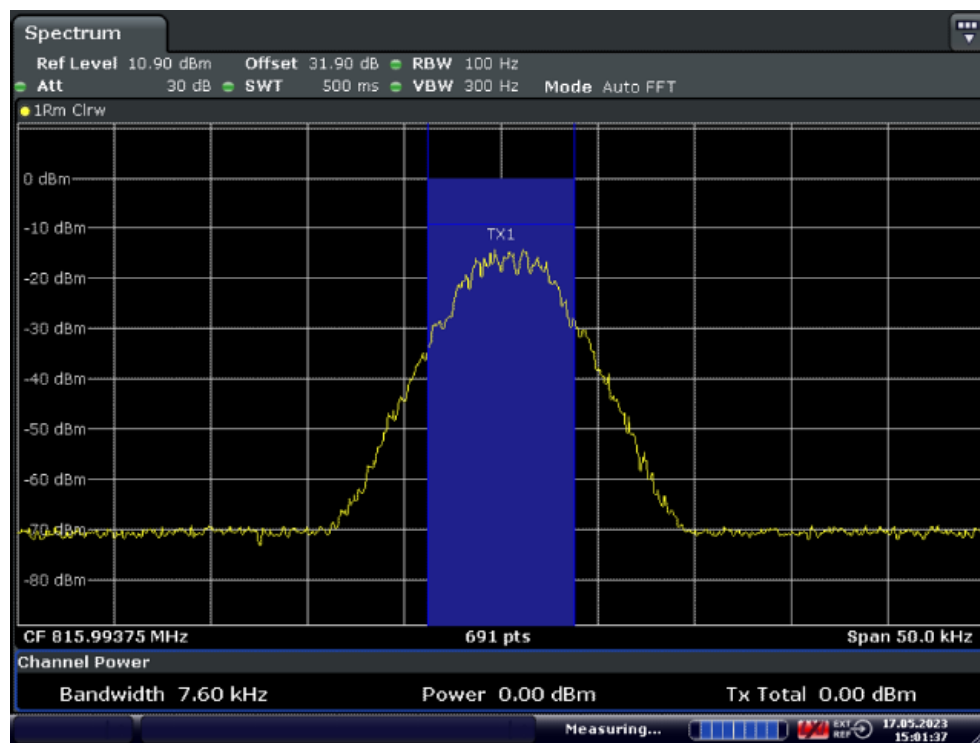


Low Frequency: 806.00625MHz



Date: 17.MAY.2023 15:01:20

Middle Frequency: 811.0MHz

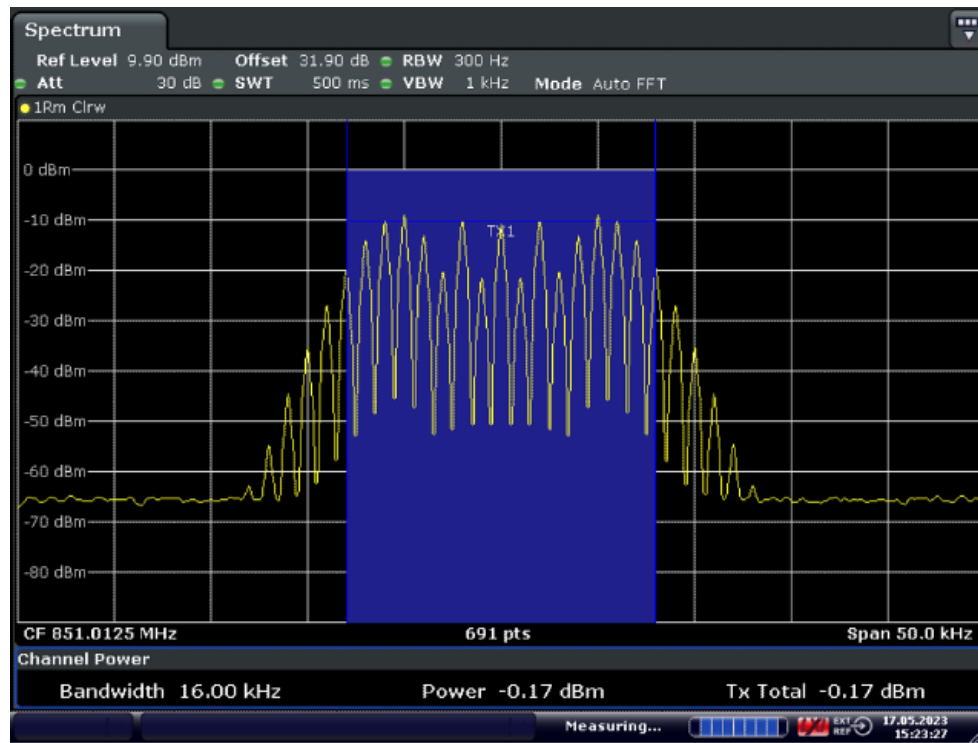


Date: 17.MAY.2023 15:01:37

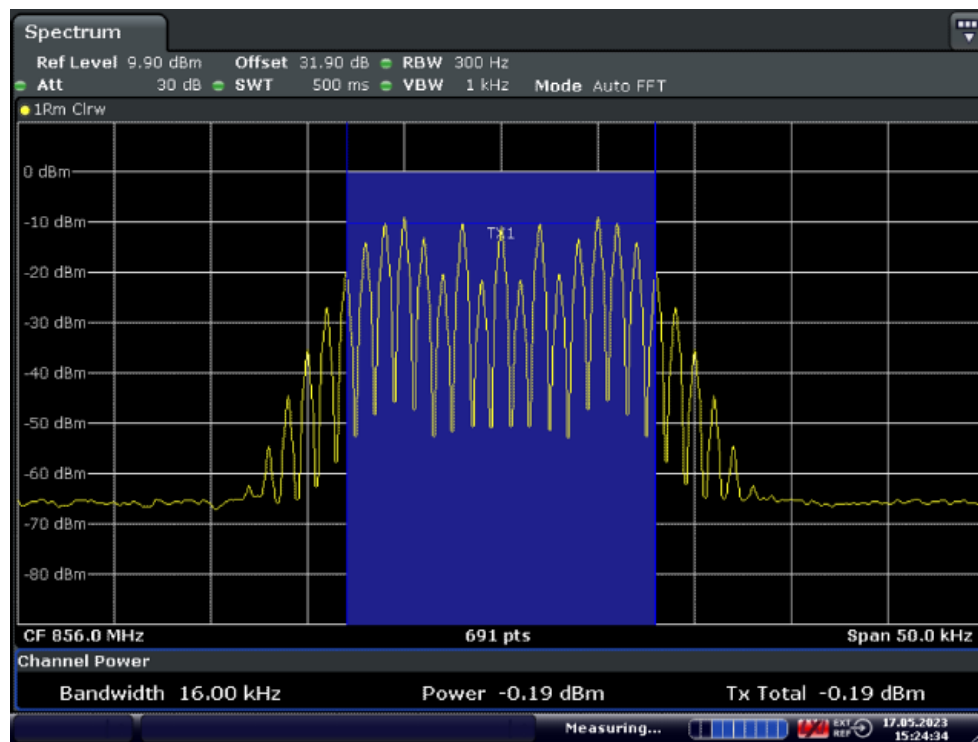
High Frequency: 815.99375MHz

11.2.3.2.4. Analog FM

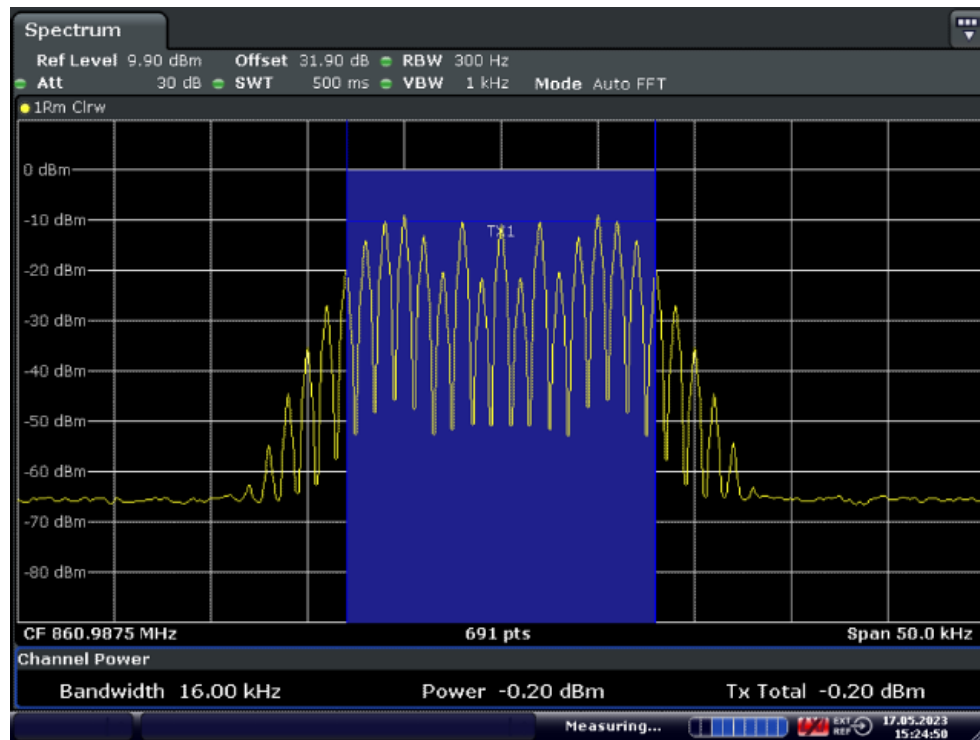
11.2.3.2.4.1. Downlink



Low Frequency: 851.0125MHz



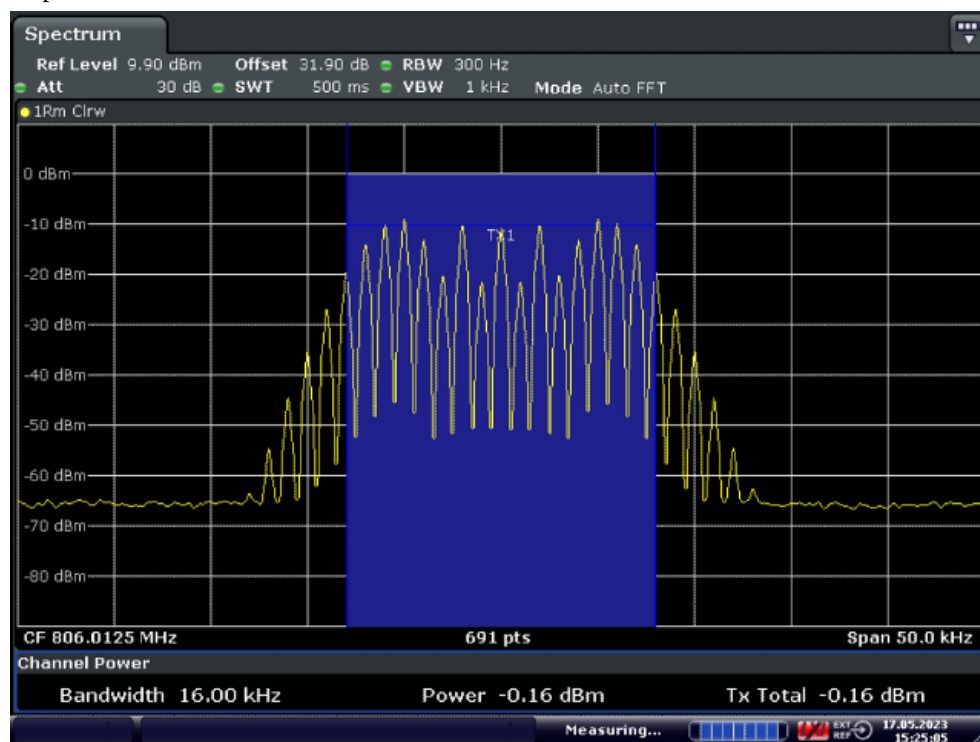
Middle Frequency: 856.0MHz



Date: 17.MAY.2023 15:24:50

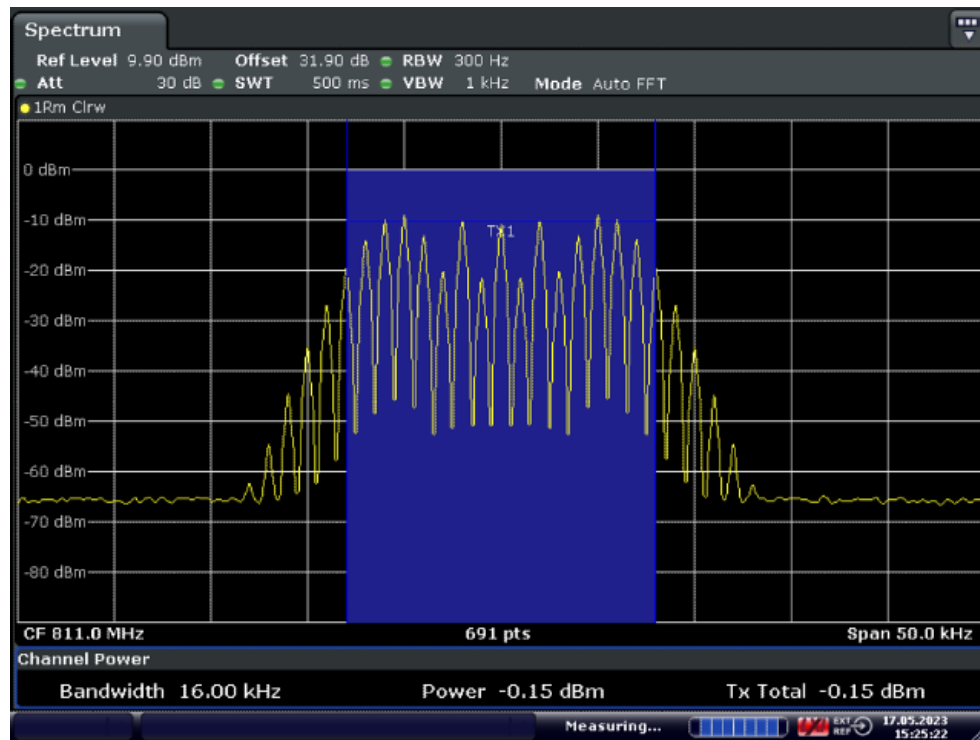
High Frequency: 860.9875MHz

11.2.3.2.4.2. Uplink



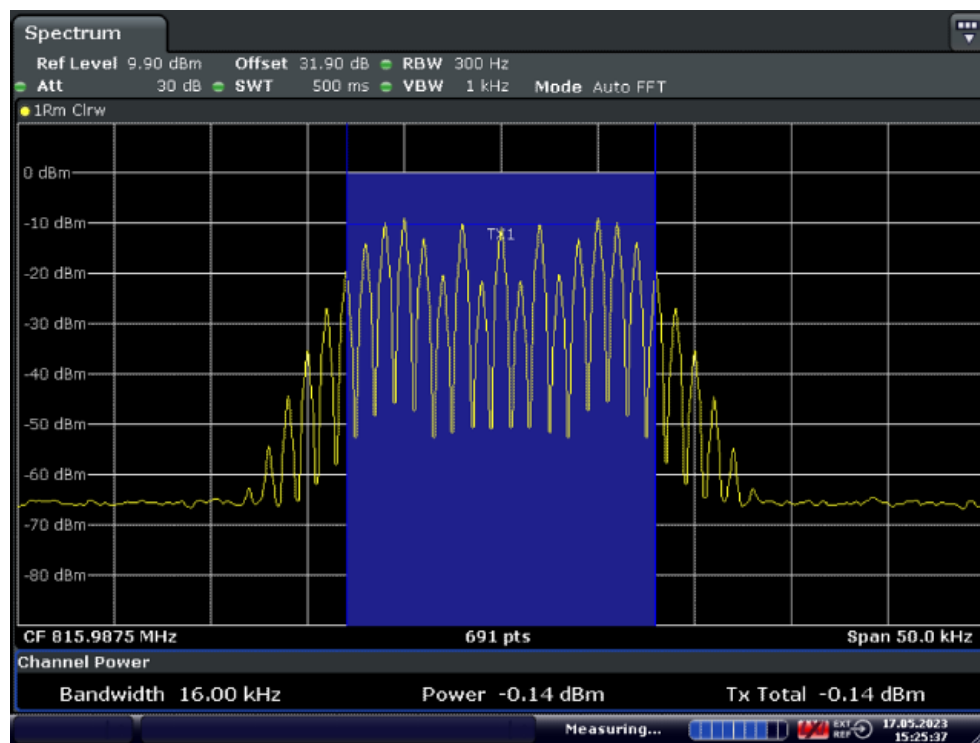
Date: 17.MAY.2023 15:25:06

Low Frequency: 806.0125MHz



Date: 17.MAY.2023 15:25:22

Middle Frequency: 811.0MHz

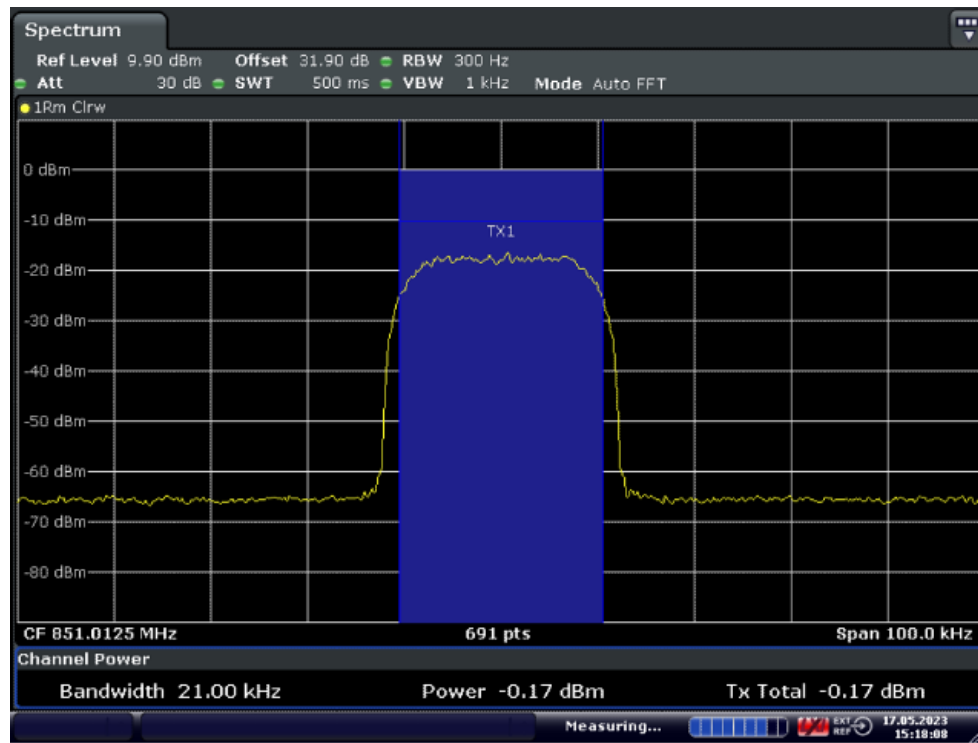


Date: 17.MAY.2023 15:25:38

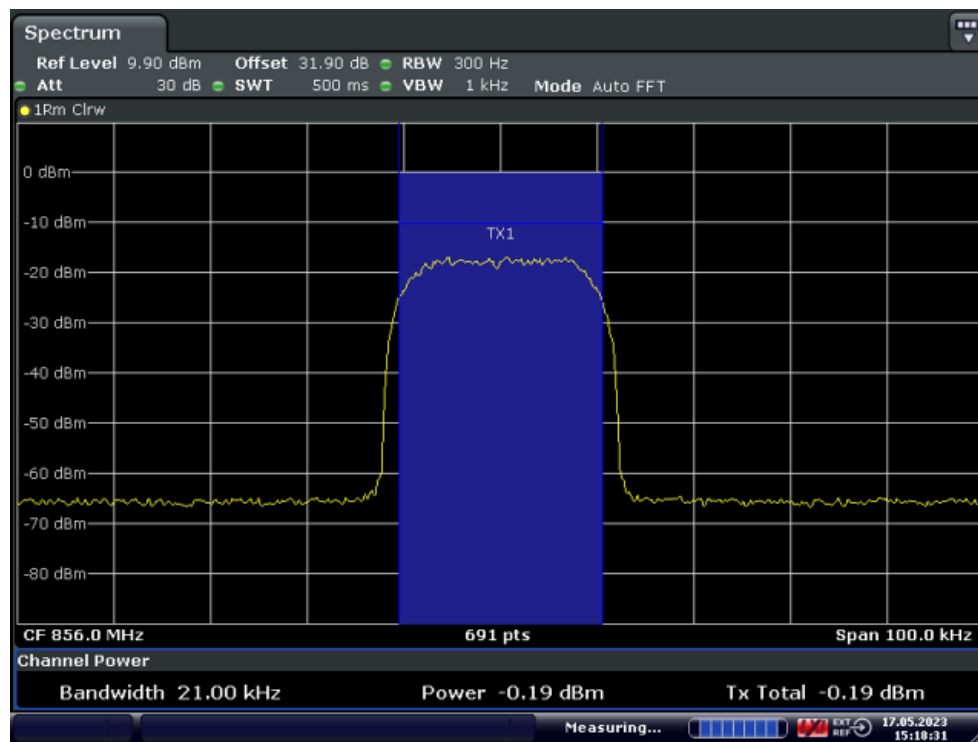
High Frequency: 815.9875MHz

11.2.3.2.5. Tetra

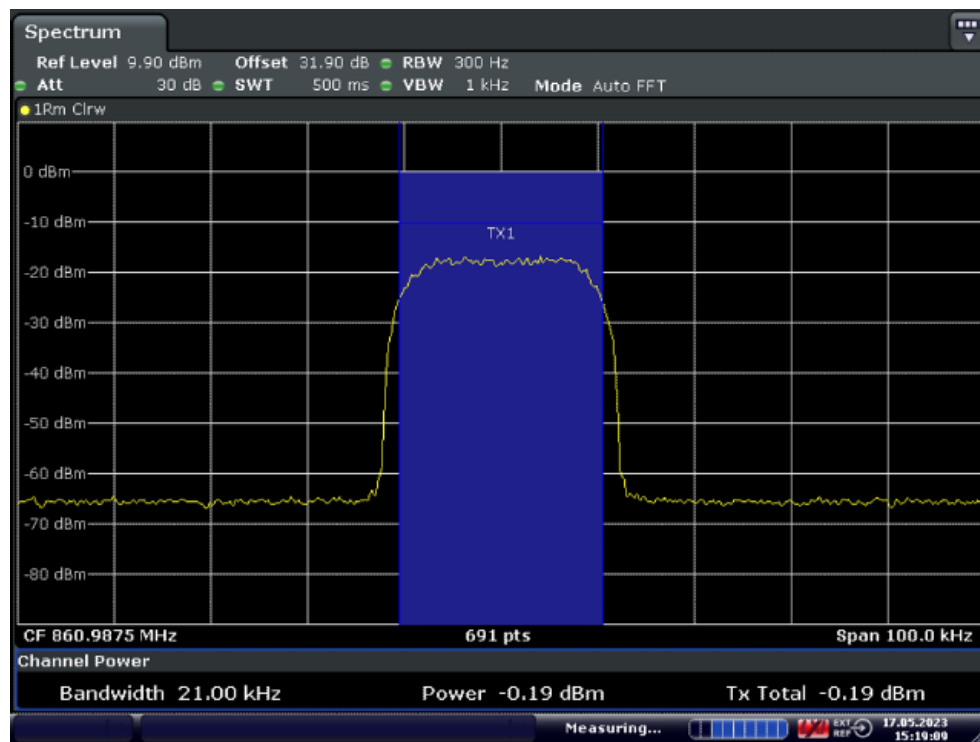
11.2.3.2.5.1. Downlink



Low Frequency: 851.0125MHz



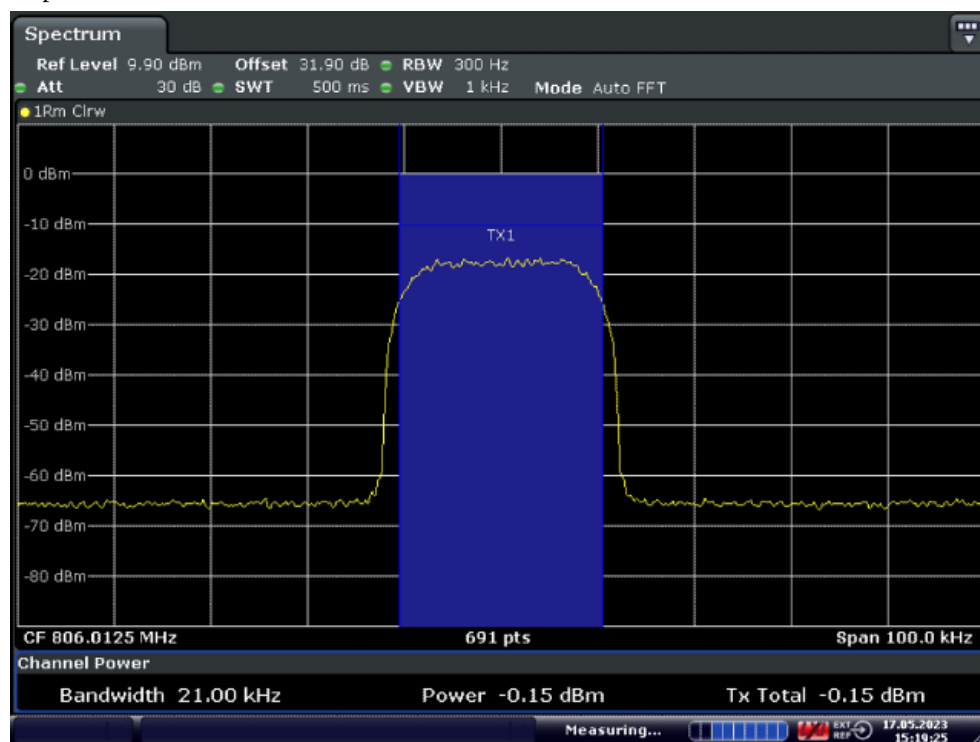
Middle Frequency: 856.0MHz



Date: 17.MAY.2023 15:19:09

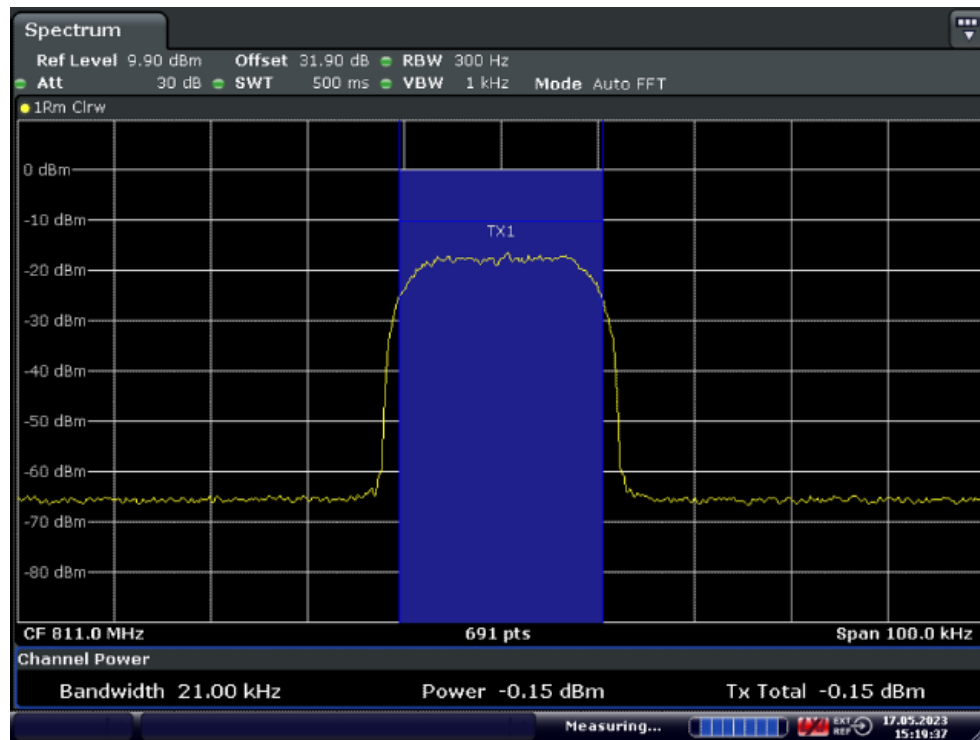
High Frequency: 860.9875MHz

11.2.3.2.5.2. Uplink



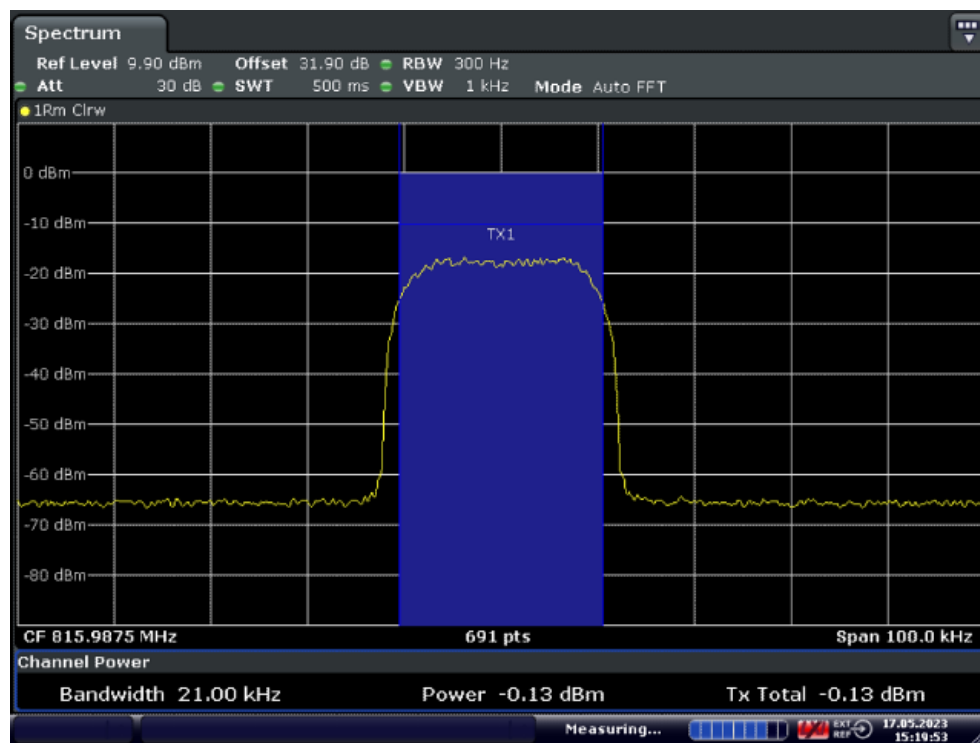
Date: 17.MAY.2023 15:19:25

Low Frequency: 806.0125MHz



Date: 17.MAY.2023 15:19:37

Middle Frequency: 811.0MHz



Date: 17.MAY.2023 15:19:53

High Frequency: 815.9875MHz

11.3. AGC Threshold

Requirements: KDB 935210 D05 clause 4.2

Test Method: KDB 935210 D05 clause 3.2

11.3.1. Requirements

Testing at and above the AGC threshold will be required.⁶ The AGC threshold shall be determined by applying the procedure of 3.2, but with the signal generator configured to produce a test signal defined in Table 1, a CW input signal, or a digitally modulated signal, consistent with the discussion about signal types in 4.1.

11.3.2. Test configuration

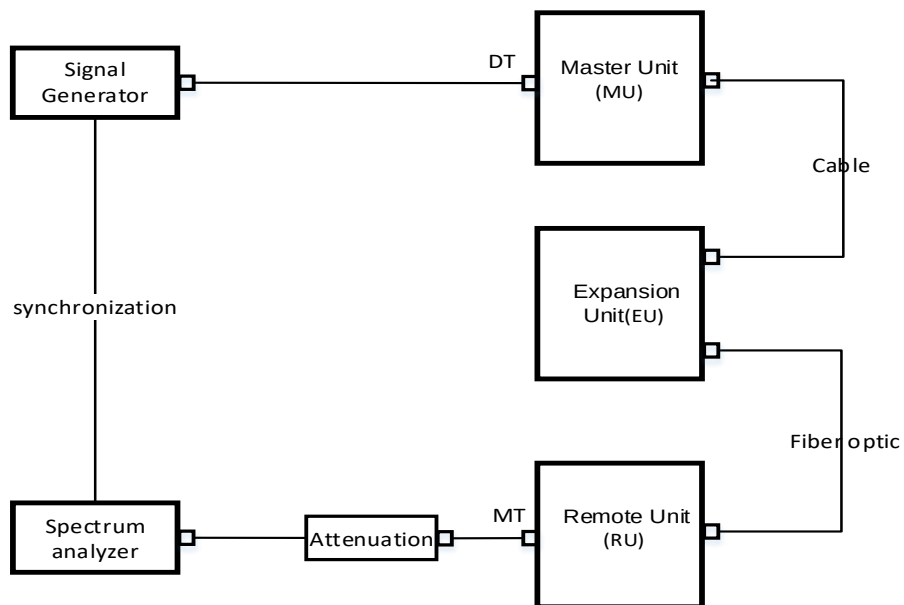


Figure 11.3-1 Downlink connection diagram

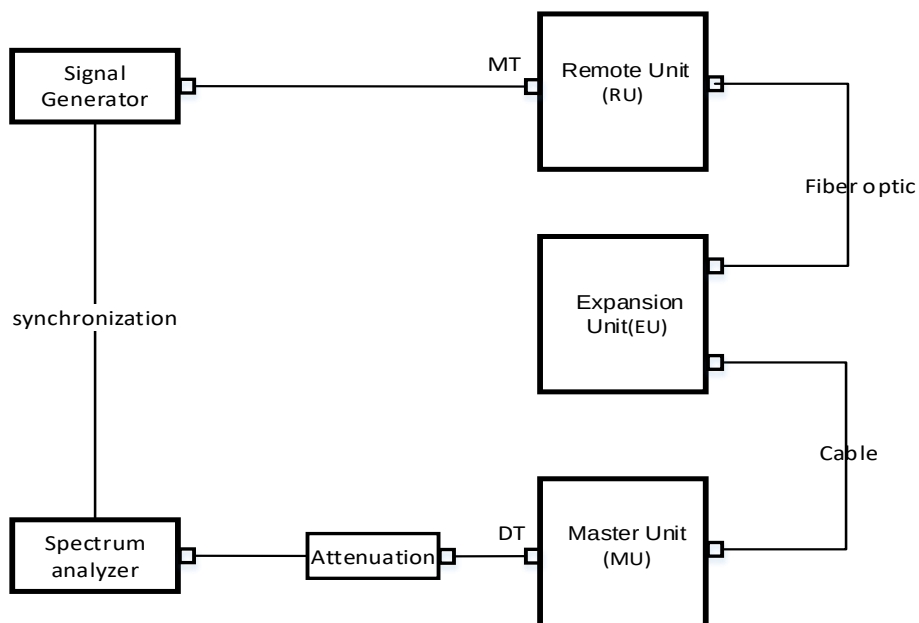


Figure 11.3-2 Uplink connection diagram

11.3.3. Test procedures

The AGC threshold is to be determined as follows.³

In the case of fiber-optic distribution systems, the RF input port of the equipment under test (EUT) refers to the RF input of the supporting equipment RF to optical convertor; see also descriptions and diagrams for typical DAS booster systems in KDB Publication 935210 D02 [R7].

Devices intended to be directly connected to an RF source (donor port) only need to be evaluated for any over-the-air transmit paths.

- a) Connect a signal generator to the input of the EUT.
- b) Connect a spectrum analyzer or power meter to the output of the EUT using appropriate attenuation as necessary.
- c) The signal generator should initially be configured to produce either of the required test signals (i.e., broadband or narrowband).
- d) Set the signal generator frequency to the center frequency of the EUT operating band.
- e) While monitoring the output power of the EUT, measured using the methods of 3.5.3 or 3.5.4, increase the input level until a 1 dB increase in the input signal power no longer causes a 1 dB increase in the output signal power.
- f) Record this level as the AGC threshold level.
- g) Repeat the procedure with the remaining test signal.

----- The following blanks -----

11.3.4. Test results

Test Date (yy-mm-dd): 2023-05-20

Normal condition: Temp: 27.1℃, Humid: 54%, Atmospheric Pressure:101kpa

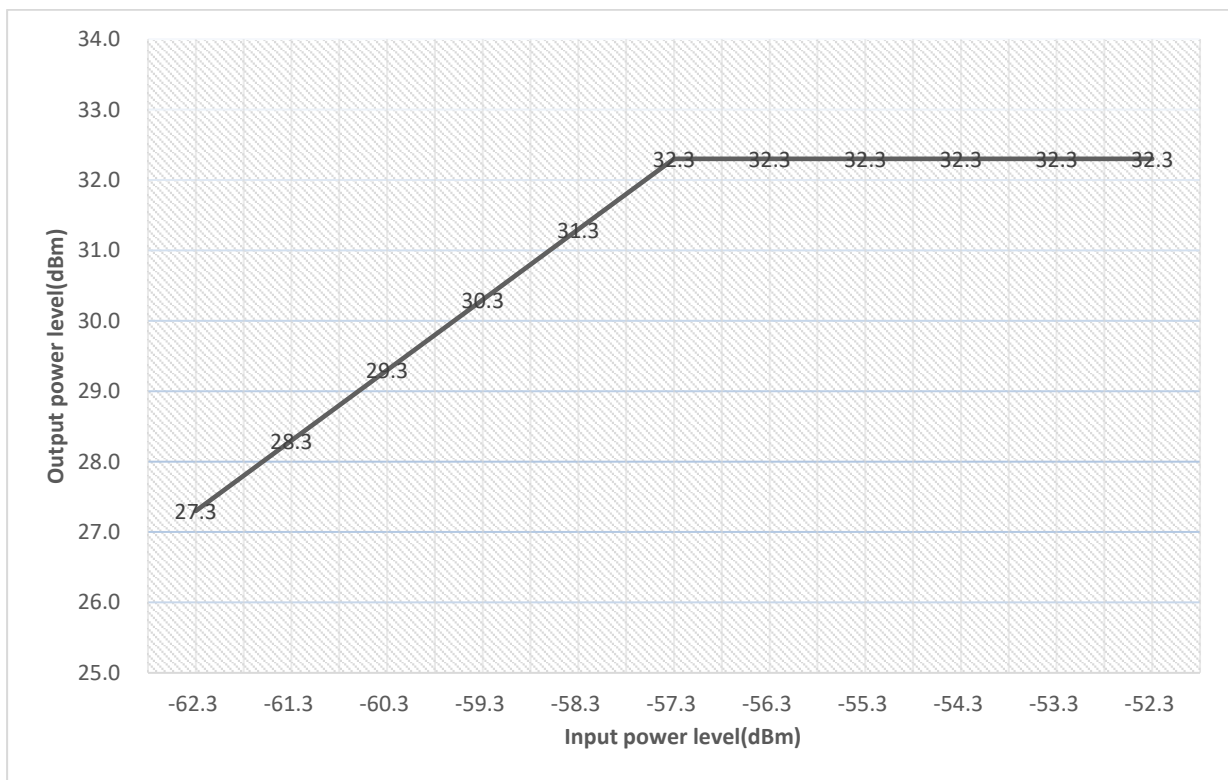
Supply Voltage: AC 110V, 50Hz

11.3.4.1. 700MHz Band

11.3.4.1.1. Downlink

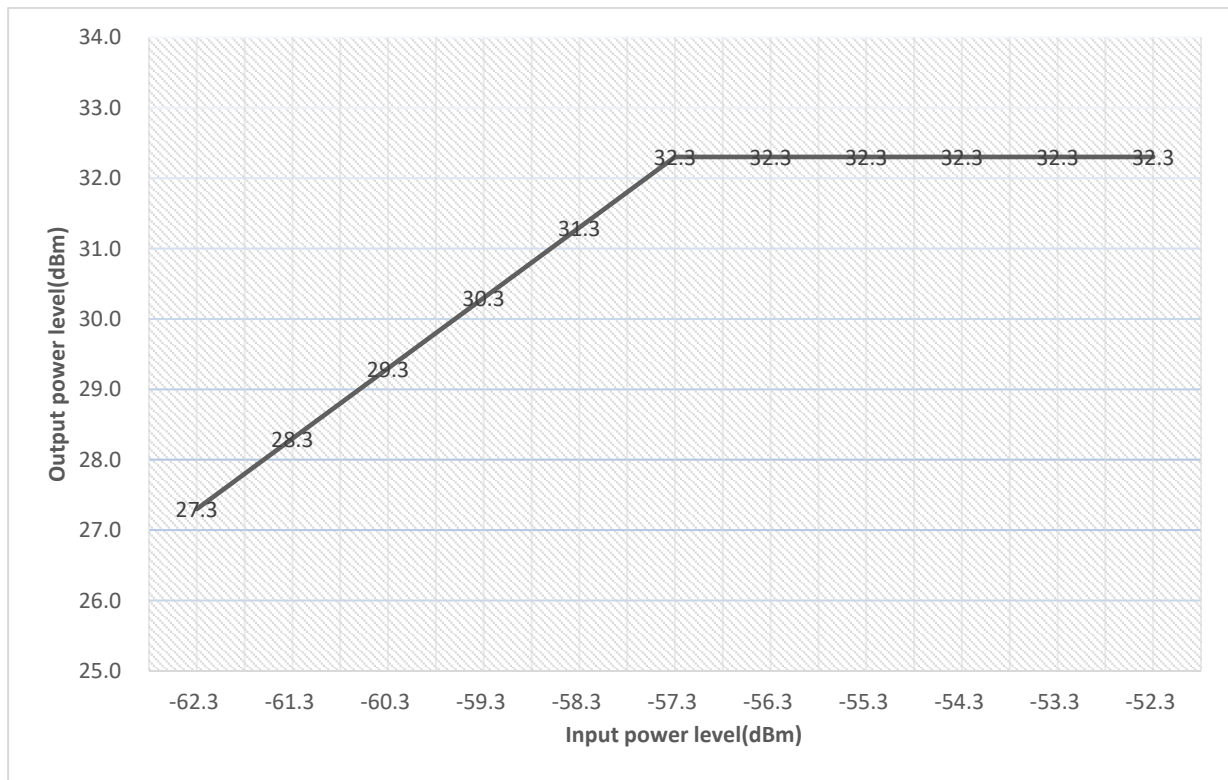
11.3.4.1.1.1. P25 Phase I(C4FM)

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
772.0MHz	-61.2	1.1	-62.3	27.3
	-60.2	1.1	-61.3	28.3
	-59.2	1.1	-60.3	29.3
	-58.2	1.1	-59.3	30.3
	-57.2	1.1	-58.3	31.3
	-56.2	1.1	-57.3	32.3
	-55.2	1.1	-56.3	32.3
	-54.2	1.1	-55.3	32.3
	-53.2	1.1	-54.3	32.3
	-52.2	1.1	-53.3	32.3
	-51.2	1.1	-52.3	32.3



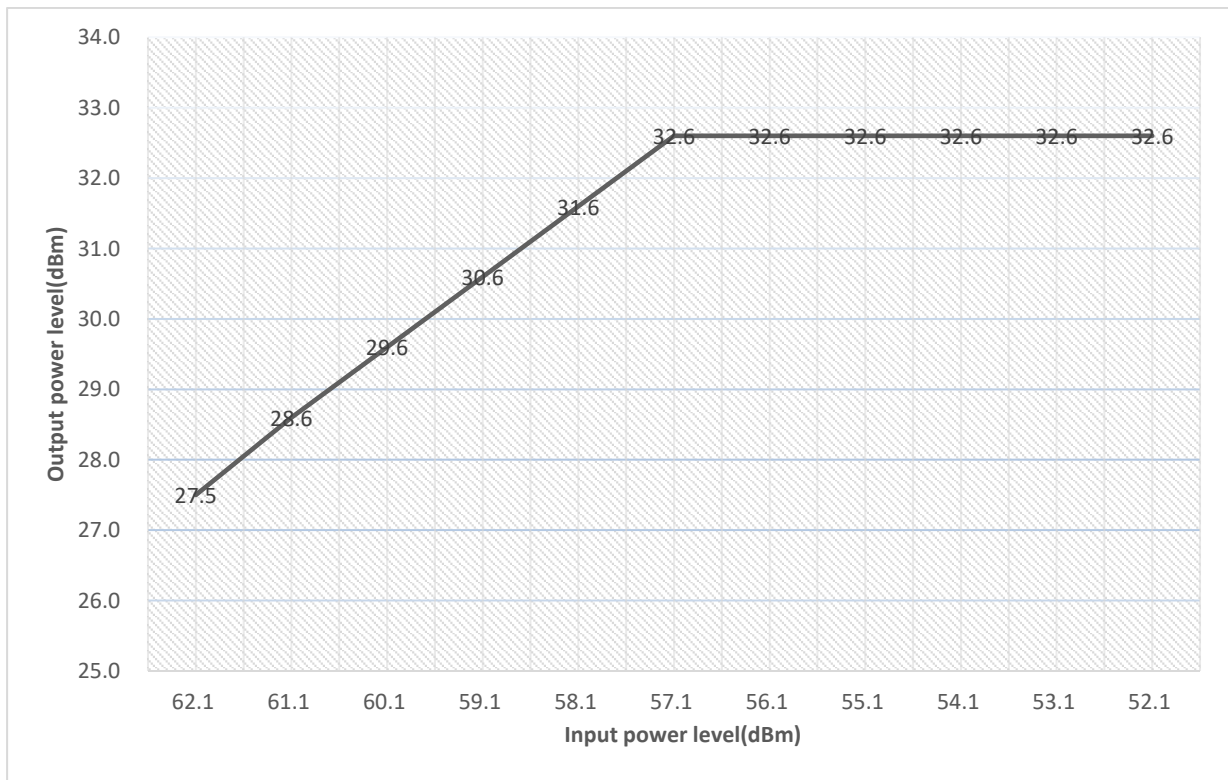
11.3.4.1.1.2. P25 Phase II(H-DQPSK)

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
772.0MHz	-61.2	1.1	-62.3	27.3
	-60.2	1.1	-61.3	28.3
	-59.2	1.1	-60.3	29.3
	-58.2	1.1	-59.3	30.3
	-57.2	1.1	-58.3	31.3
	-56.2	1.1	-57.3	32.3
	-55.2	1.1	-56.3	32.3
	-54.2	1.1	-55.3	32.3
	-53.2	1.1	-54.3	32.3
	-52.2	1.1	-53.3	32.3
	-51.2	1.1	-52.3	32.3



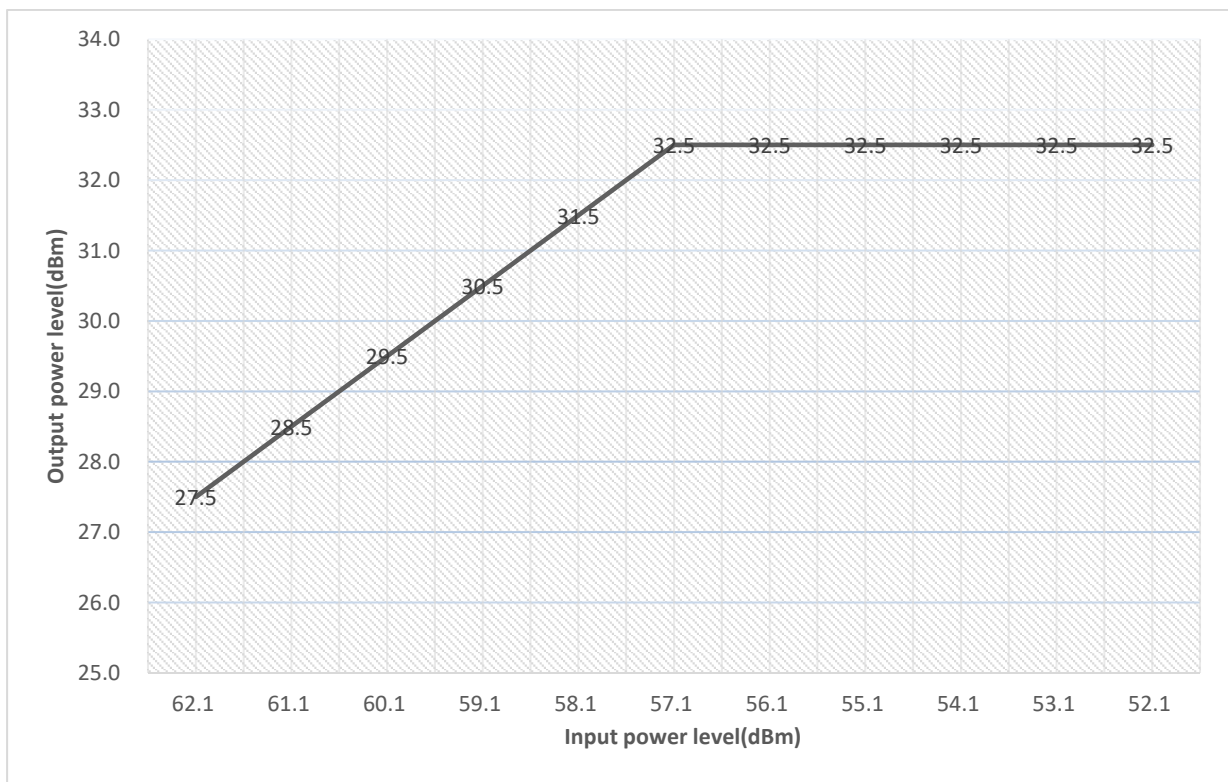
11.3.4.1.1.3. DMR

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
772.0MHz	-61.0	1.1	-62.1	27.5
	-60.0	1.1	-61.1	28.6
	-59.0	1.1	-60.1	29.6
	-58.0	1.1	-59.1	30.6
	-57.0	1.1	-58.1	31.6
	-56.0	1.1	-57.1	32.6
	-55.0	1.1	-56.1	32.6
	-54.0	1.1	-55.1	32.6
	-53.0	1.1	-54.1	32.6
	-52.0	1.1	-53.1	32.6
	-51.0	1.1	-52.1	32.6



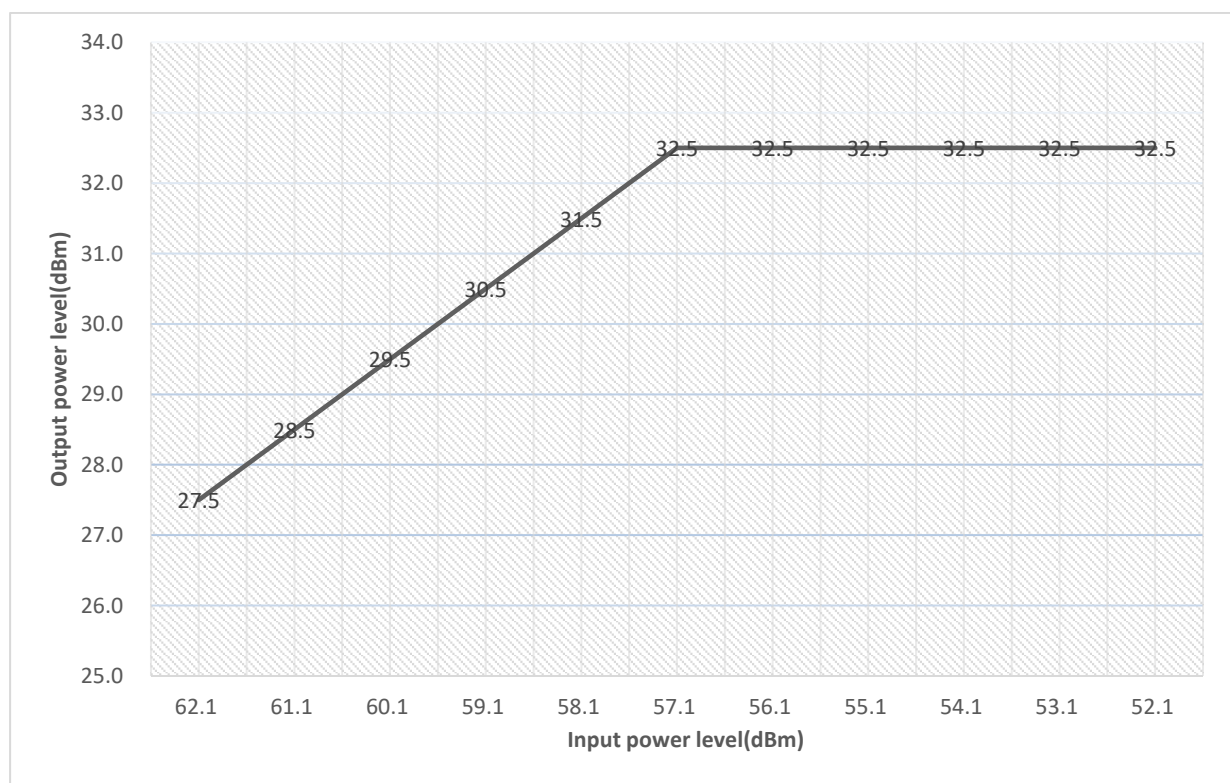
11.3.4.1.1.4. Analog FM

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
772.0MHz	-61.0	1.1	-62.1	27.5
	-60.0	1.1	-61.1	28.5
	-59.0	1.1	-60.1	29.5
	-58.0	1.1	-59.1	30.5
	-57.0	1.1	-58.1	31.5
	-56.0	1.1	-57.1	32.5
	-55.0	1.1	-56.1	32.5
	-54.0	1.1	-55.1	32.5
	-53.0	1.1	-54.1	32.5
	-52.0	1.1	-53.1	32.5
	-51.0	1.1	-52.1	32.5



11.3.4.1.1.5. Tetra

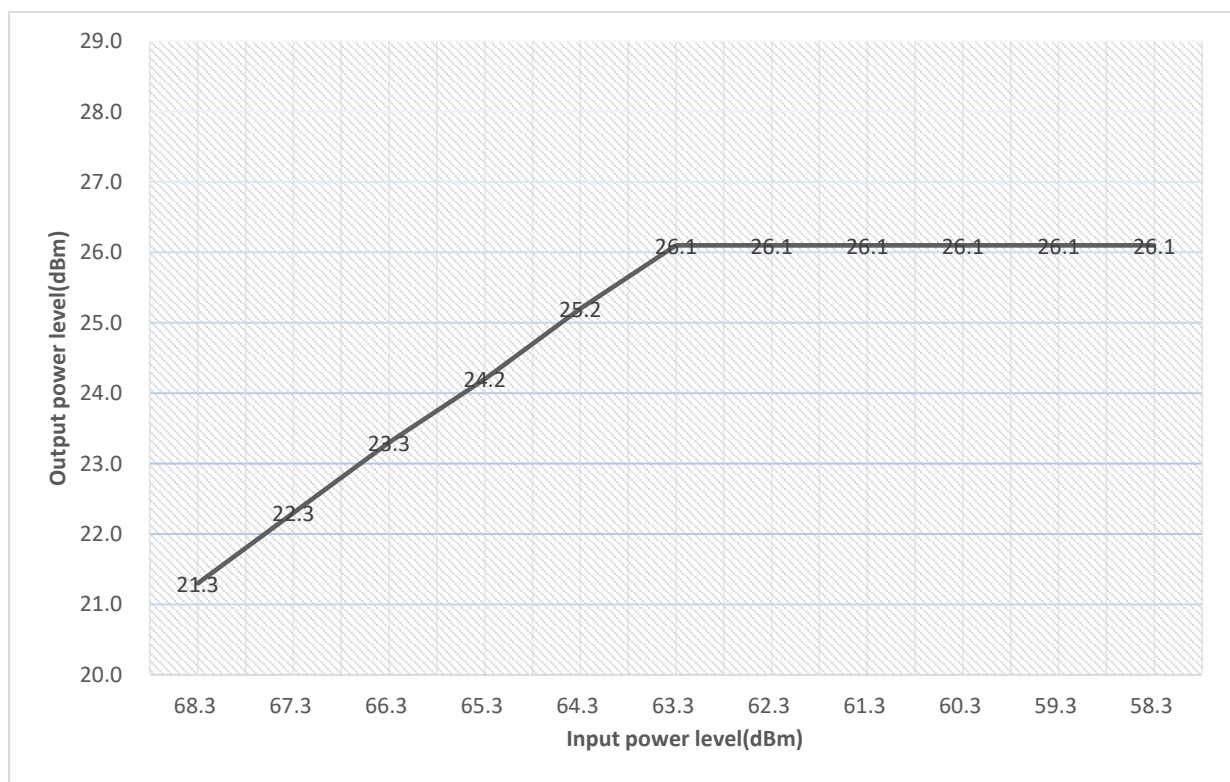
Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
772.0MHz	-61.0	1.1	-62.1	27.5
	-60.0	1.1	-61.1	28.5
	-59.0	1.1	-60.1	29.5
	-58.0	1.1	-59.1	30.5
	-57.0	1.1	-58.1	31.5
	-56.0	1.1	-57.1	32.5
	-55.0	1.1	-56.1	32.5
	-54.0	1.1	-55.1	32.5
	-53.0	1.1	-54.1	32.5
	-52.0	1.1	-53.1	32.5
	-51.0	1.1	-52.1	32.5



11.3.4.1.2. Uplink

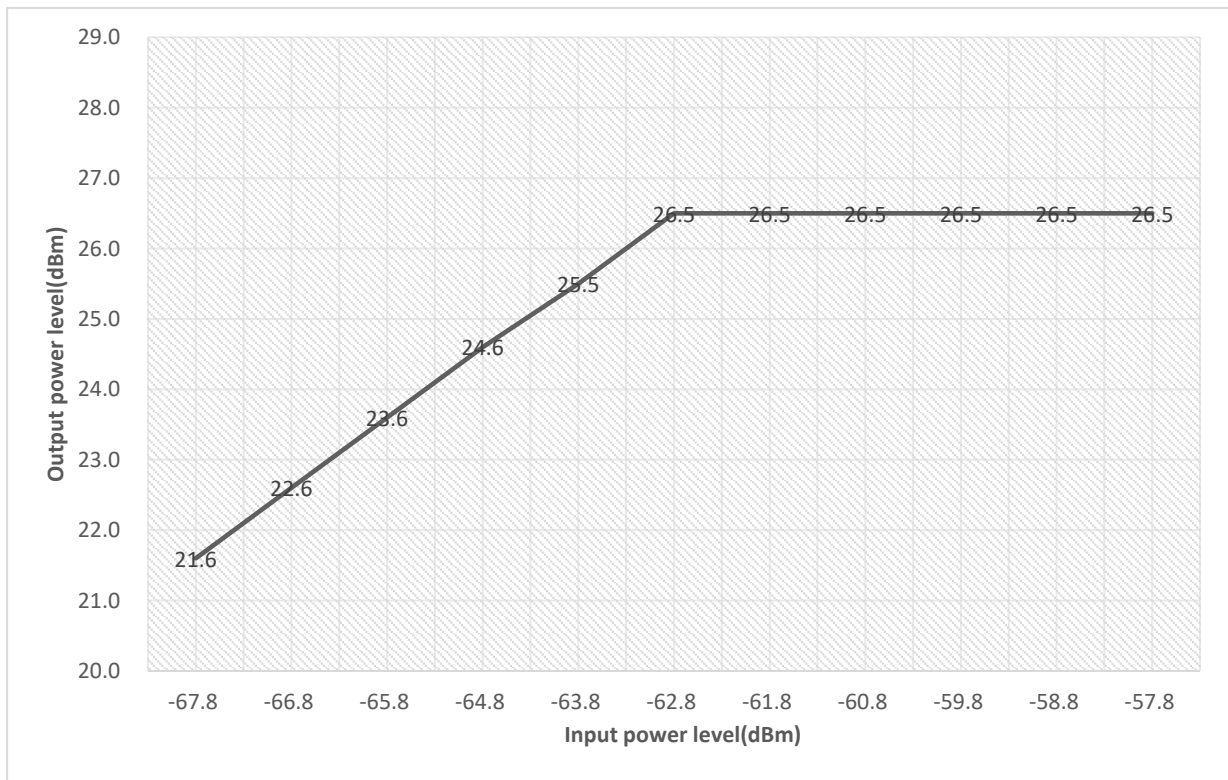
11.3.4.1.2.1. P25 Phase I(C4FM)

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
802.0MHz	-67.2	1.1	-68.3	21.3
	-66.2	1.1	-67.3	22.3
	-65.2	1.1	-66.3	23.3
	-64.2	1.1	-65.3	24.2
	-63.2	1.1	-64.3	25.2
	-62.2	1.1	-63.3	26.1
	-61.2	1.1	-62.3	26.1
	-60.2	1.1	-61.3	26.1
	-59.2	1.1	-60.3	26.1
	-58.2	1.1	-59.3	26.1
	-57.2	1.1	-58.3	26.1



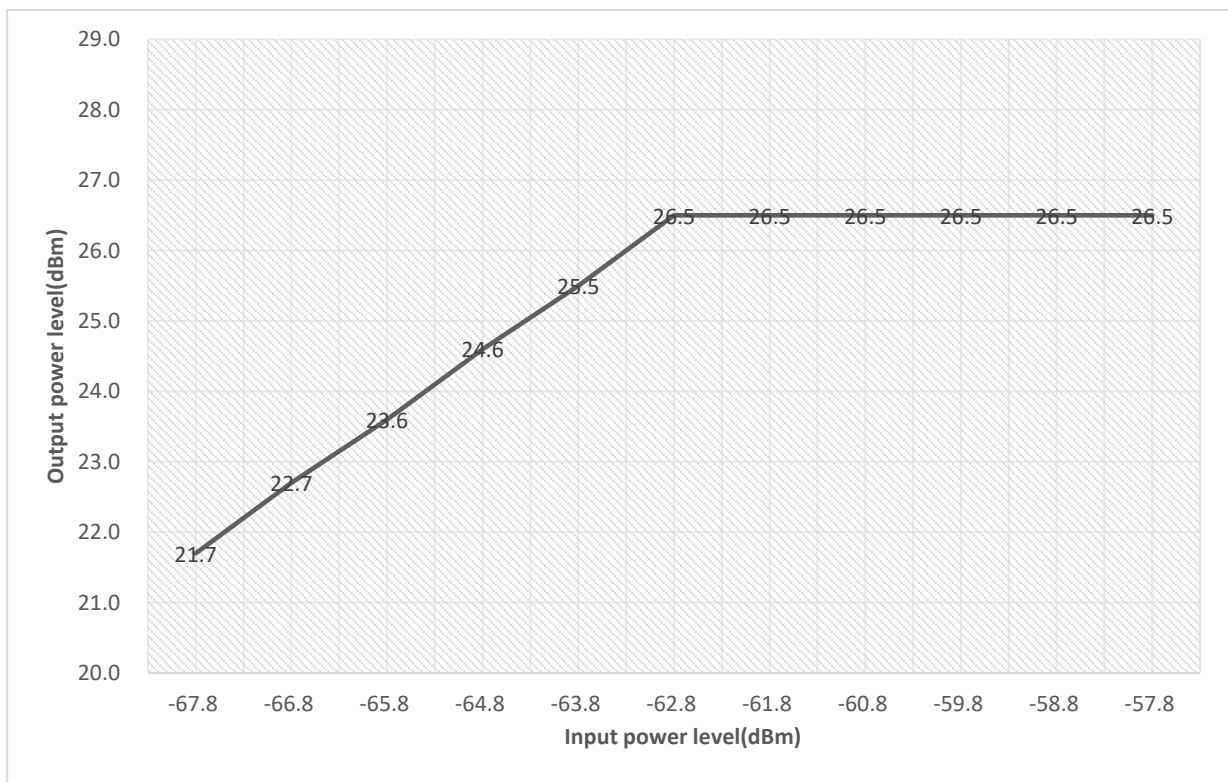
11.3.4.1.2.2. P25 Phase II(H-DQPSK)

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
802.0MHz	-66.7	1.1	-67.8	21.6
	-65.7	1.1	-66.8	22.6
	-64.7	1.1	-65.8	23.6
	-63.7	1.1	-64.8	24.6
	-62.7	1.1	-63.8	25.5
	-61.7	1.1	-62.8	26.5
	-60.7	1.1	-61.8	26.5
	-59.7	1.1	-60.8	26.5
	-58.7	1.1	-59.8	26.5
	-57.7	1.1	-58.8	26.5
	-56.7	1.1	-57.8	26.5



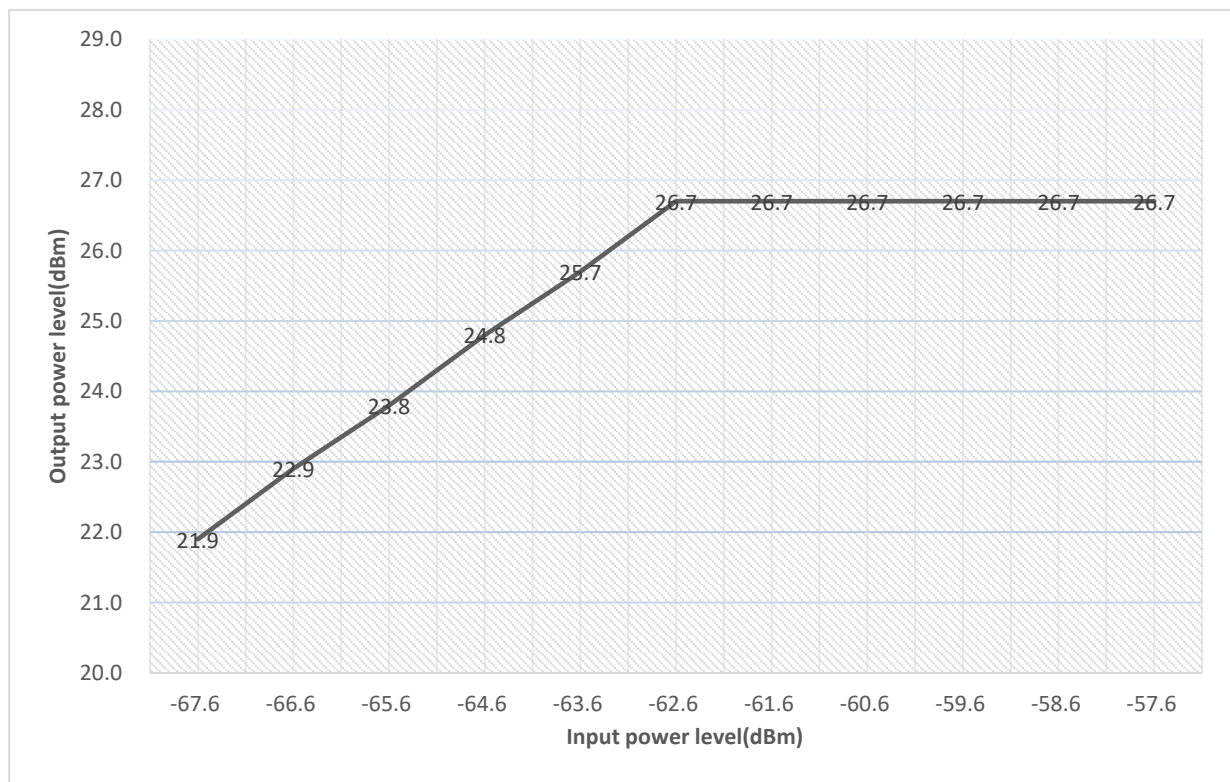
11.3.4.1.2.3. DMR

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
802.0MHz	-66.7	1.1	-67.8	21.7
	-65.7	1.1	-66.8	22.7
	-64.7	1.1	-65.8	23.6
	-63.7	1.1	-64.8	24.6
	-62.7	1.1	-63.8	25.5
	-61.7	1.1	-62.8	26.5
	-60.7	1.1	-61.8	26.5
	-59.7	1.1	-60.8	26.5
	-58.7	1.1	-59.8	26.5
	-57.7	1.1	-58.8	26.5
	-56.7	1.1	-57.8	26.5



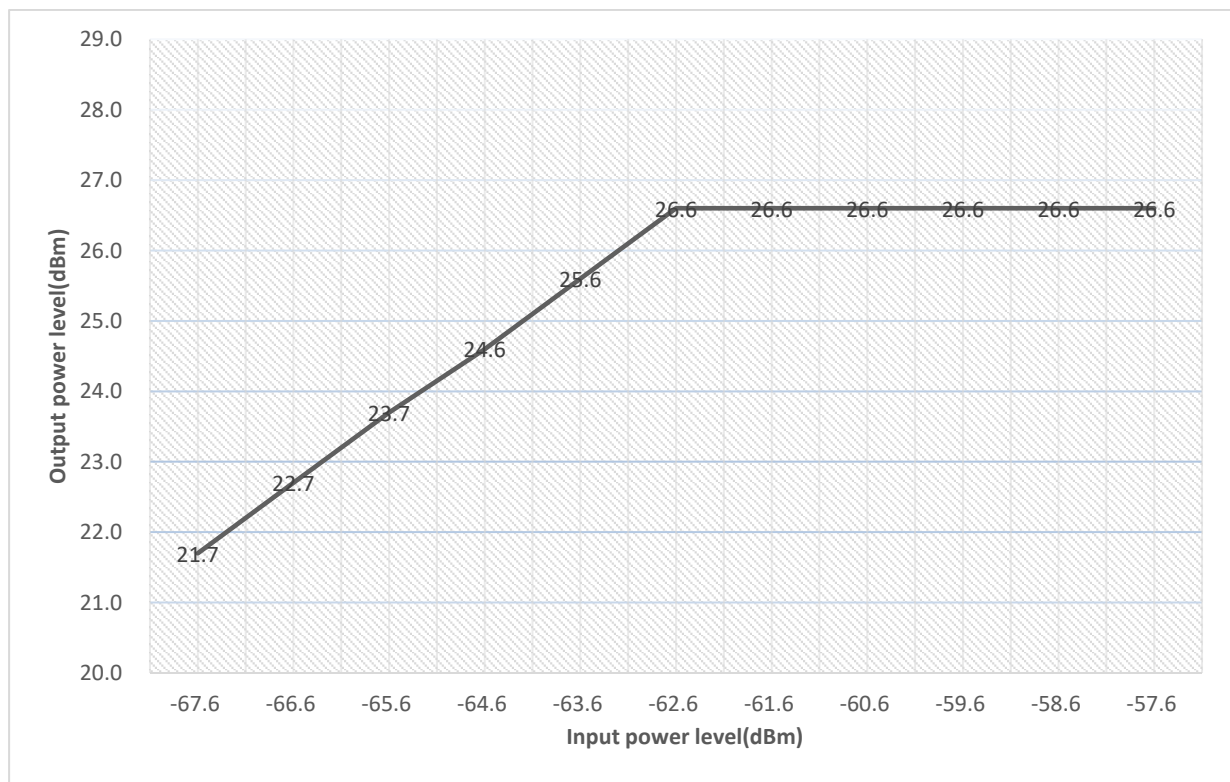
11.3.4.1.2.4. Analog FM

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
802.0MHz	-66.5	1.1	-67.6	21.9
	-65.5	1.1	-66.6	22.9
	-64.5	1.1	-65.6	23.8
	-63.5	1.1	-64.6	24.8
	-62.5	1.1	-63.6	25.7
	-61.5	1.1	-62.6	26.7
	-60.5	1.1	-61.6	26.7
	-59.5	1.1	-60.6	26.7
	-58.5	1.1	-59.6	26.7
	-57.5	1.1	-58.6	26.7
	-56.5	1.1	-57.6	26.7



11.3.4.1.2.5. Tetra

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
802.0MHz	-66.5	1.1	-67.6	21.7
	-65.5	1.1	-66.6	22.7
	-64.5	1.1	-65.6	23.7
	-63.5	1.1	-64.6	24.6
	-62.5	1.1	-63.6	25.6
	-61.5	1.1	-62.6	26.6
	-60.5	1.1	-61.6	26.6
	-59.5	1.1	-60.6	26.6
	-58.5	1.1	-59.6	26.6
	-57.5	1.1	-58.6	26.6
	-56.5	1.1	-57.6	26.6

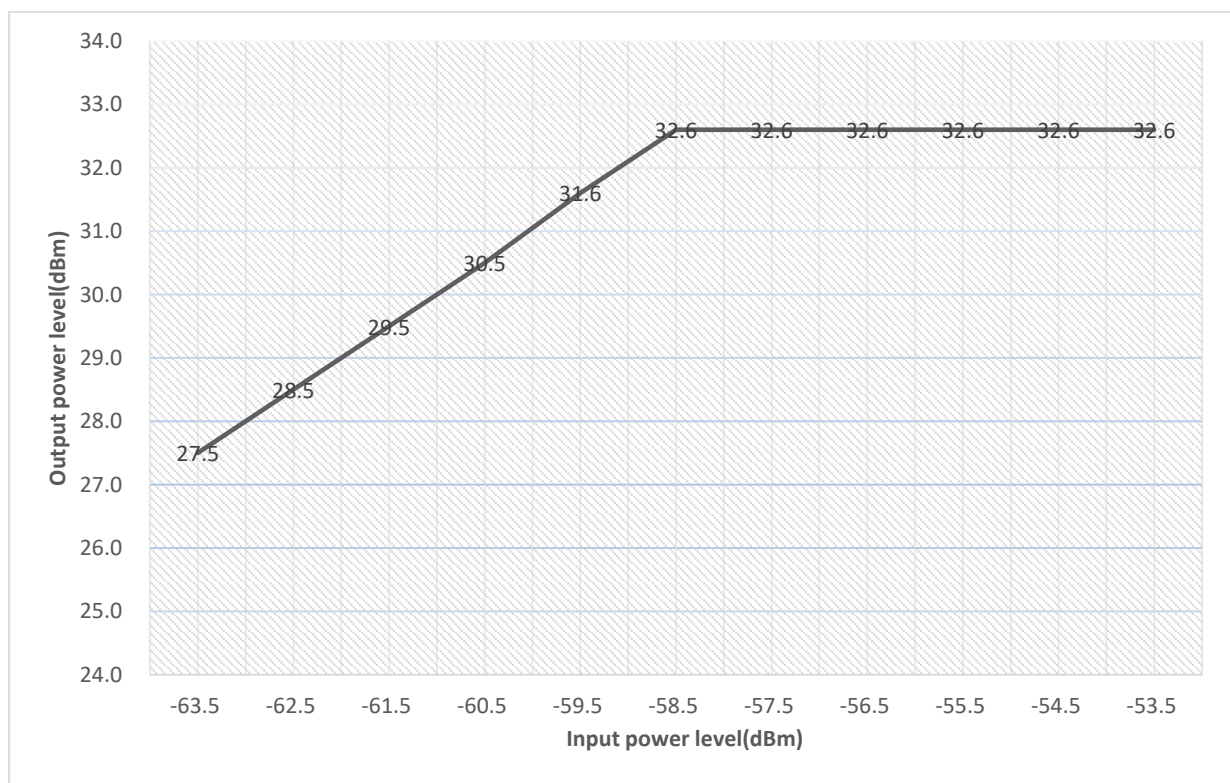


11.3.4.2. 800MHz Band

11.3.4.2.1. Downlink

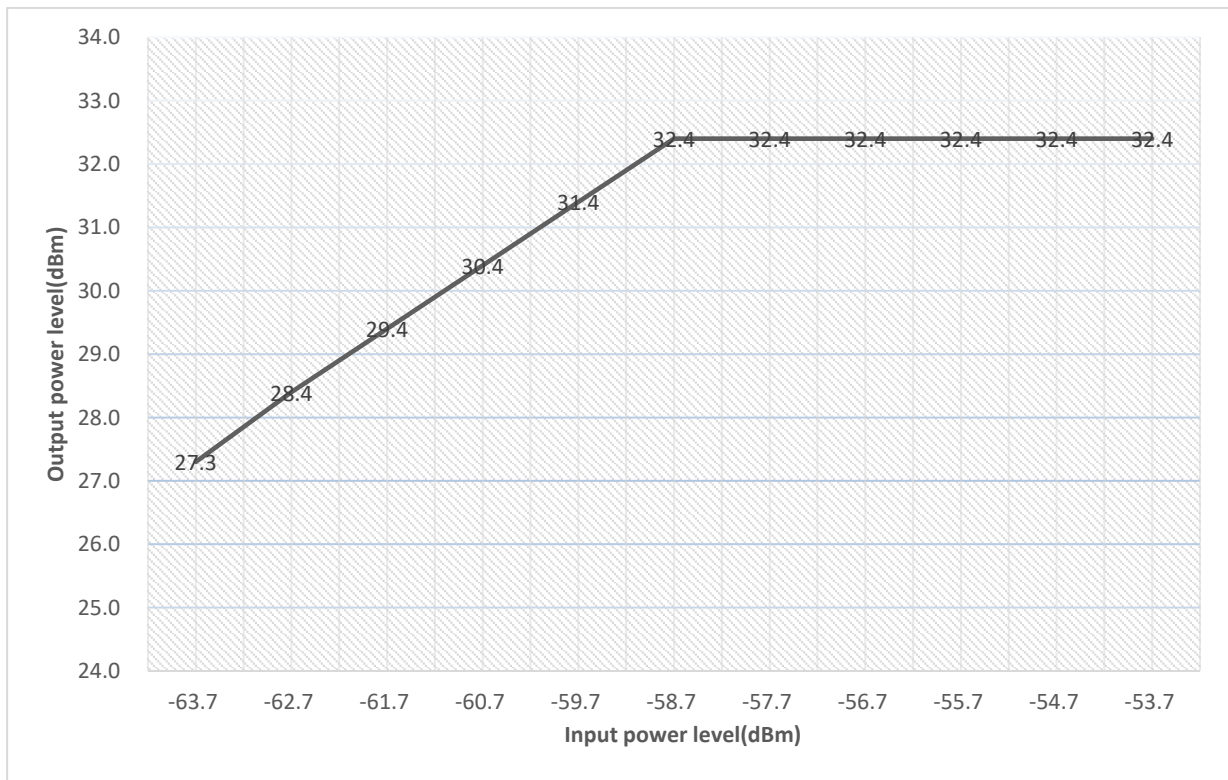
11.3.4.2.1.1. P25 Phase I(C4FM)

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
856.0MHz	-62.4	1.1	-63.5	27.5
	-61.4	1.1	-62.5	28.5
	-60.4	1.1	-61.5	29.5
	-59.4	1.1	-60.5	30.5
	-58.4	1.1	-59.5	31.6
	-57.4	1.1	-58.5	32.6
	-56.4	1.1	-57.5	32.6
	-55.4	1.1	-56.5	32.6
	-54.4	1.1	-55.5	32.6
	-53.4	1.1	-54.5	32.6
	-52.4	1.1	-53.5	32.6



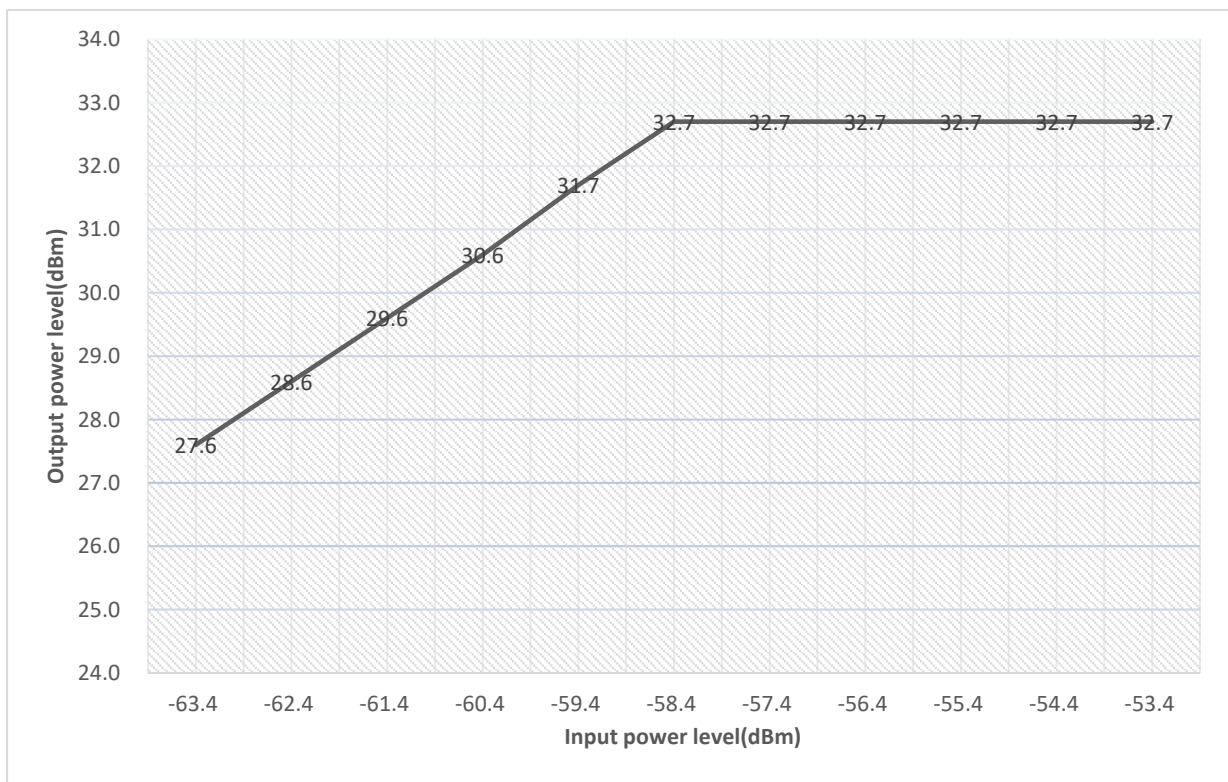
11.3.4.2.1.2. P25 Phase II(H-DQPSK)

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
856.0MHz	-62.6	1.1	-63.7	27.3
	-61.6	1.1	-62.7	28.4
	-60.6	1.1	-61.7	29.4
	-59.6	1.1	-60.7	30.4
	-58.6	1.1	-59.7	31.4
	-57.6	1.1	-58.7	32.4
	-56.6	1.1	-57.7	32.4
	-55.6	1.1	-56.7	32.4
	-54.6	1.1	-55.7	32.4
	-53.6	1.1	-54.7	32.4
	-52.6	1.1	-53.7	32.4



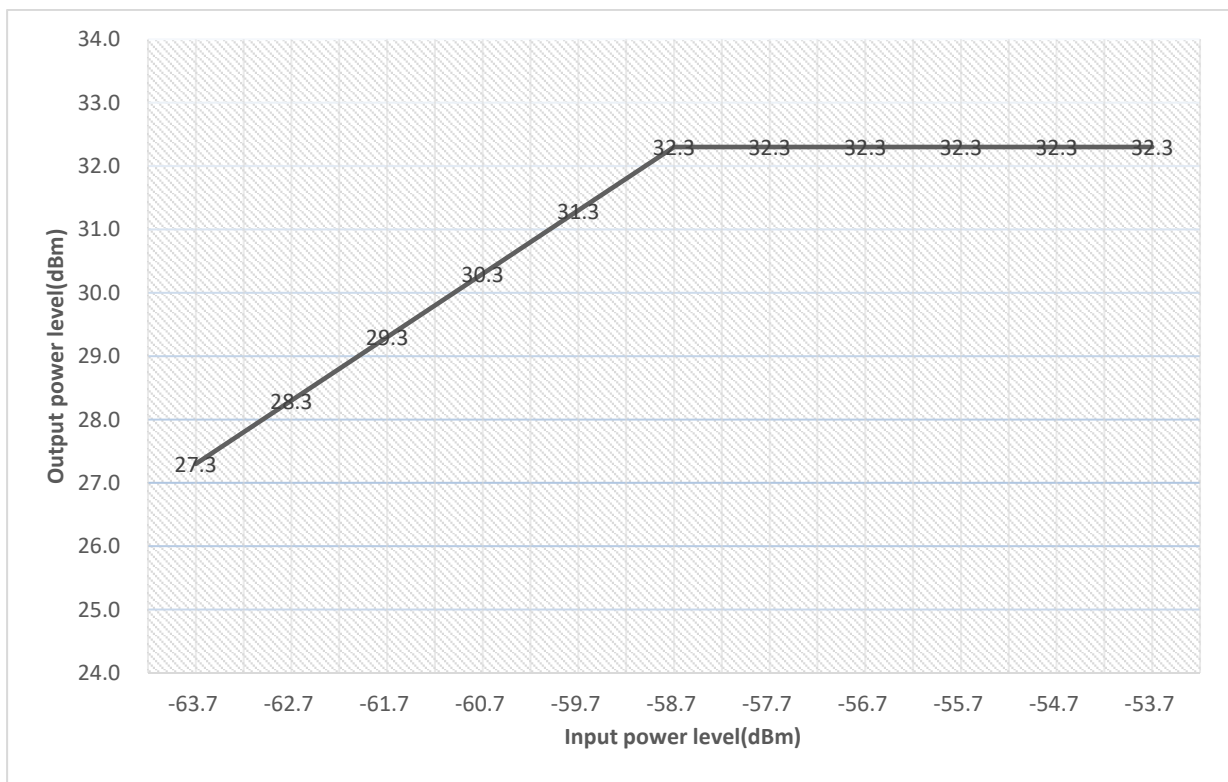
11.3.4.2.1.3. DMR

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
856.0MHz	-62.3	1.1	-63.4	27.6
	-61.3	1.1	-62.4	28.6
	-60.3	1.1	-61.4	29.6
	-59.3	1.1	-60.4	30.6
	-58.3	1.1	-59.4	31.7
	-57.3	1.1	-58.4	32.7
	-56.3	1.1	-57.4	32.7
	-55.3	1.1	-56.4	32.7
	-54.3	1.1	-55.4	32.7
	-53.3	1.1	-54.4	32.7
	-52.3	1.1	-53.4	32.7



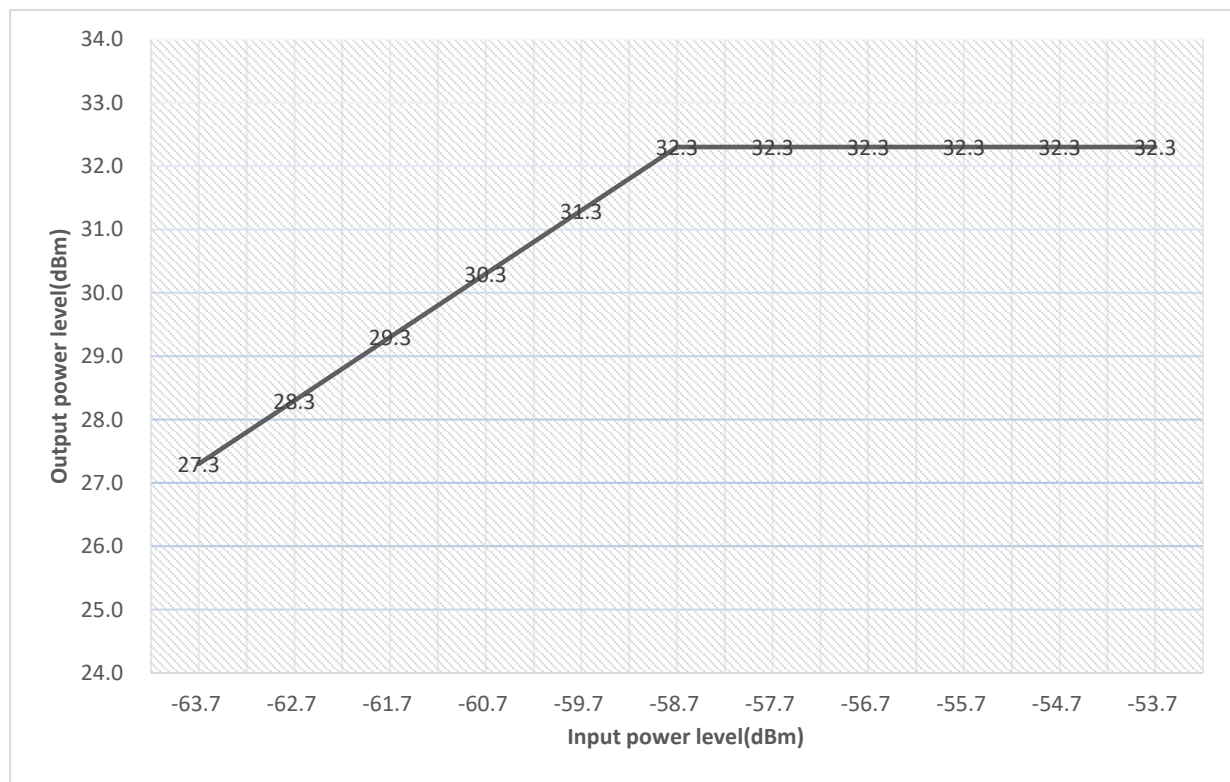
11.3.4.2.1.4. Analog FM

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
856.0MHz	-62.6	1.1	-63.7	27.3
	-61.6	1.1	-62.7	28.3
	-60.6	1.1	-61.7	29.3
	-59.6	1.1	-60.7	30.3
	-58.6	1.1	-59.7	31.3
	-57.6	1.1	-58.7	32.3
	-56.6	1.1	-57.7	32.3
	-55.6	1.1	-56.7	32.3
	-54.6	1.1	-55.7	32.3
	-53.6	1.1	-54.7	32.3
	-52.6	1.1	-53.7	32.3



11.3.4.2.1.5. Tetra

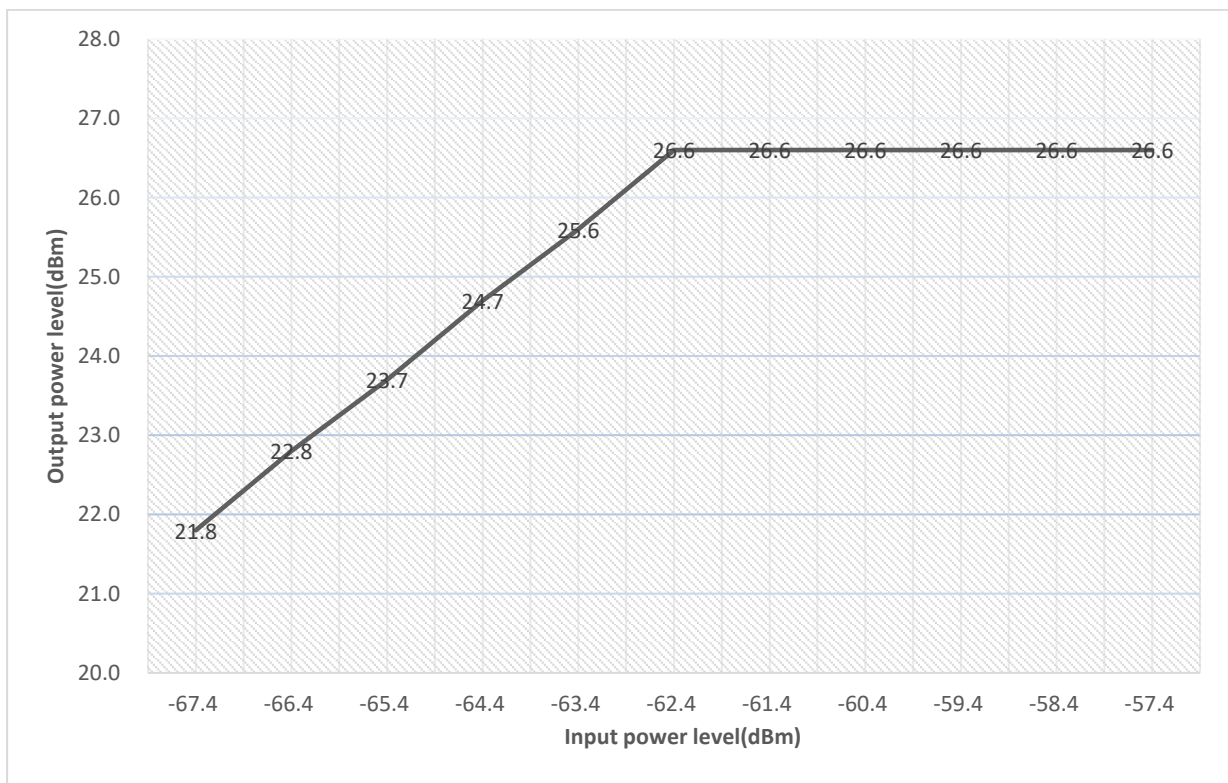
Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
856.0MHz	-62.6	1.1	-63.7	27.3
	-61.6	1.1	-62.7	28.3
	-60.6	1.1	-61.7	29.3
	-59.6	1.1	-60.7	30.3
	-58.6	1.1	-59.7	31.3
	-57.6	1.1	-58.7	32.3
	-56.6	1.1	-57.7	32.3
	-55.6	1.1	-56.7	32.3
	-54.6	1.1	-55.7	32.3
	-53.6	1.1	-54.7	32.3
	-52.6	1.1	-53.7	32.3



11.3.4.2.2. Uplink

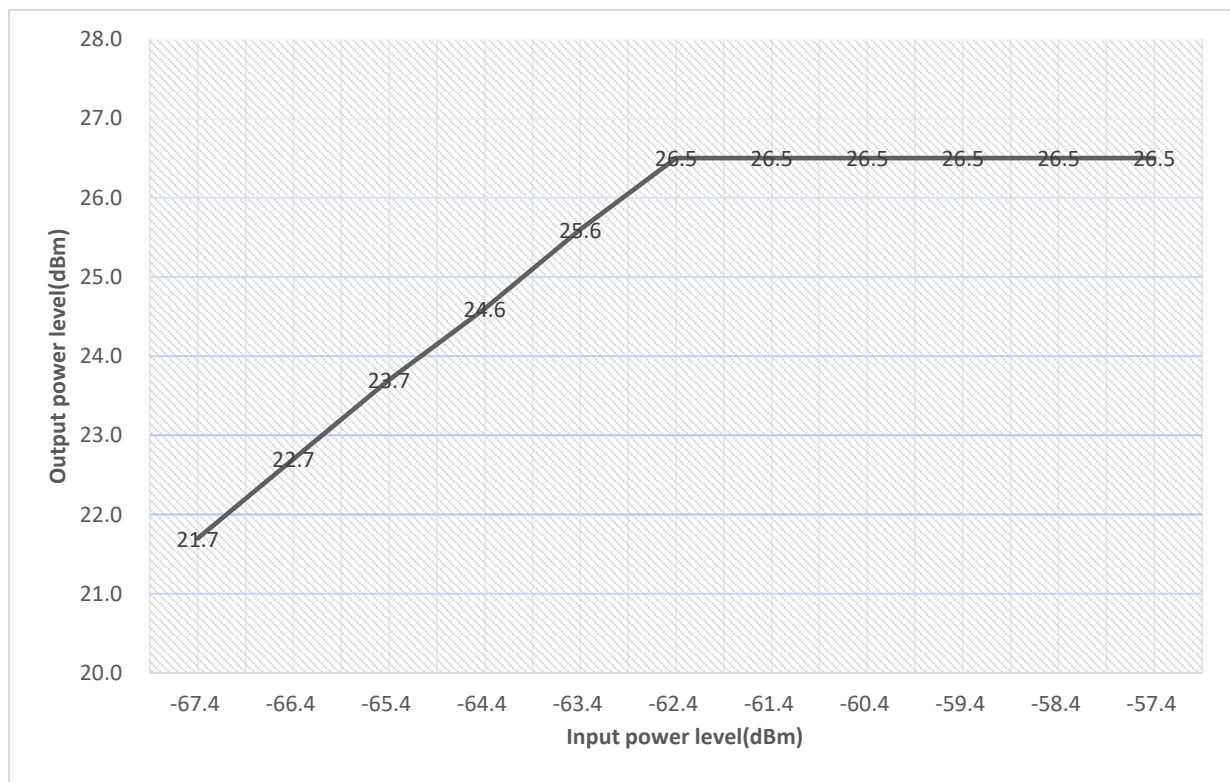
11.3.4.2.2.1. P25 Phase I(C4FM)

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
811.0MHz	-66.3	1.1	-67.4	21.8
	-65.3	1.1	-66.4	22.8
	-64.3	1.1	-65.4	23.7
	-63.3	1.1	-64.4	24.7
	-62.3	1.1	-63.4	25.6
	-61.3	1.1	-62.4	26.6
	-60.3	1.1	-61.4	26.6
	-59.3	1.1	-60.4	26.6
	-58.3	1.1	-59.4	26.6
	-57.3	1.1	-58.4	26.6
	-56.3	1.1	-57.4	26.6



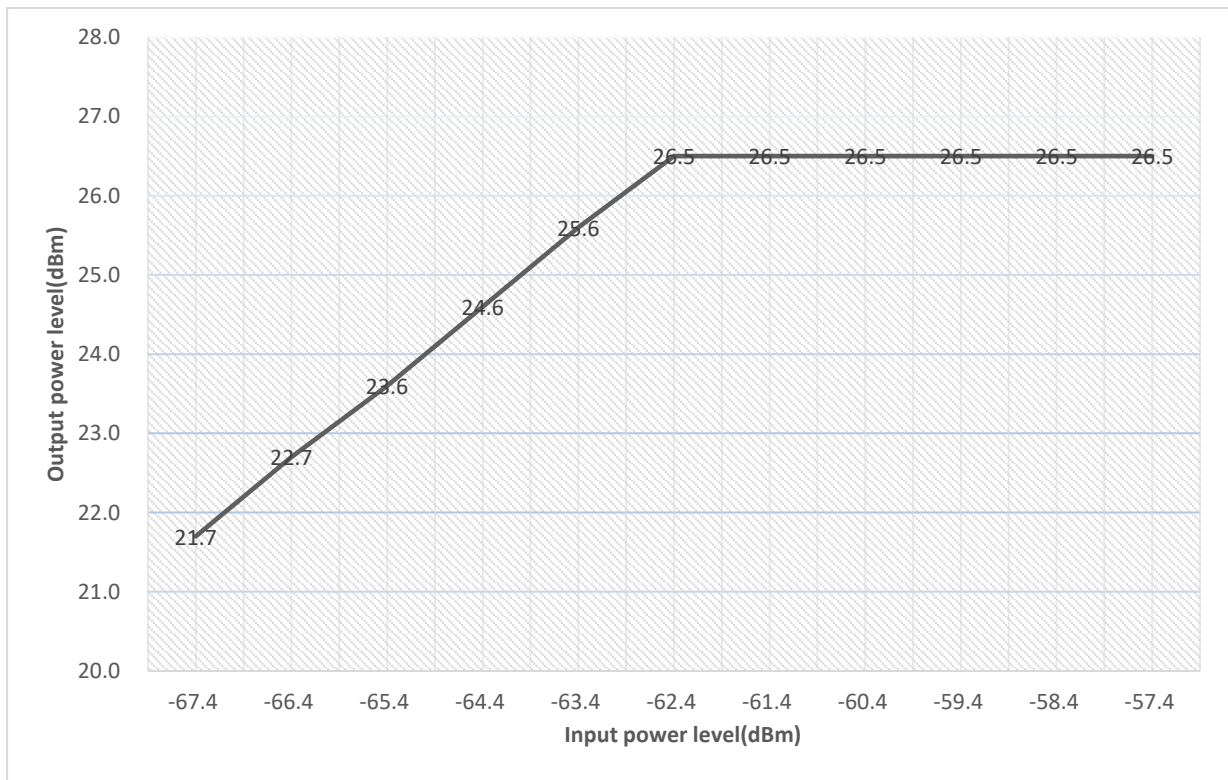
11.3.4.2.2.2. P25 Phase II(H-DQPSK)

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
811.0MHz	-66.3	1.1	-67.4	21.7
	-65.3	1.1	-66.4	22.7
	-64.3	1.1	-65.4	23.7
	-63.3	1.1	-64.4	24.6
	-62.3	1.1	-63.4	25.6
	-61.3	1.1	-62.4	26.5
	-60.3	1.1	-61.4	26.5
	-59.3	1.1	-60.4	26.5
	-58.3	1.1	-59.4	26.5
	-57.3	1.1	-58.4	26.5
	-56.3	1.1	-57.4	26.5



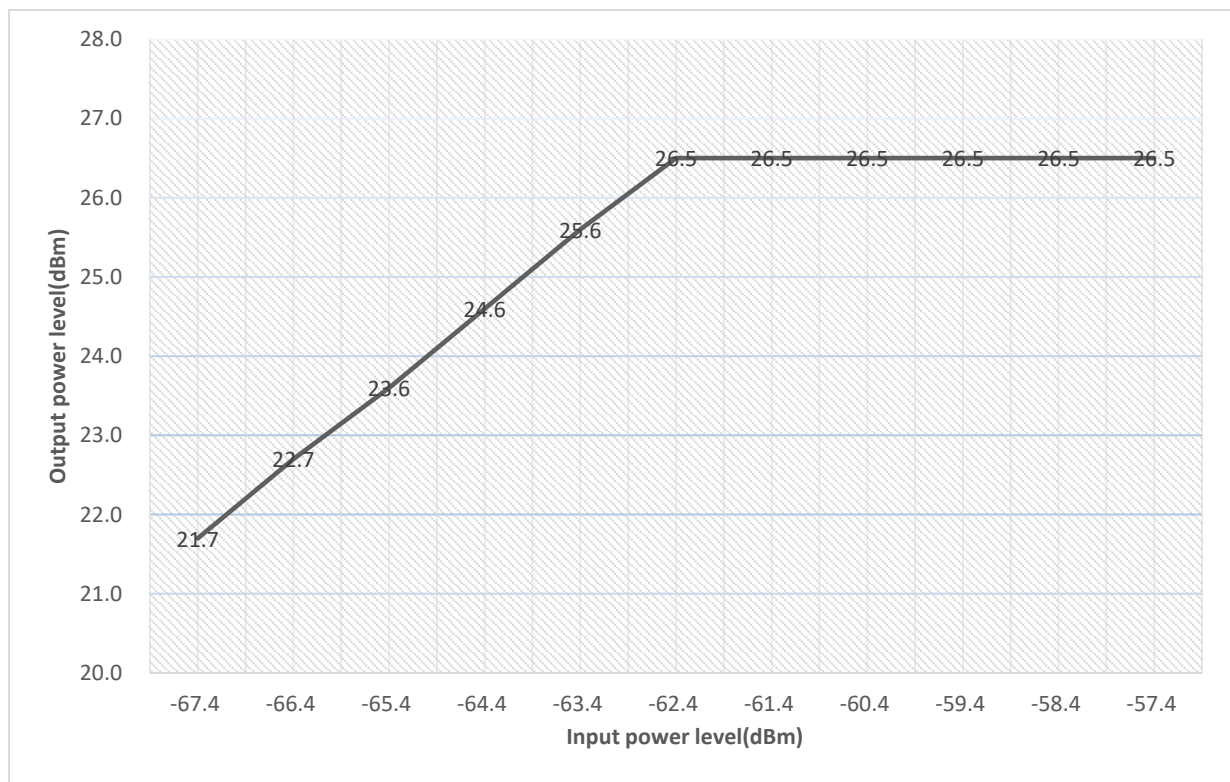
11.3.4.2.2.3. DMR

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
811.0MHz	-66.3	1.1	-67.4	21.7
	-65.3	1.1	-66.4	22.7
	-64.3	1.1	-65.4	23.6
	-63.3	1.1	-64.4	24.6
	-62.3	1.1	-63.4	25.6
	-61.3	1.1	-62.4	26.5
	-60.3	1.1	-61.4	26.5
	-59.3	1.1	-60.4	26.5
	-58.3	1.1	-59.4	26.5
	-57.3	1.1	-58.4	26.5
	-56.3	1.1	-57.4	26.5



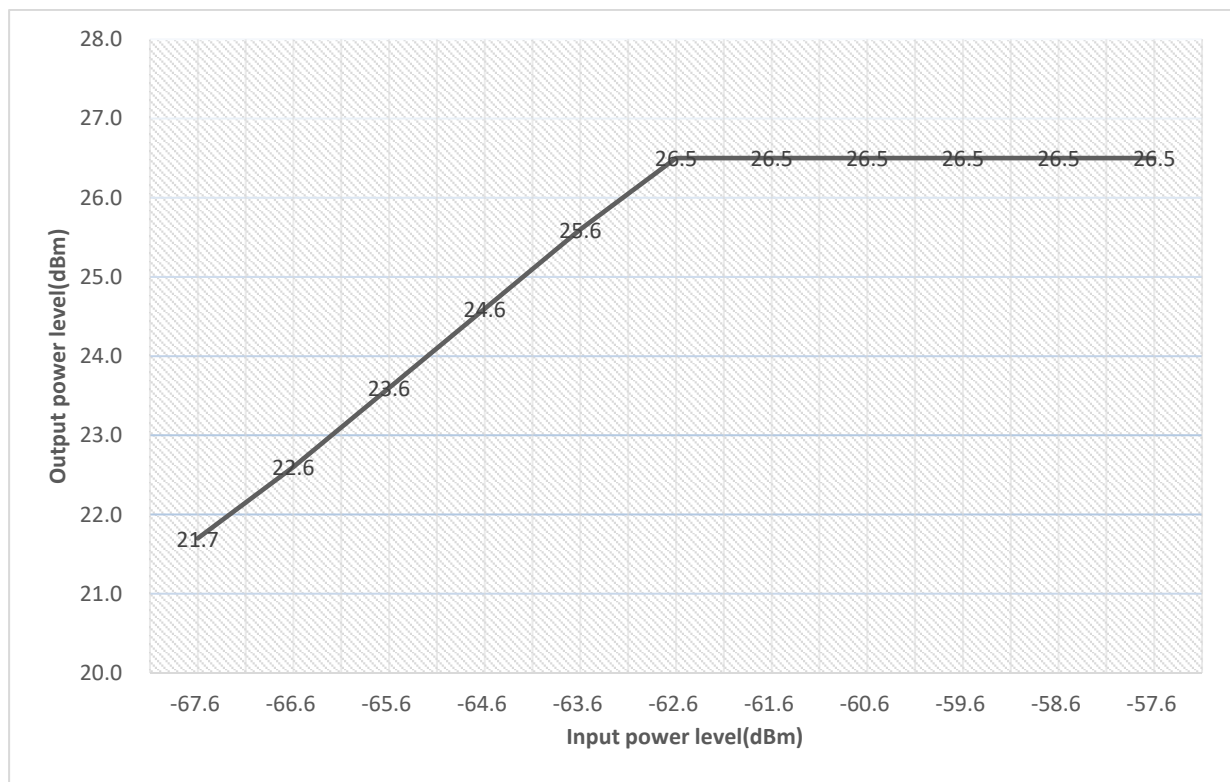
11.3.4.2.2.4. Analog FM

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
811.0MHz	-66.3	1.1	-67.4	21.7
	-65.3	1.1	-66.4	22.7
	-64.3	1.1	-65.4	23.6
	-63.3	1.1	-64.4	24.6
	-62.3	1.1	-63.4	25.6
	-61.3	1.1	-62.4	26.5
	-60.3	1.1	-61.4	26.5
	-59.3	1.1	-60.4	26.5
	-58.3	1.1	-59.4	26.5
	-57.3	1.1	-58.4	26.5
	-56.3	1.1	-57.4	26.5



11.3.4.2.2.5. Tetra

Test frequency	Signal output power (dBm)	EUT Input cable loss (dB)	EUT Corrected Input power (dBm)	EUT Corrected Output power (dBm)
811.0MHz	-66.5	1.1	-67.6	21.7
	-65.5	1.1	-66.6	22.6
	-64.5	1.1	-65.6	23.6
	-63.5	1.1	-64.6	24.6
	-62.5	1.1	-63.6	25.6
	-61.5	1.1	-62.6	26.5
	-60.5	1.1	-61.6	26.5
	-59.5	1.1	-60.6	26.5
	-58.5	1.1	-59.6	26.5
	-57.5	1.1	-58.6	26.5
	-56.5	1.1	-57.6	26.5



11.4. Out-of-band rejection

Test requirement: FCC PART 90.219 (a)
FCC PART 90.219 (d)((7))

Test Method: KDB 935210 D05 clause 4.3

11.4.1. Requirements

According to KDB 935210 D05 clause 4.3 requirement, A signal booster shall reject amplification of other signals outside of its pass band. Adjust the internal gain control of the EUT to the maximum gain for which equipment certification is sought.

11.4.2. Test configuration

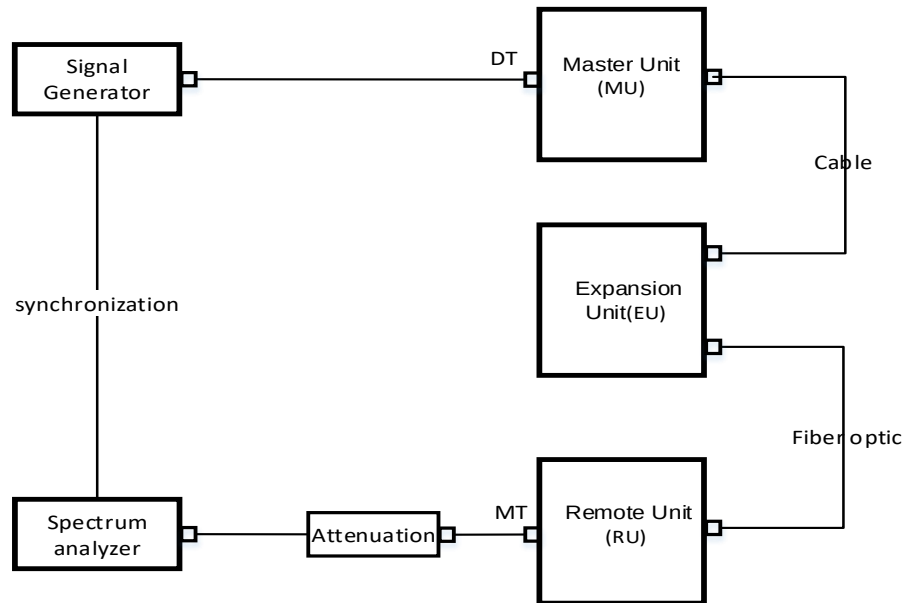


Figure 11.4-1 Downlink connection diagram

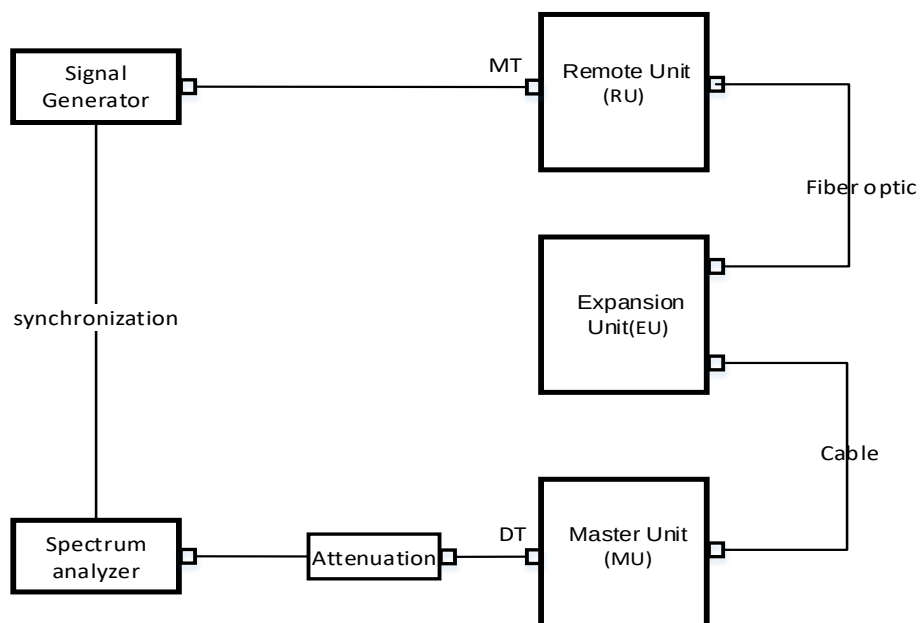


Figure 11.4-2 Uplink connection diagram

11.4.3. Test procedures

- a) Connect a signal generator to the input of the EUT.
- b) Configure a swept CW signal with the following parameters:
 - 1) Frequency range = ± 250 % of the manufacturer's specified pass band.
 - 2) The CW amplitude shall be 3 dB below the AGC threshold (see 4.2), and shall not activate the AGC threshold throughout the test.
 - 3) Dwell time = approximately 10 ms.
 - 4) Frequency step = 50 kHz.
- c) Connect a spectrum analyzer to the output of the EUT using appropriate attenuation.
- d) Set the RBW of the spectrum analyzer to between 1 % and 5 % of the manufacturer's rated passband, and VBW = $3 \times$ RBW.
- e) Set the detector to Peak and the trace to Max-Hold.
- f) After the trace is completely filled, place a marker at the peak amplitude, which is designated as f_0 , and with two additional markers (use the marker-delta method) at the 20 dB bandwidth (i.e., at the points where the level has fallen by 20 dB).
- g) Capture the frequency response plot for inclusion in the test report.

----- The following blanks -----