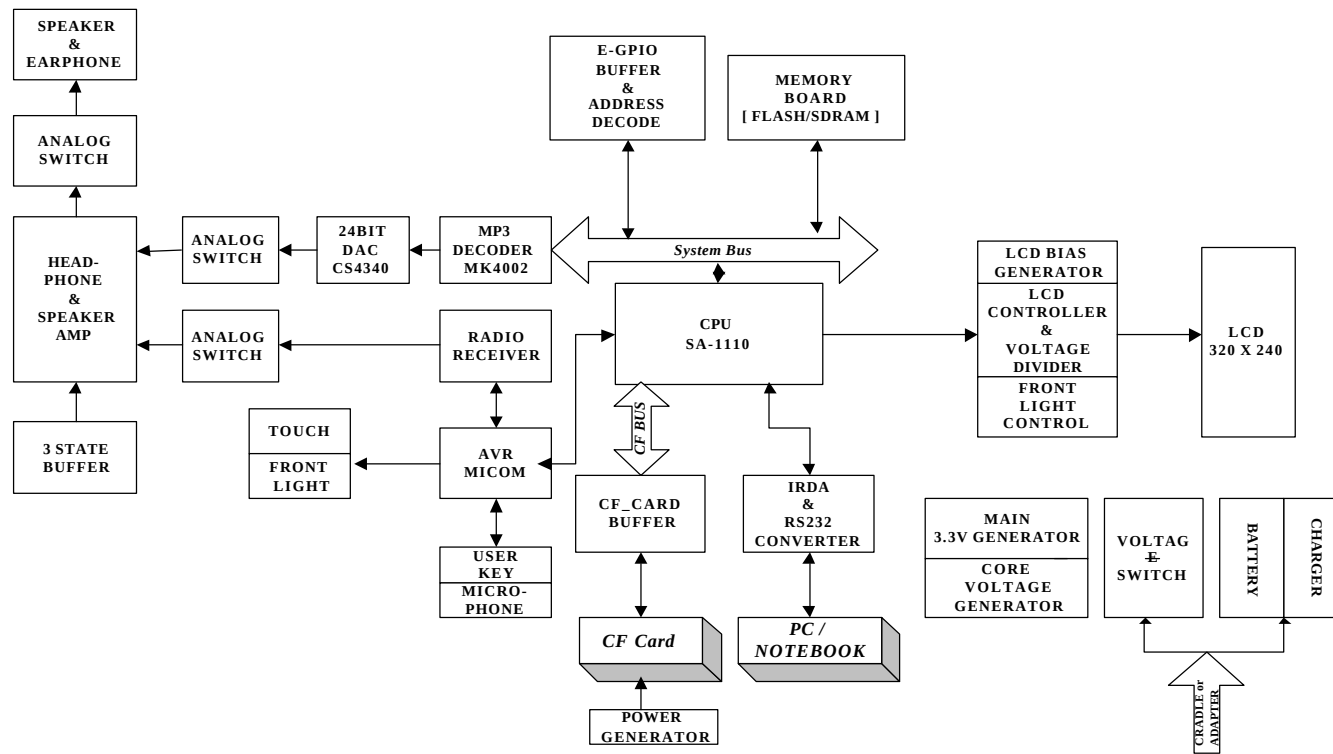


BLOCK DIAGRAM FOR C3224

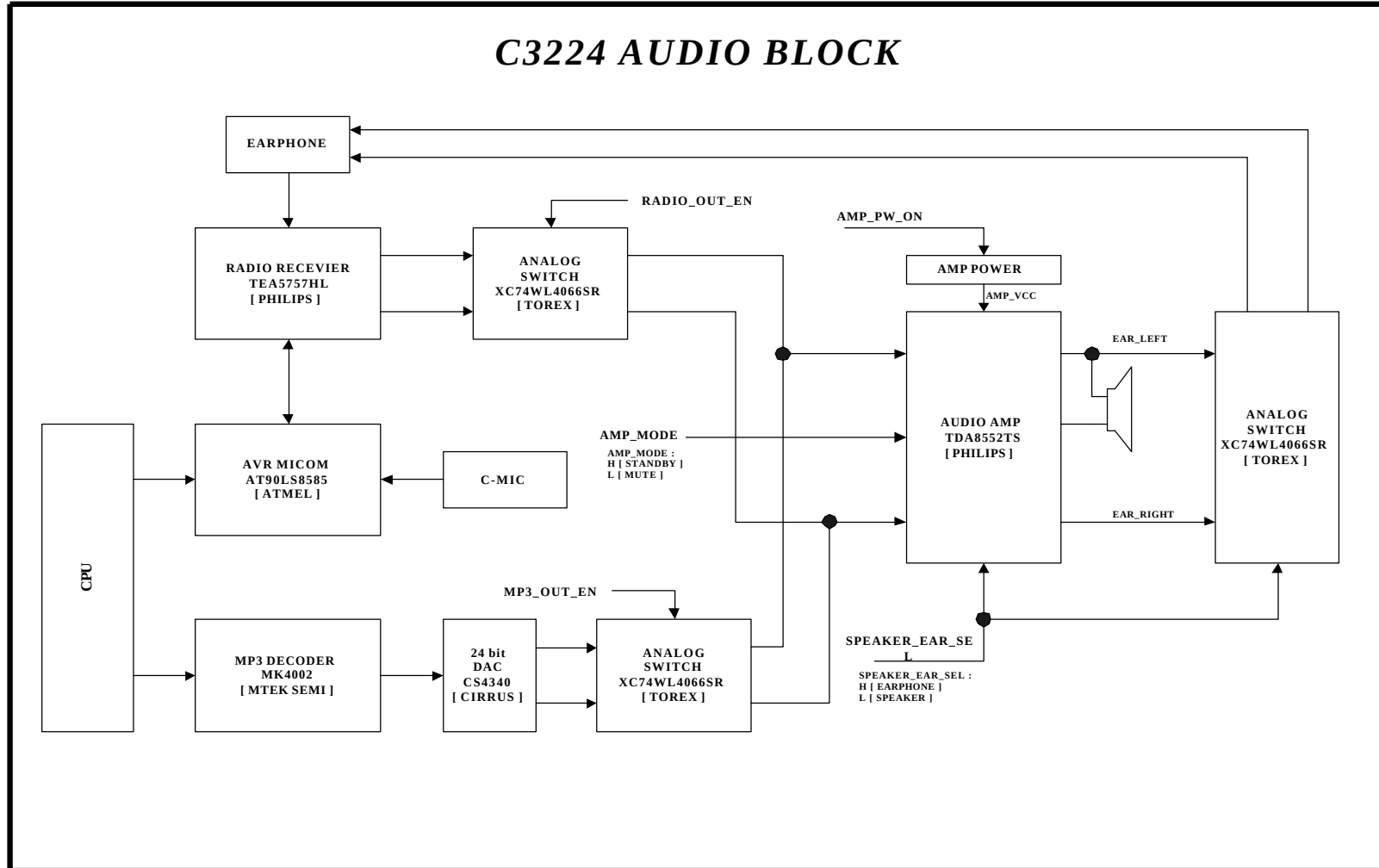
1. THE WHOLE BLOCK DIAGRAM
2. AUDIO PATH BLOCK DIAGRAM
3. MAIN POWER BLOCK DIAGRAM
4. PCMCIA PATH BLOCK DIAGRAM

THE WHOLE BLOCK DIAGRAM

C3224 BLOCK DIAGRAM

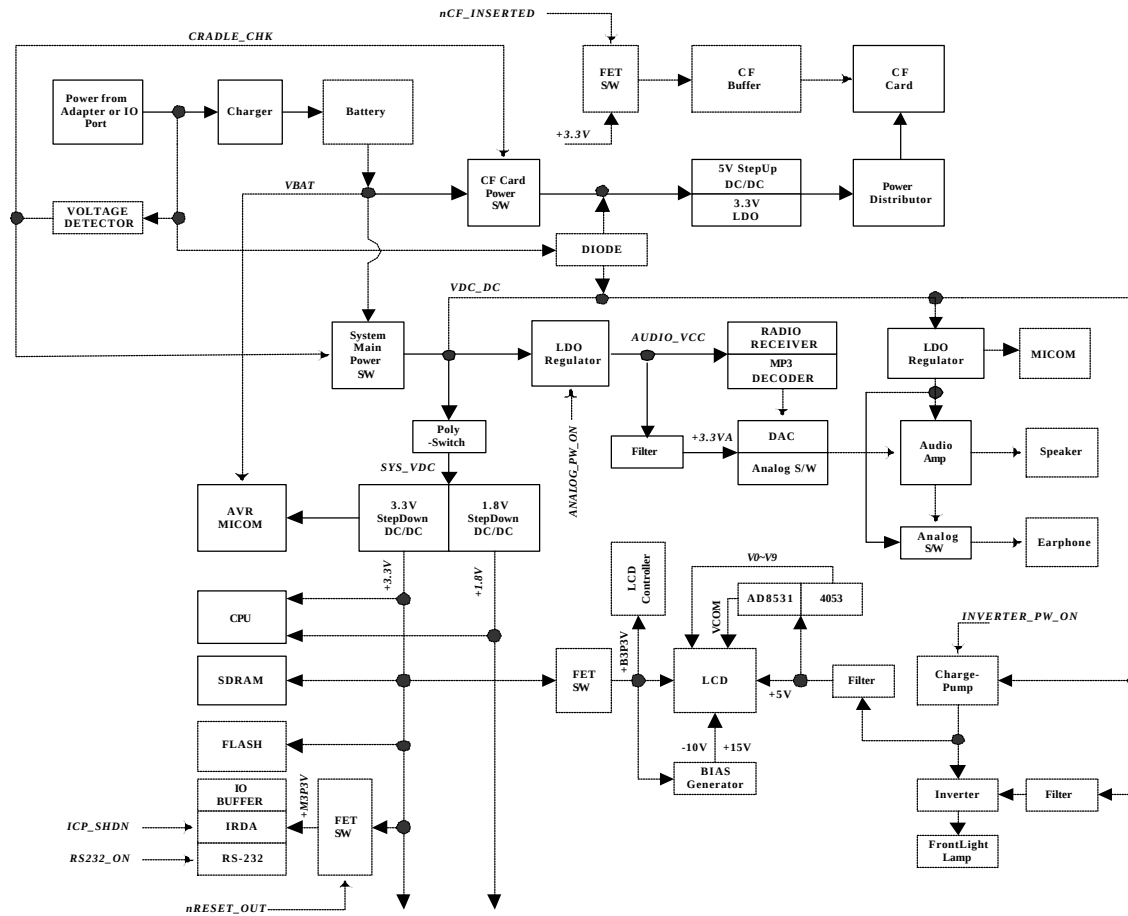


AUDIO PATH BLOCK DIAGRAM



C3224 POWER DISTRIBUTION

The diagram illustrates the power distribution architecture for the C3224 device. Power enters from an external source (Adapter or IO Port) and is managed by a Voltage Detector and a Charger connected to a Battery. The system includes a CF Card Power SW, a 5V StepUp DC/DC converter, and a Power Distributor. The main power path is controlled by the System Main Power SW, which feeds into an LDO Regulator. A Poly-Switch is used for system shutdown. The LDO Regulator provides power to various components, including the AVR MICOM, CPU, SDRAM, FLASH, IO BUFFER, IRDA, RS-232, FET SW, LCD Controller, LCD, BIAS Generator, RADIO RECEIVER MP3 DECODER, DAC, Analog S/W, Audio Amp, Speaker, Earphone, LDO Regulator, MICOM, Charge-Pump, Inverter, and FrontLight Lamp. The diagram also shows control signals like CRADLE_CHK, nCF_INSERTED, VBAT, VDC_DC, SYS_VDC, AUDIO_VCC, INVERTER_PW_ON, ICP_SHDN, and nRESET_OUT.



CF PATH BLOCK DIAGRAM

