

The AD BOARD block diagram illustrates the internal architecture of the system. It features a central MICRO CONTROLLER (M6759) which manages the SCALER (ZiproSX-t0947), VIDEO DECODER IC (VPX3226E), and AUDIO PROCESSOR IC (PT2313L). The SCALER is connected to an X-TAL 14.31818MHz crystal and an LVDS CS5824 * 2ea. The VIDEO DECODER IC is connected to an X-TAL 20.25MHz crystal and an X-TAL 12.000MHz crystal. The AUDIO PROCESSOR IC is connected to an AUDIO IC (TDA7496L) and an 15PIN WAFER FUNCTION CONNECTOR. The MICRO CONTROLLER is also connected to an EEPROM 24LC16B, an EEPROM 24LC02, and an X-TAL 20.25MHz crystal. The AD BOARD is connected to an INVERTER, an AC INLET (100-220V), an ADAPTOR (+12V: 4.16A), and a TV TUNER BOARD. The TV TUNER BOARD is connected to an HT17E11-300 LTM170EH-L01 LCD PANEL. The AD BOARD also includes a 74AHC157D, a 74AHC244D, and a 74AHC16B. The AD BOARD is connected to a 29 PIN DVI CONNECTOR, a 15 PIN D-SUB CONNECTOR, a HEADPHONE JACK, an RCA JACK, a TUNER JACK, and an S-VIDEO JACK. The AD BOARD is also connected to a FUNCTION BOARD and a 4PIN WAFER SPEAKER CONNECTOR.