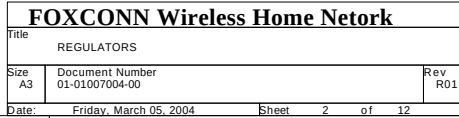
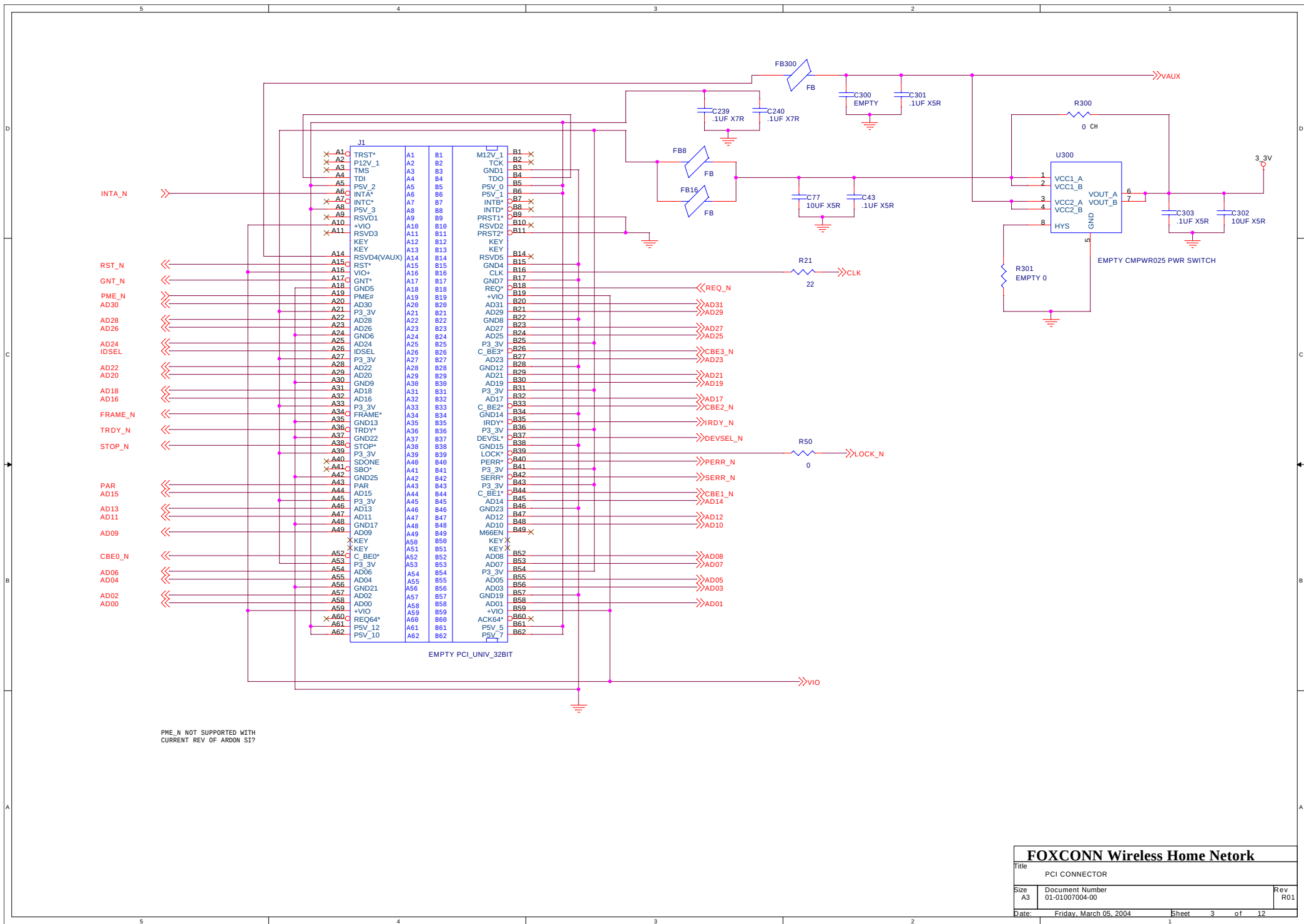


DATE	REVISION NUMBER	DESCRIPTION
		INTERNAL RELEASE
11/13/03	R03	Intel Reference Release
11/17/03	RL-01-R0A	Schematic draft DHL type change to DSN type
11/19/03	RL-01-R0A	Change PCB 8 layer to 4 layer and one side placement of component
11/25/03	RL-01-R0A	Delete J4,J7,J8,R36,R37,R38,R39,R46 of Intel ARDON/RAMON Test connector
11/26/03	RL-01-R0A	Delete XTALOFF circuit
11/27/03	RL-01-R0A	Change U11 to ingnle port switch
11/28/03	RL-01-R0A	Add low pass filter on transmit loop.
3/05/04	R01	

FOXCONN 802.11B/G PCI CARD

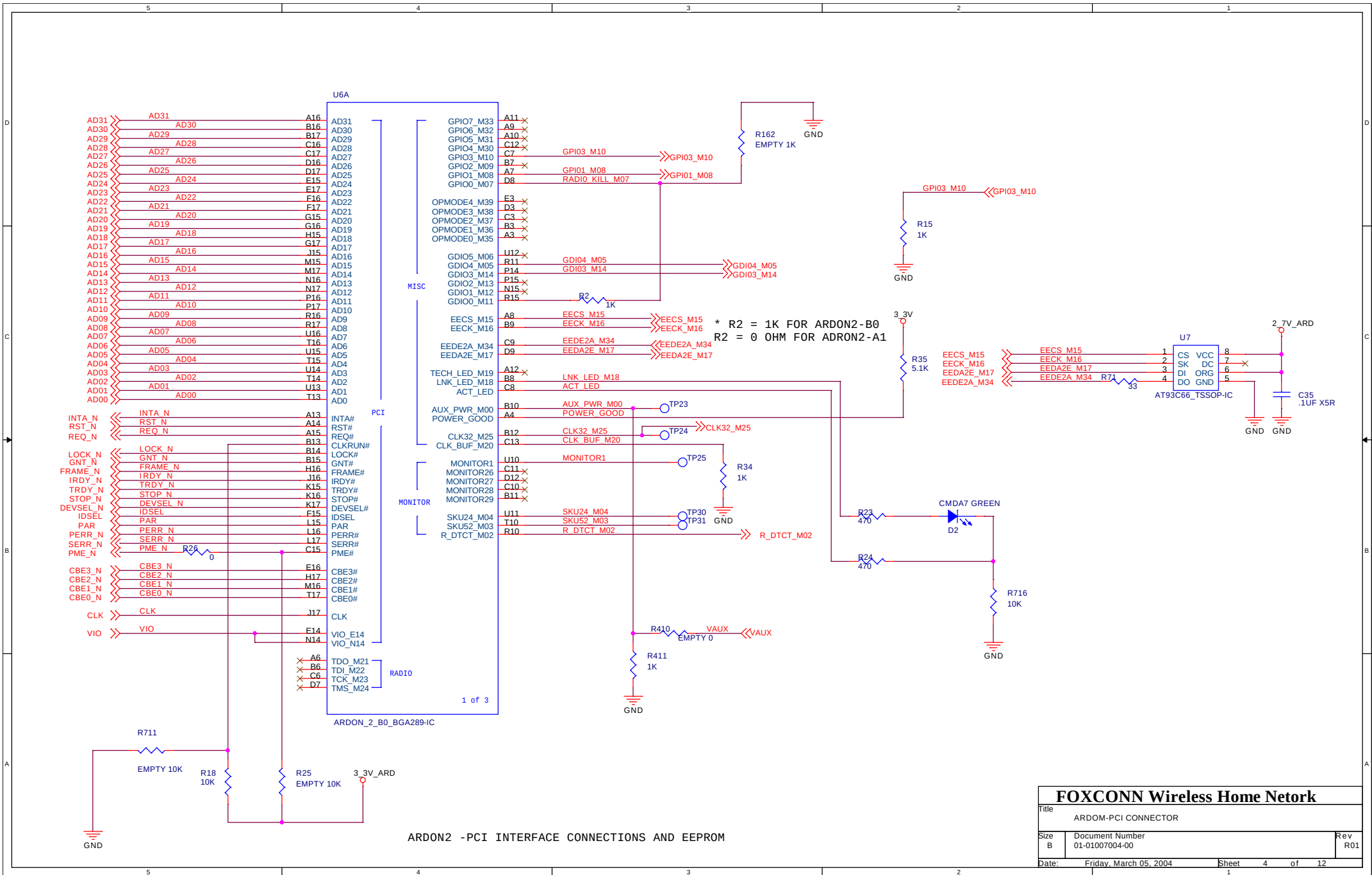


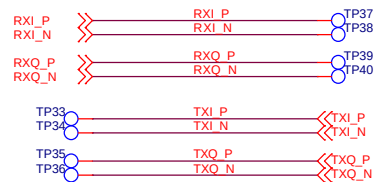
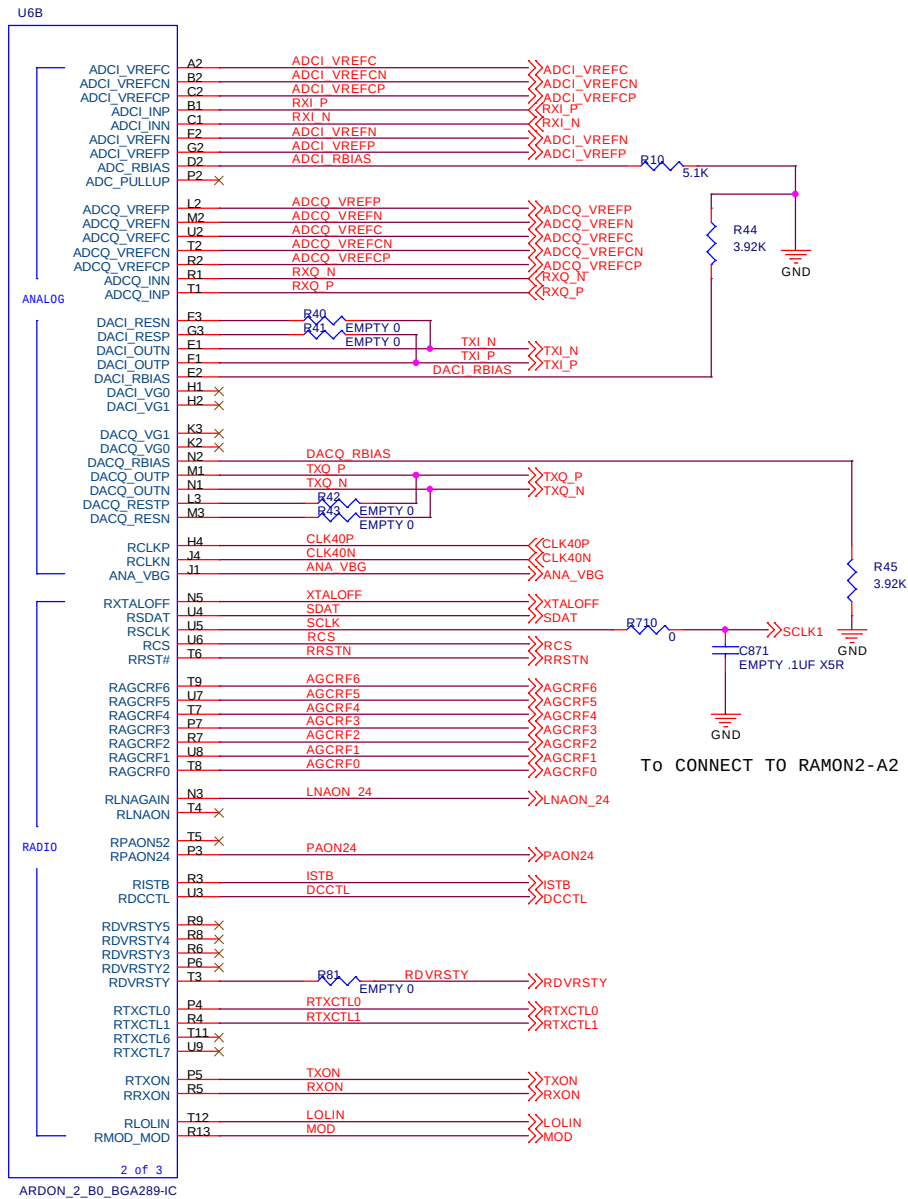


PME_N NOT SUPPORTED WITH
CURRENT REV OF ARDON S17

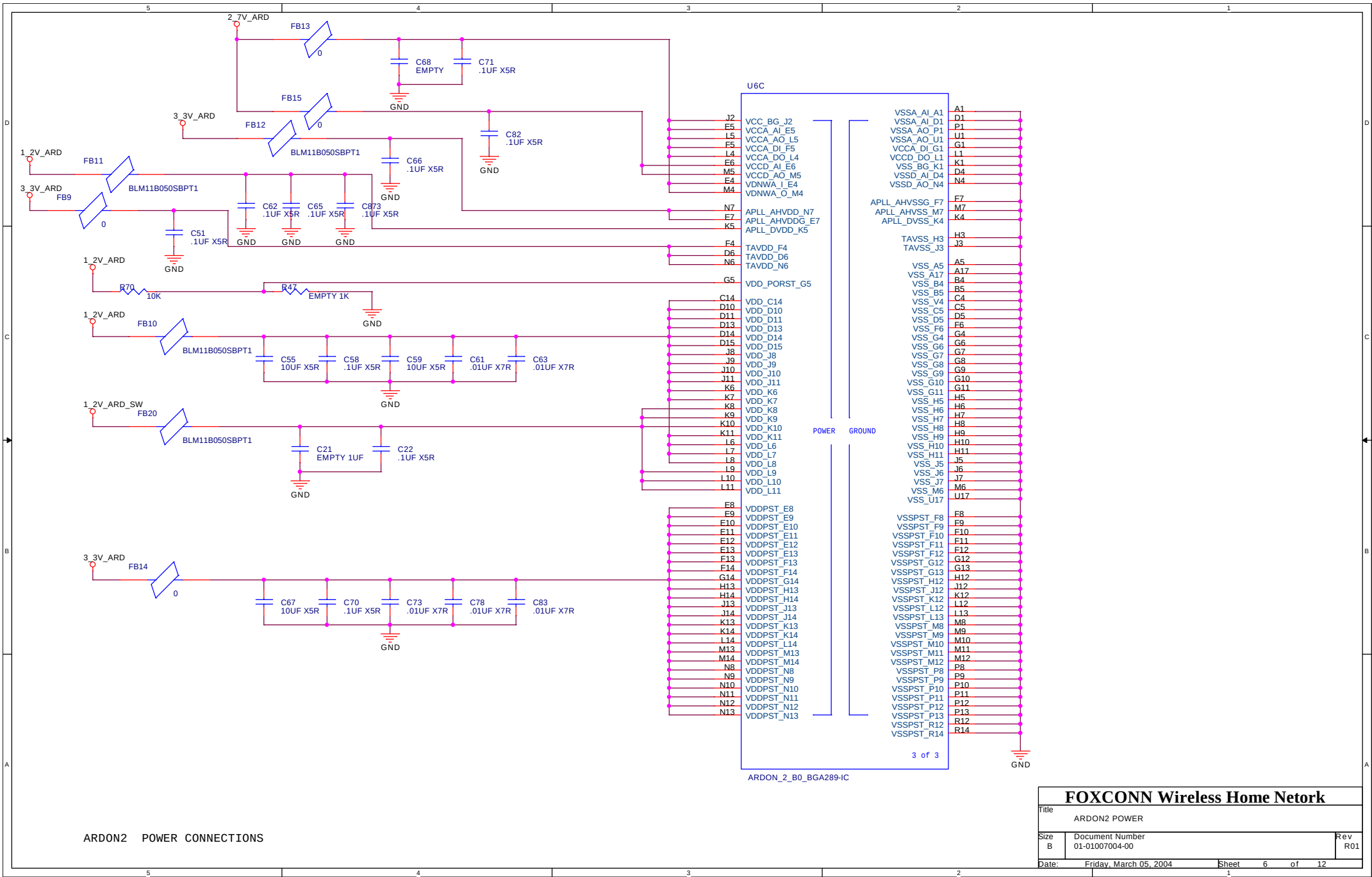
FOXCONN Wireless Home Network

Title		PCI CONNECTOR
Size	Document Number	Rev
A3	01-01007004-00	R01
Date:	Friday, March 05, 2004	Sheet 3 of 12

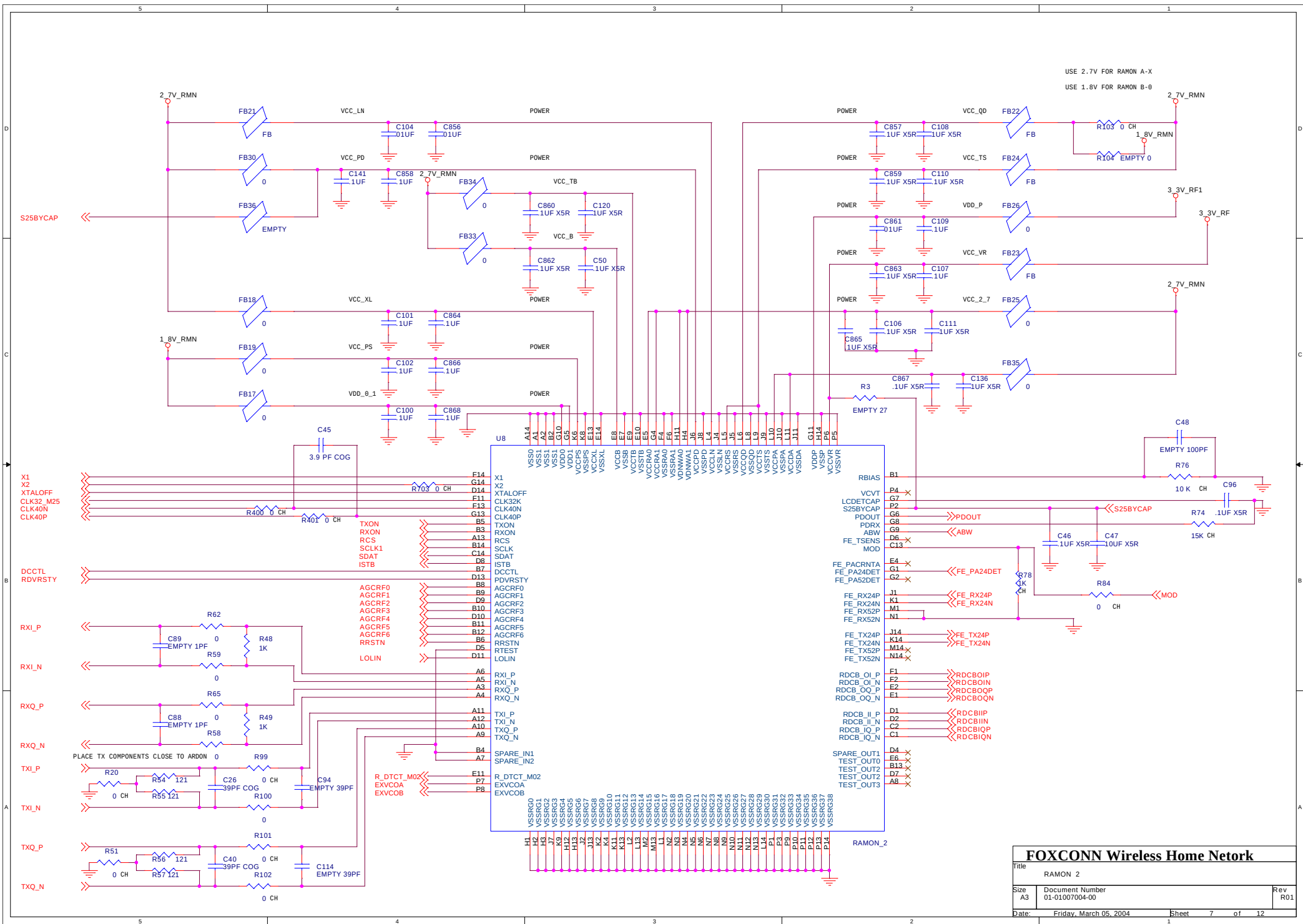


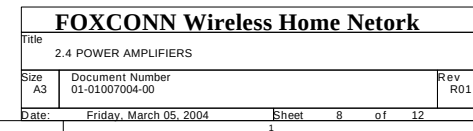


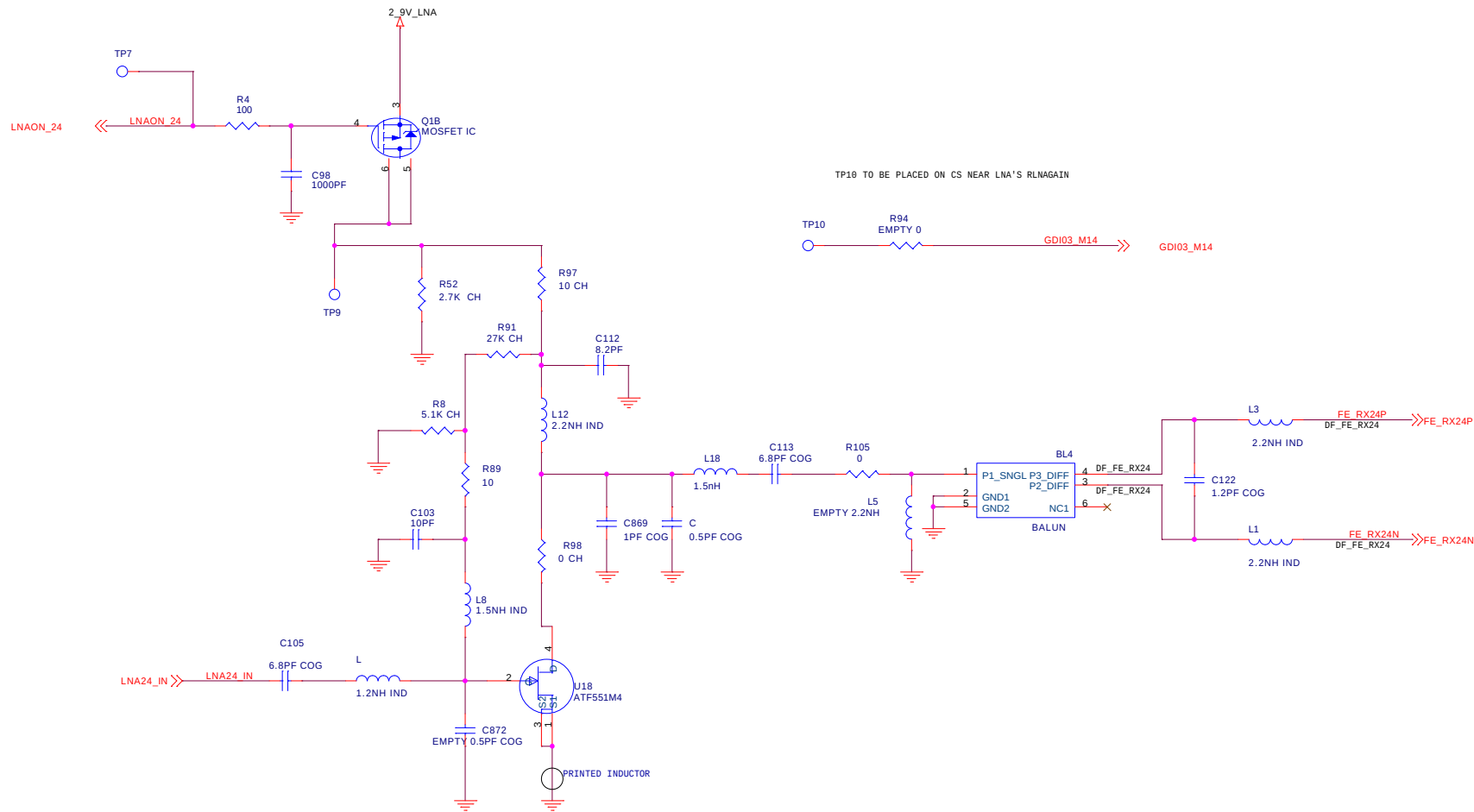
FOXCONN Wireless Home Netork			
Title	ARDON ANALOG & BB INTERFACE		
Size	Document Number	Rev	
B	01-01007004-00	R01	
Date:	Friday, March 05, 2004	Sheet	5 of 12



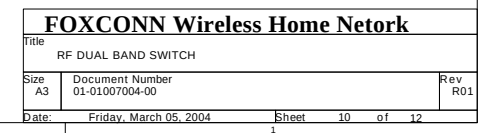
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Title	ARDON2 POWER	
Size	Document Number	Rev
B	01-01007004-00	R01
Date:	Friday, March 05, 2004	Sheet 6 of 12



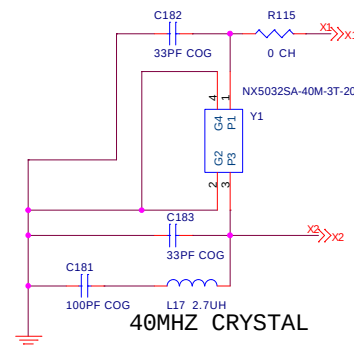
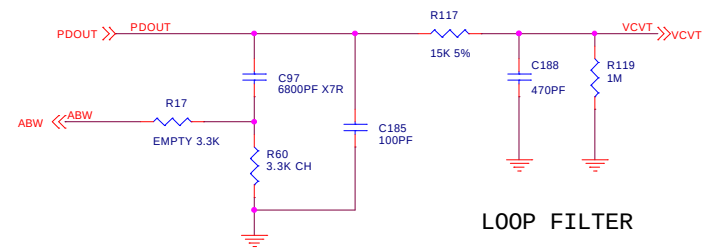
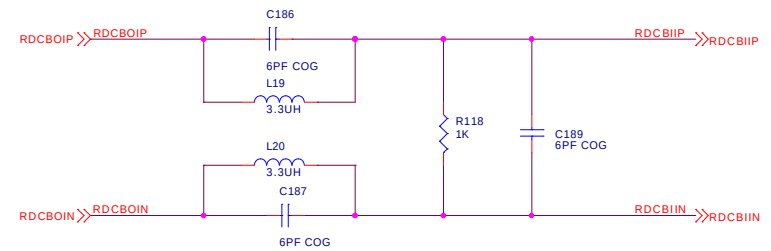
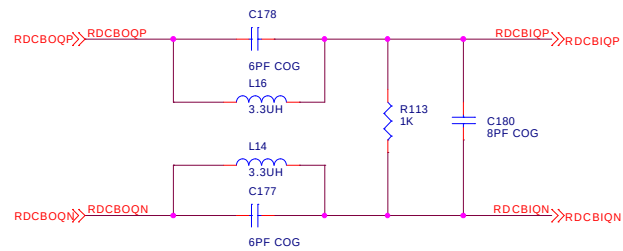




TP10 TO BE PLACED ON CS NEAR LNA'S RLNAGAIN



BASEBAND FILTER SIGNAL LINES TO BE SYMMETRICAL



BASE BAND FILTER, 40MHZ CLOCK AND PLL

FOXCONN Wireless Home Netork			
Title	40MHZ CLOCK AND BB FILTER		
Size	Document Number	Rev	
A3	01-01007004-00	R01	
Date:	Friday, March 05, 2004	Sheet	11 of 12

