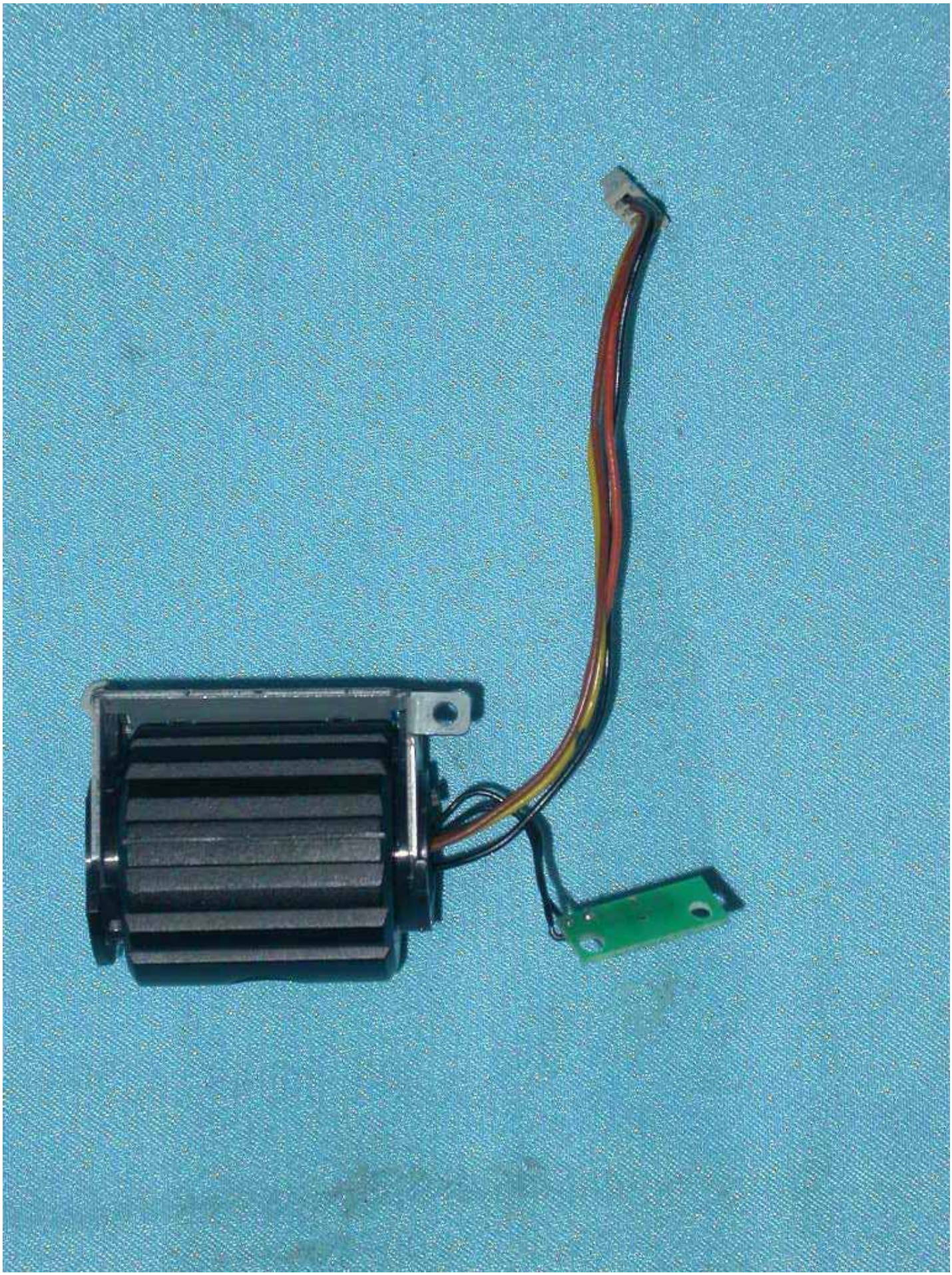
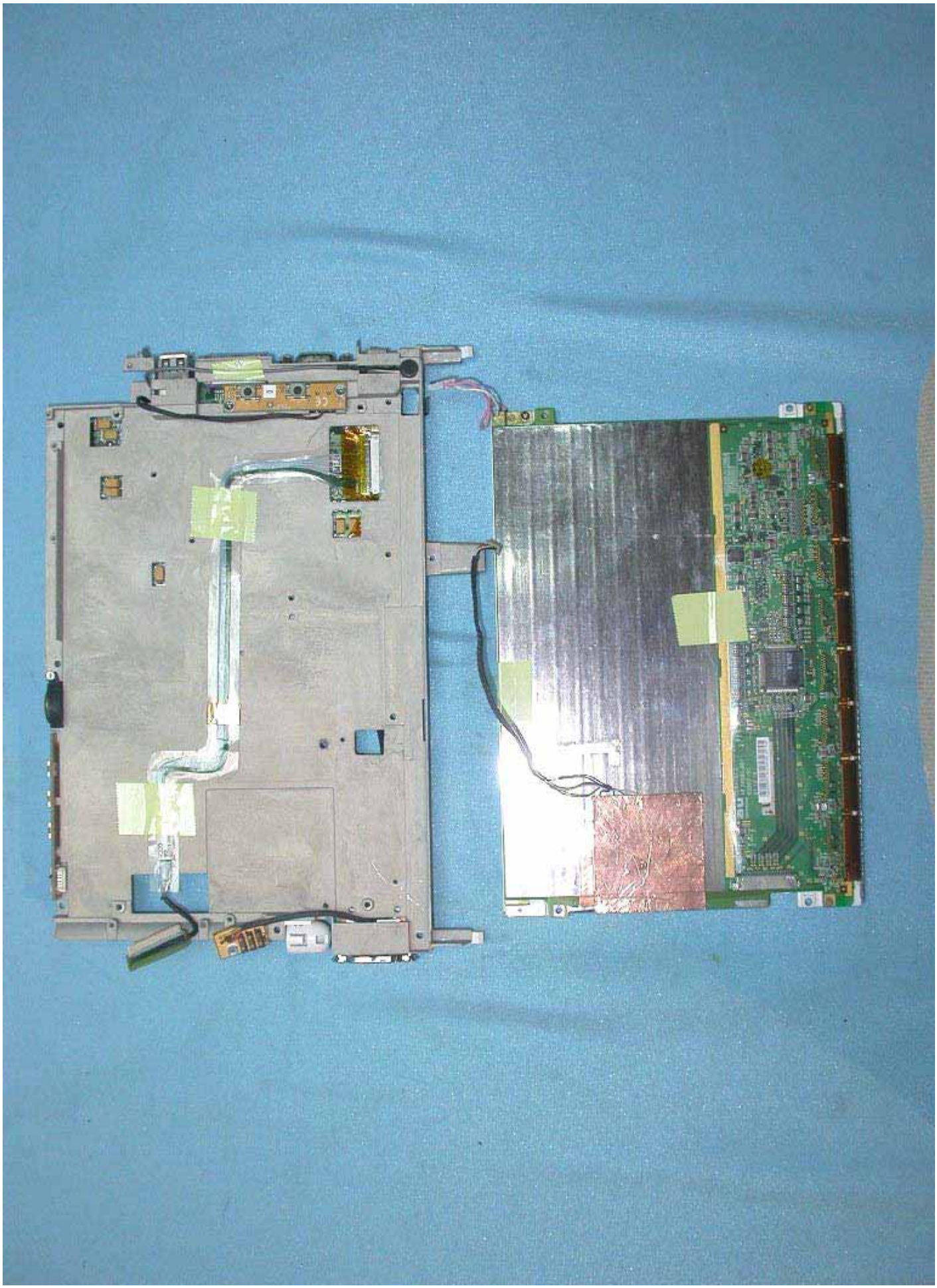
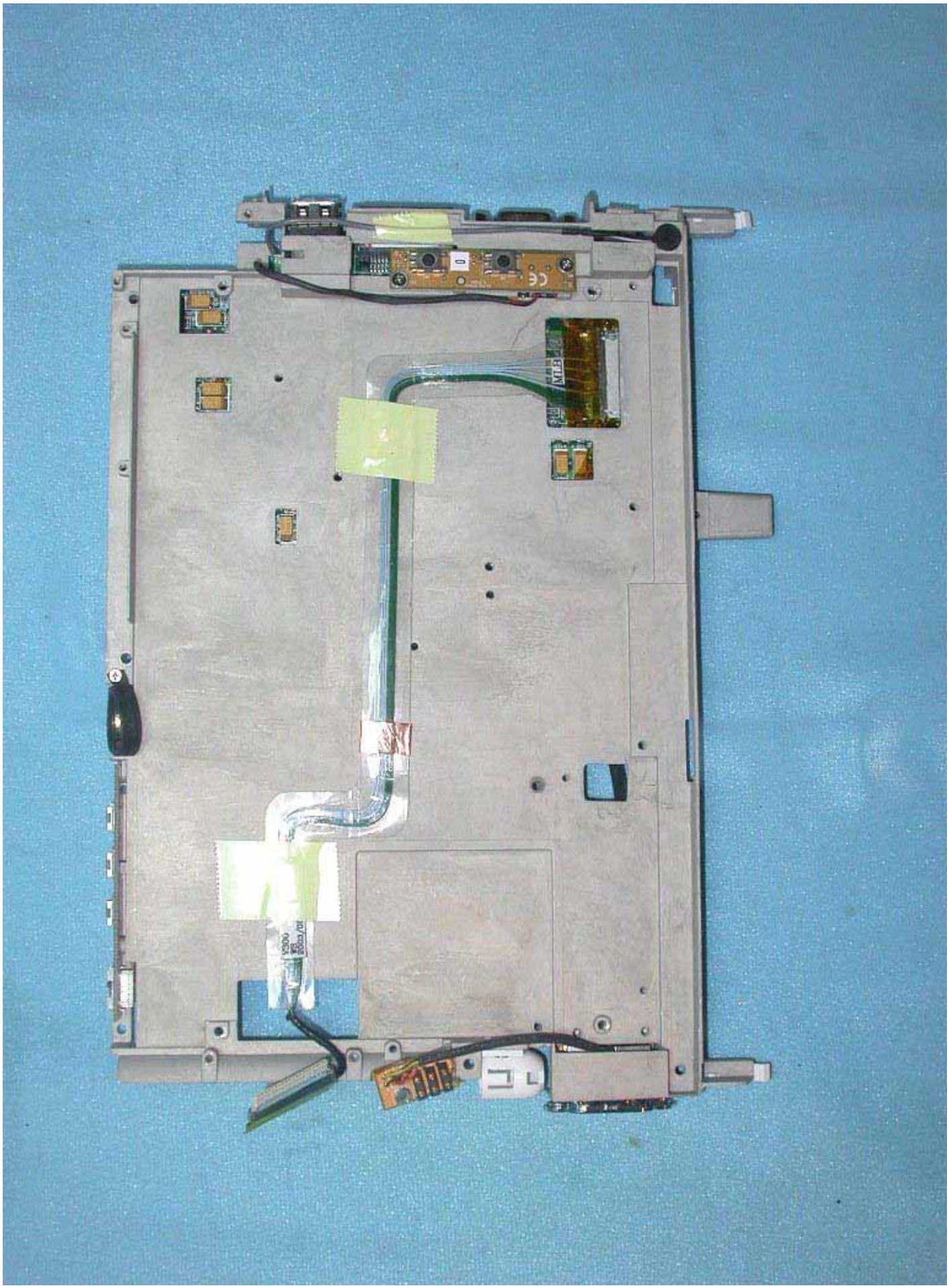
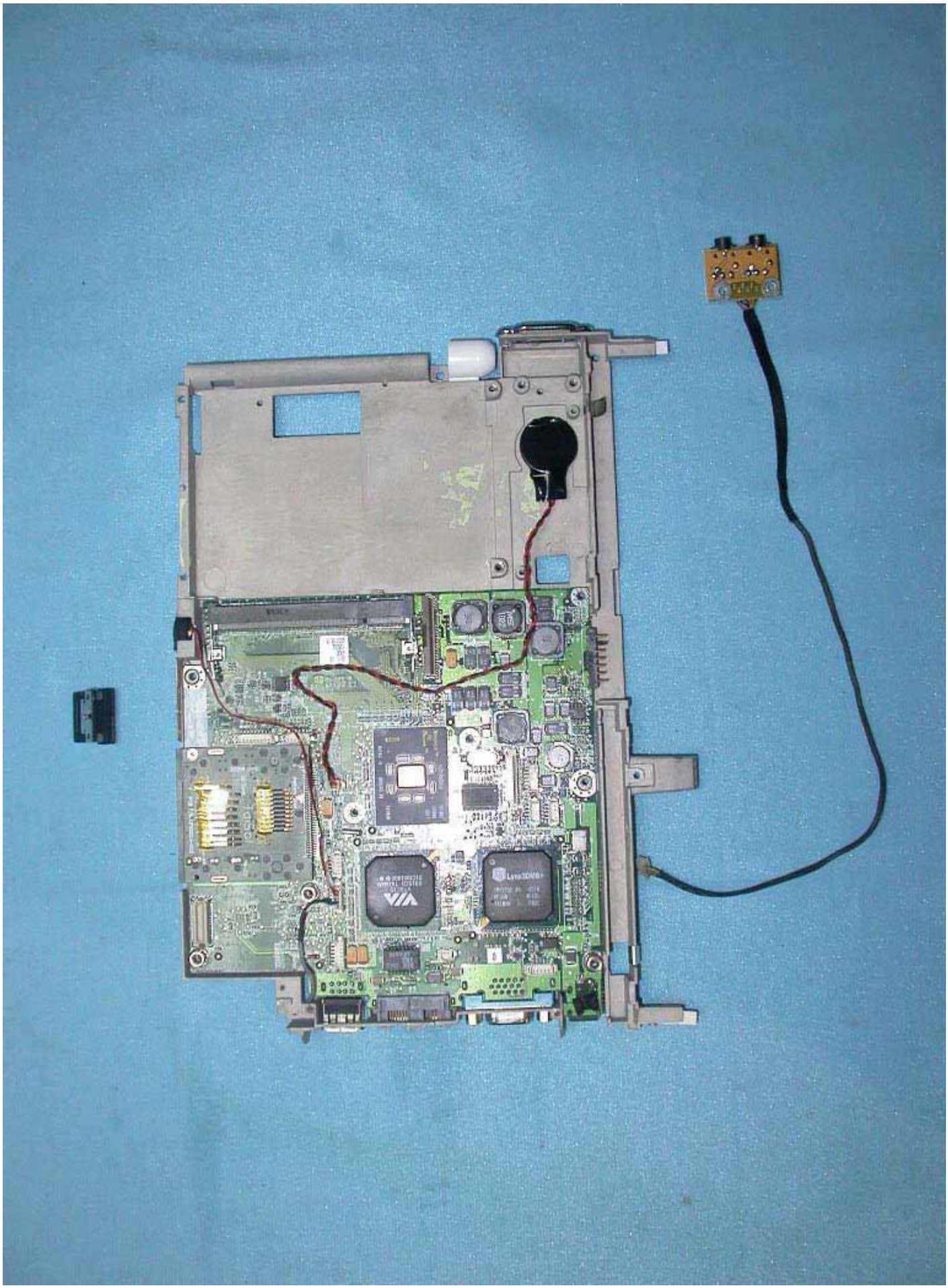


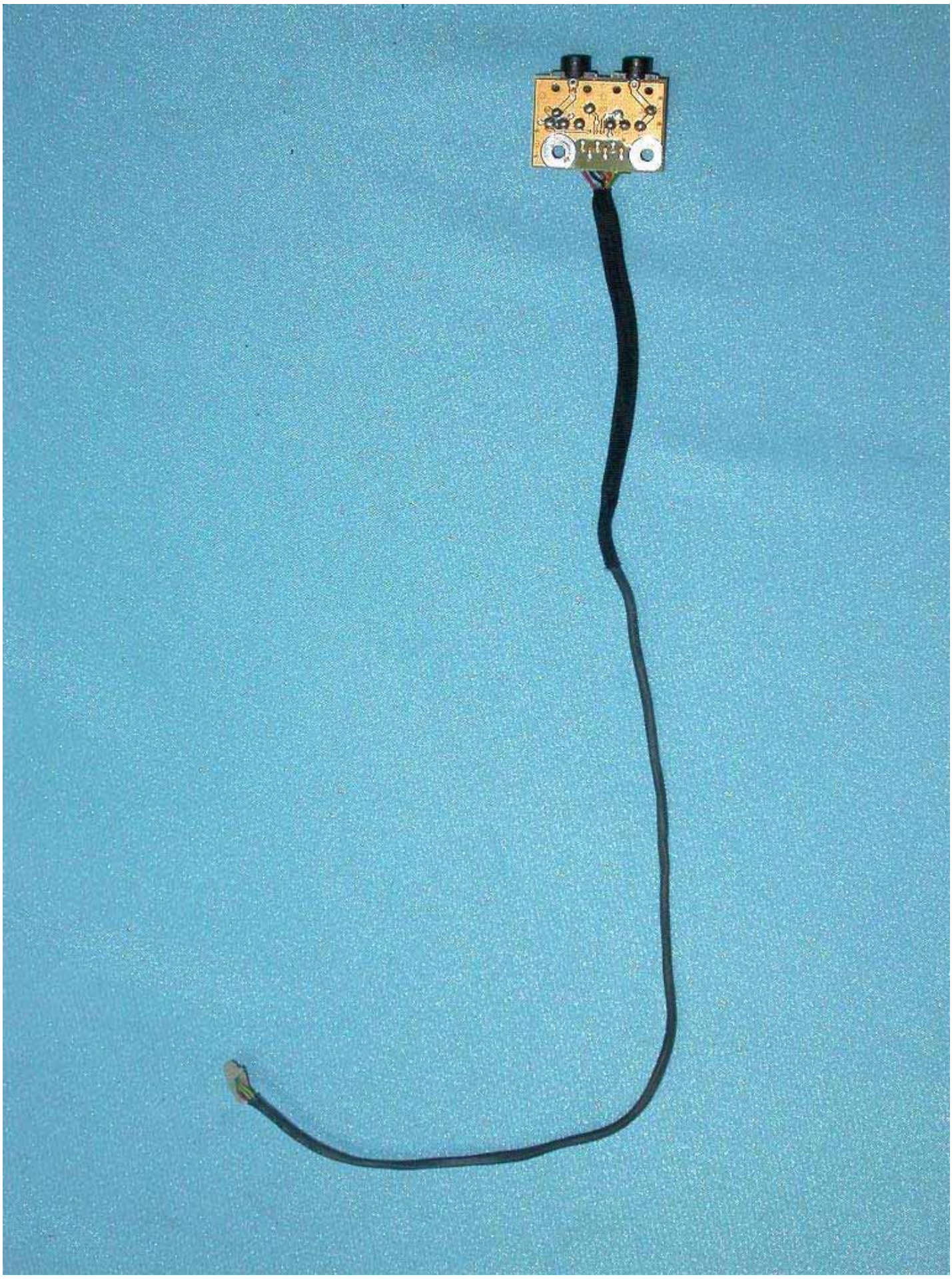


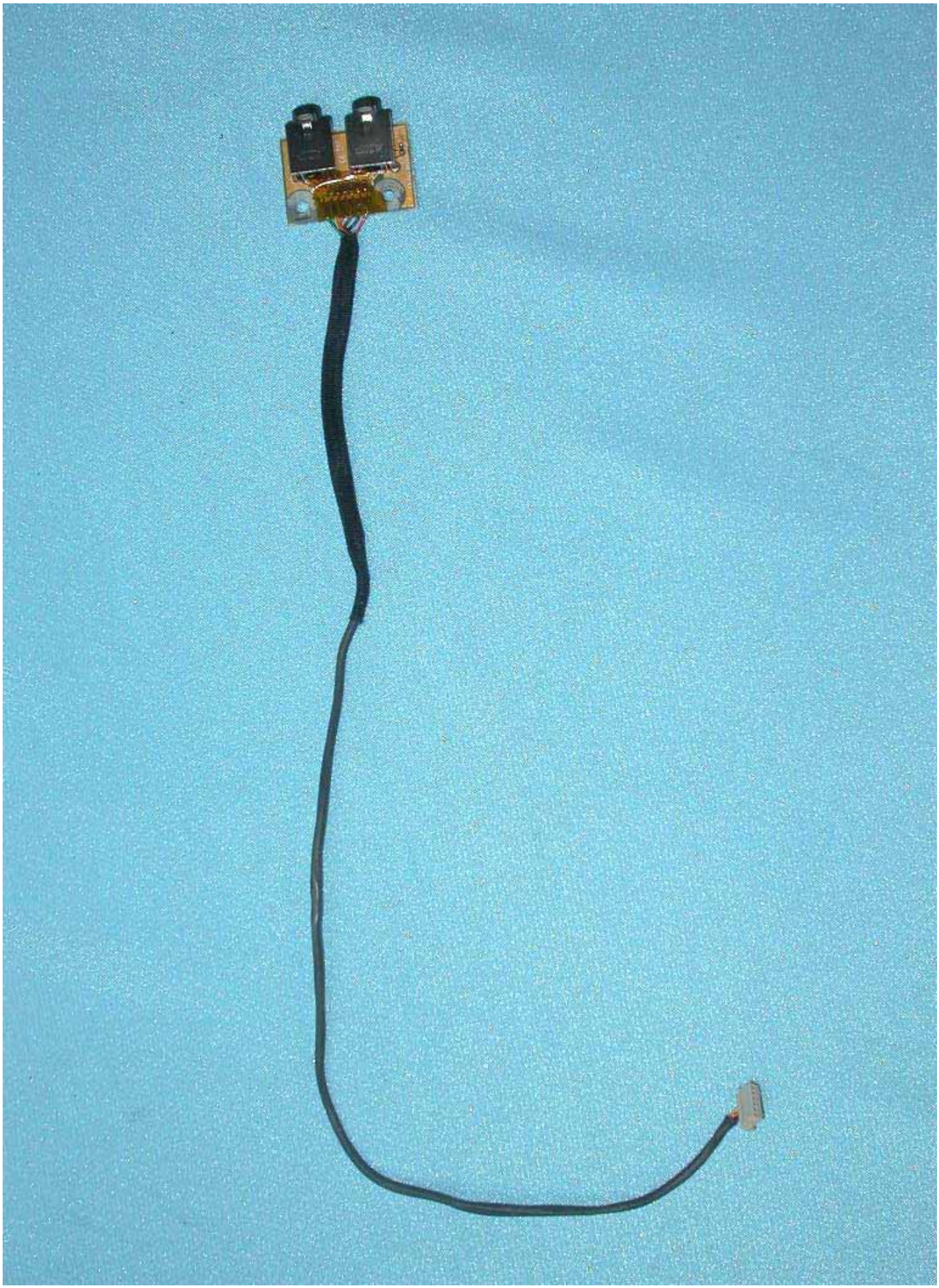


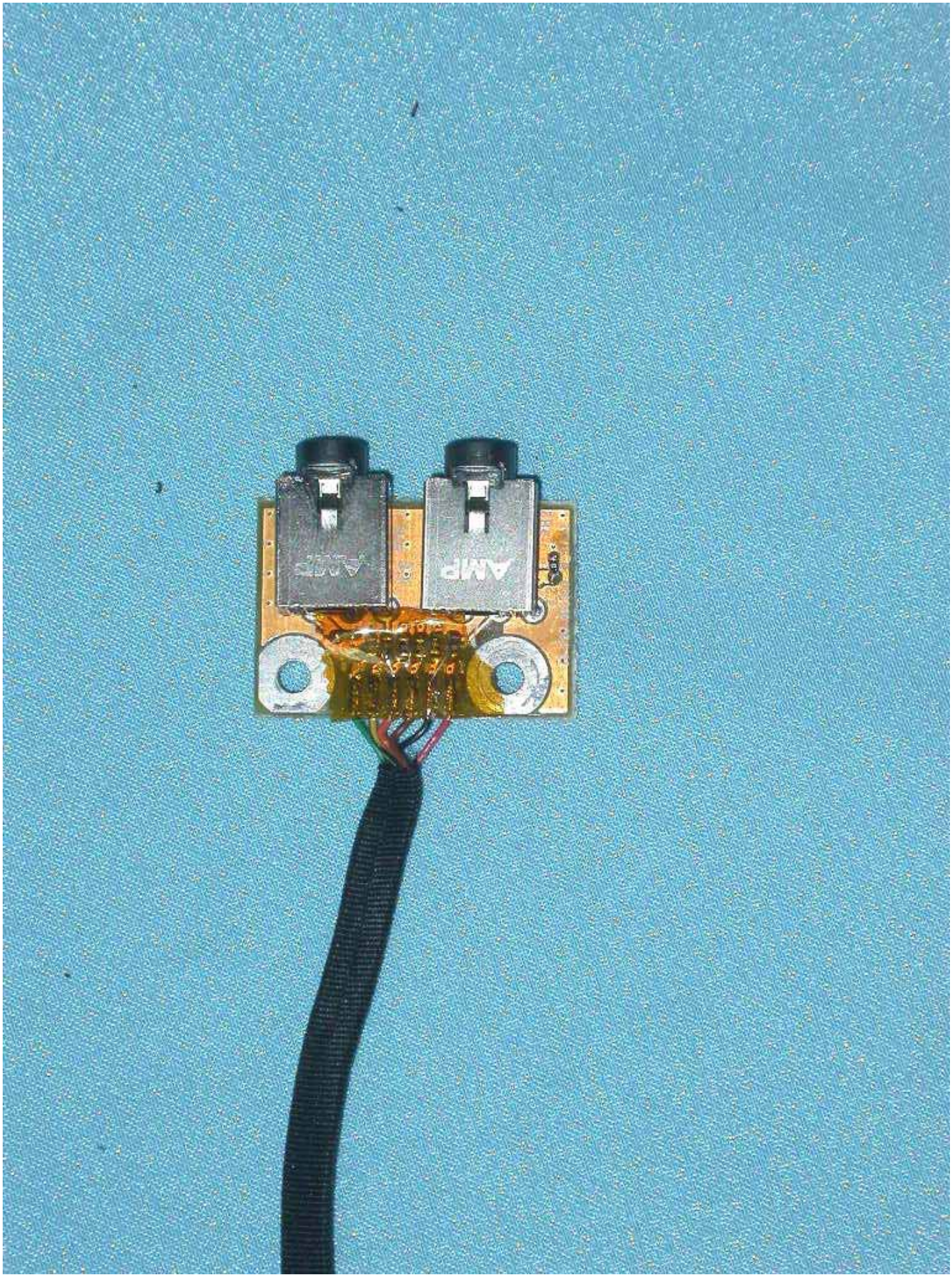


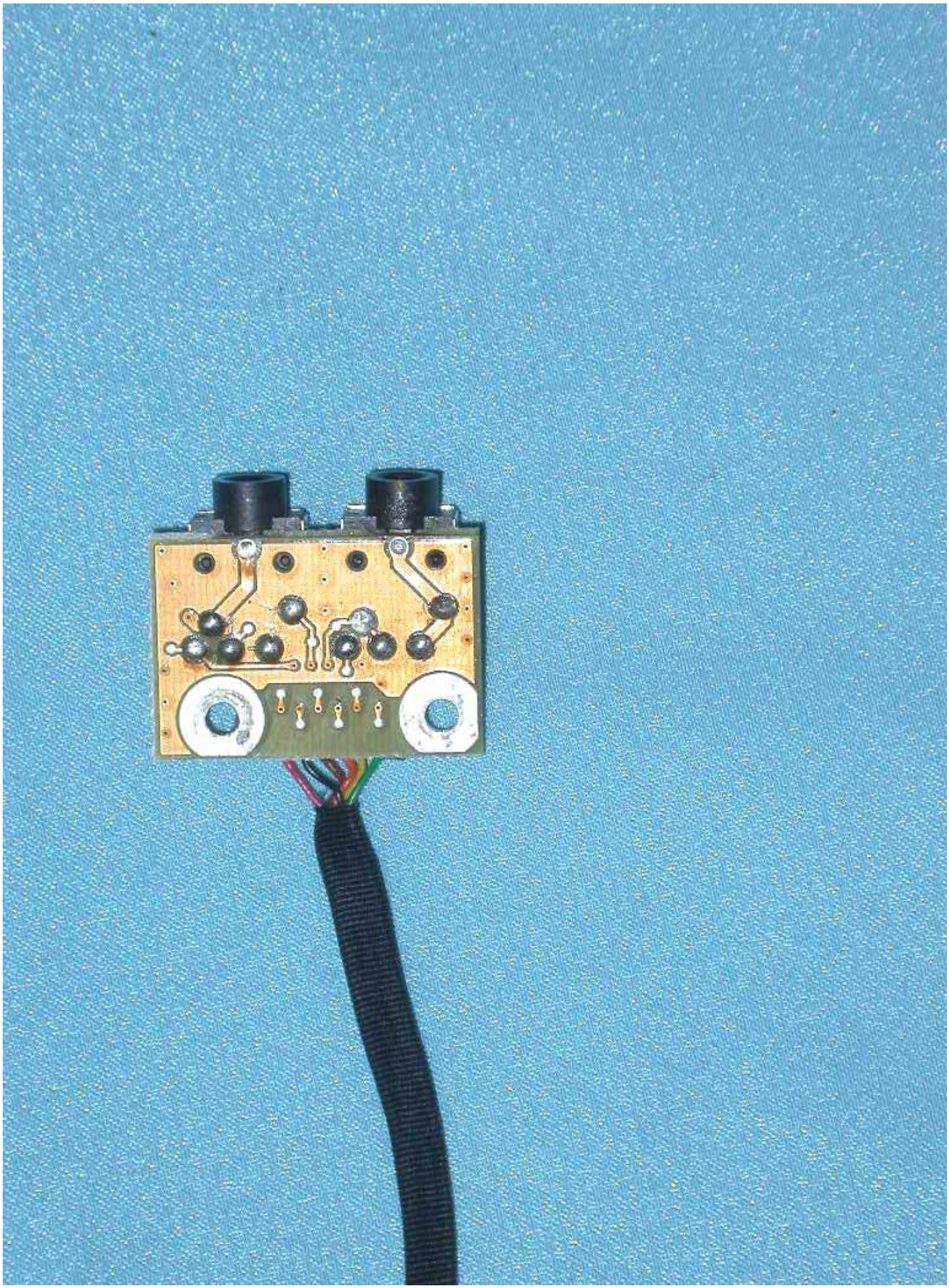






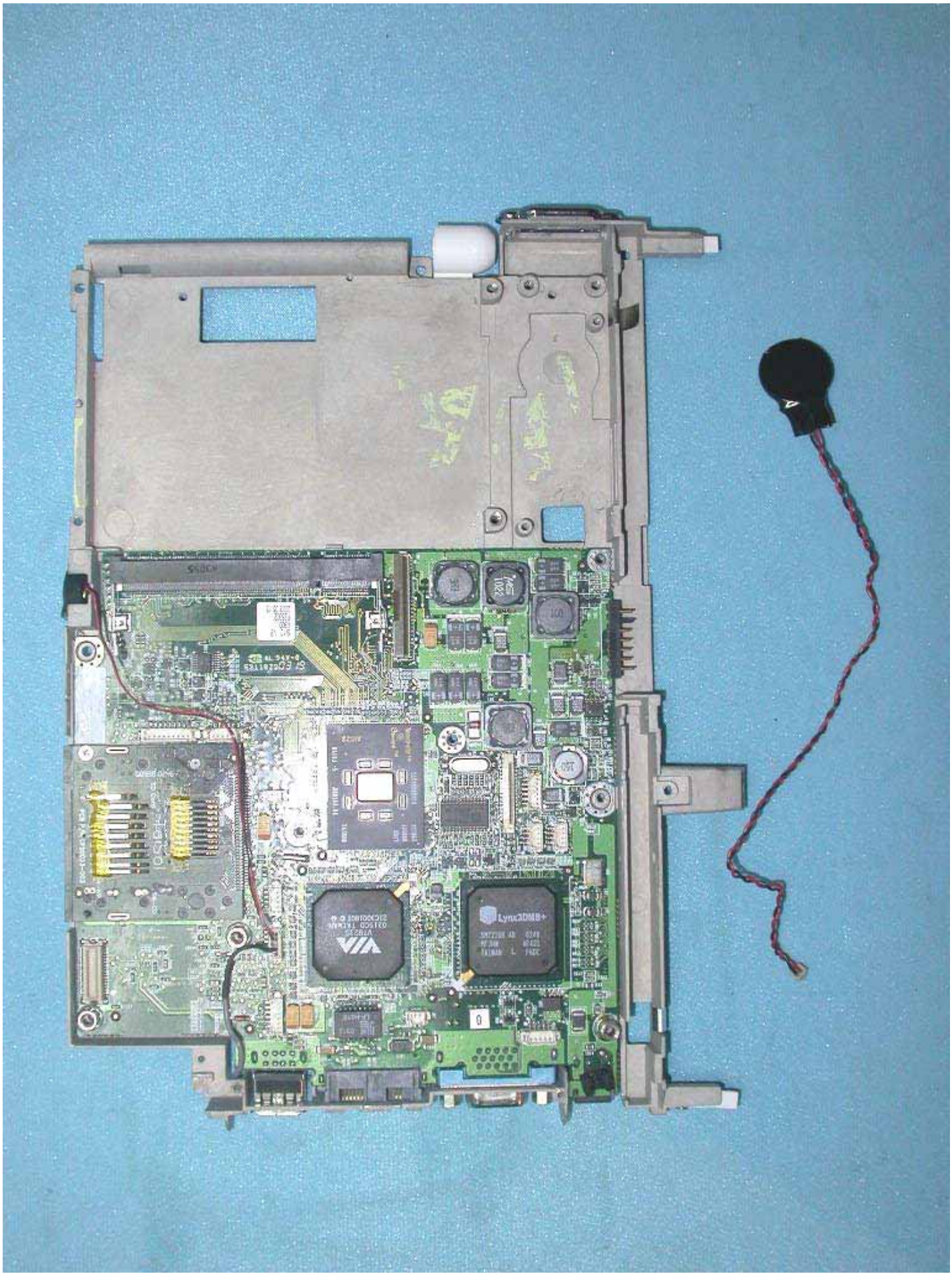


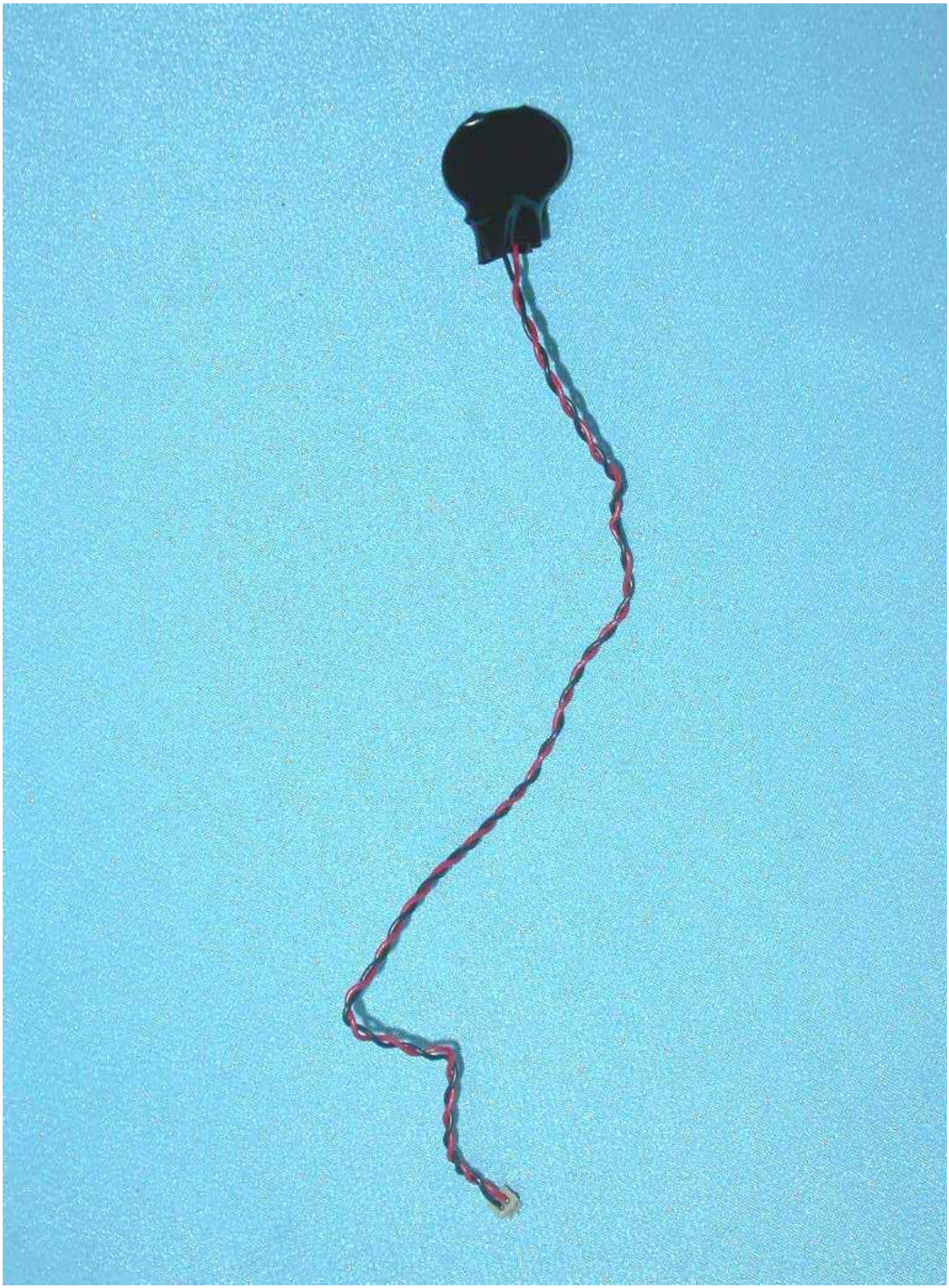

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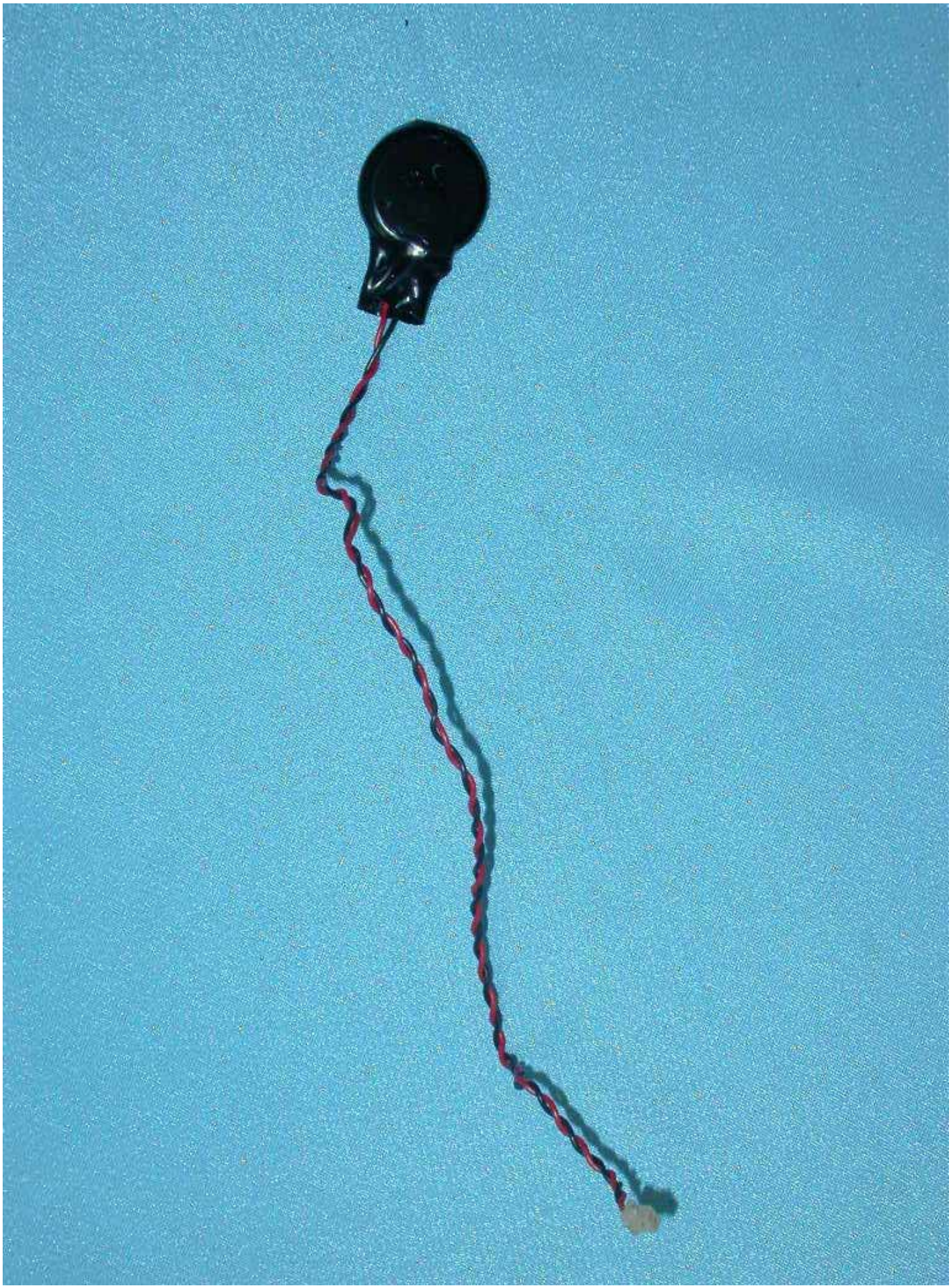


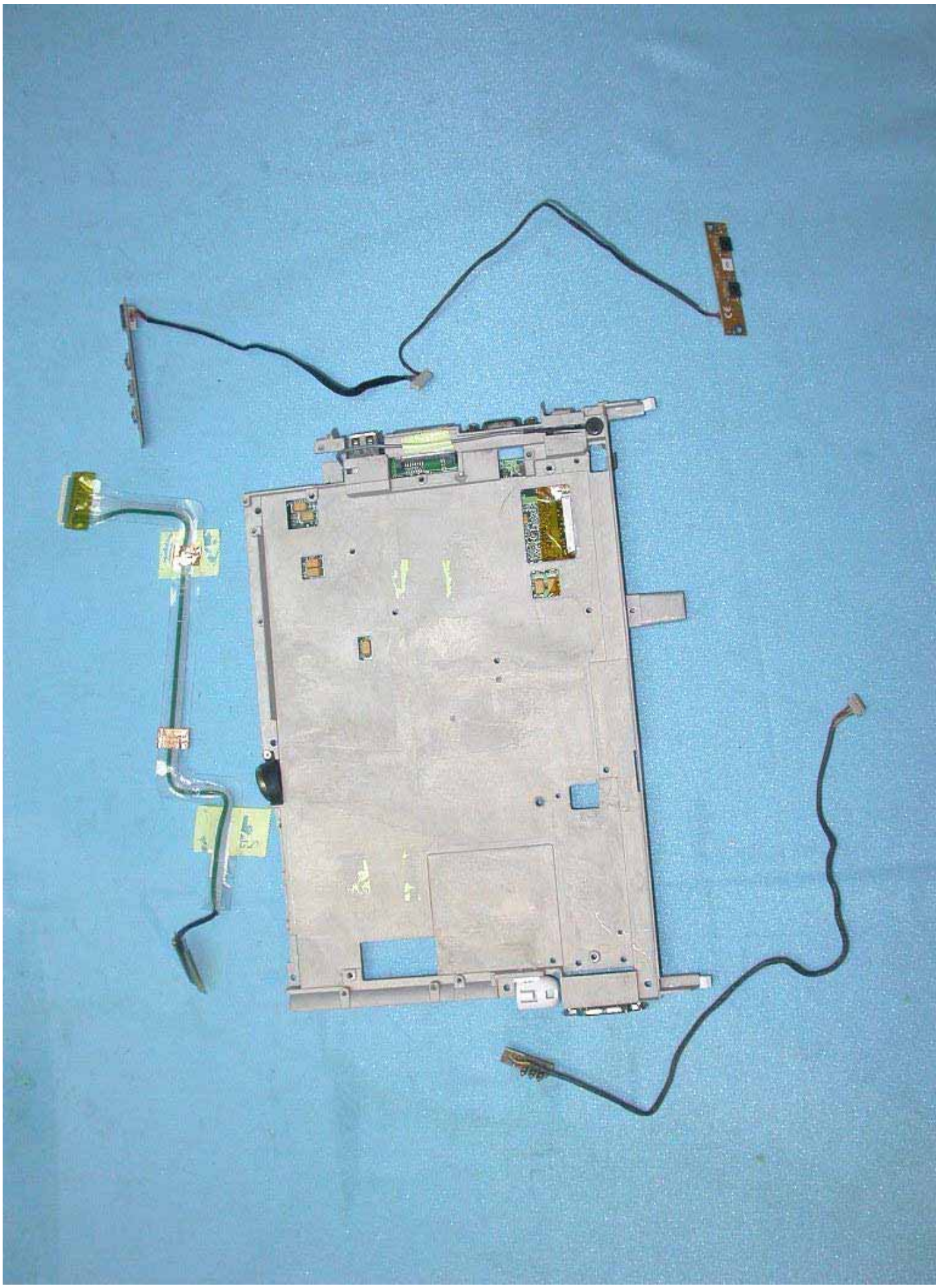


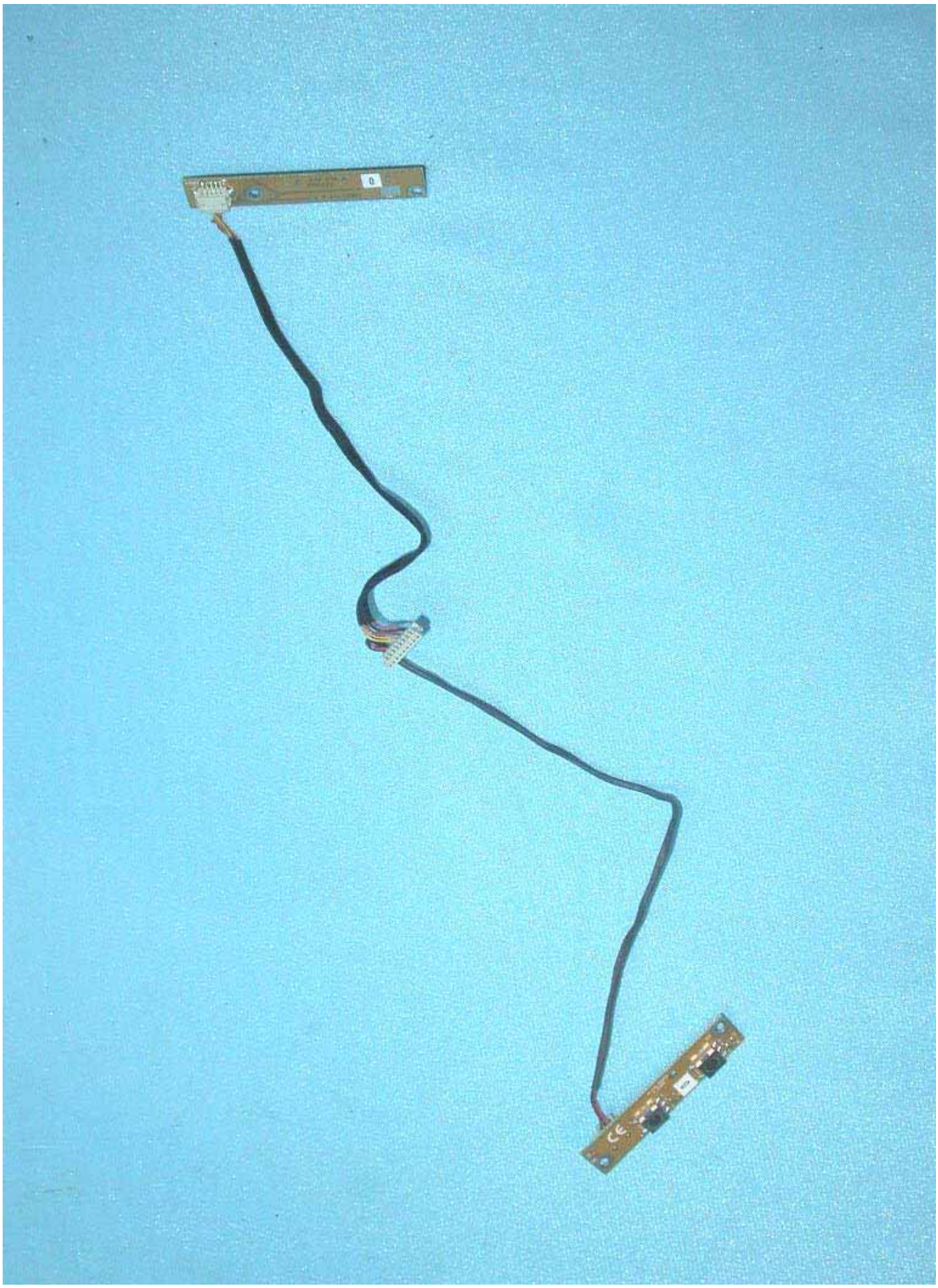


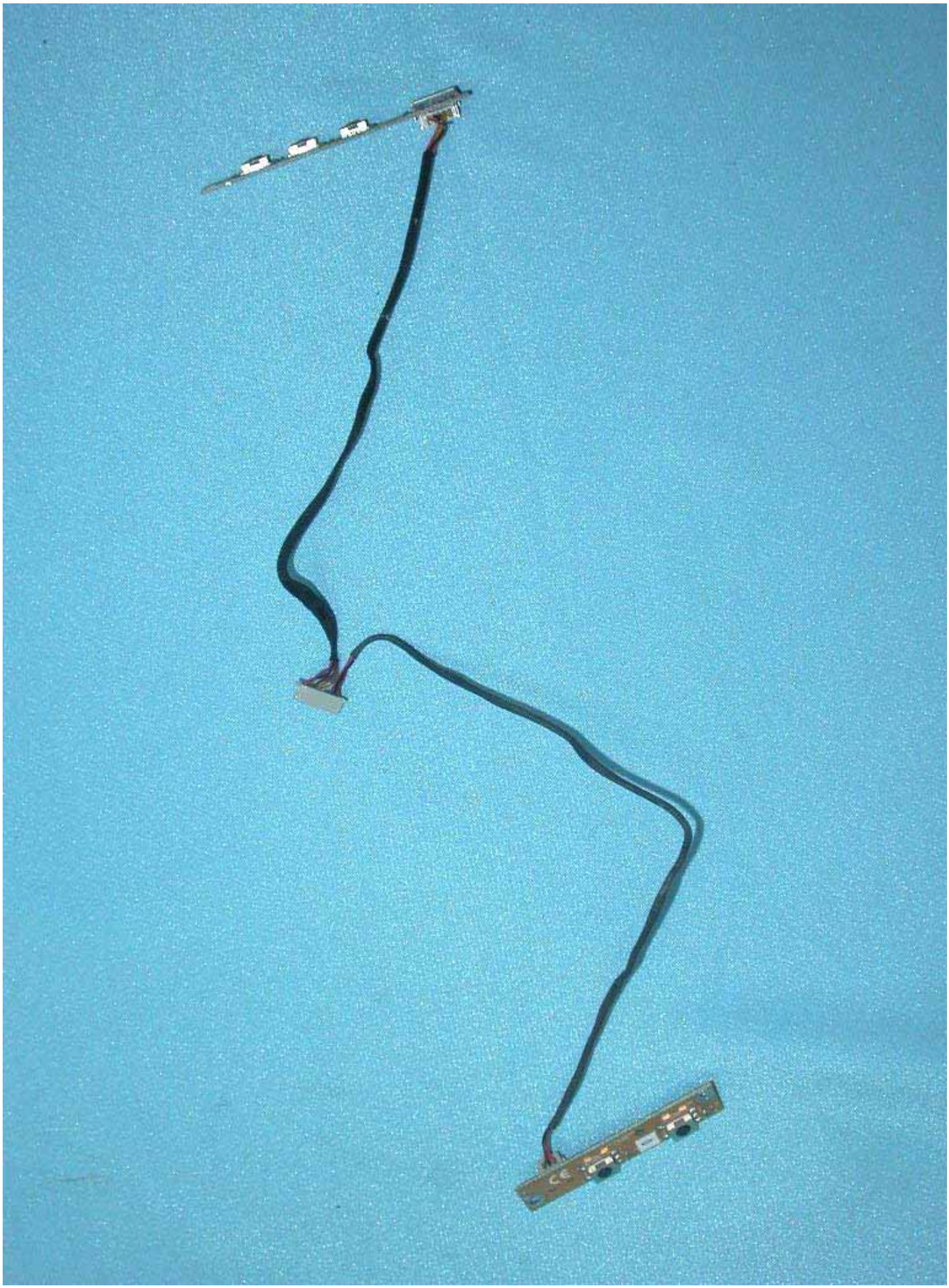

VIA
Lynx3DMB+

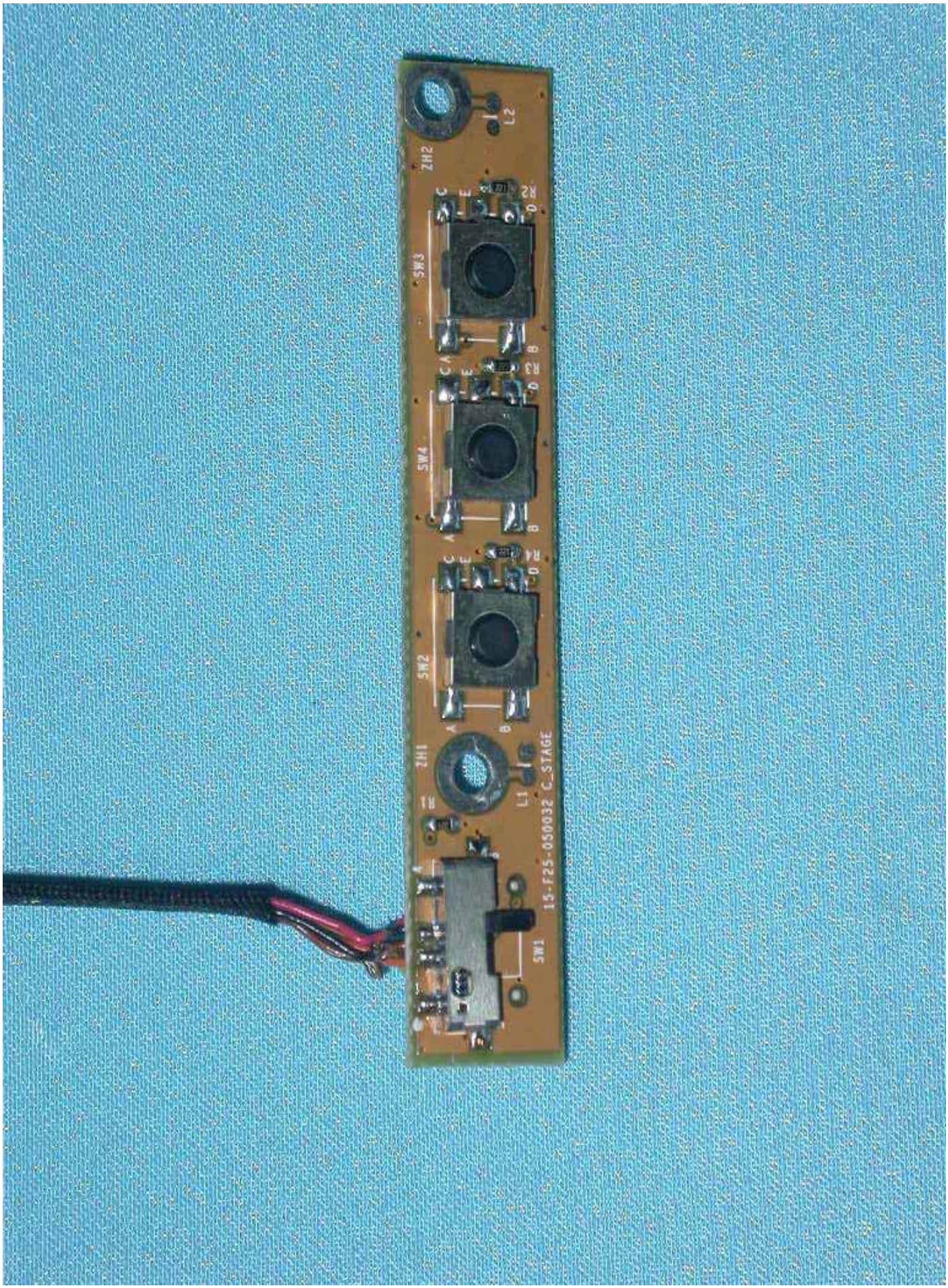











L2
ZH2
R2
SW3
R3
SW4
R4
SW2
ZH1
L1
SW1
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