

THEORY OF OPERATION

VG-329

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This PLL – controlled FRS (Family Radio Service) transceiver provides an accurate and stable multi-channels operation.

The transceiver consists of 8 main sections:

- **Transmitting Stage**
- **Receiving Stage**
- **Squelch Control**
- **Low Voltage Detection**
- **VCO and PLL**
- **Memory Backup**
- **VOX**

- **Transmitter**

The audio is picked up either from the internal or external MIC, the audio signal is then amplified by Audio Amplifier, IC4 LM324V (A.C), and filtered by a band pass filter IC4 (C.D) . The audio is adjusted by VR1 to obtain a suitable Audio frequency response, and then modulated with the carrier by VCO.

The modulated signal output from the VCO is pre-amplified by Q13 2SC4901, Q12 2SC4901 and Q14 DRF1401. The RF output power is 0.5W. The amplified signal then passes through a low pass filter network, which consists of L12, C37 and C64 to be filtered out spurious emission, then arrive at the antenna switching circuit D4. The signal is filtered by another low pass filter circuit, which consists of L15, C62, L2, C66,NOTE2. These low pass filters are necessary to suppress the second and third harmonics. The signal is then fed into the antenna and is radiated out.

- **Receiver**

The receiver uses a double frequency super-heterodyne circuit. The first Intermediate Frequency (IF) is 21.4MHz and the second is 450kHz.

The RF signal is received by the antenna, and passes through a low pass filter network, L2, C66, C65,L14, C81, C6, L19 and C4 to filter out the unwanted signals. The RF signal is amplified by Q1. This allows only the required band signal to pass through the SAW band pass filter (BPF) F101. Then the RF signal is mixed with the local oscillation frequency by the mixer Q2. A first IF (Intermediate Frequency) 21.4MHz is produced therefore. This IF passes through crystal filter F102 to be further filtered from other unwanted signals. The first IF then is amplified by Q3 and the IF amplifier IC7 (DBL5018V). IC7 is an integrated RF amplifier which consists of a local oscillator, a demodulator, the second mixer, squelch control circuit, and RF demodulation. The 21.4MHz IF goes to IC7 pin16, and is mixed here within the second mixer and converted to second IF, which then passes through a ceramic filter F103 to filter out the unwanted signal at pin 5 of IC7 (DBL5018V). It is the final IF signal and the audio signal is output from pin9 of IC7 (DBL5018V).

IC2 LM324(A,D) is the CTCSS processing circuit, from pin 14 of which, CTCSS signal is output. The

amplified audio is output from IC2(B,C) LM324 pin 8, then passes through a volume control circuit, MCU pin 28,29,30 and 31. Finally it is amplified by Audio amplifier IC3 (NJM2070) and heard from the speaker.

- **Squelch Control**

The squelch control is also fulfilled by IC7 (DBL5018V). The audio signal passes through IC7 (DBL5018V) internal squelch control Q4,D7, C5, R19,R25,R24,R23 and C53 that form as a squelch amplifier. The filter produces a squelch signal (RF noise).

The Squelch signal is converted to DC signal by Q4 and is output to IC8 MCU pin 42 from IC7 pin 13. Mute signal is produced by MCU to control the audio amplifier circuit.

- **Low Voltage Detection**

The battery voltage divided by R79, R78 is input to IC8 pin 23 for voltage level comparison. If the battery voltage drops below 3.2 V, the low voltage symbol is to be displayed on LCD.

- **VCO and PLL**

Both signal reception and transmission share the same PLL (Phase Lock Loop) Circuit to produce the carrier or the receiving frequency. The local oscillator consists of a fundamental frequency oscillator X1 and a Phase Lock Loop (PLL) IC1 (KB8825). The fundamental frequency is determined by X1 (20.950 MHz) that is regarded as the PLL reference oscillator. This signal frequency is divided by IC1 and a 12.5 kHz signal is produced. When the frequency of VCO divided by IC1 is comparable to 12.5 kHz, PLL will control the VCO. When these two frequencies are matched, a constant control voltage is output from PLL to lock VCO in desired frequency.

- **Memory Backup**

IC5 is an EEPROM AT24C02, which acts as memory backup for the working channel code and the system parameters. Every time when the unit is switched on, the MCU will reset the system, clear the RAM, and recall the memory from the EEPROM to refresh the RAM in MCU IC8.

- **VOX**

The MIC signal is amplified and filtered by IC4 LM324V (A,D). The amplified signal is detected through a AD converter of MCU and is compared to DC level that can be detected by MCU. When the DC level matches the setting level. MCU then switches the unit into TX mode. VOX sensitivity can be adjusted by selecting MENU button for different environmental condition.