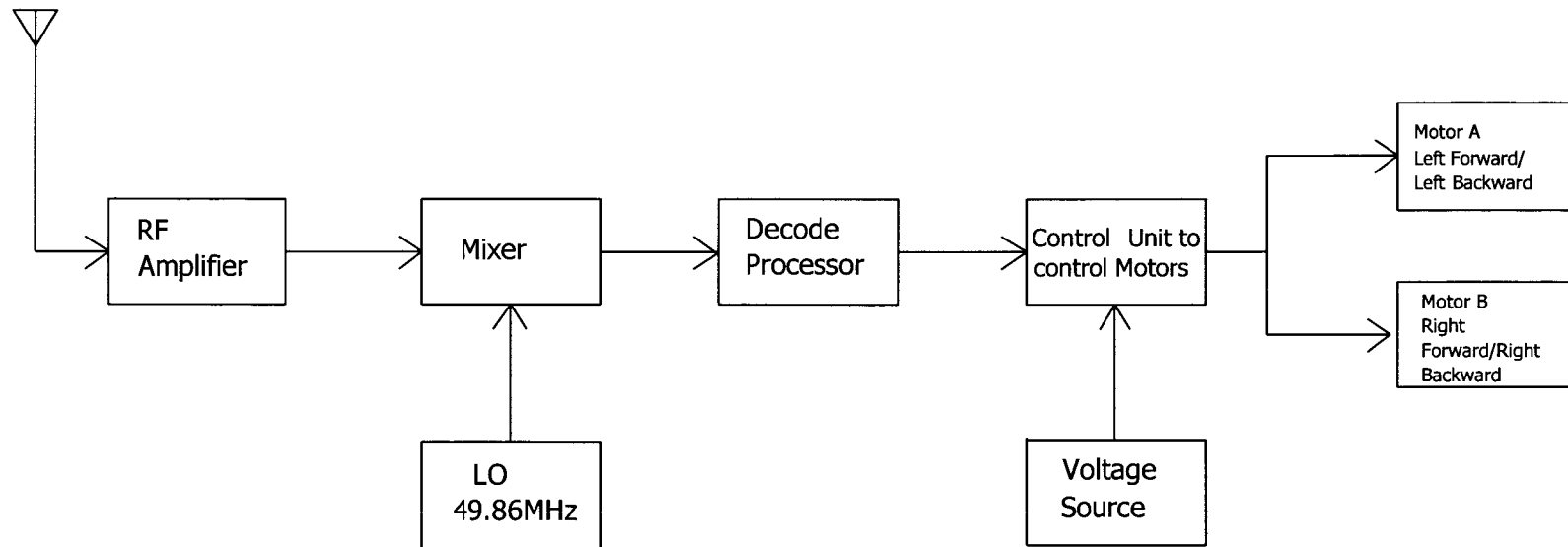




Functional block diagram of Receiver for 49.860MHz Formula One