

Electrical Description

Block Diagram

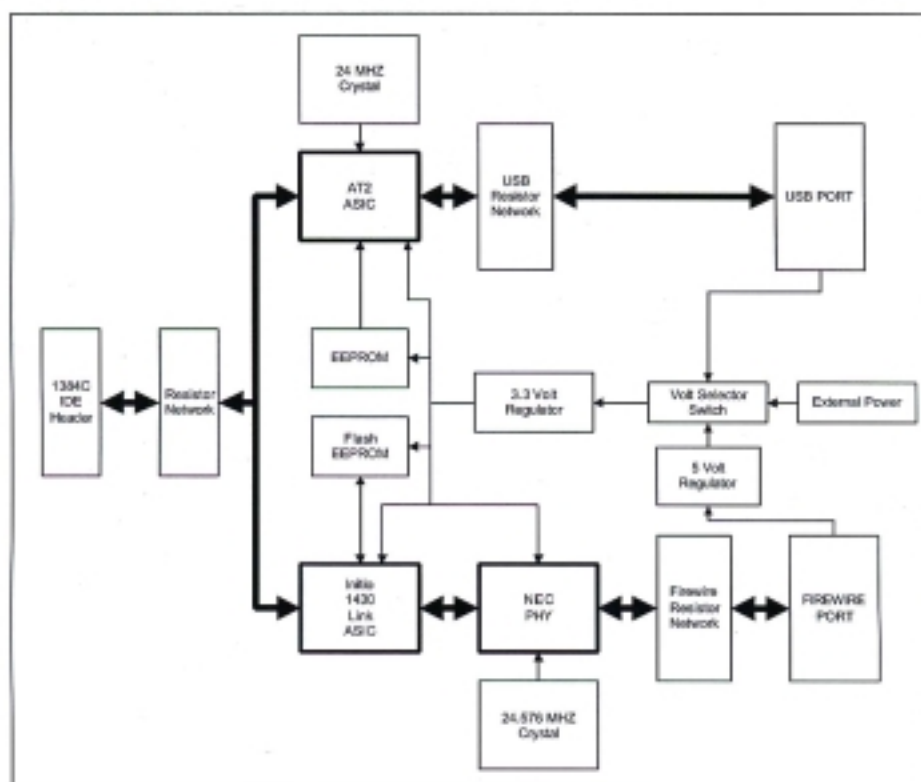


Figure 2: Electrical Block Diagram

USB Resistor Network

This resistor network consists of two 36 Ω resistors and a 1.5 k Ω resistor. Each differential signal requires a 36 Ω series resistor for impedance matching between the device transceiver and the host transceiver. The 1.5 k Ω resistor is used as a pull-up resistor on the D+ signal. This pull-up placed on the D+ line signals the host that it is communicating with a "High Speed", 480 M bits/sec device.