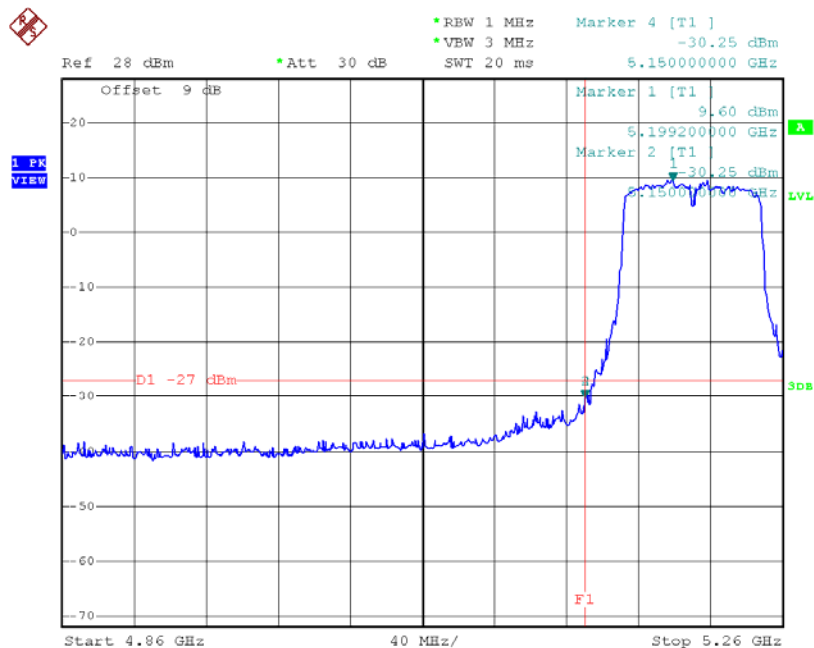
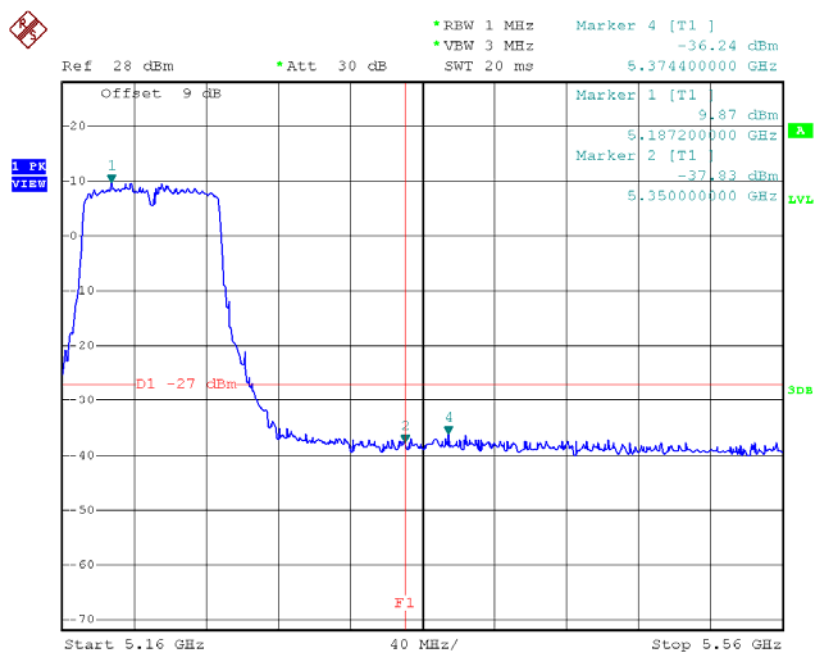


Test Mode: UNII-1/TX AC80 Mode

TX mode CH42



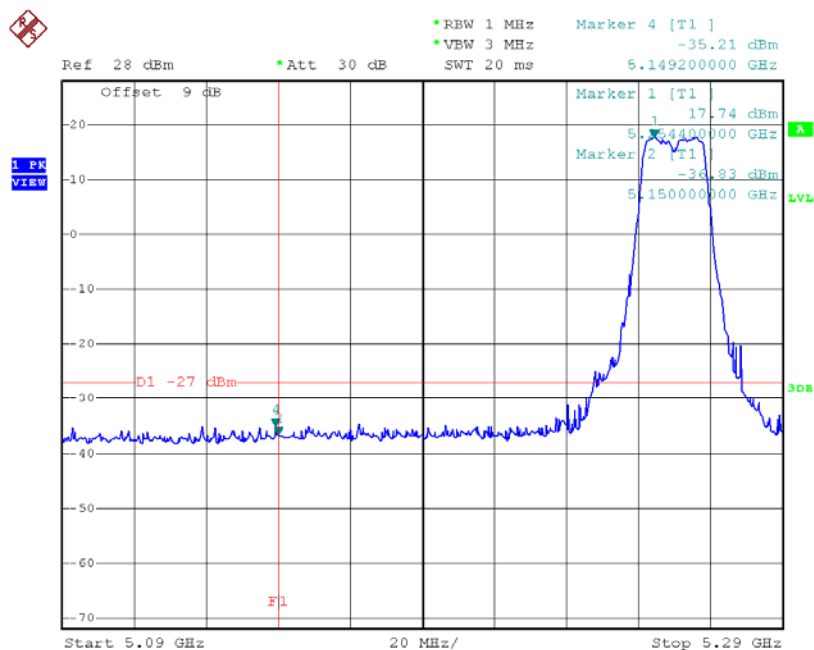
Date: 1.APR.2015 20:01:59



Date: 1.APR.2015 20:02:06

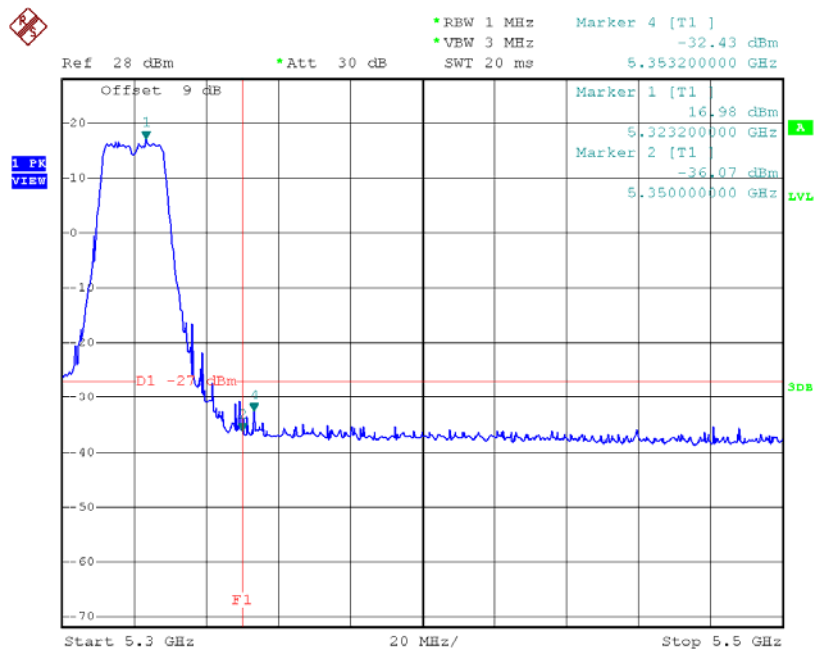
Test Mode: UNII-2A/TX AC20 Mode

TX mode CH52



Date: 1.APR.2015 16:04:30

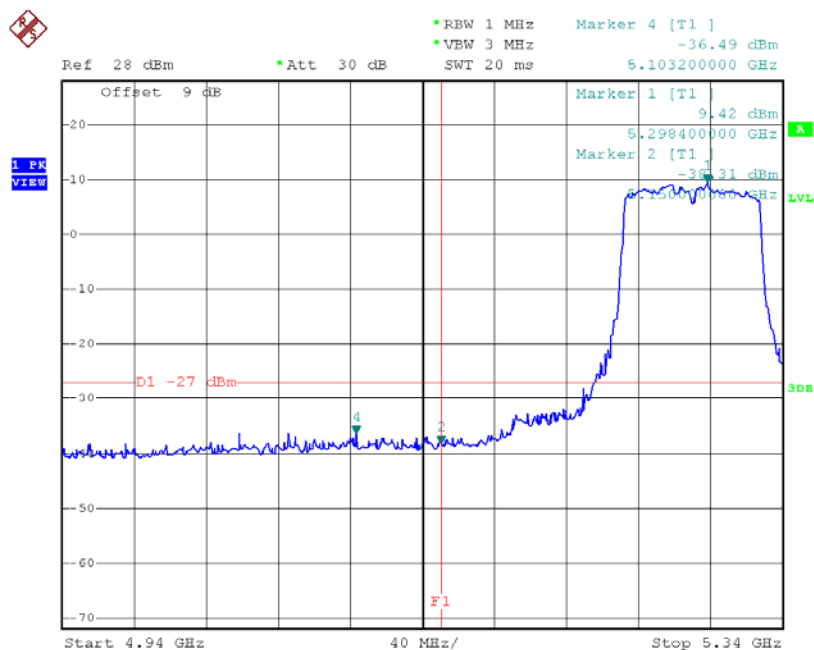
TX mode CH64



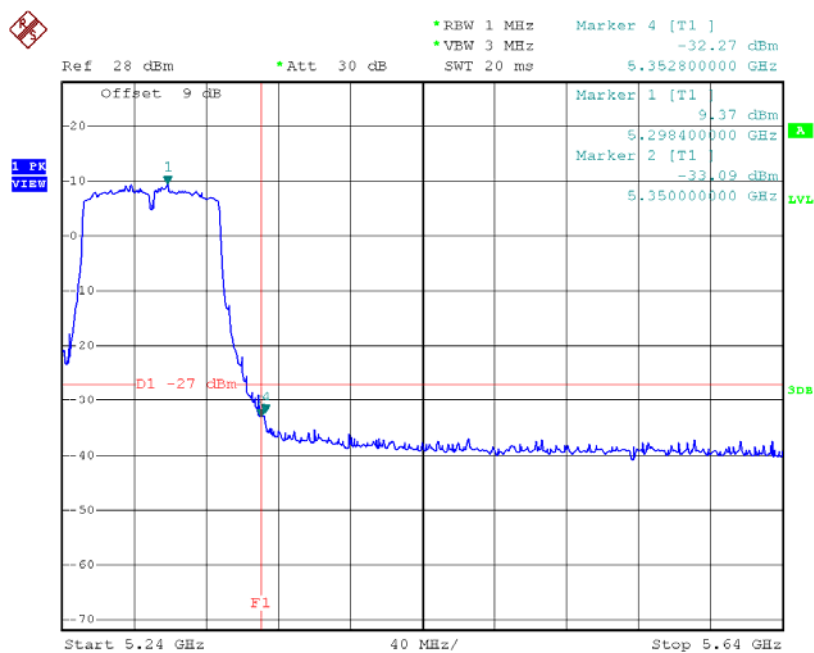
Date: 1.APR.2015 16:06:52

Test Mode: UNII-2A/TX AC80 Mode

TX mode CH58



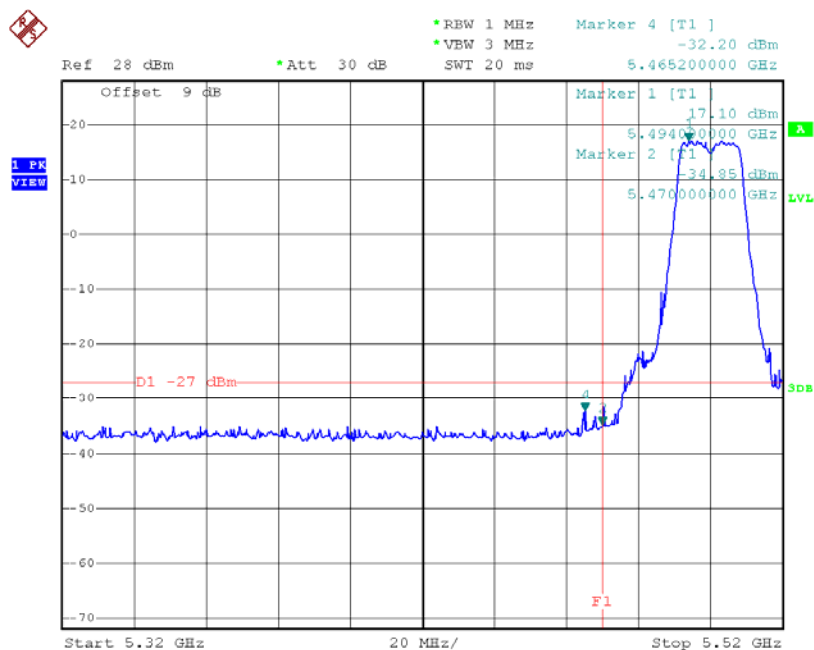
Date: 1.APR.2015 20:03:07



Date: 1.APR.2015 20:03:23

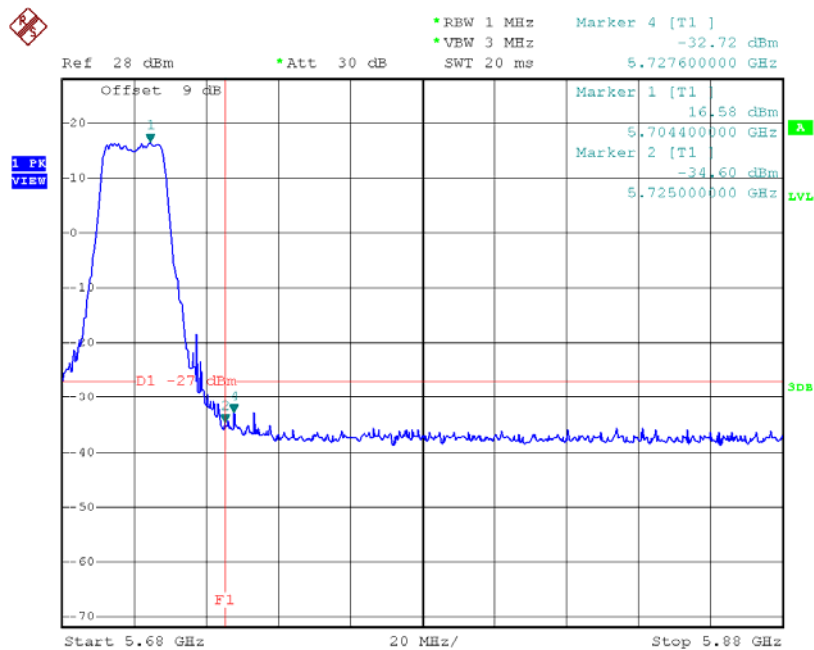
Test Mode: UNII-2C/TX AC20 Mode

TX mode CH100



Date: 1.APR.2015 16:08:54

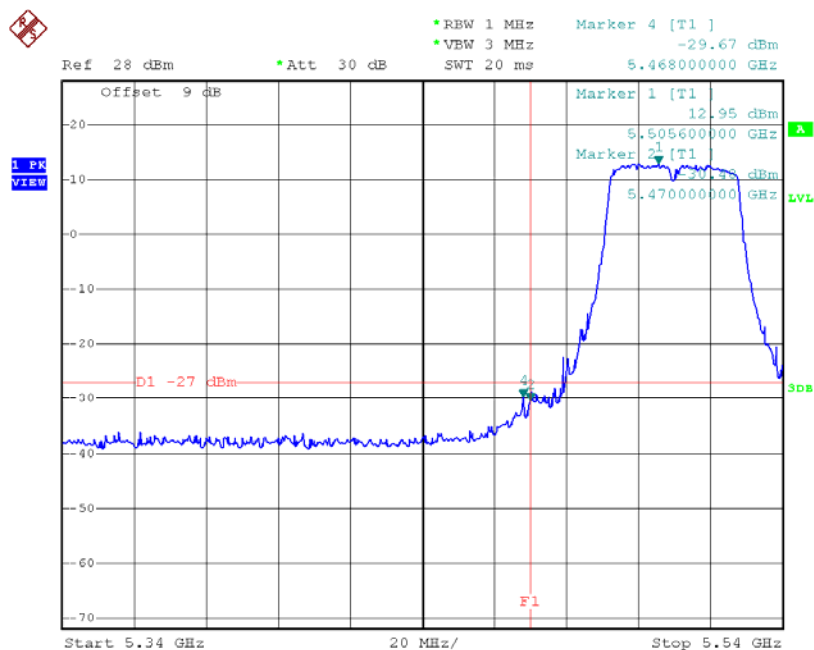
TX mode CH140



Date: 1.APR.2015 16:10:06

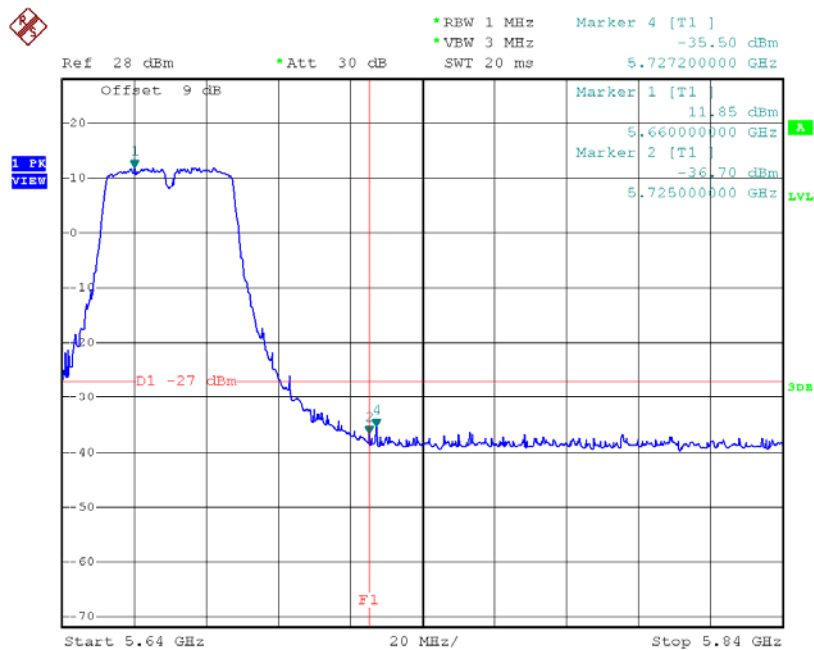
Test Mode:	UNII-2C/TX AC40 Mode
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TX mode CH102



Date: 1.APR.2015 19:56:11

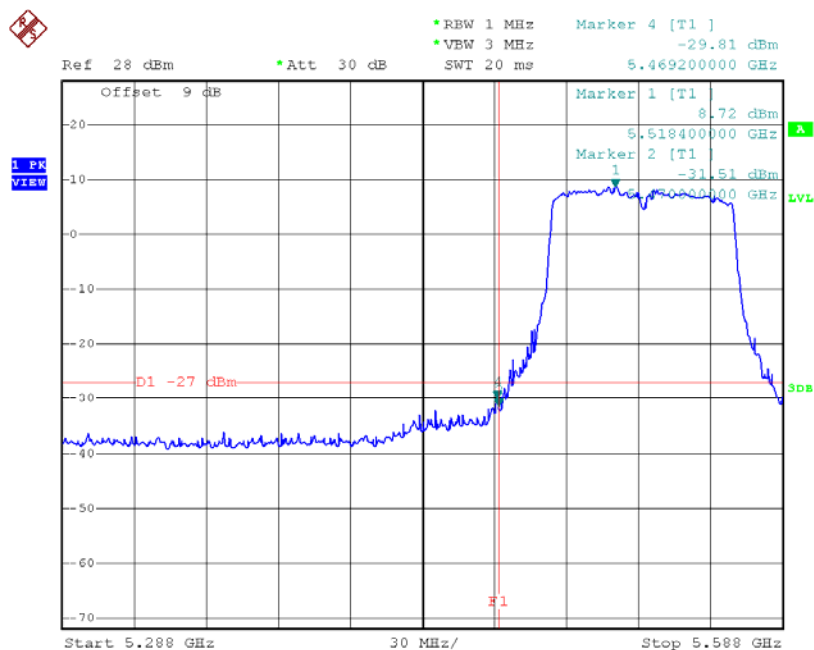
TX mode CH134



Date: 1.APR.2015 19:58:10

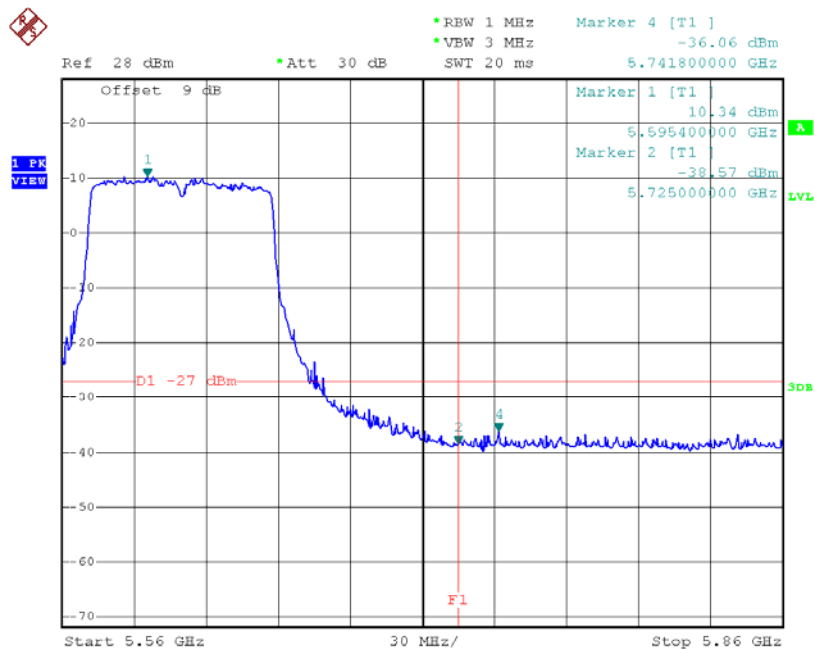
Test Mode: UNII-2C/TX AC80 Mode

TX mode CH106



Date: 1.APR.2015 20:04:20

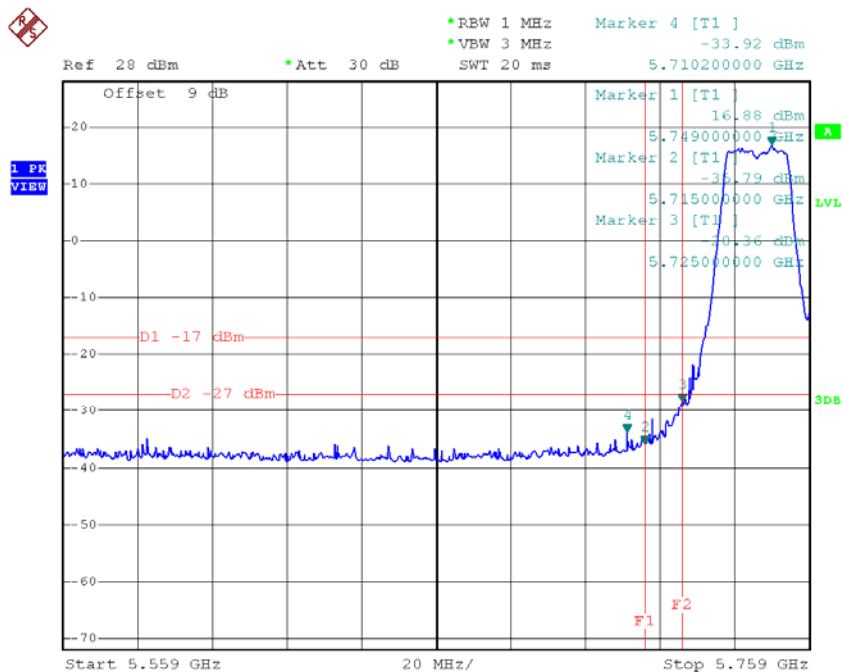
TX mode CH122



Date: 1.APR.2015 20:05:29

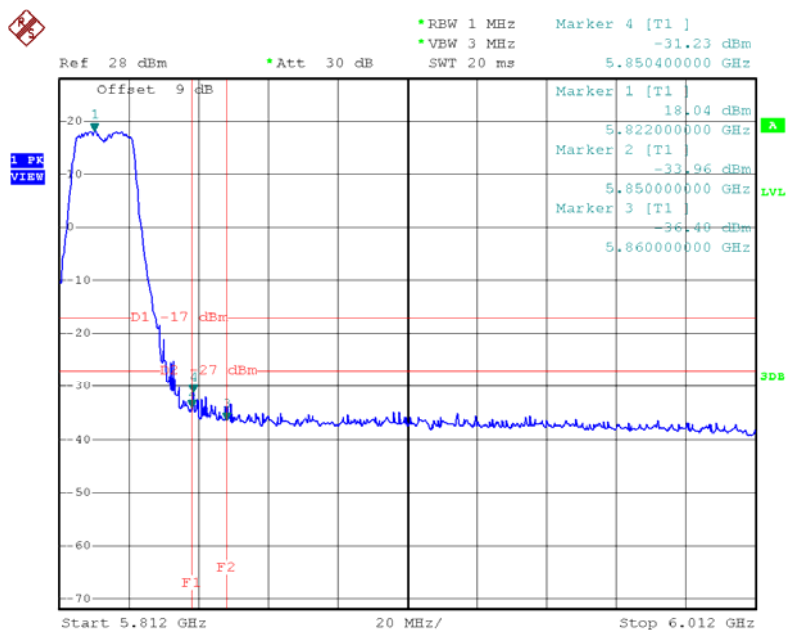
Test Mode: UNII-3/TX AC20 Mode

TX AC HT20 mode CH149



Date: 1.APR.2015 16:11:14

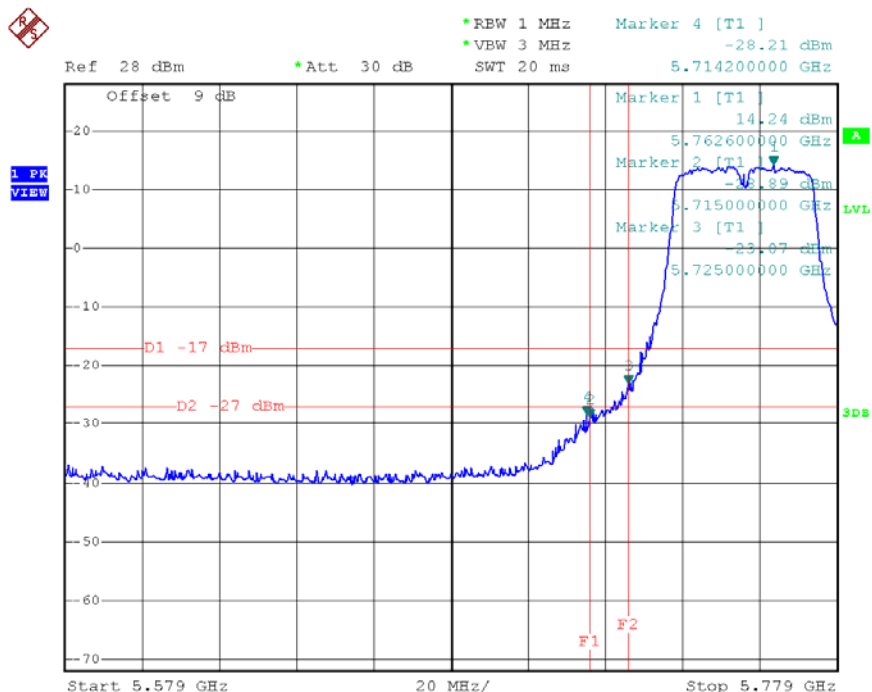
TX AC HT20 mode CH165



Date: 1.APR.2015 19:38:07

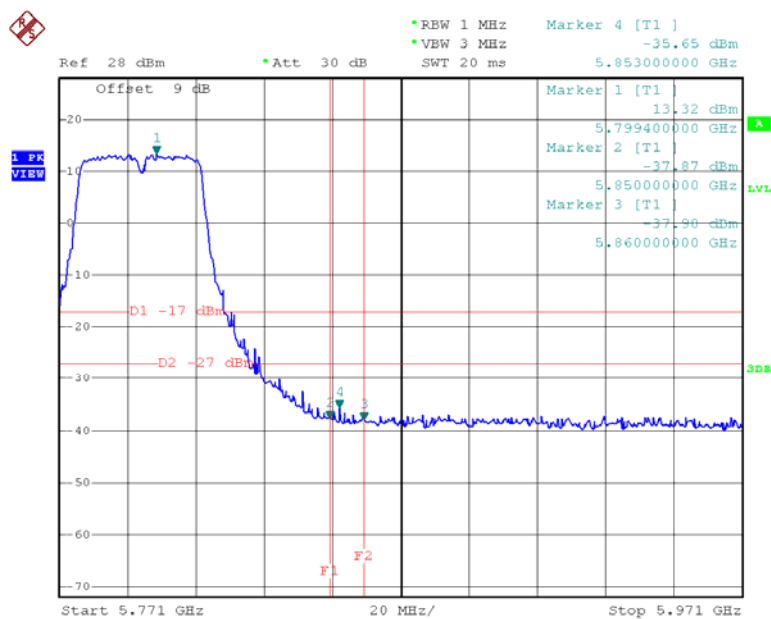
Test Mode: UNII-3/TX AC40 Mode

TX AC HT40 mode CH151



Date: 1.APR.2015 19:59:04

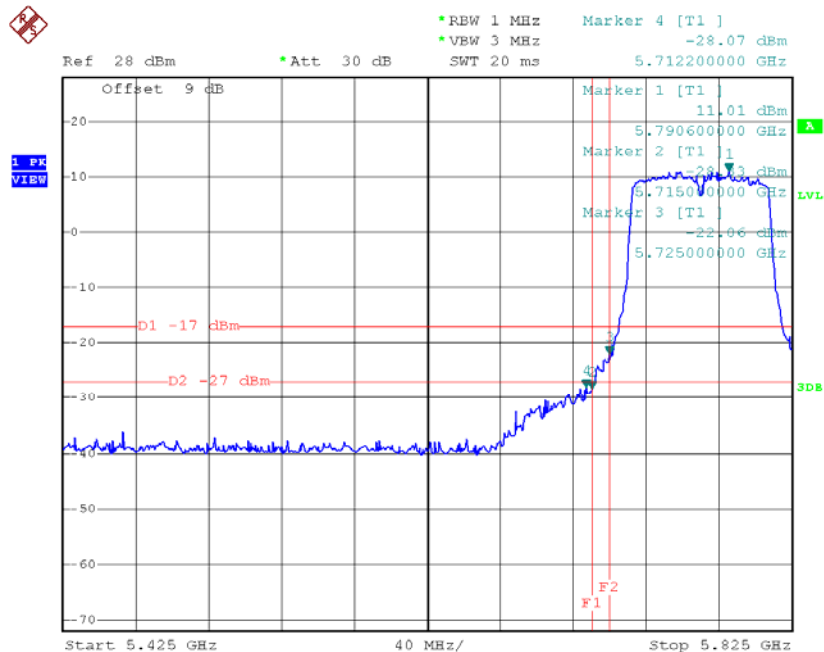
TX AC HT40 mode CH159



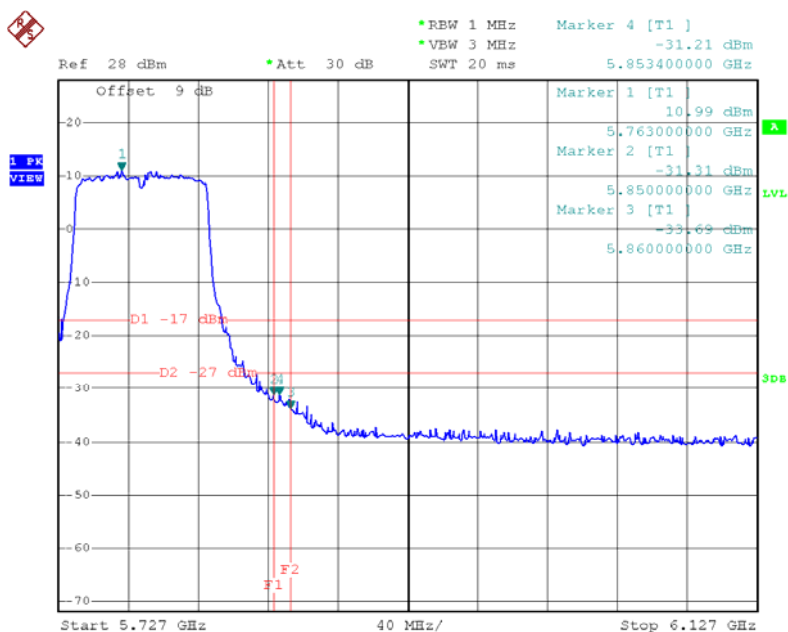
Date: 1.APR.2015 19:59:47

Test Mode: UNII-3/TX AC80 Mode

TX AC HT80 mode CH155



Date: 1.APR.2015 20:06:22

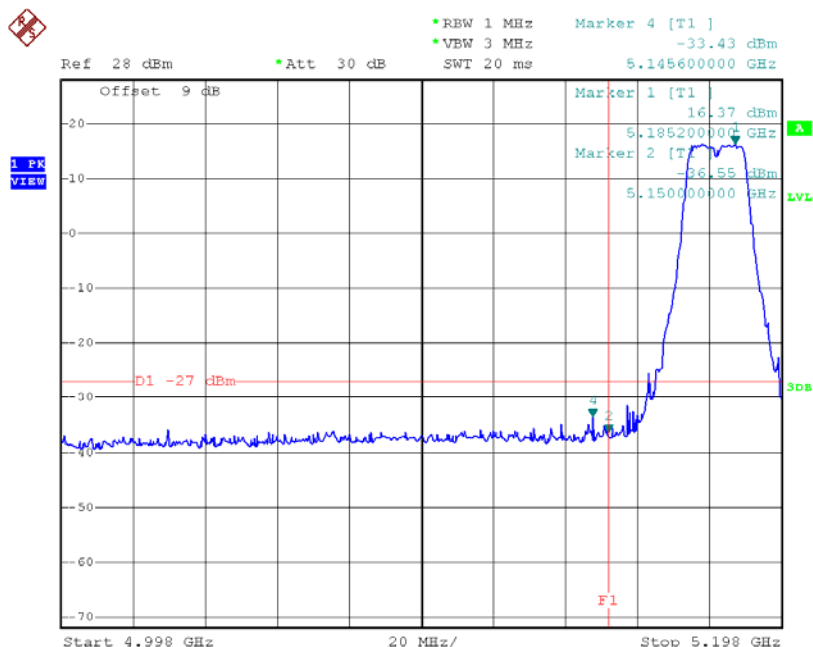


Date: 1.APR.2015 20:06:29

For 2TX

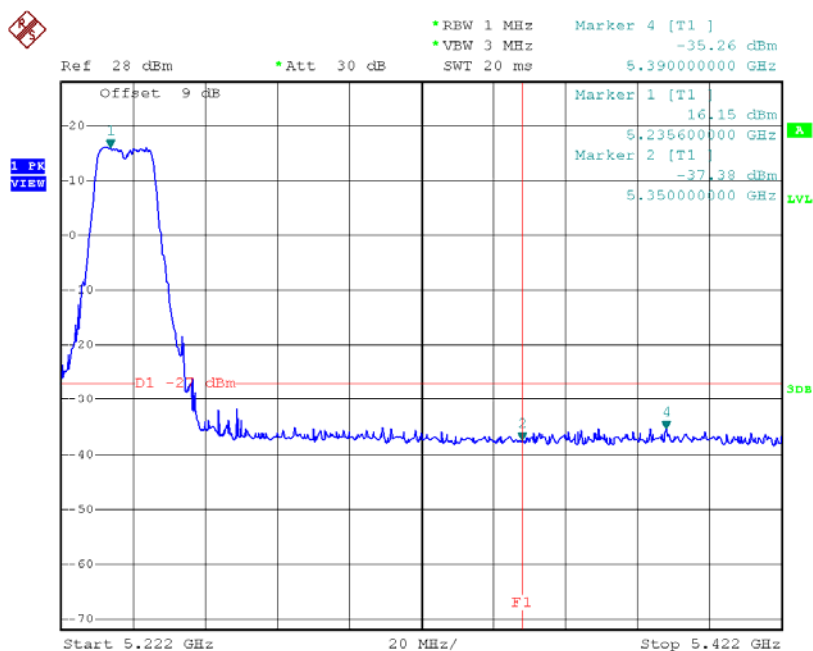
Test Mode: UNII-1/TX A Mode_ANT 1

TX mode CH36



Date: 1.APR.2015 21:25:15

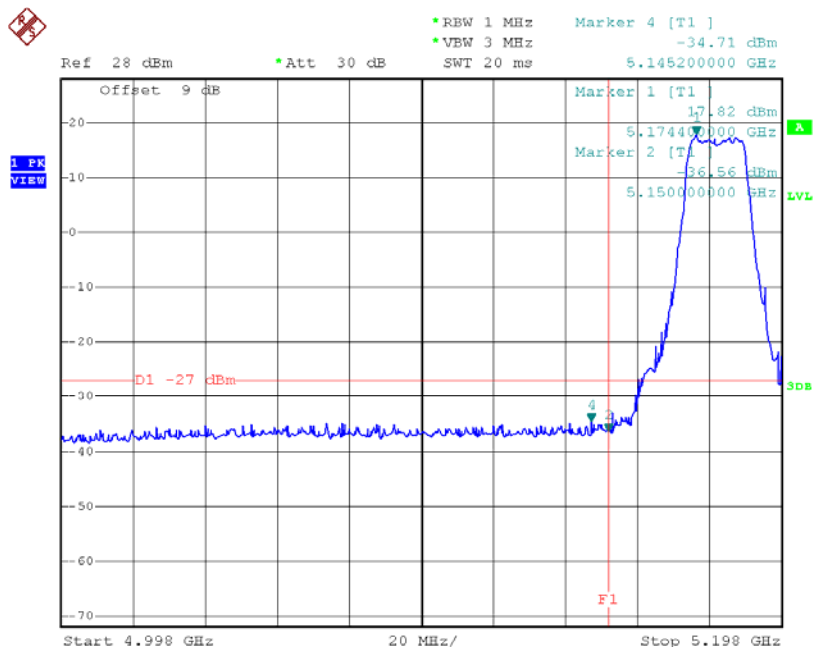
TX modeCH48



Date: 1.APR.2015 20:34:19

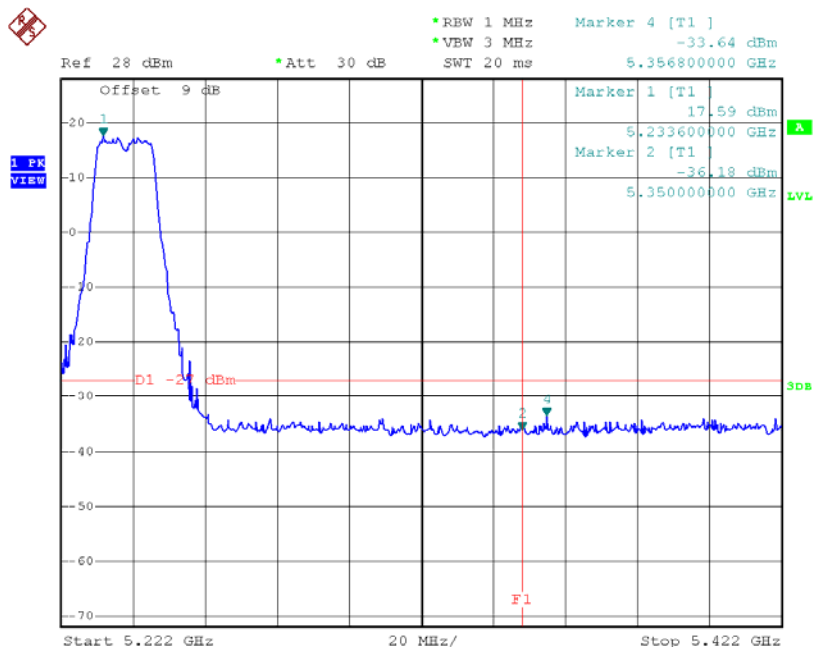
Test Mode: UNII-1/TX A Mode_ANT 2

TX mode CH36



Date: 1.APR.2015 21:29:25

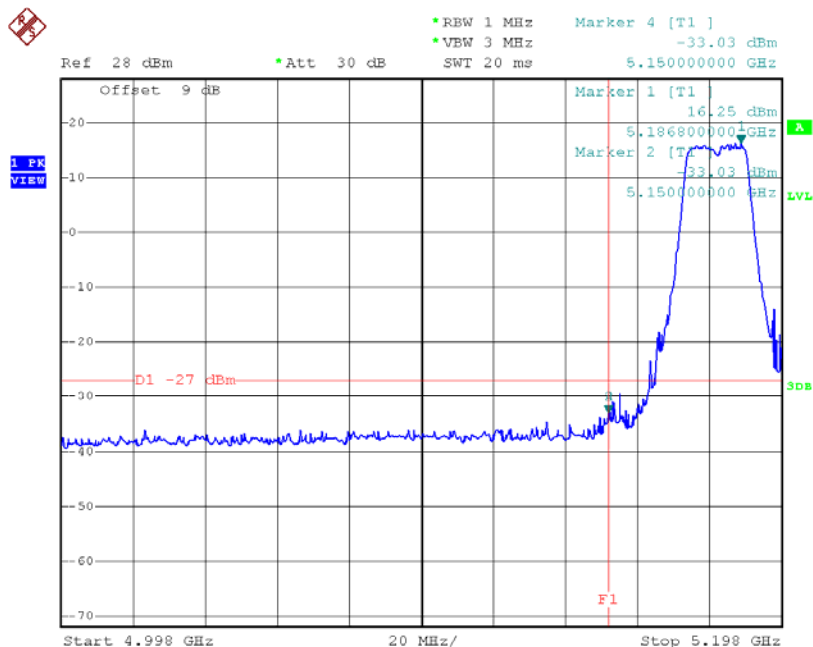
TX modeCH48



Date: 1.APR.2015 21:30:50

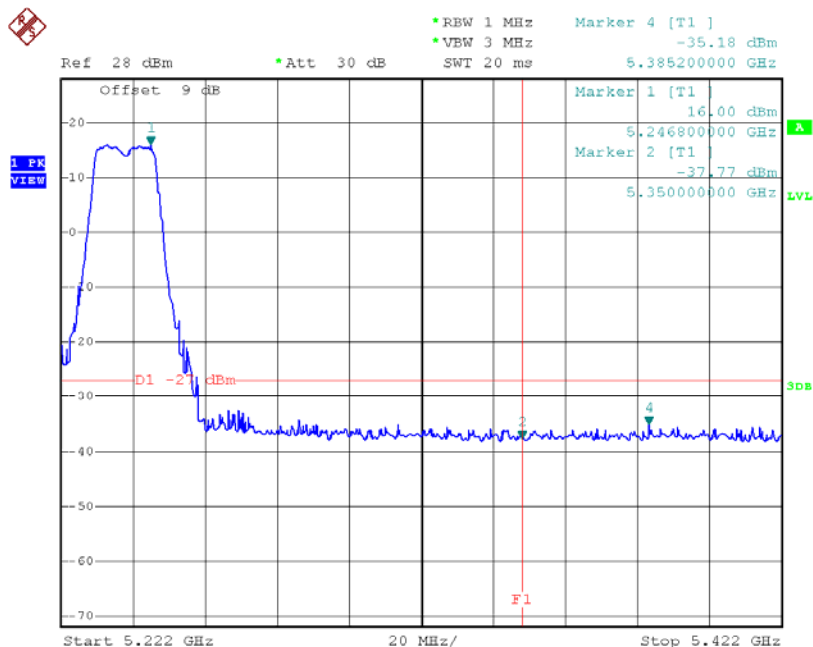
Test Mode: UNII-1/TX N20 Mode_ANT 1

TX mode CH36



Date: 1.APR.2015 20:42:02

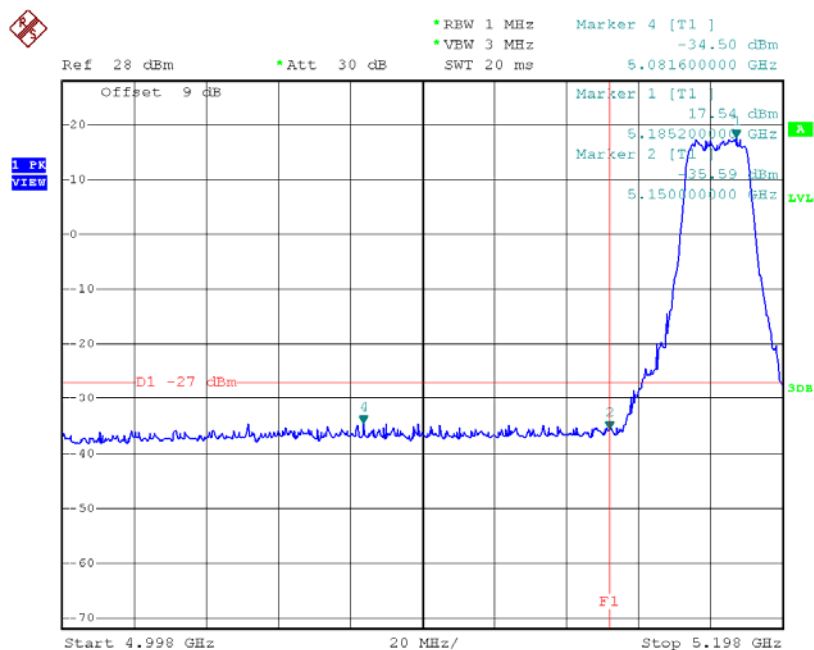
TX modeCH48



Date: 1.APR.2015 20:42:57

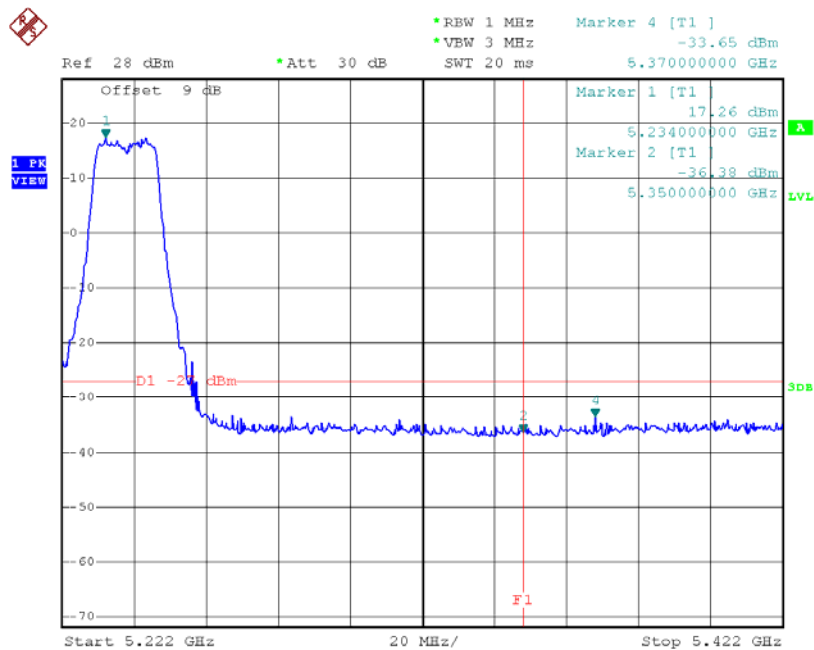
Test Mode: UNII-1/TX N20 Mode_ANT 2

TX mode CH36



Date: 1.APR.2015 21:37:55

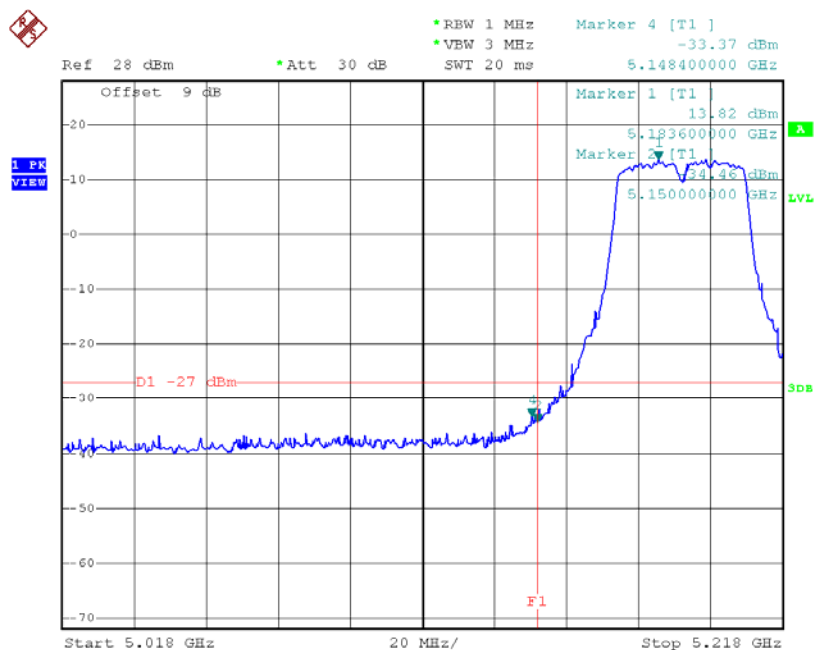
TX mode CH48



Date: 1.APR.2015 21:38:56

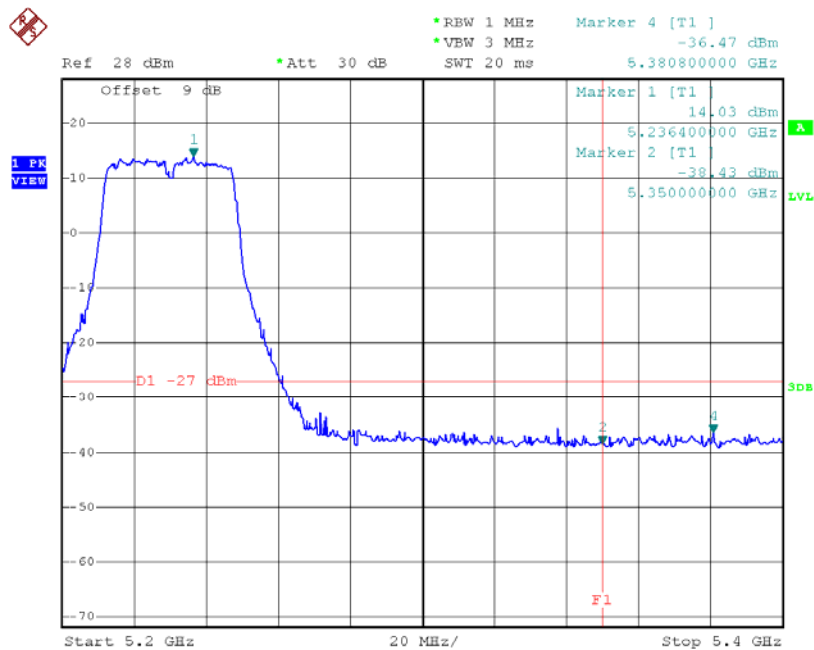
Test Mode:	UNII-1/TX N40 Mode_ANT 1
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TX mode CH38



Date: 1.APR.2015 20:53:17

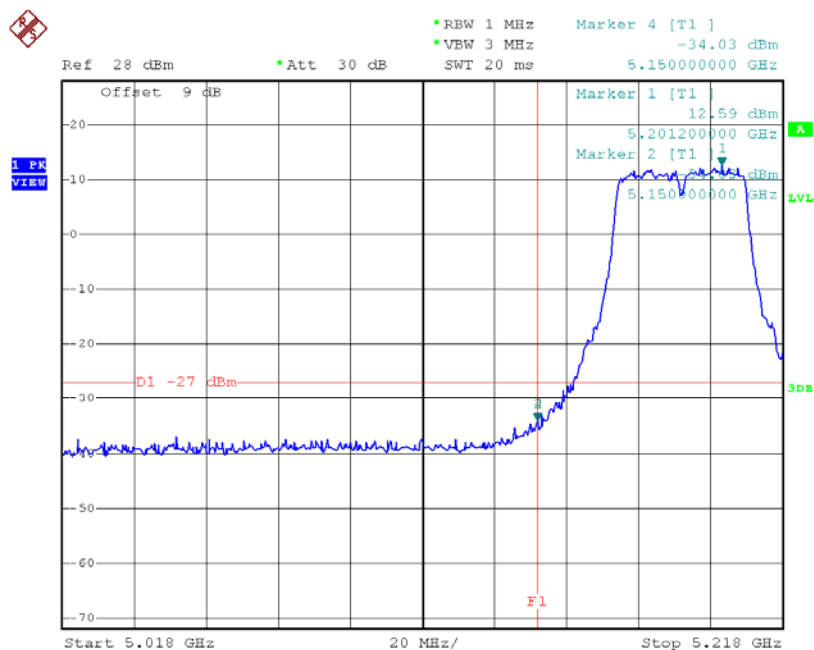
TX mode CH46



Date: 1.APR.2015 20:53:47

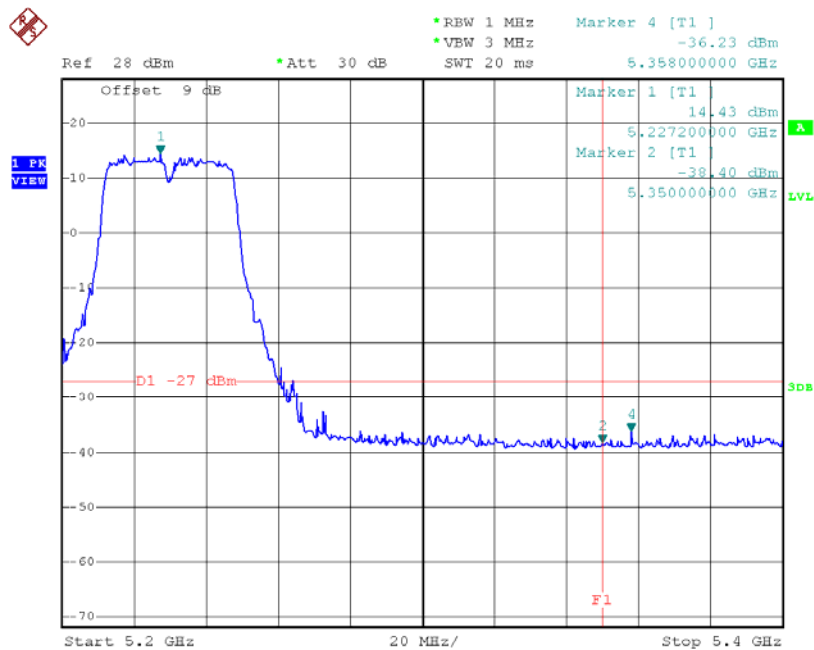
Test Mode:	UNII-1/TX N40 Mode_ANT 2
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TX mode CH38



Date: 1.APR.2015 21:48:15

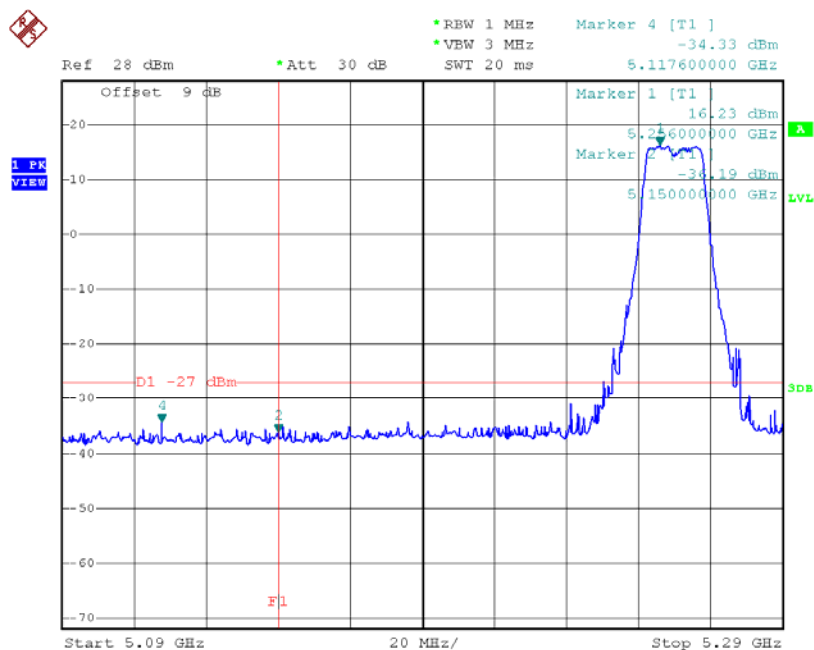
TX mode CH46



Date: 1.APR.2015 21:48:45

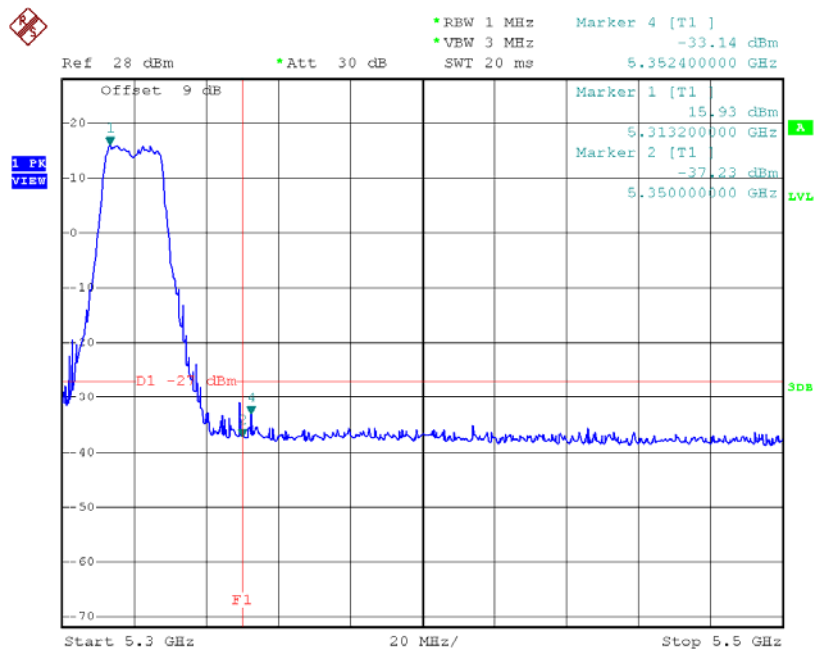
Test Mode:	UNII-2A/TX A Mode_ANT 1
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TX mode CH52



Date: 1.APR.2015 20:35:19

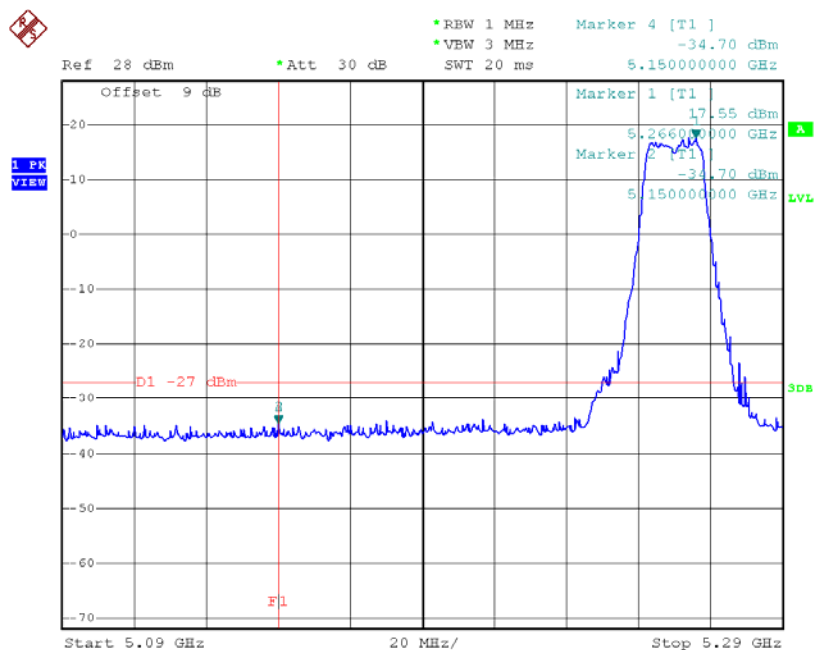
TX mode CH64



Date: 1.APR.2015 20:36:41

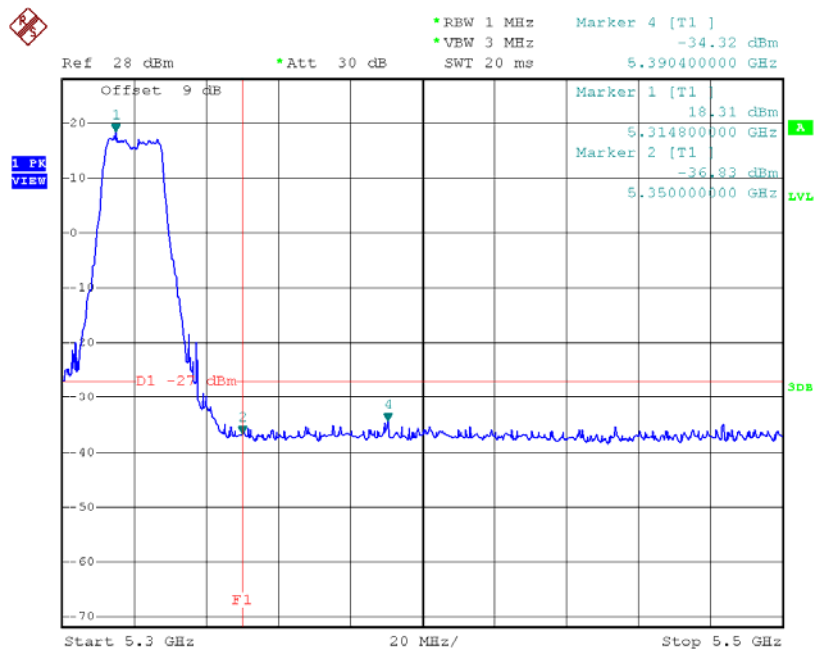
Test Mode:	UNII-2A/TX A Mode_ANT 2
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TX mode CH52



Date: 1.APR.2015 21:31:29

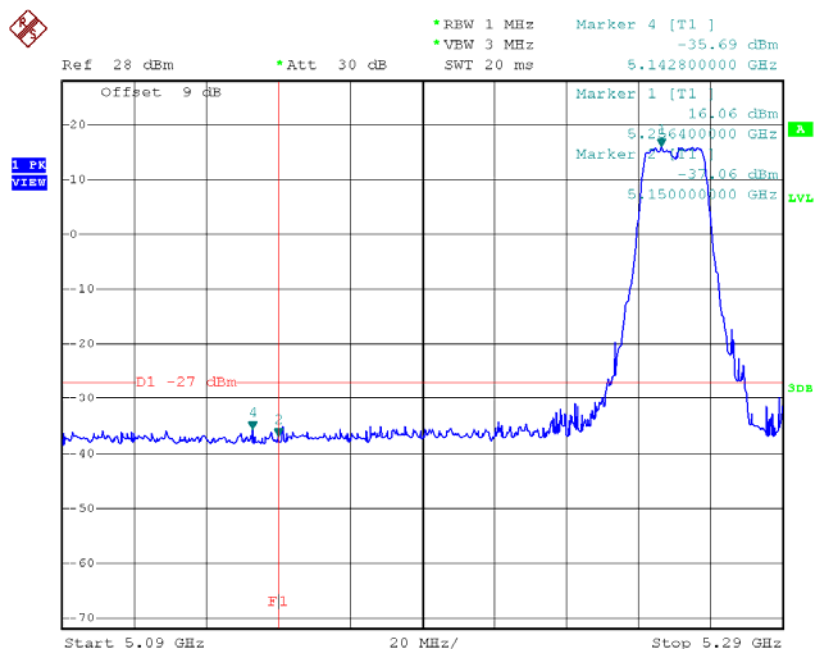
TX mode CH64



Date: 1.APR.2015 21:32:59

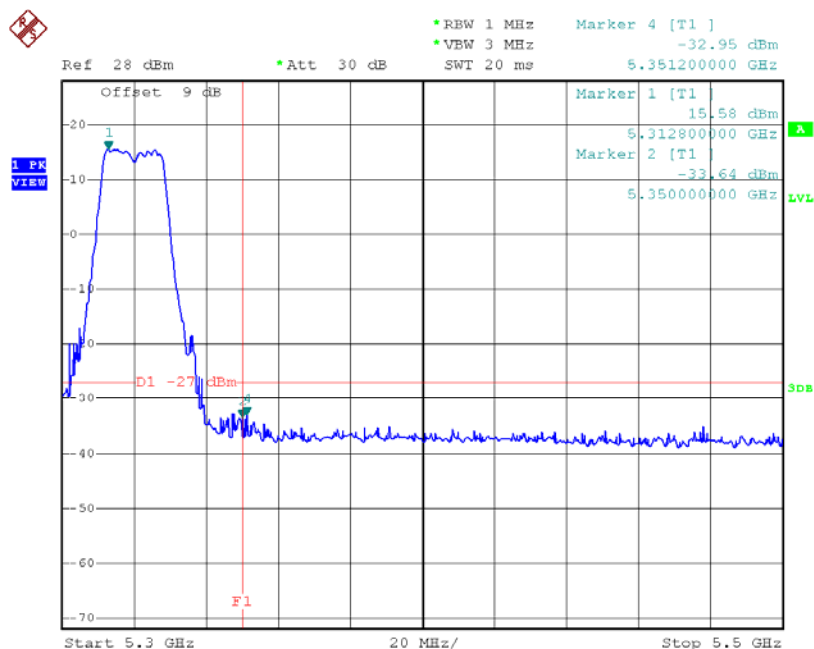
Test Mode: UNII-2A/TX N20 Mode_ANT 1

TX mode CH52



Date: 1.APR.2015 20:43:35

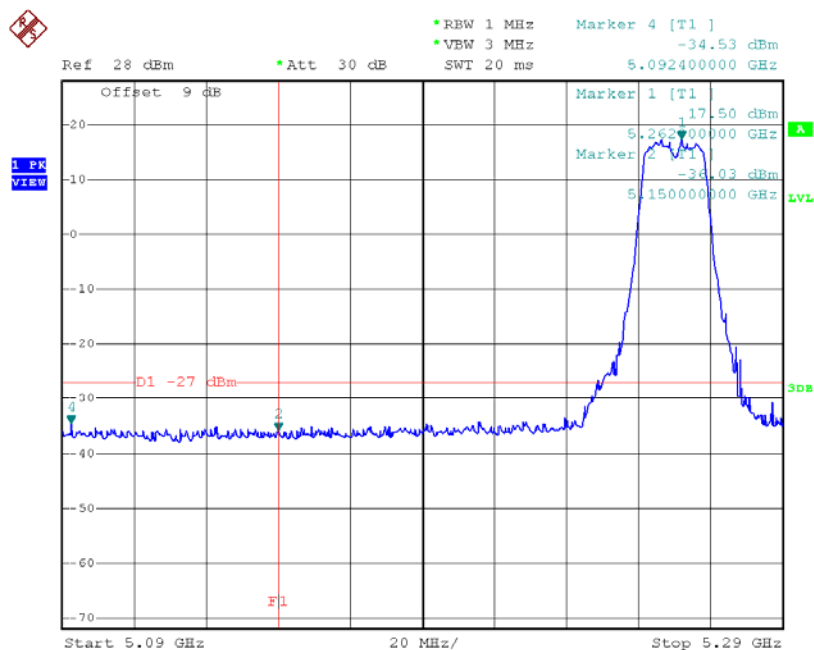
TX modeCH64



Date: 1.APR.2015 20:44:20

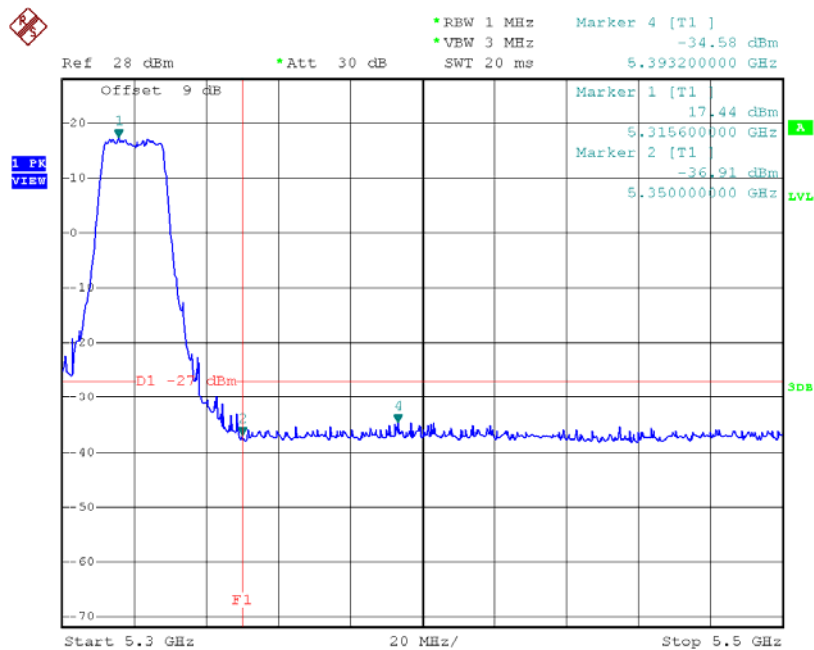
Test Mode: UNII-2A/TX N20 Mode_ANT 2

TX mode CH52



Date: 1.APR.2015 21:39:20

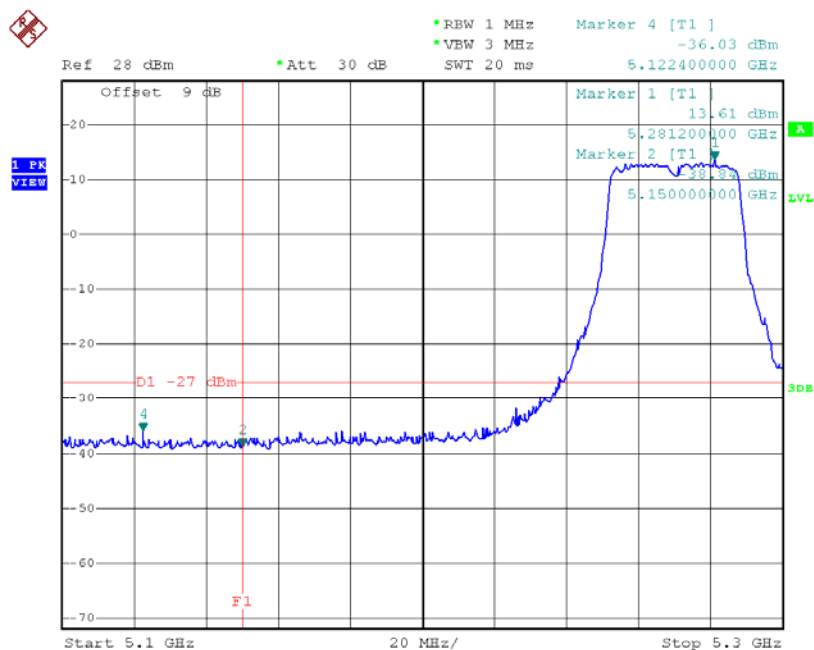
TX mode CH64



Date: 1.APR.2015 21:40:05

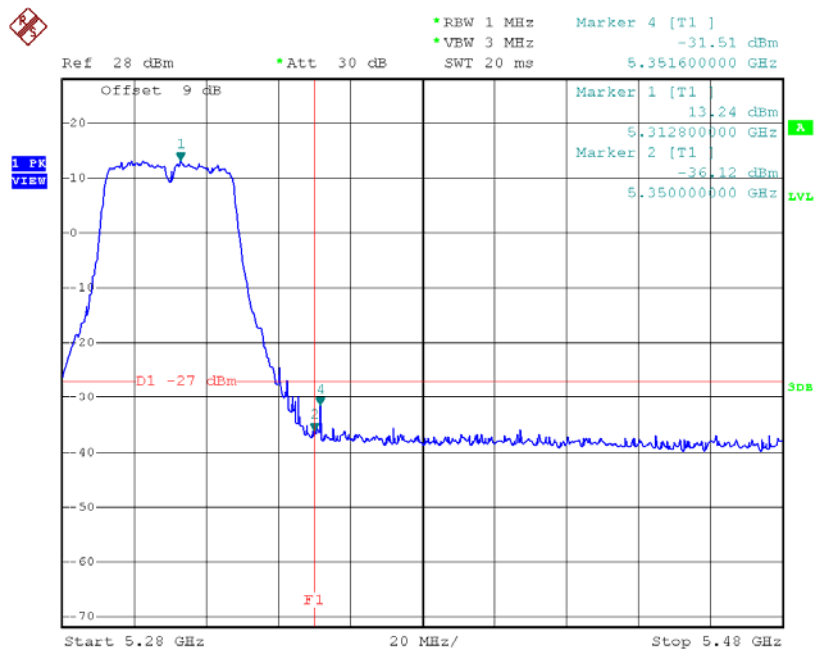
Test Mode: UNII-2A/TX N40 Mode_ANT 1

TX mode CH54



Date: 1.APR.2015 20:54:14

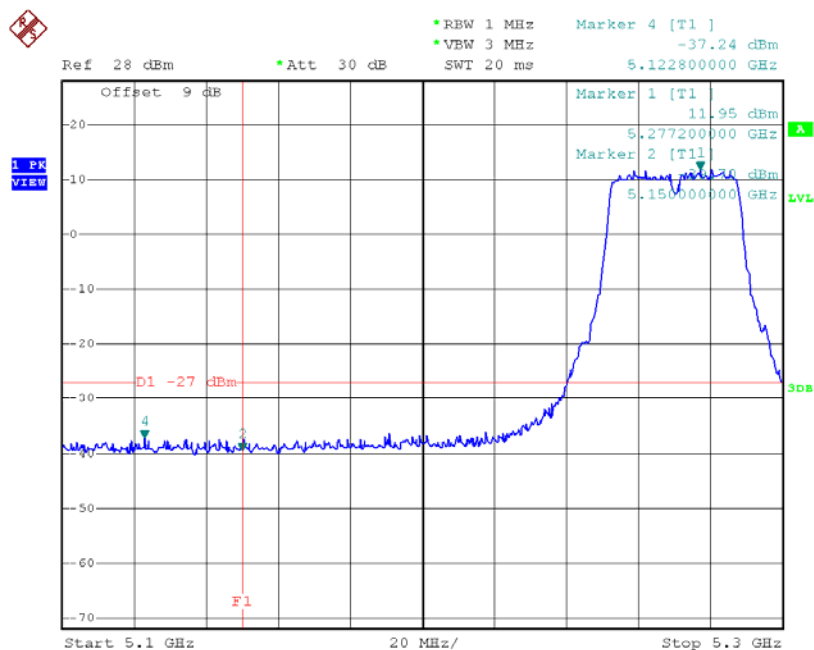
TX mode CH62



Date: 1.APR.2015 20:54:41

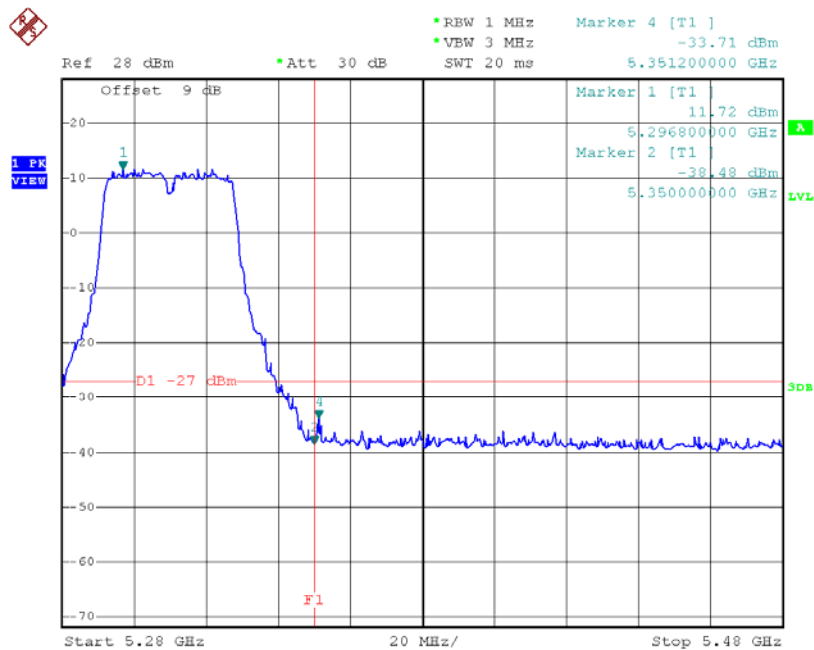
Test Mode: UNII-2A/TX N40 Mode_ANT 2

TX mode CH54



Date: 1.APR.2015 21:49:10

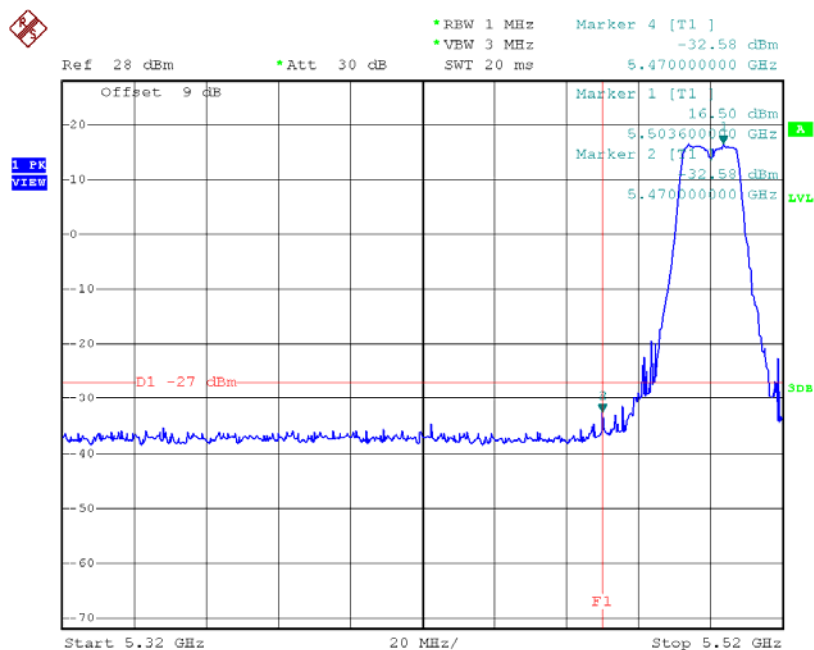
TX mode CH62



Date: 1.APR.2015 21:49:38

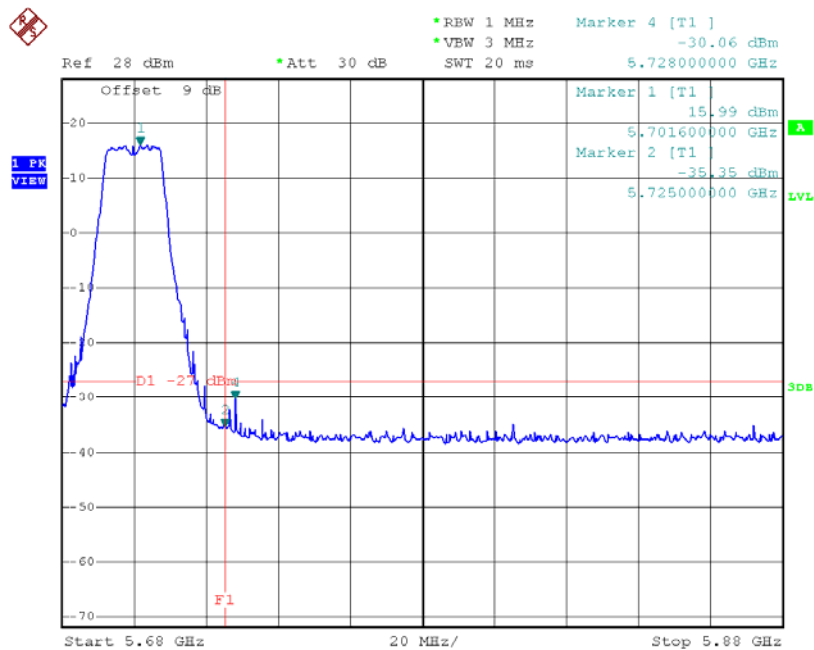
Test Mode:	UNII-2C/TX A Mode_ANT 1
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TX mode CH100



Date: 1.APR.2015 20:37:22

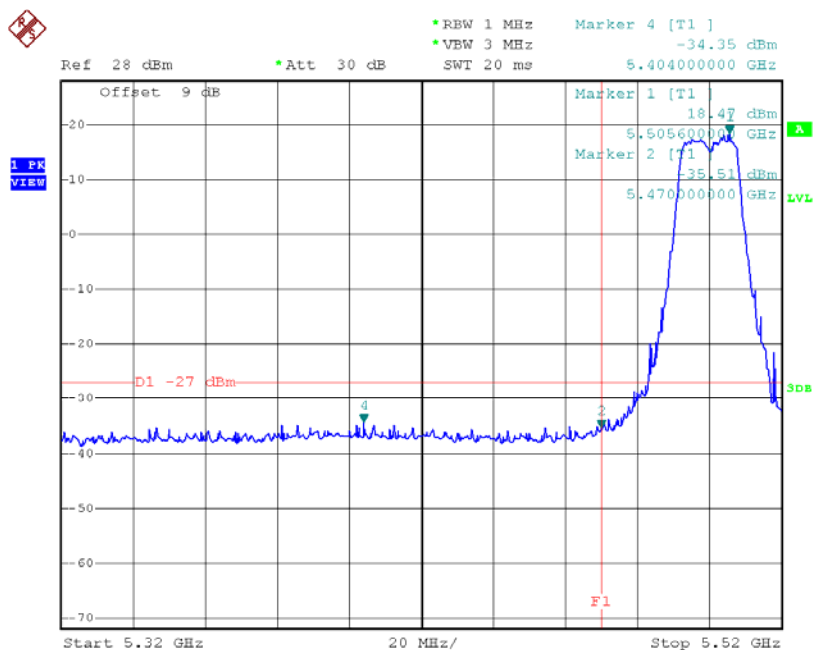
TX mode CH140



Date: 1.APR.2015 20:38:42

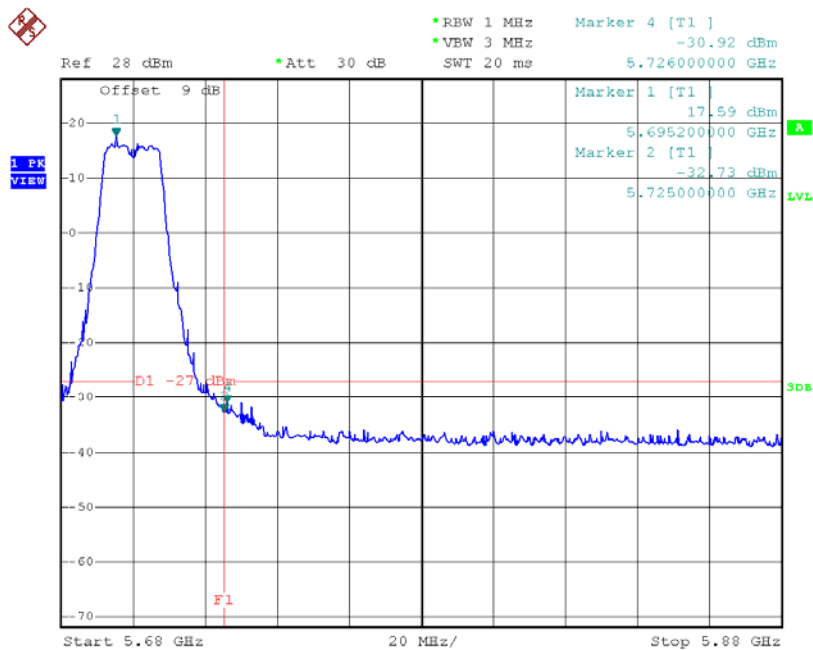
Test Mode:	UNII-2C/TX A Mode_ANT 2
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TX mode CH100



Date: 1.APR.2015 21:33:41

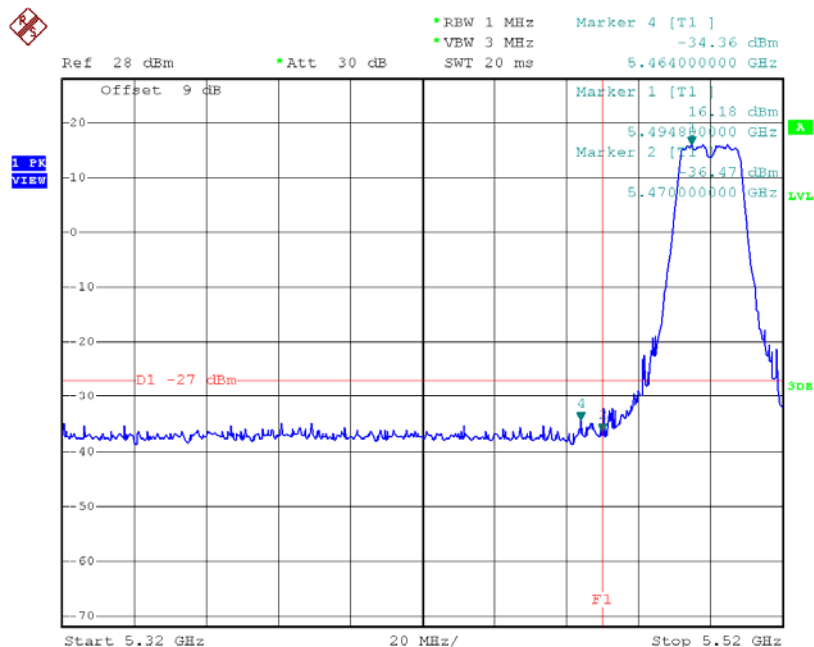
TX mode CH140



Date: 1.APR.2015 21:35:02

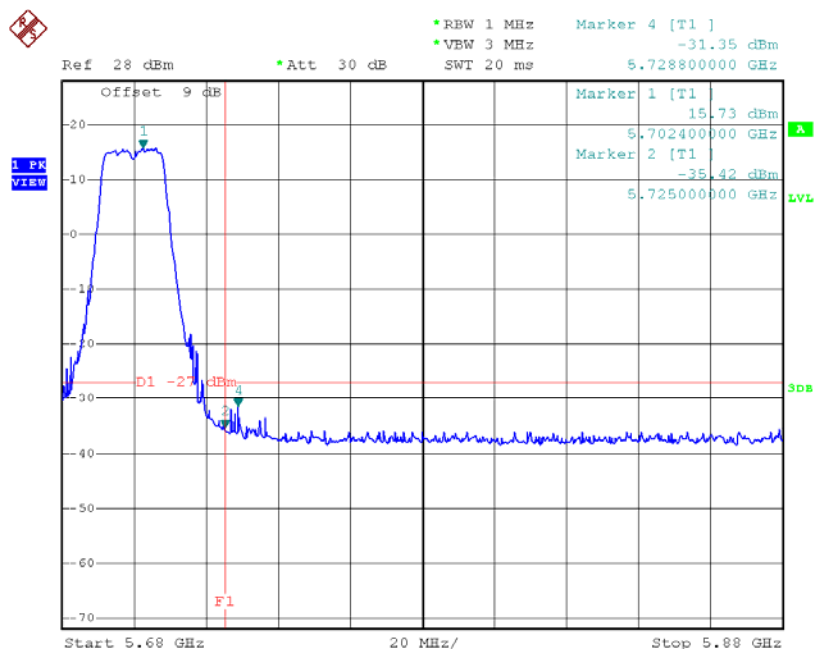
Test Mode: UNII-2C/TX N20 Mode_ANT 1

TX mode CH100



Date: 1.APR.2015 20:44:54

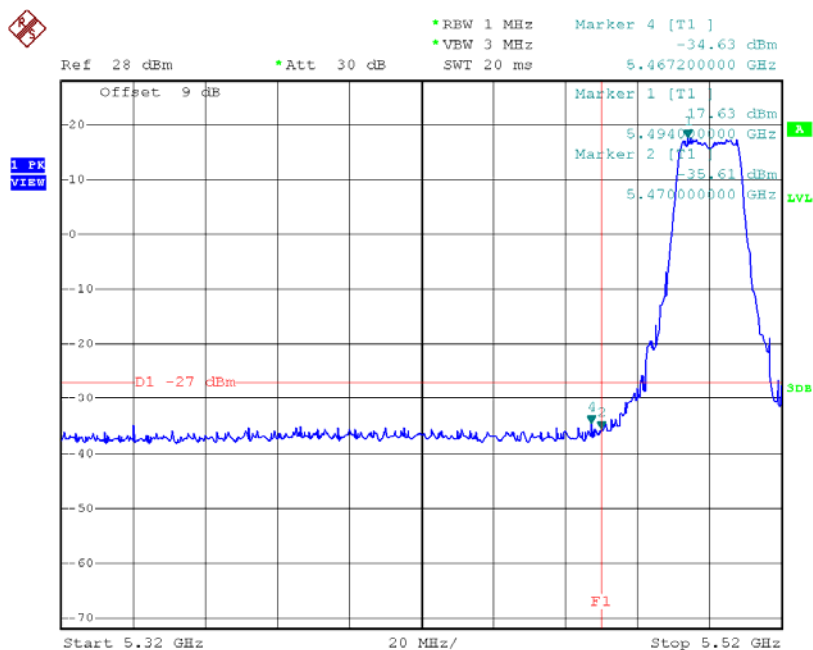
TX mode CH140



Date: 1.APR.2015 20:45:55

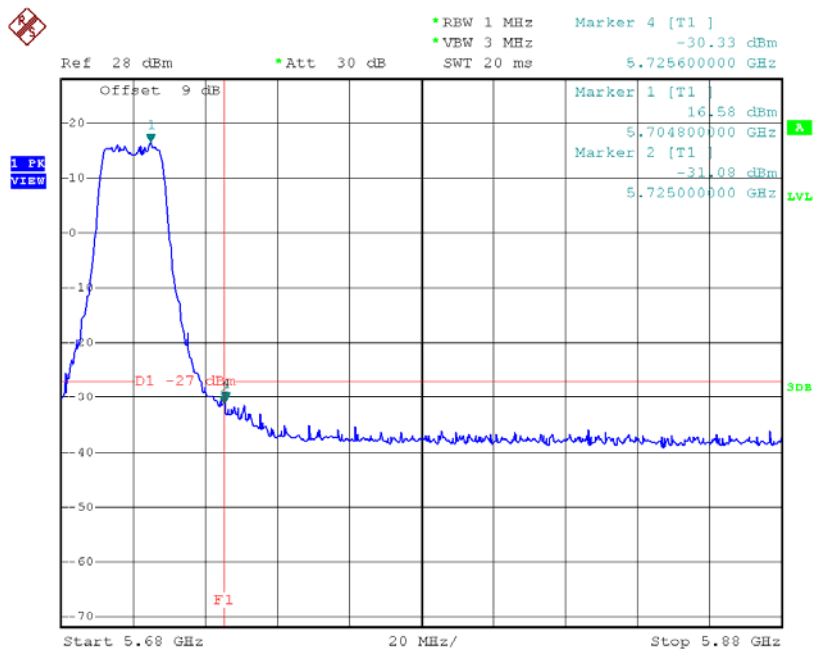
Test Mode: UNII-2C/TX N20 Mode_ANT 2

TX mode CH100



Date: 1.APR.2015 21:40:30

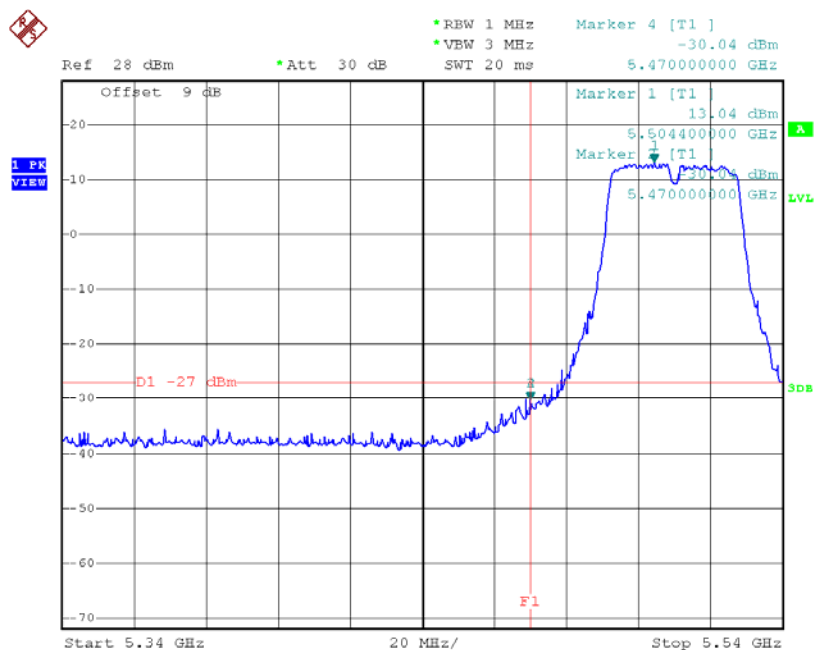
TX mode CH140



Date: 1.APR.2015 21:41:14

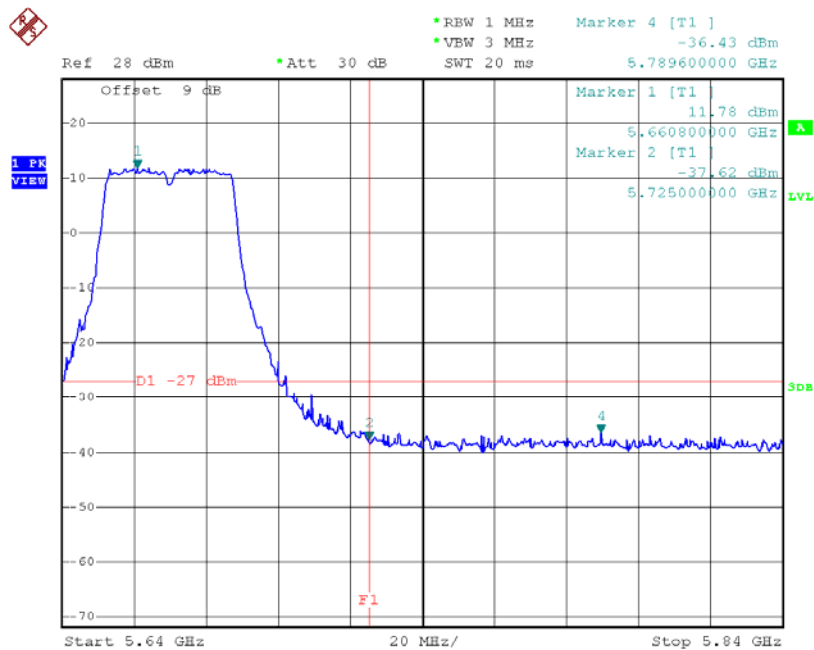
Test Mode: UNII-2C/TX N40 Mode_ANT 1

TX mode CH102



Date: 1.APR.2015 20:55:08

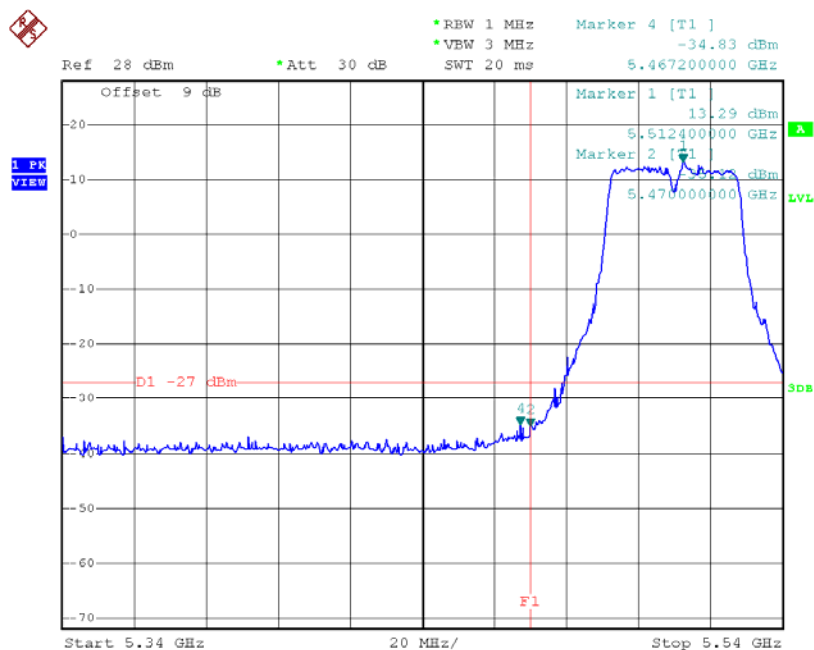
TX mode CH134



Date: 1.APR.2015 20:55:53

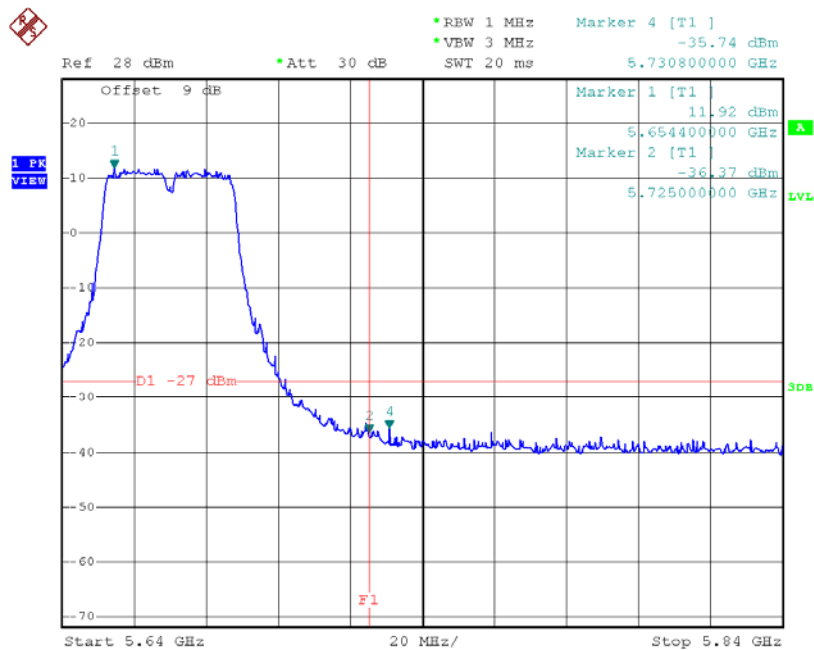
Test Mode: UNII-2C/TX N40 Mode_ANT 2

TX mode CH102



Date: 1.APR.2015 21:50:13

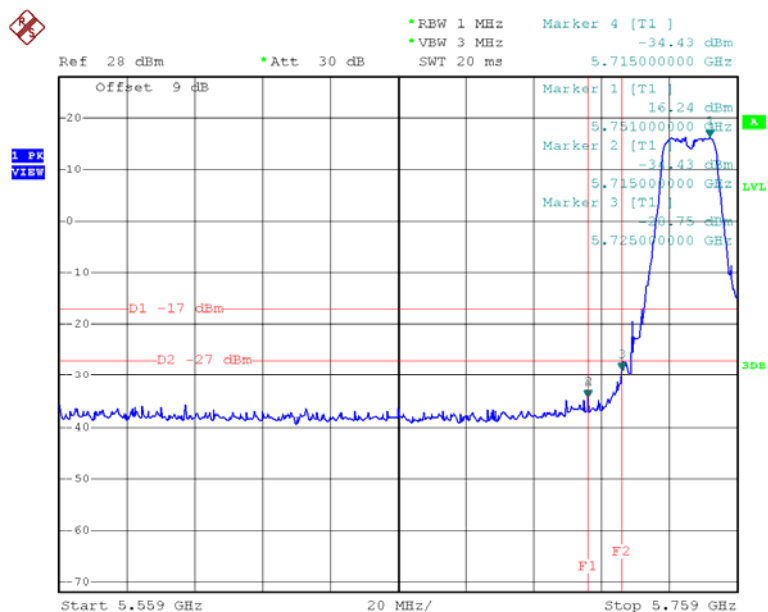
TX mode CH134



Date: 1.APR.2015 21:51:03

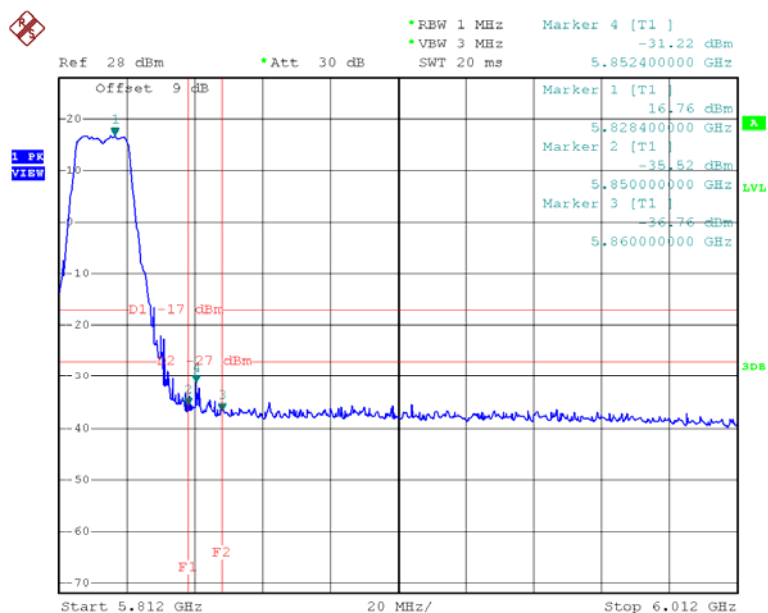
Test Mode: UNII-3/TX A Mode_ANT 1

TX A Mode CH149



Date: 1.APR.2015 20:39:28

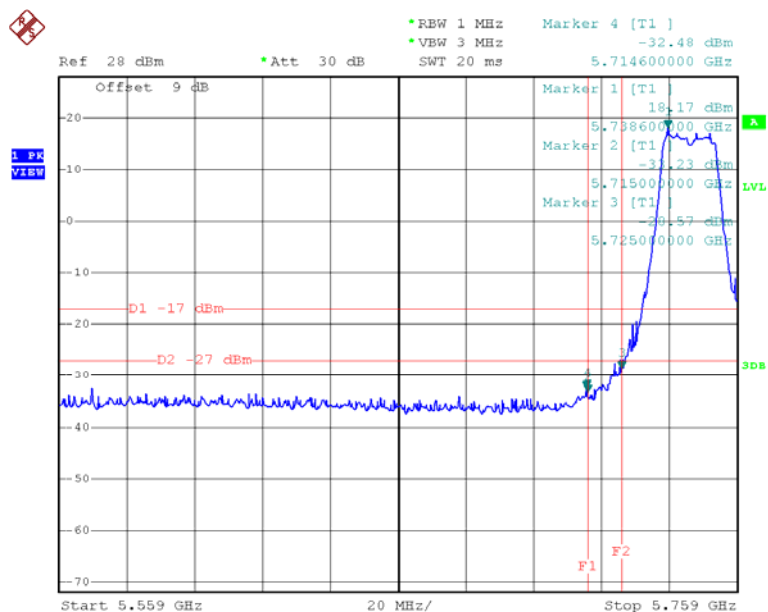
TX A Mode CH165



Date: 1.APR.2015 20:41:12

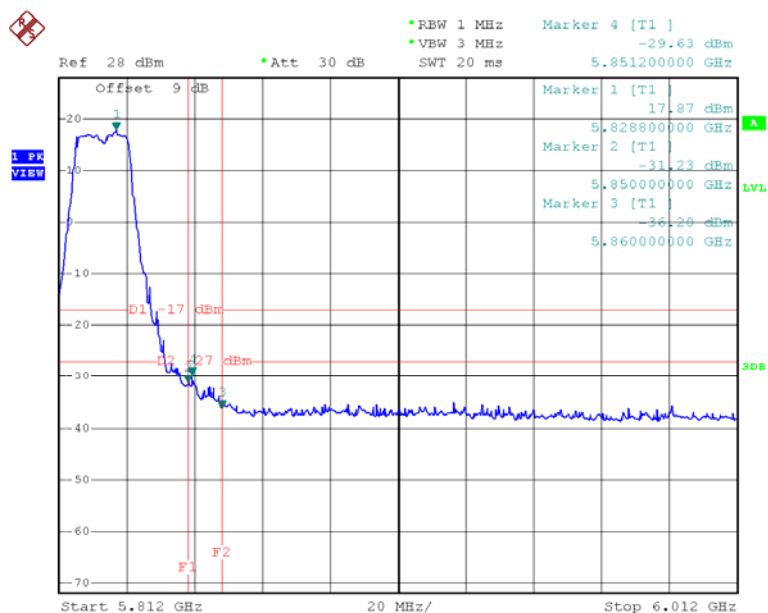
Test Mode: UNII-3/TX A Mode_ANT 2

TX A Mode CH149



Date: 1.APR.2015 21:35:46

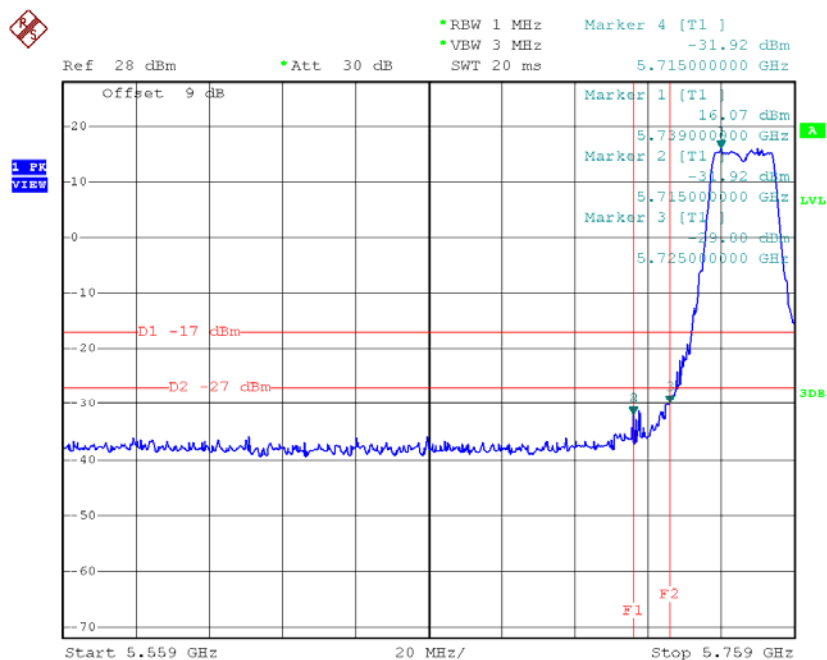
TX A Mode CH165



Date: 1.APR.2015 21:37:06

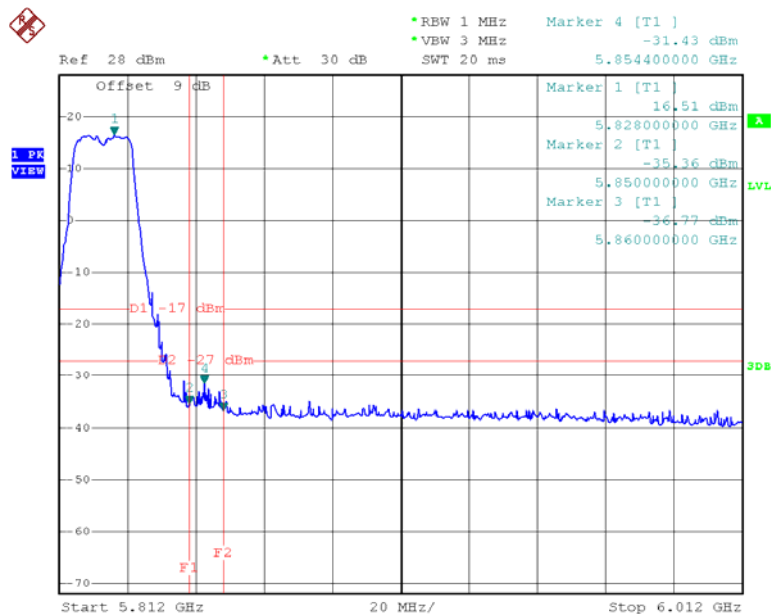
Test Mode: UNII-3/TX N20 Mode_ANT 1

TX HT20 mode CH149



Date: 1.APR.2015 20:46:22

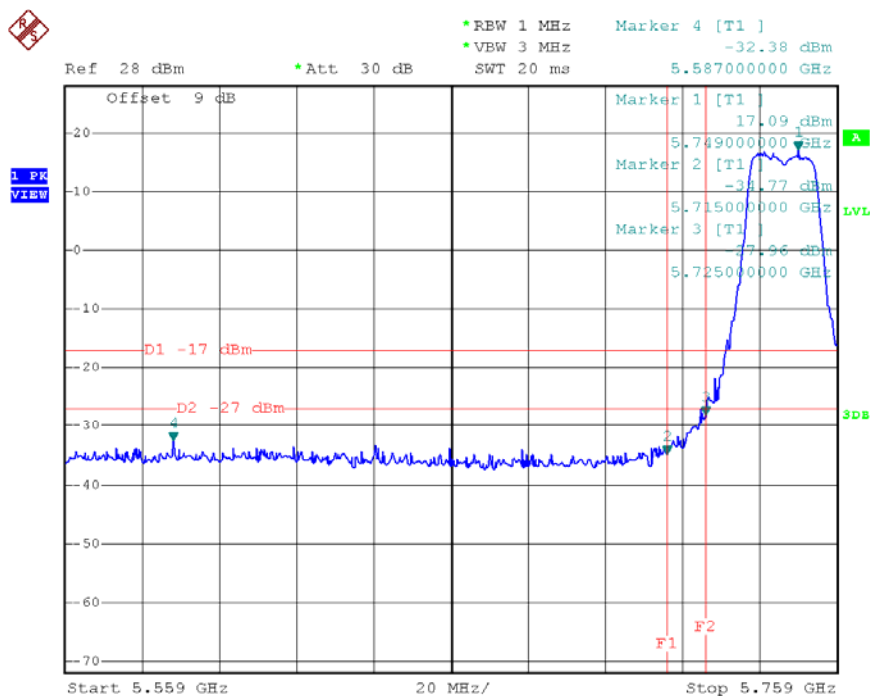
TX HT20 mode CH165



Date: 1.APR.2015 20:47:08

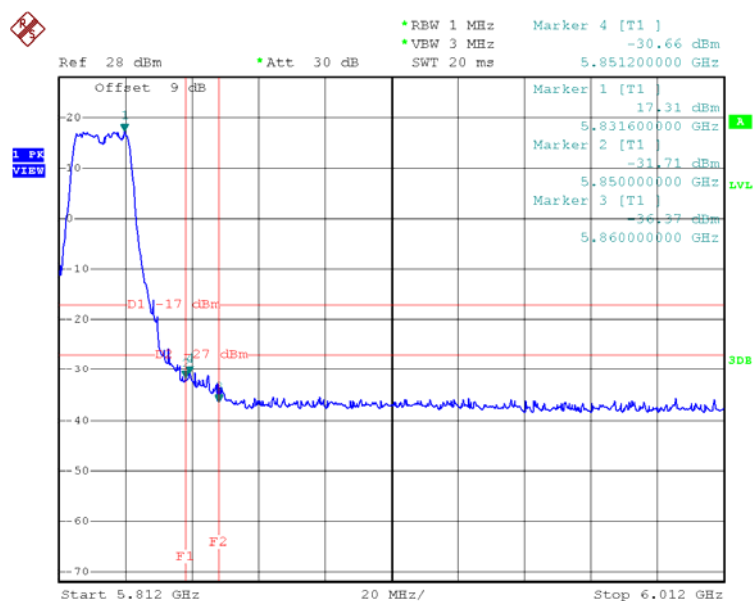
Test Mode: UNII-3/TX N20 Mode_ANT 2

TX HT20 mode CH149



Date: 1.APR.2015 21:41:38

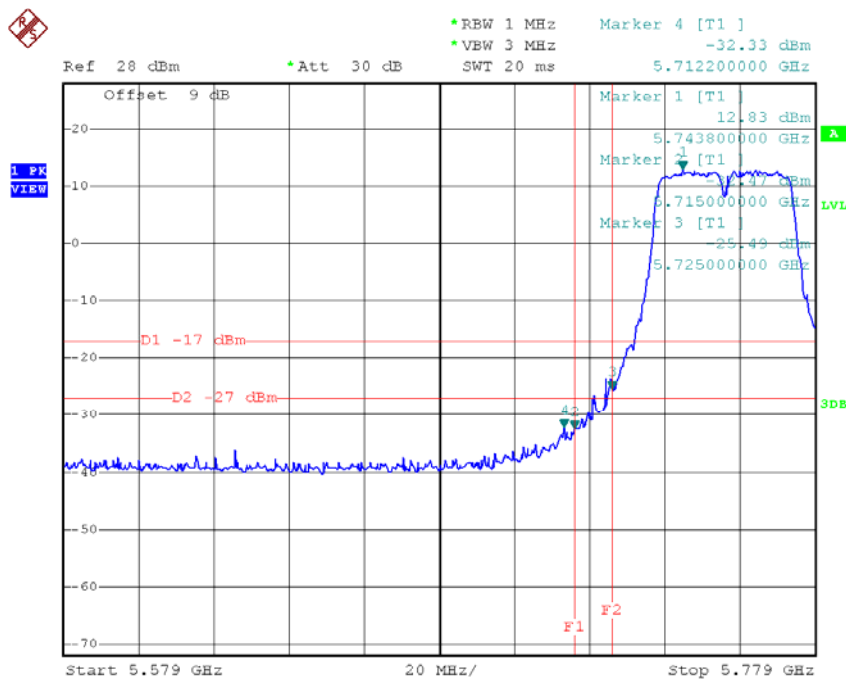
X HT20 mode CH165



Date: 1.APR.2015 21:42:23

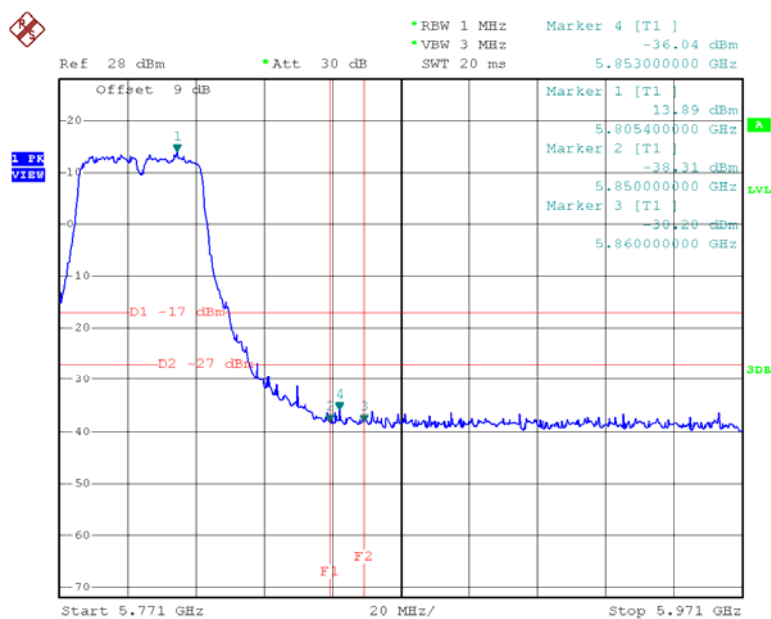
Test Mode:	UNII-3/TX N40 Mode_ANT 1
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UNII-3/TX HT40 mode CH151



Date: 1.APR.2015 20:56:20

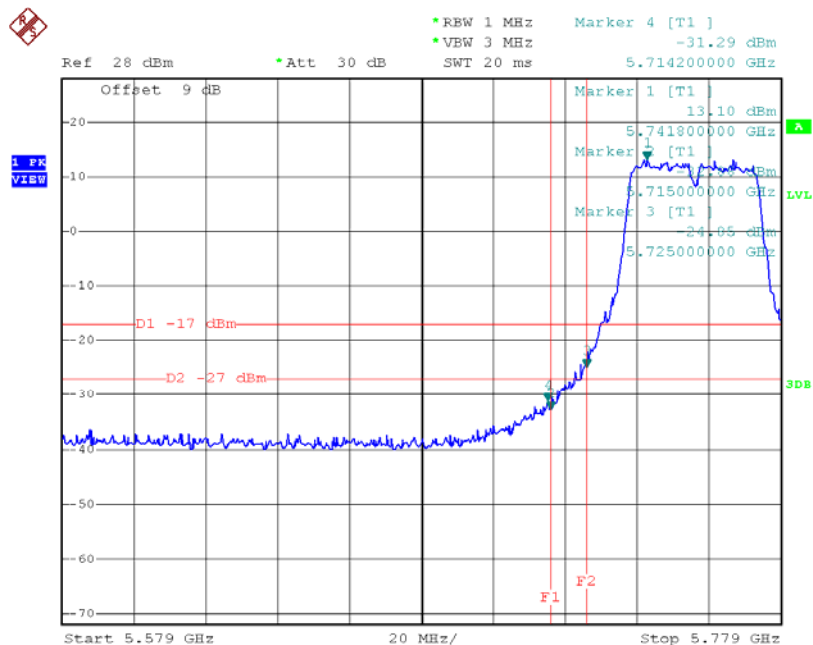
UNII-3/TX HT40 mode CH159



Date: 1.APR.2015 20:56:49

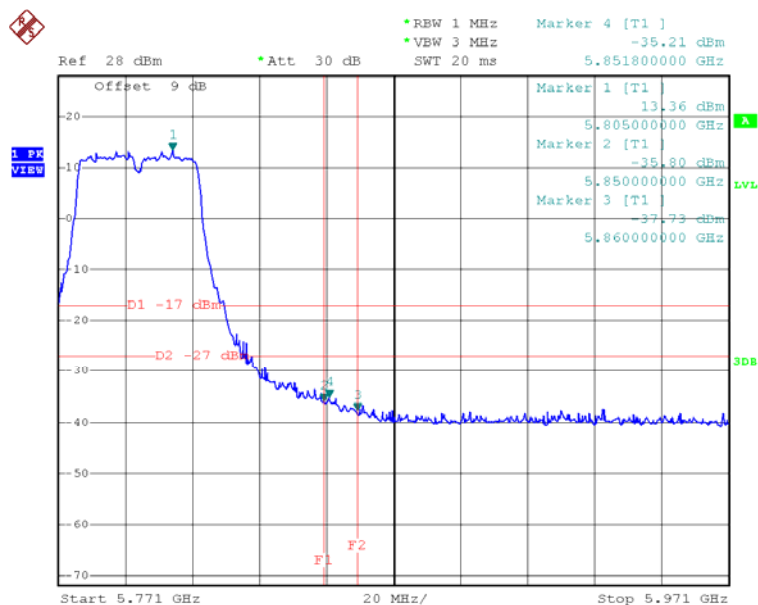
Test Mode: UNII-3/TX N40 Mode_ANT 2

TX HT40 mode CH151



Date: 1.APR.2015 21:51:35

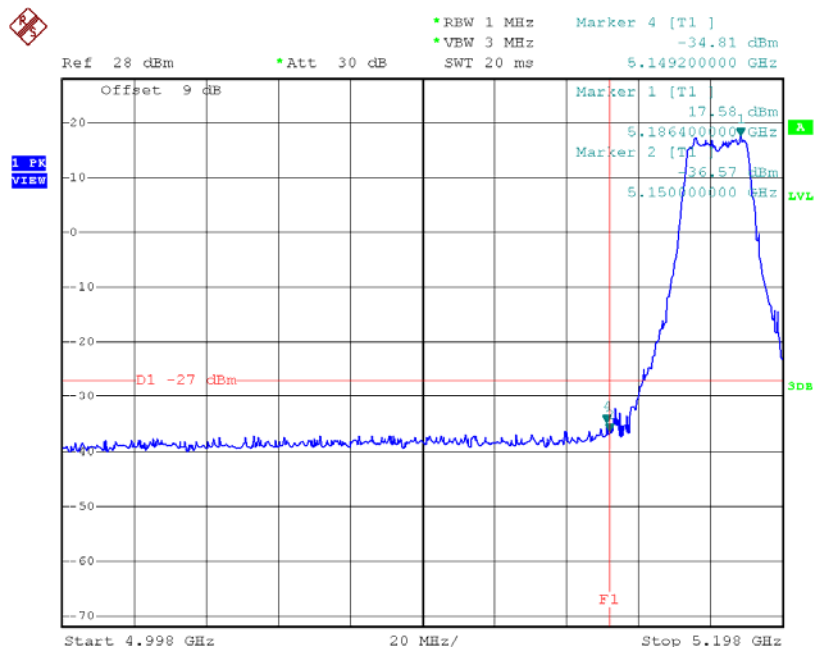
HT40 mode CH159



Date: 1.APR.2015 21:52:04

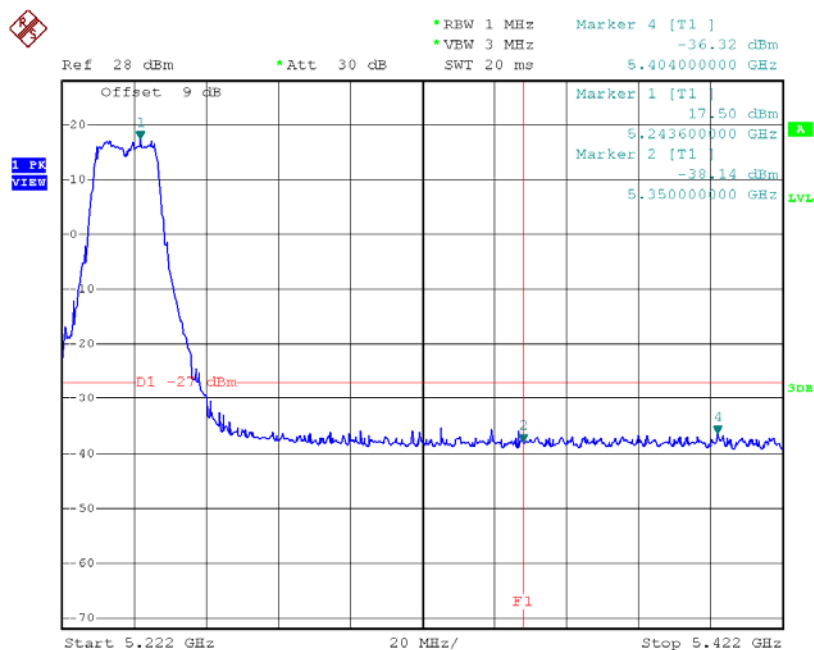
Test Mode: UNII-1/TX AC20 Mode_ANT 1

TX mode CH36



Date: 1.APR.2015 20:47:45

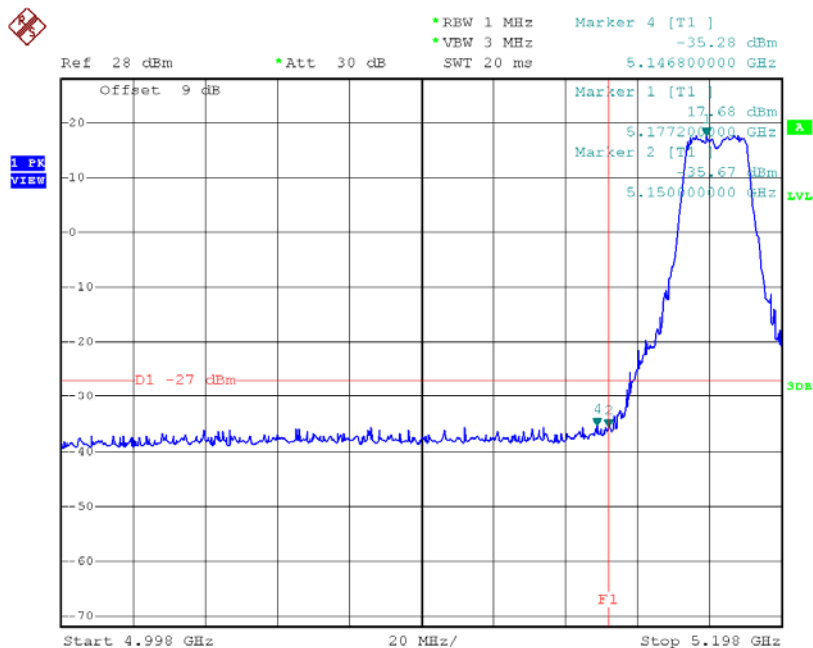
TX mode CH48



Date: 1.APR.2015 20:48:58

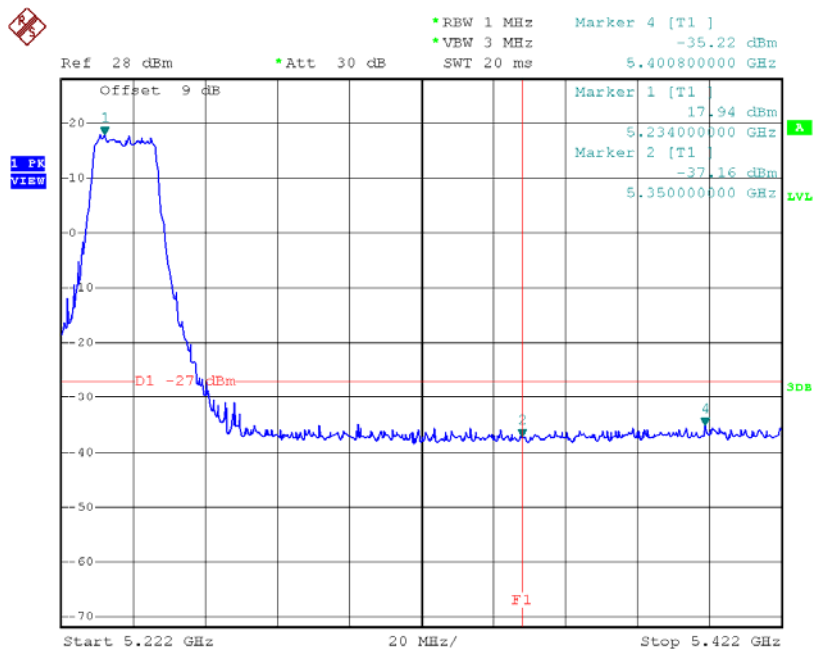
Test Mode: UNII-1/TX AC20 Mode_ANT 2

TX mode CH36



Date: 1.APR.2015 21:42:57

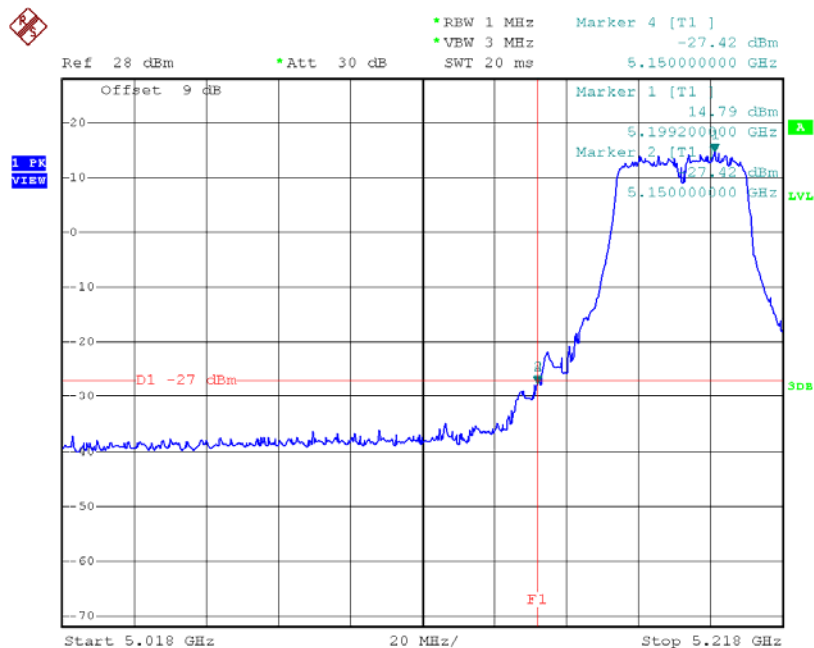
TX mode CH48



Date: 1.APR.2015 21:43:44

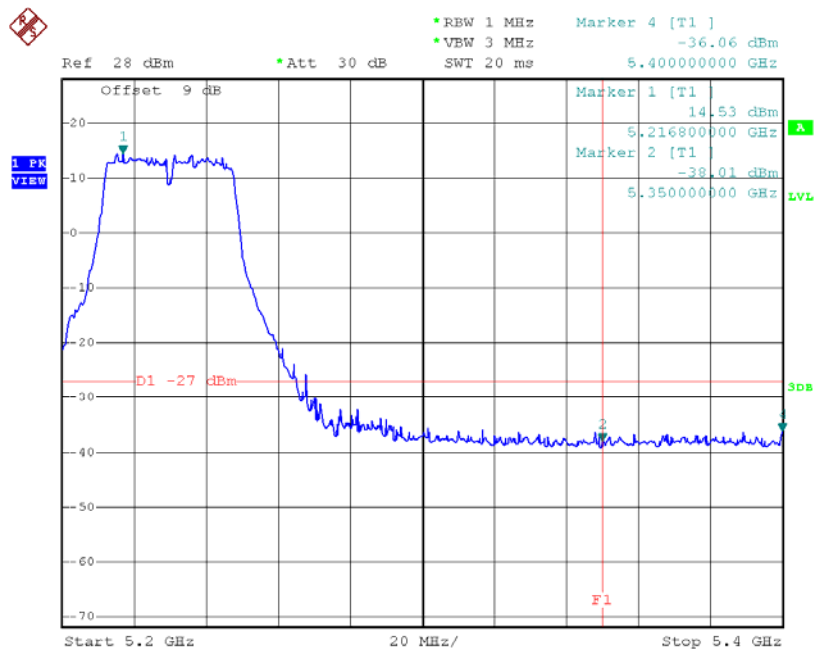
Test Mode: UNII-1/TX AC40 Mode_ANT 1

TX mode CH38



Date: 1.APR.2015 20:57:20

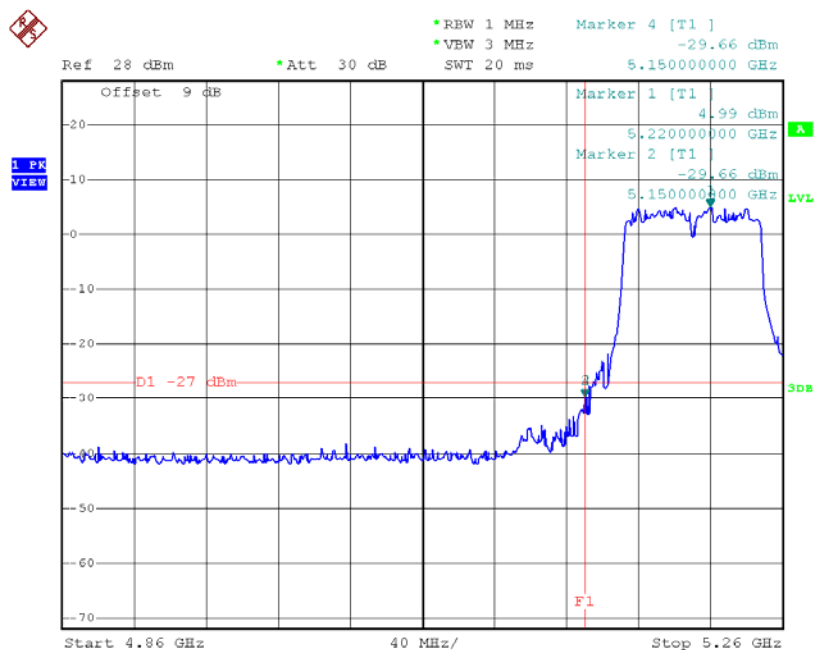
TX modeCH46



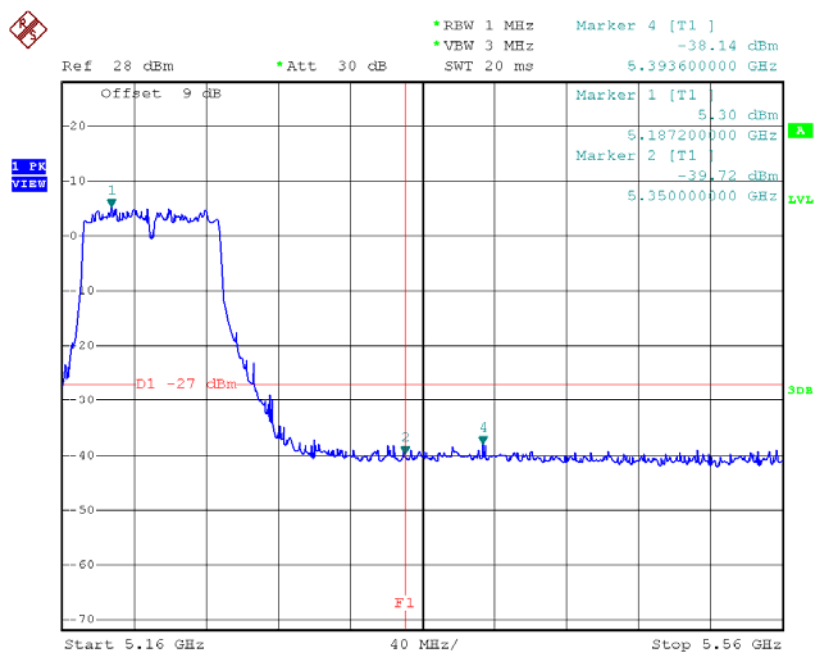
Date: 1.APR.2015 20:57:53

Test Mode: UNII-1/TX AC80 Mode_ANT 1

TX mode CH42



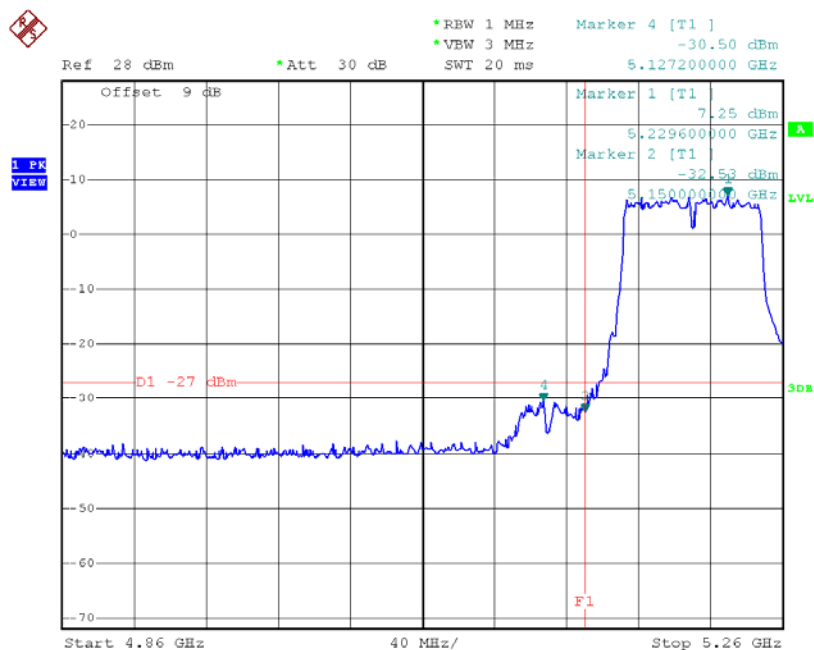
Date: 1.APR.2015 21:15:24



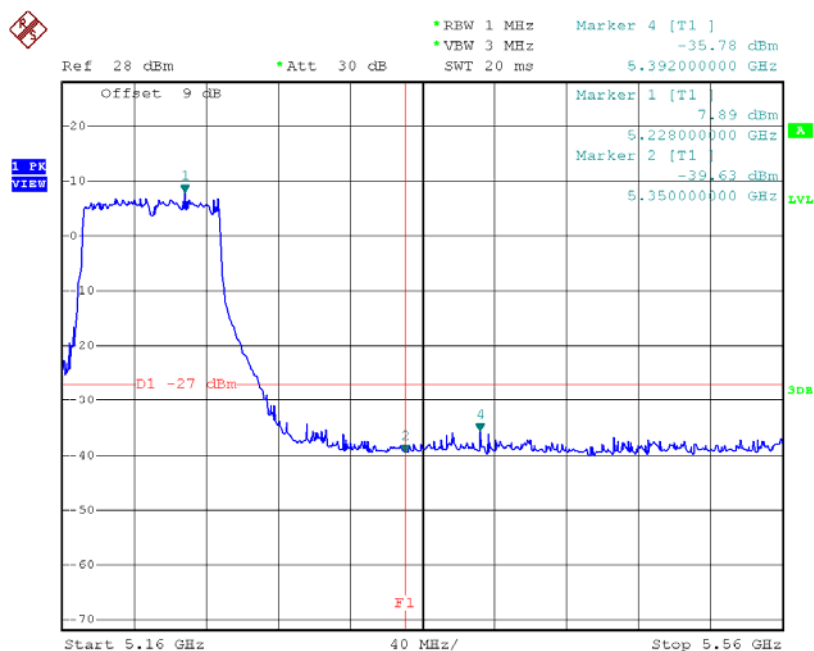
Date: 1.APR.2015 21:15:31

Test Mode:	UNII-1/TX AC80 Mode_ANT 2
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TX mode CH42



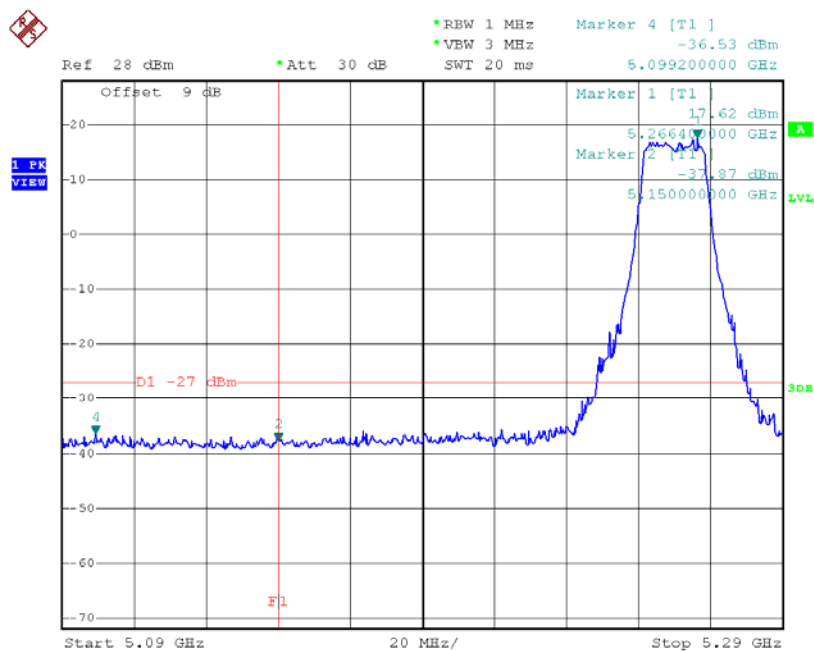
Date: 1.APR.2015 21:58:16



Date: 1.APR.2015 21:58:24

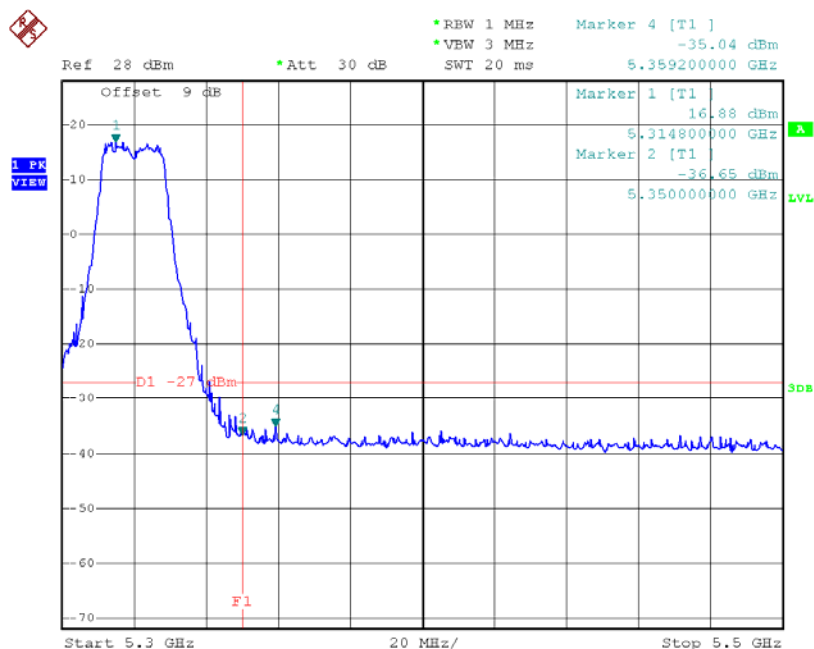
Test Mode: UNII-2A/TX AC20 Mode_ANT 1

TX mode CH52



Date: 1.APR.2015 20:49:24

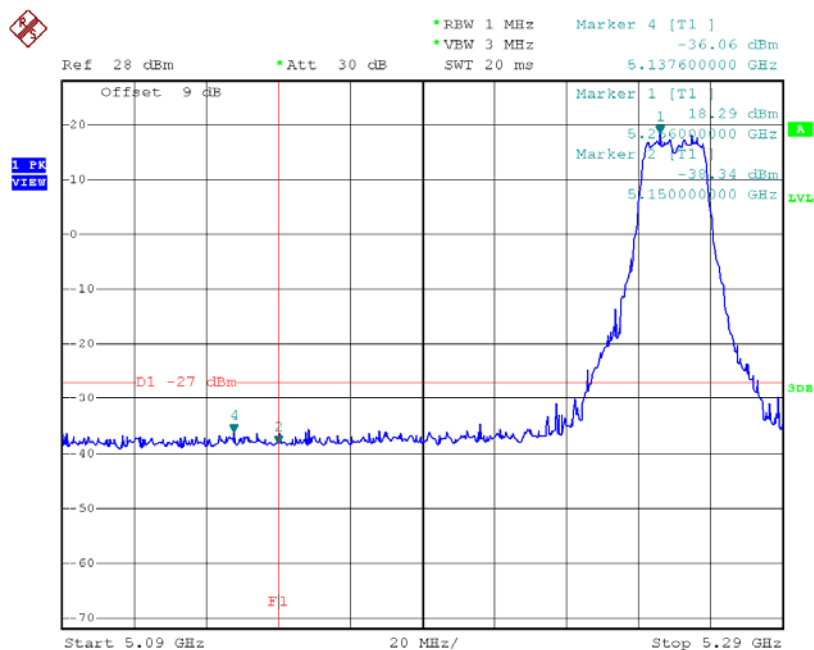
TX modeCH64



Date: 1.APR.2015 20:50:14

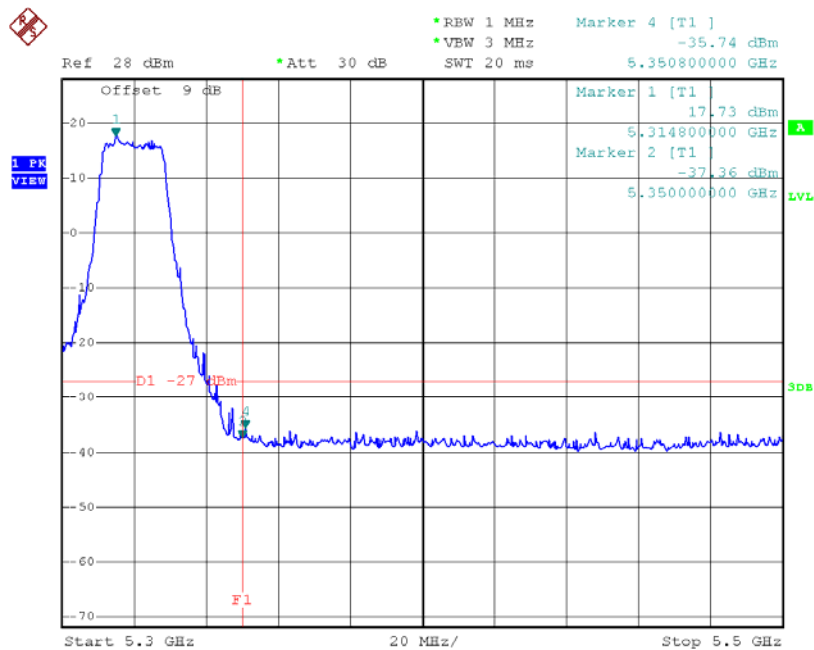
Test Mode:	UNII-2A/TX AC20 Mode_ANT 2
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TX mode CH52



Date: 1.APR.2015 21:44:09

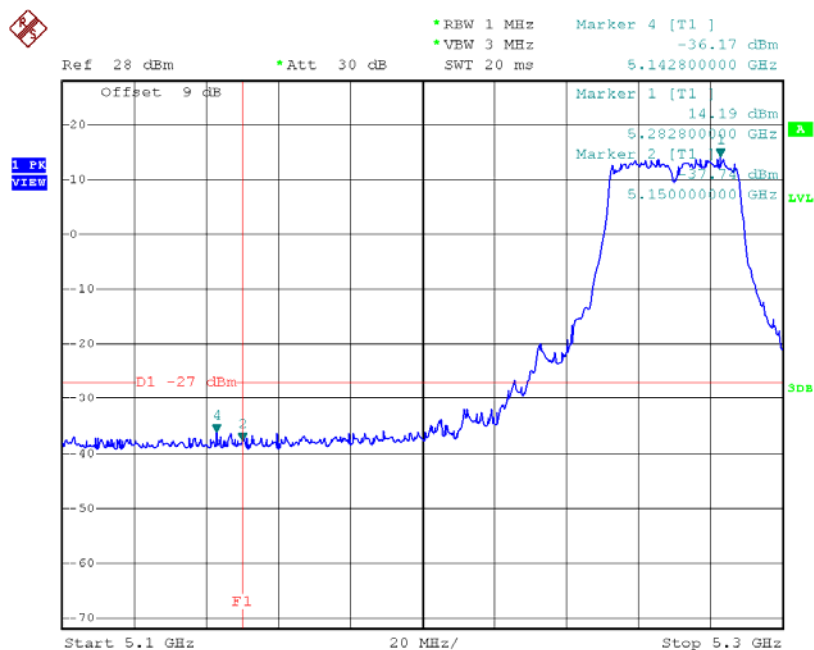
TX mode CH64



Date: 1.APR.2015 21:45:02

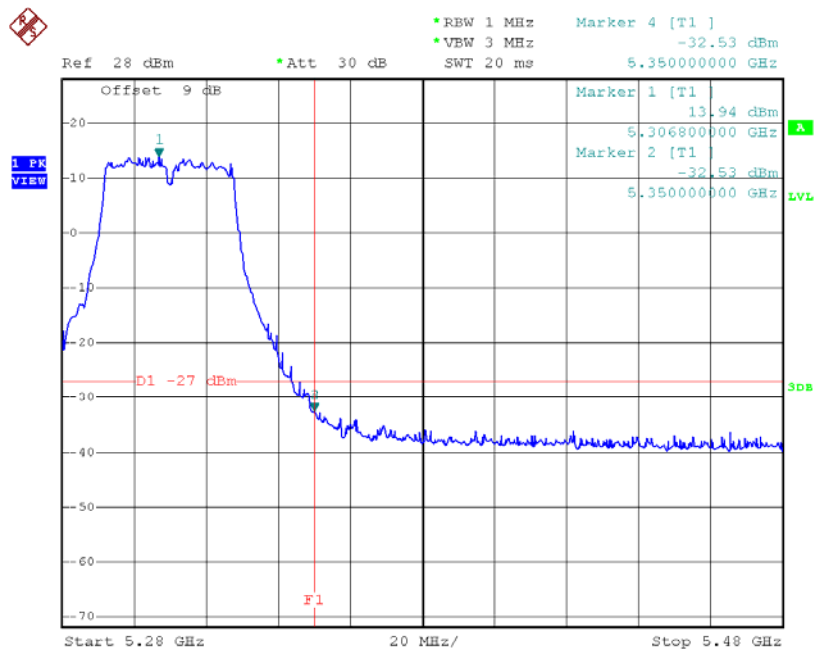
Test Mode: UNII-2A/TX AC40 Mode_ANT 1

TX mode CH54



Date: 1.APR.2015 20:58:21

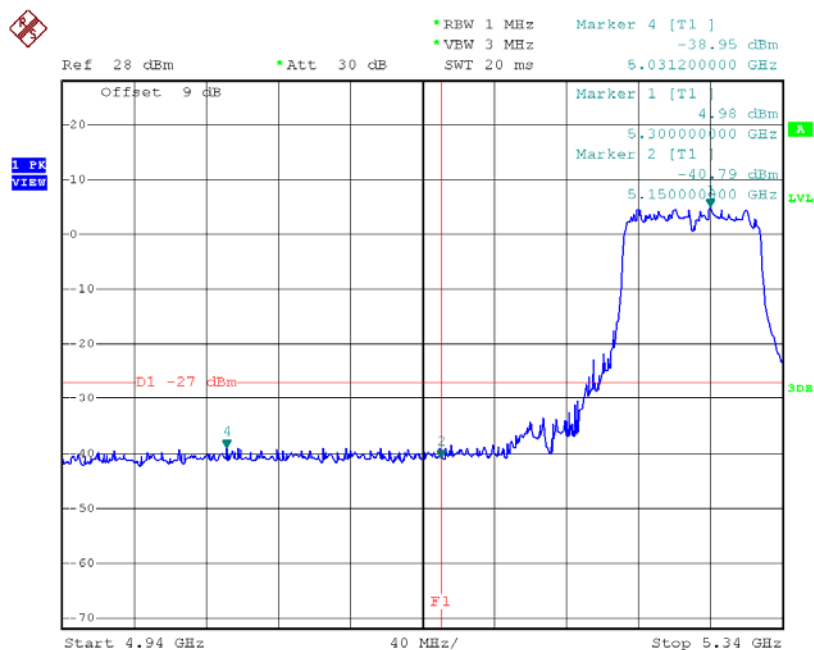
TX mode CH62



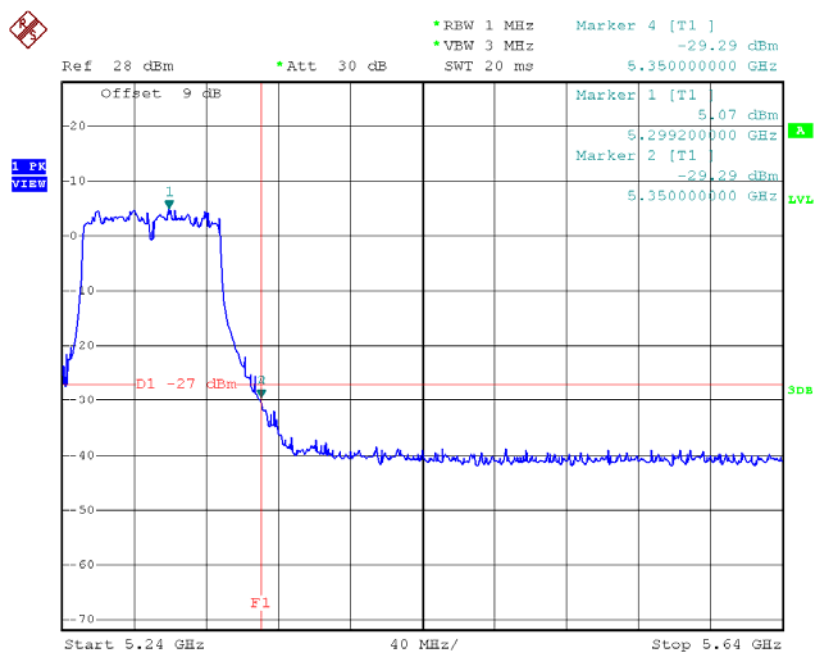
Date: 1.APR.2015 20:59:09

Test Mode:	UNII-2A/TX AC80 Mode_ANT 1
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TX mode CH58



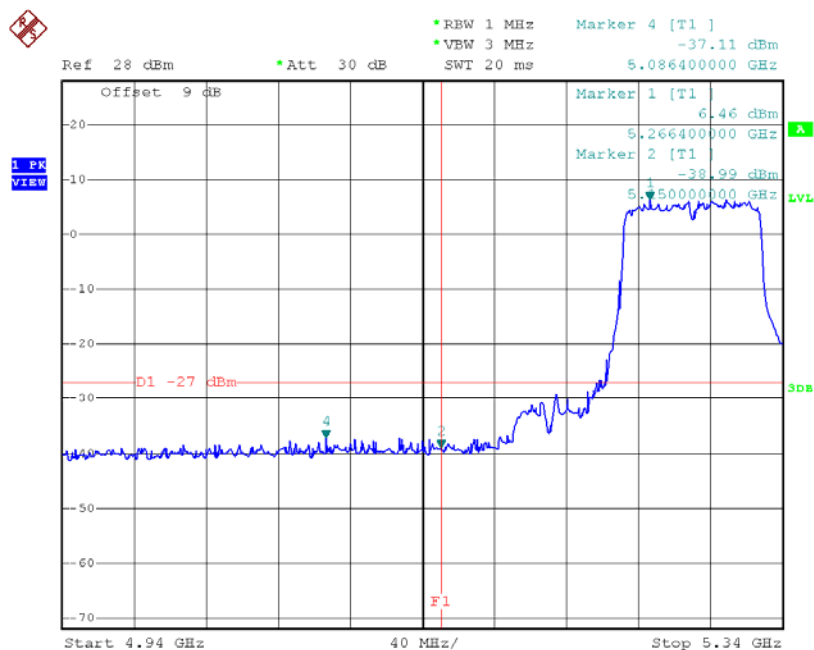
Date: 1.APR.2015 21:16:20



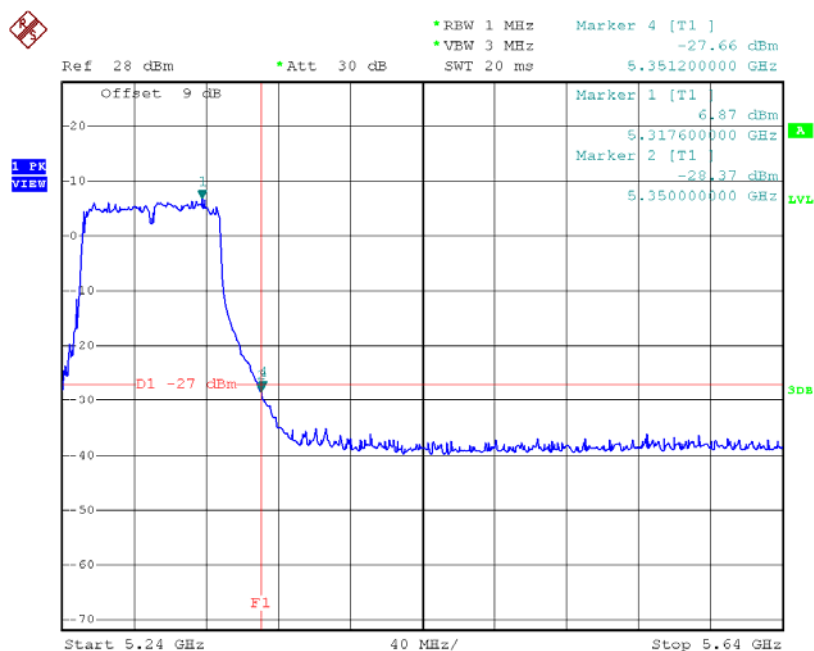
Date: 1.APR.2015 21:16:27

Test Mode: UNII-2A/TX AC80 Mode_ANT 2

TX mode CH58



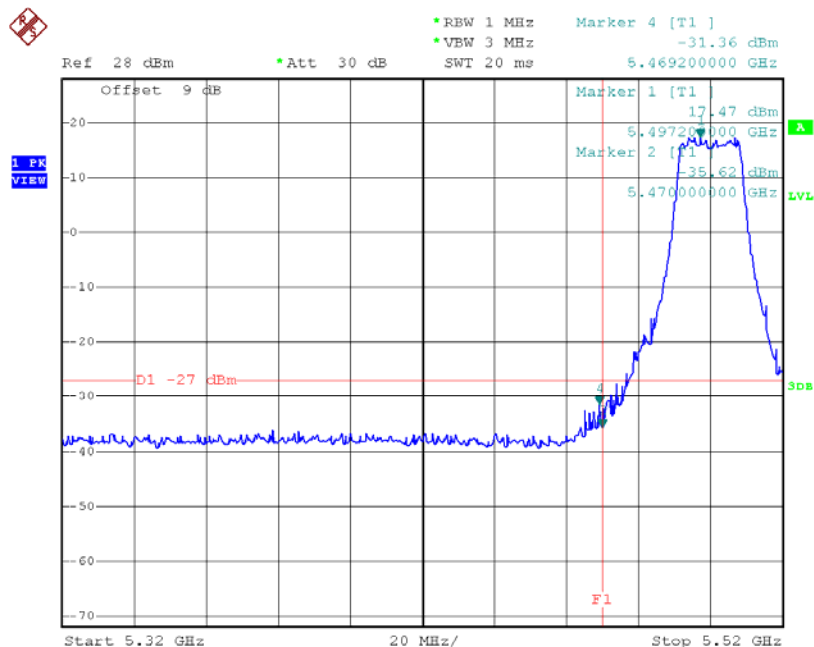
Date: 1.APR.2015 21:59:01



Date: 1.APR.2015 21:59:09

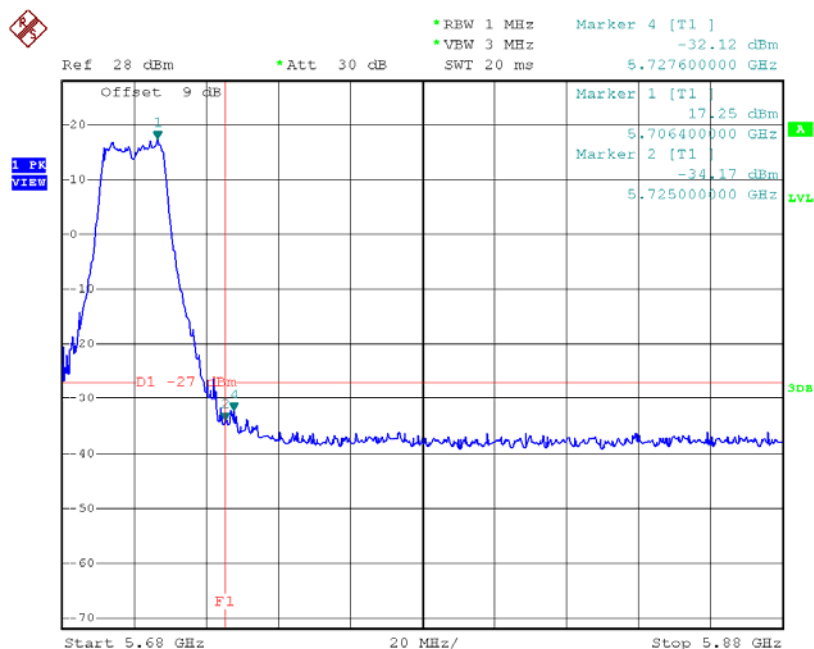
Test Mode: UNII-2C/TX AC20 Mode_ANT 1

TX mode CH100



Date: 1.APR.2015 20:50:39

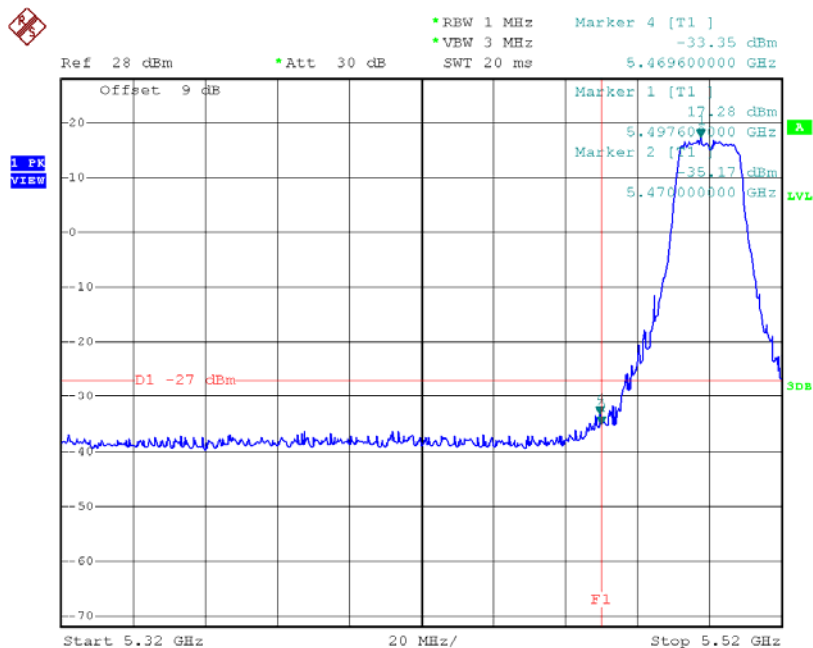
TX mode CH140



Date: 1.APR.2015 20:51:25

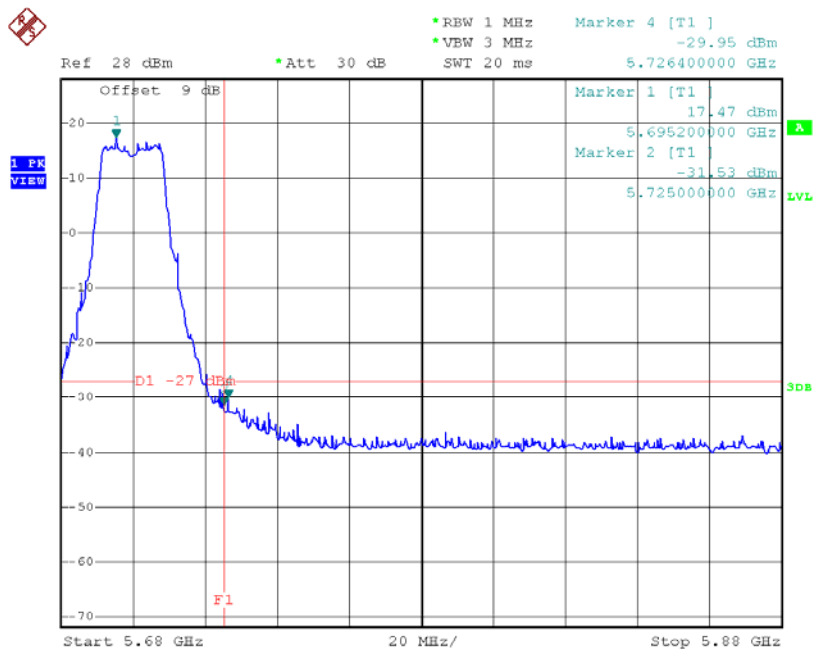
Test Mode:	UNII-2C/TX AC20 Mode_ANT 2
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TX mode CH100



Date: 1.APR.2015 21:45:25

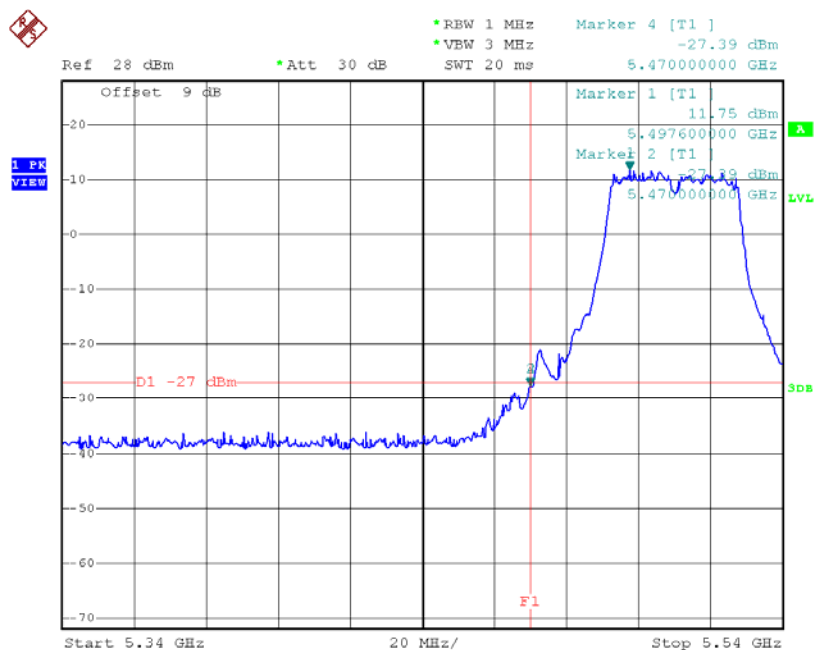
TX mode CH140



Date: 1.APR.2015 21:46:07

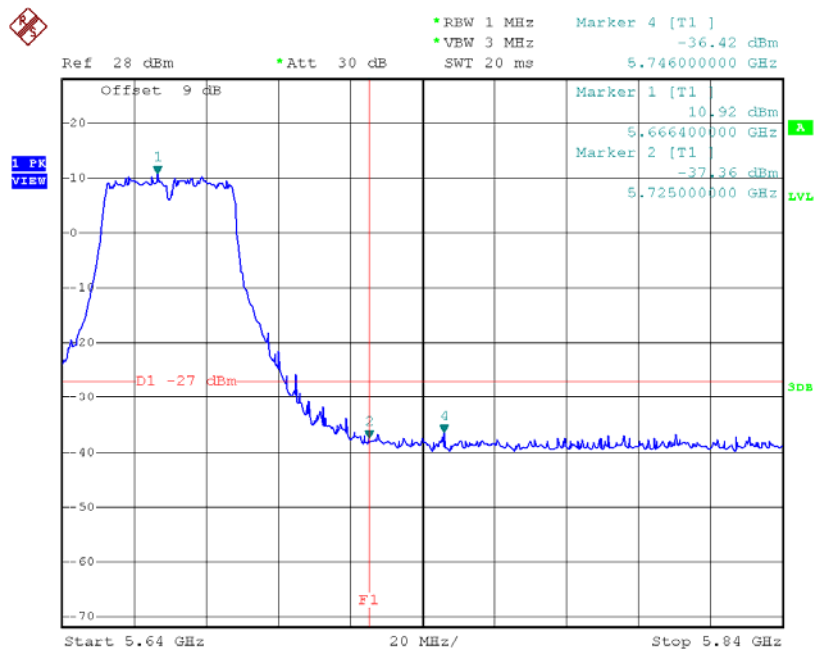
Test Mode: UNII-2C/TX AC40 Mode_ANT 1

TX mode CH102



Date: 1.APR.2015 21:01:59

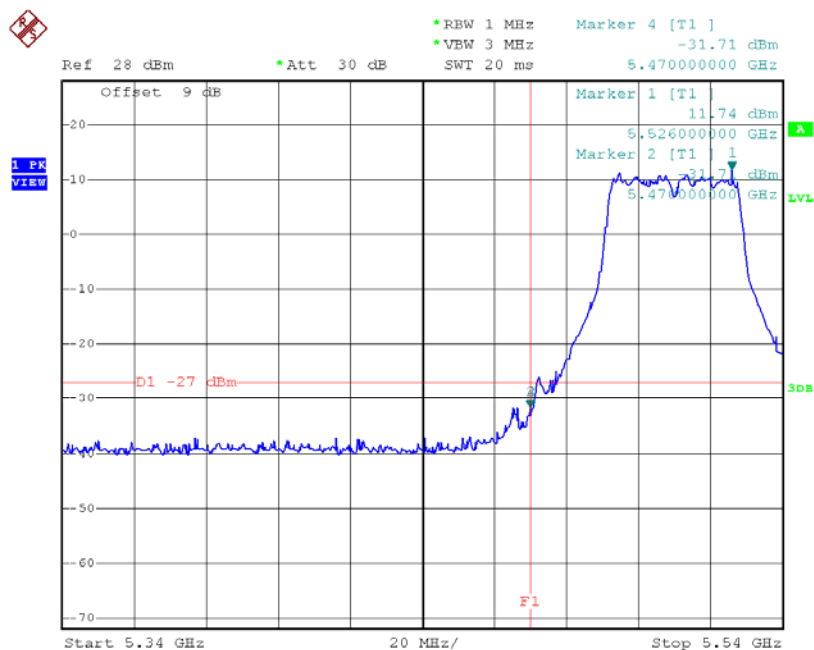
TX mode CH134



Date: 1.APR.2015 21:03:21

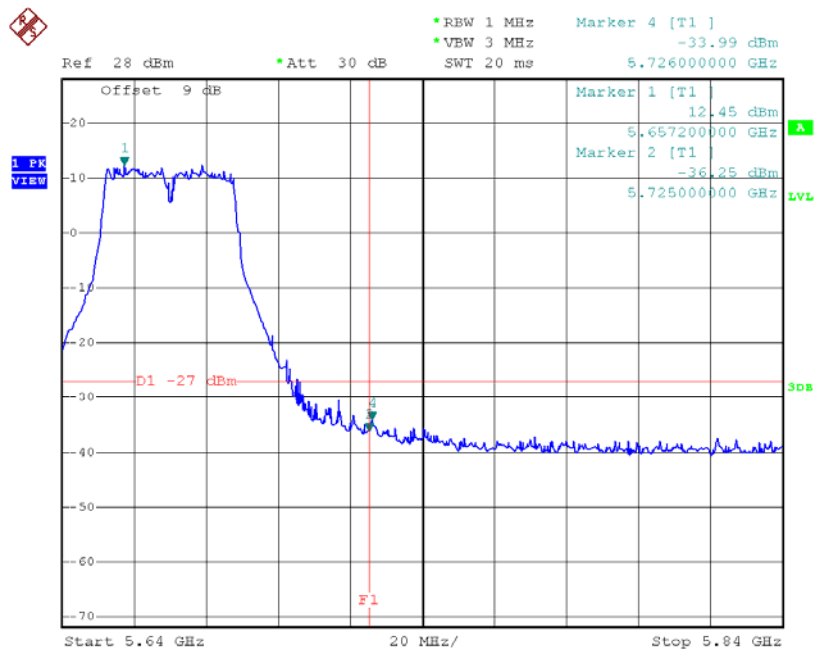
Test Mode:	UNII-2C/TX AC40 Mode_ANT 2
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TX mode CH102



Date: 1.APR.2015 21:55:23

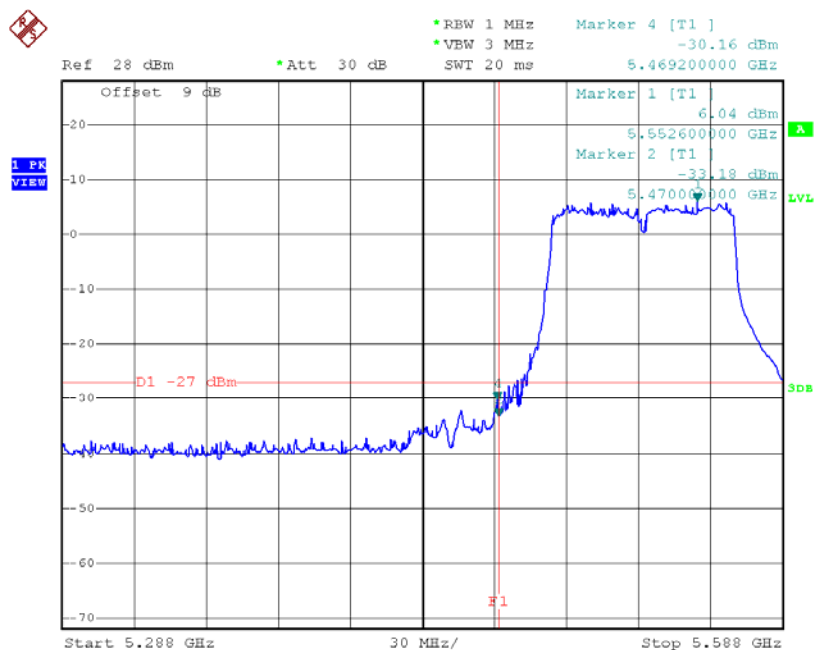
TX mode CH134



Date: 1.APR.2015 21:56:14

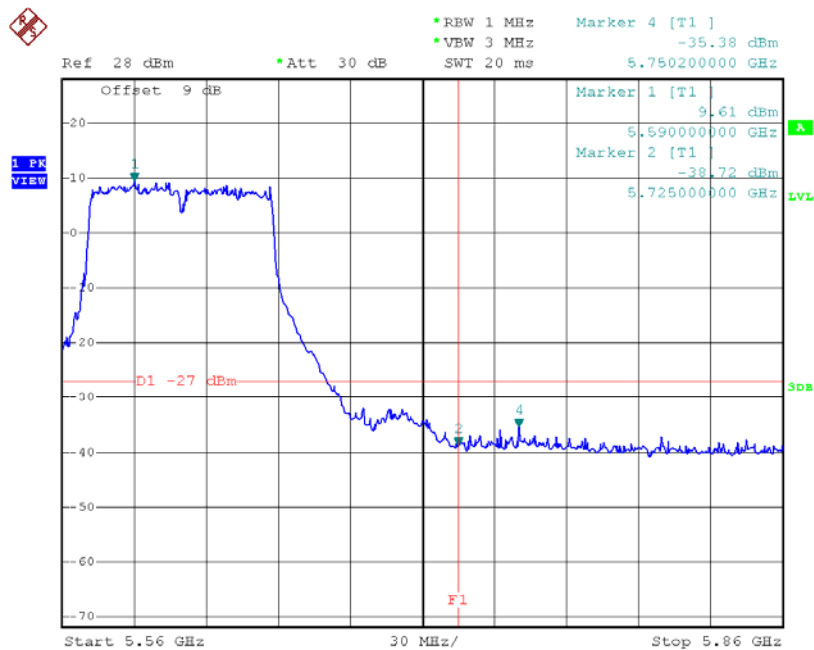
Test Mode: UNII-2C/TX AC80 Mode_ANT 2

TX mode CH106



Date: 1.APR.2015 21:59:38

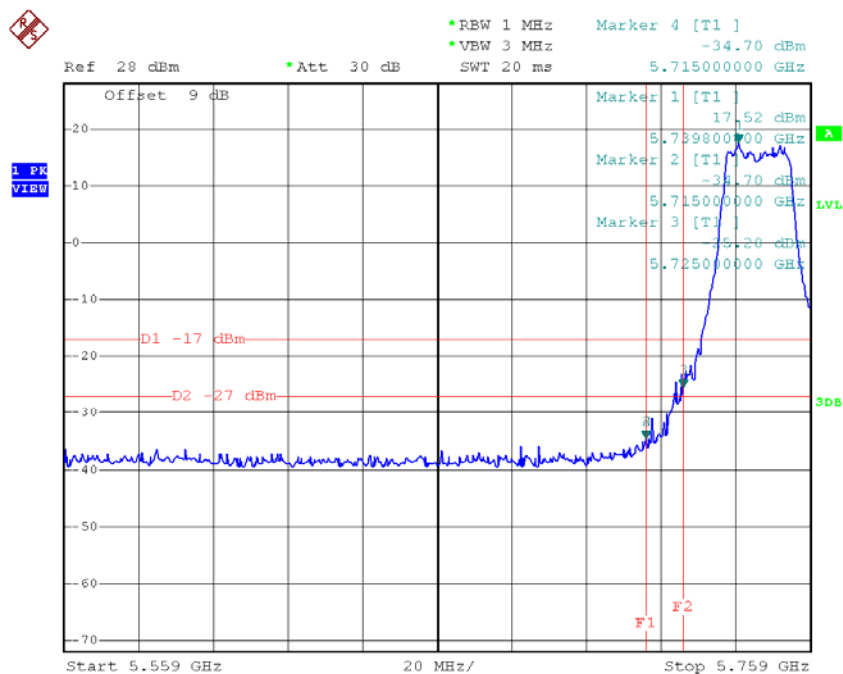
TX mode CH122



Date: 1.APR.2015 22:00:14

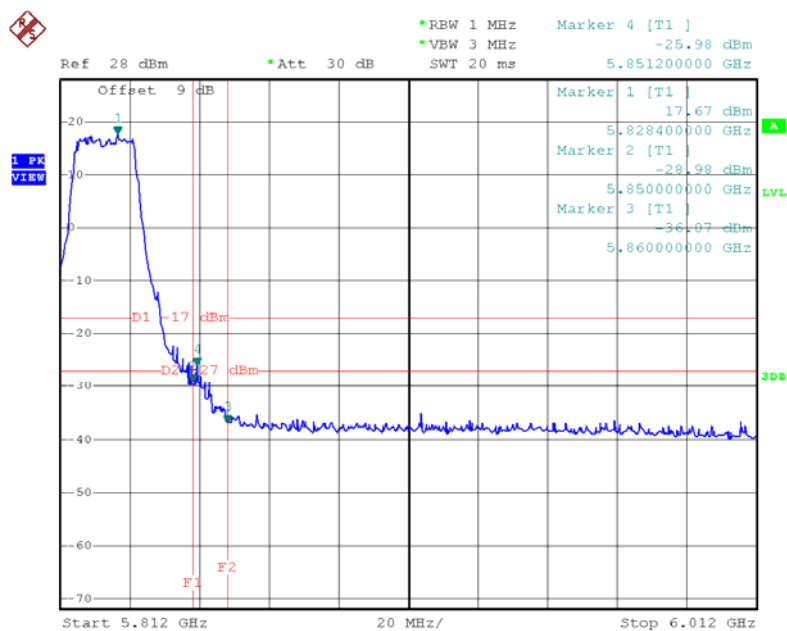
Test Mode: UNII-3/TX AC20 Mode_ANT 1

TXAC HT20 mode CH149



Date: 1.APR.2015 20:51:52

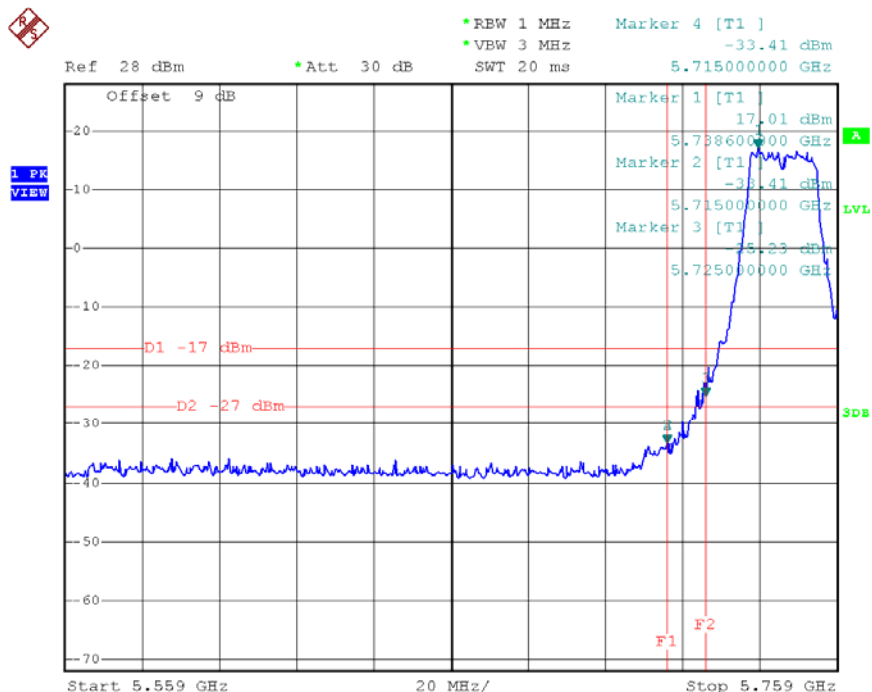
TXAC HT20 mode CH165



Date: 1.APR.2015 20:52:39

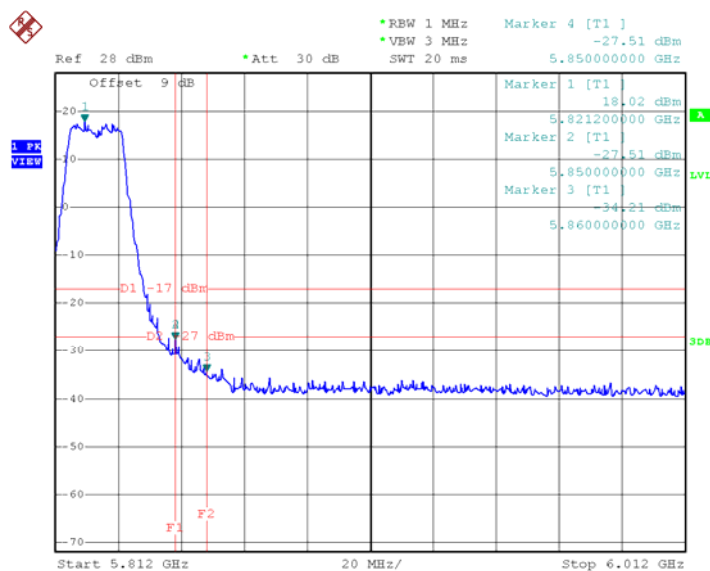
Test Mode: UNII-3/TX AC20 Mode_ANT 2

TXAC HT20 mode CH149



Date: 1.APR.2015 21:46:32

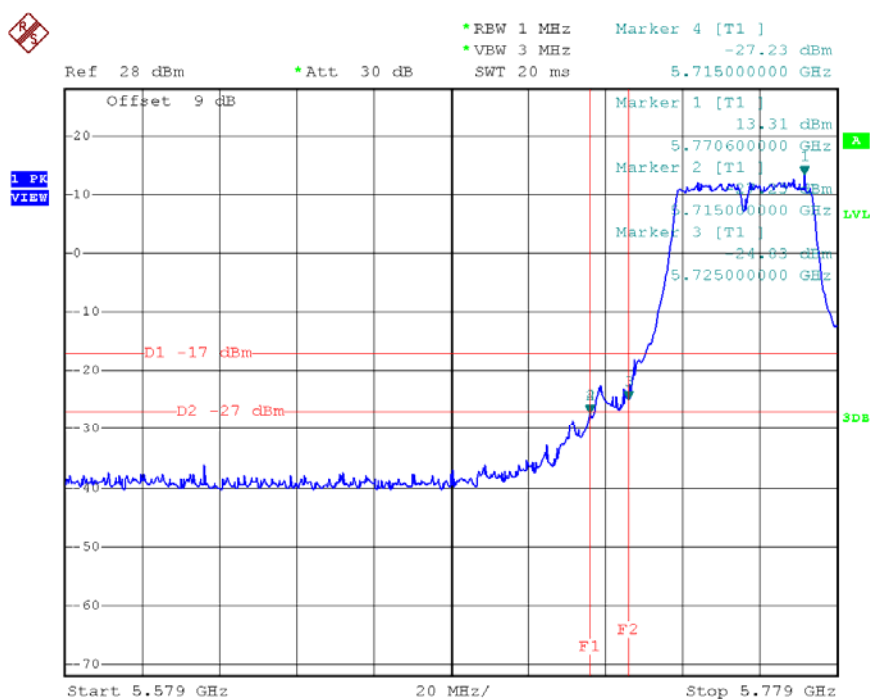
TXAC HT20 mode CH165



Date: 1.APR.2015 21:47:15

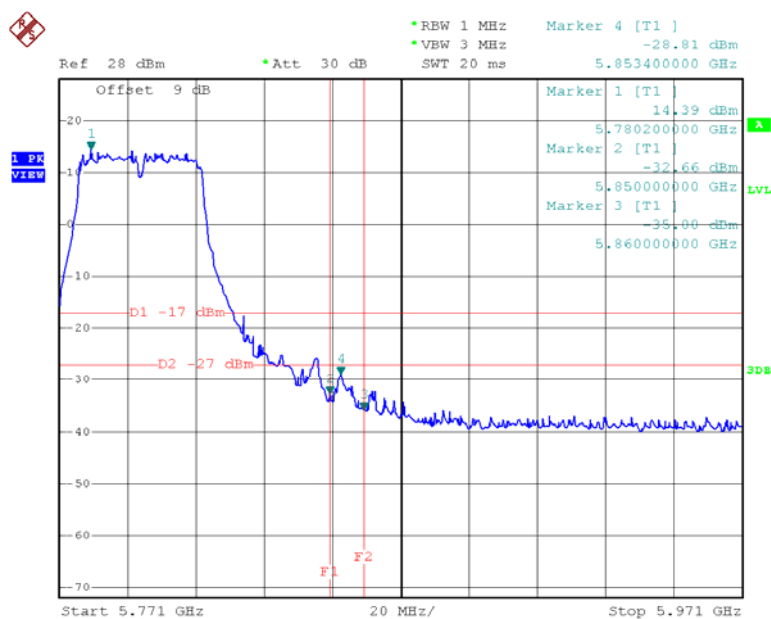
Test Mode: UNII-3/TX AC40 Mode_ANT 1

TXAC HT40 mode CH151



Date: 1.APR.2015 21:04:34

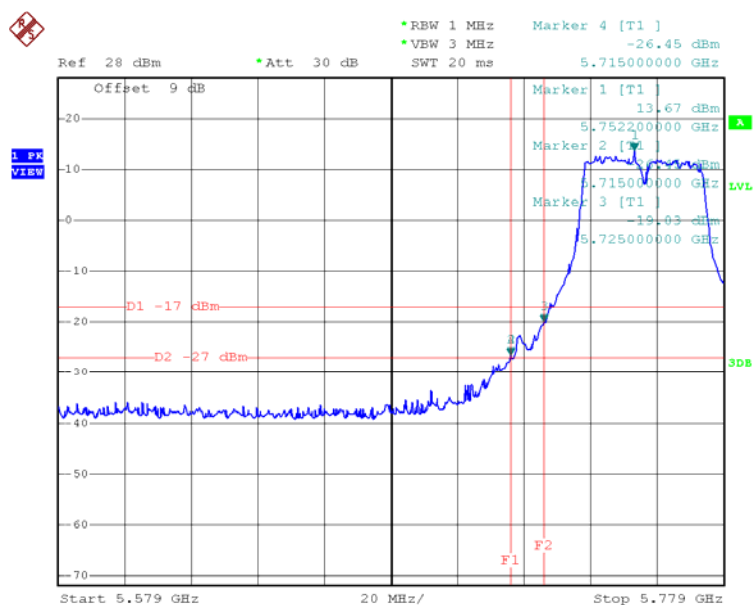
TXAC HT40 mode CH159



Date: 1.APR.2015 21:06:03

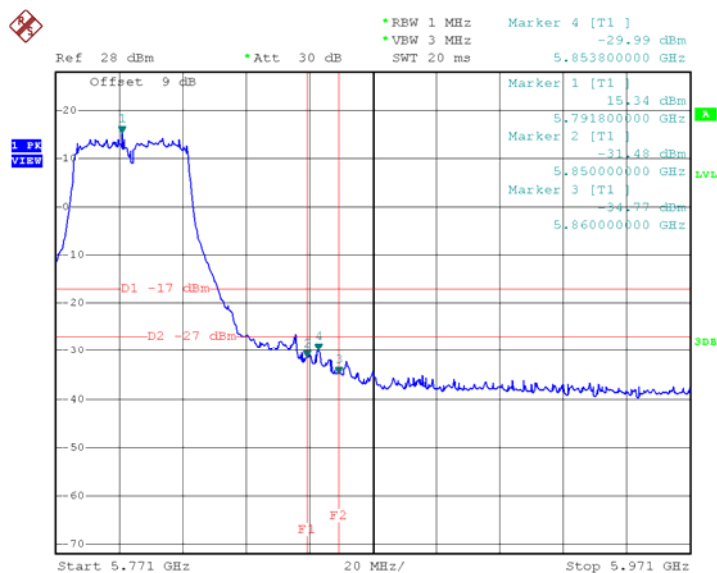
Test Mode: UNII-3/TX AC40 Mode_ANT 2

TX AC HT40 mode CH151



Date: 1.APR.2015 21:56:48

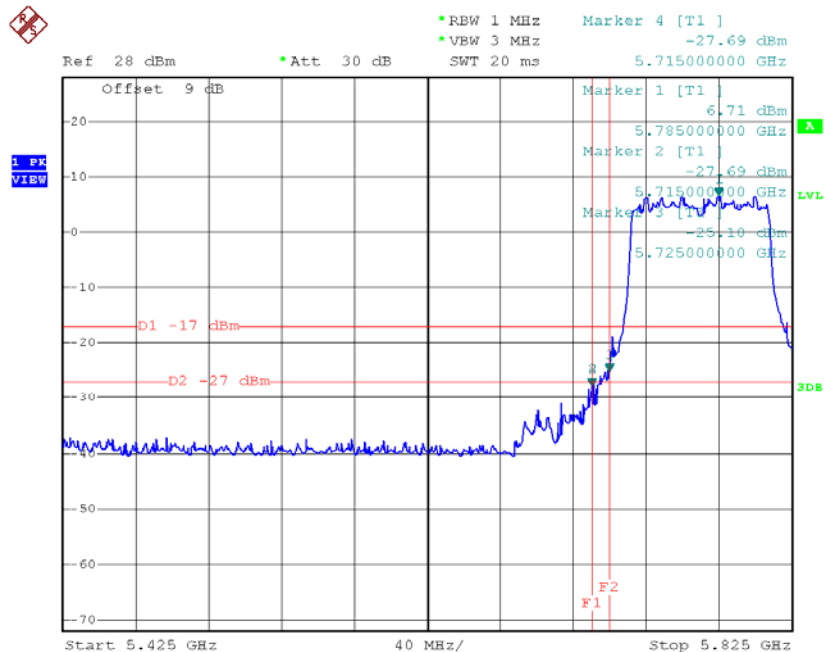
TX AC HT40 mode CH159



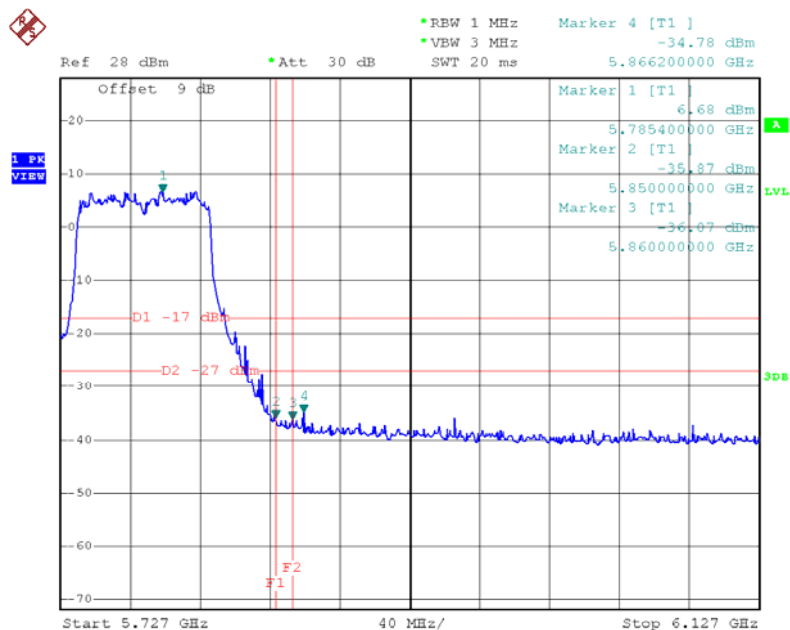
Date: 1.APR.2015 21:57:30

Test Mode: UNII-3/TX AC80 Mode_ANT 1

TXAC HT80 mode CH155



Date: 1.APR.2015 21:21:17

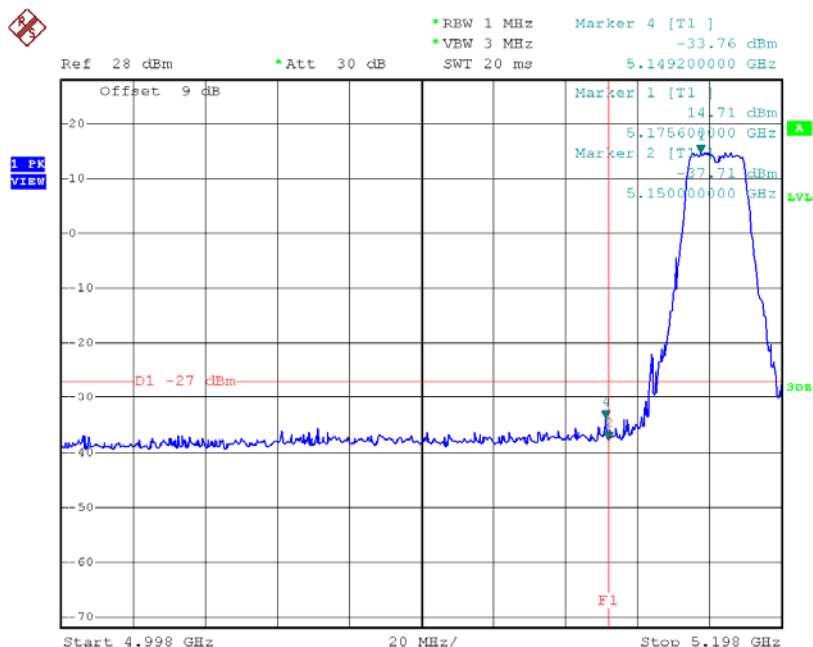


Date: 1.APR.2015 21:21:24

For 3TX

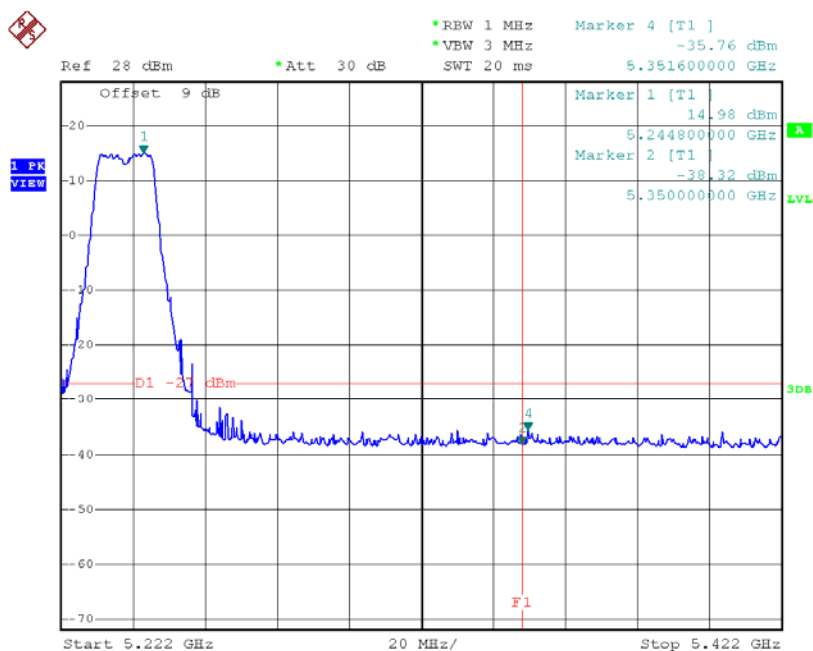
Test Mode:	UNII-1/TX A Mode_ANT 1
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TX mode CH36



Date: 2.APR.2015 16:36:28

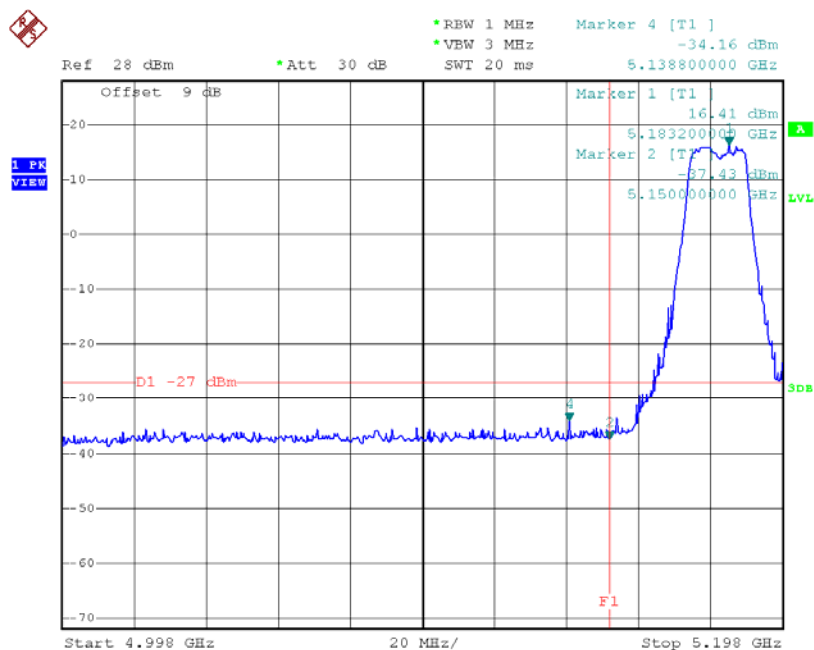
TX modeCH48



Date: 2.APR.2015 16:37:54

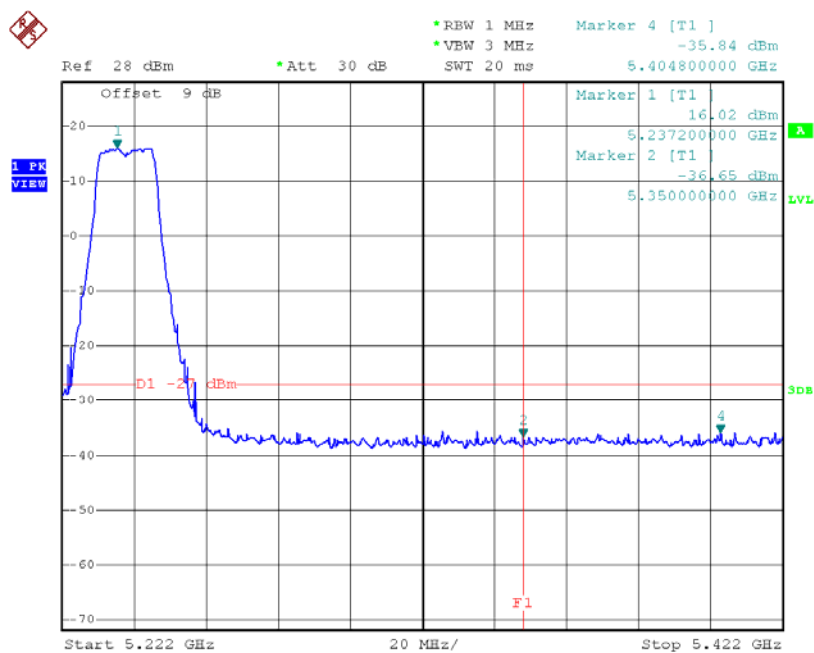
Test Mode:	UNII-1/TX A Mode_ANT 2
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TX mode CH36



Date: 2.APR.2015 17:29:50

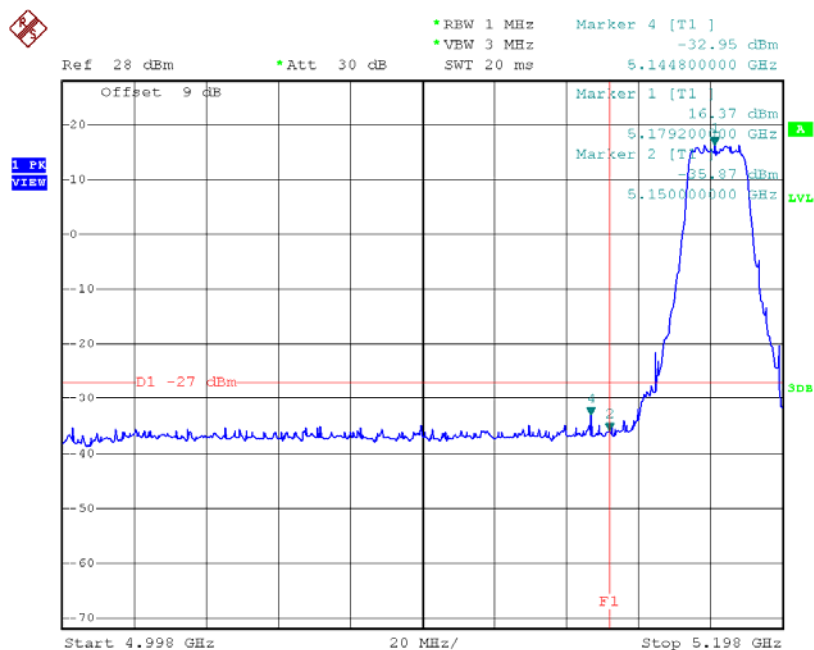
TX mode CH48



Date: 2.APR.2015 17:31:23

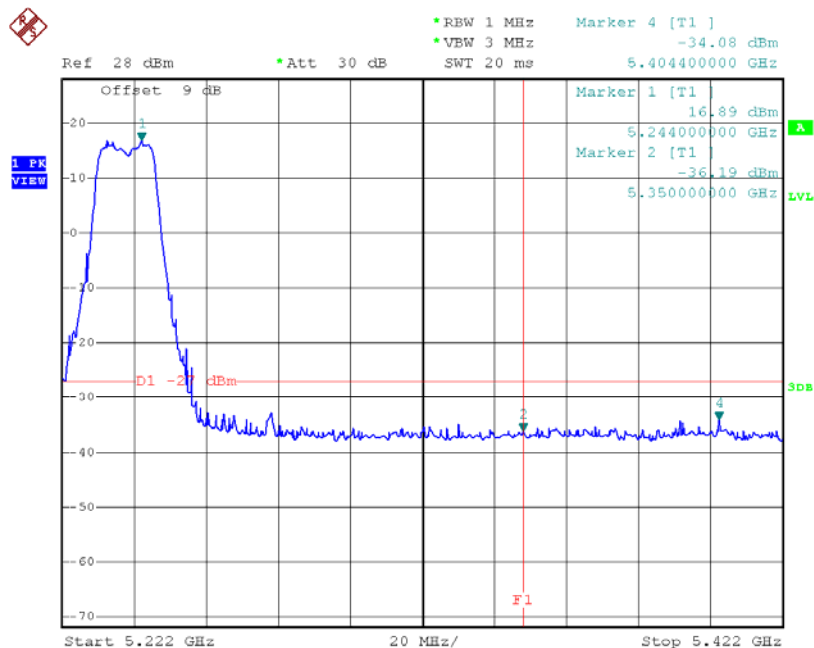
Test Mode:	UNII-1/TX A Mode_ANT 3
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TX mode CH36



Date: 2.APR.2015 18:18:49

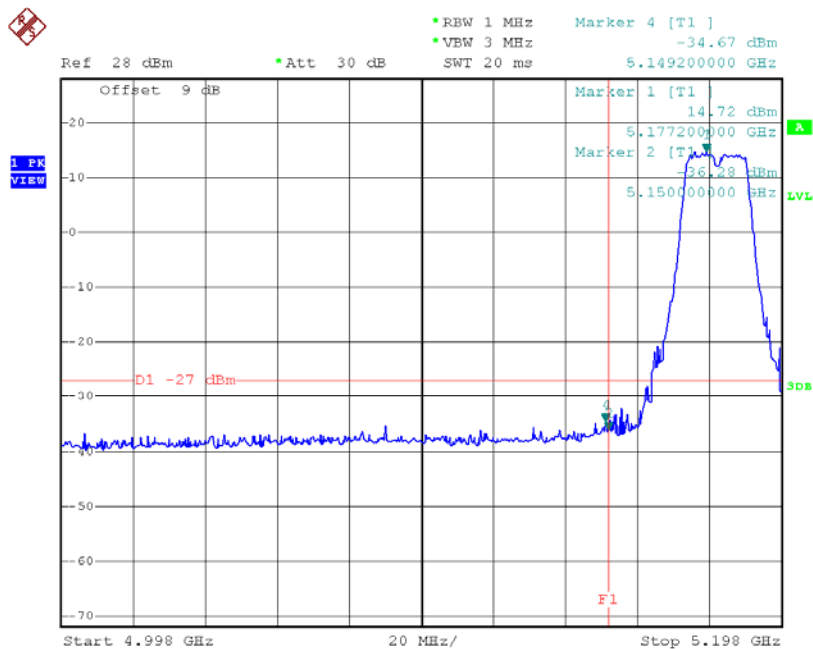
TX mode CH48



Date: 2.APR.2015 18:21:29

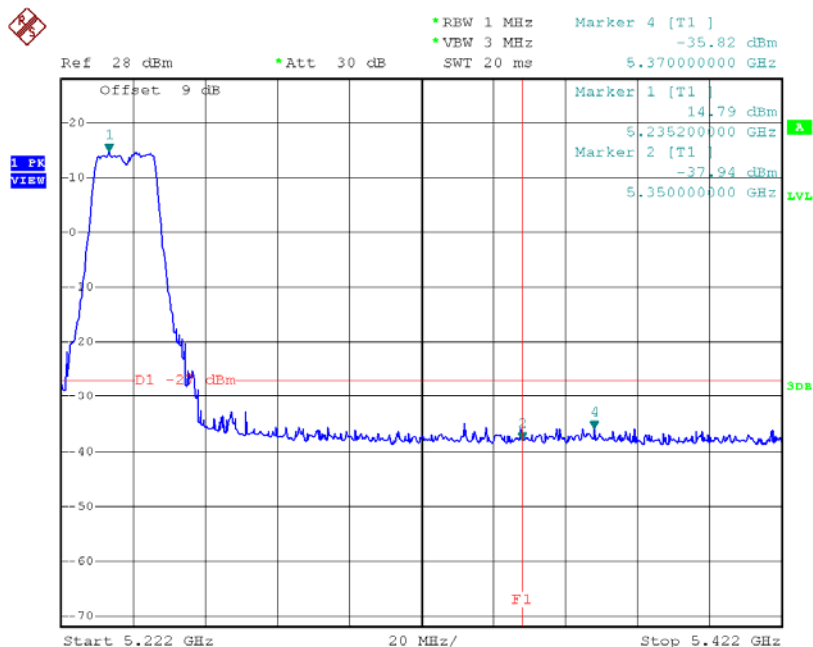
Test Mode: UNII-1/TX N20 Mode_ANT 1

TX mode CH36



Date: 2.APR.2015 16:49:07

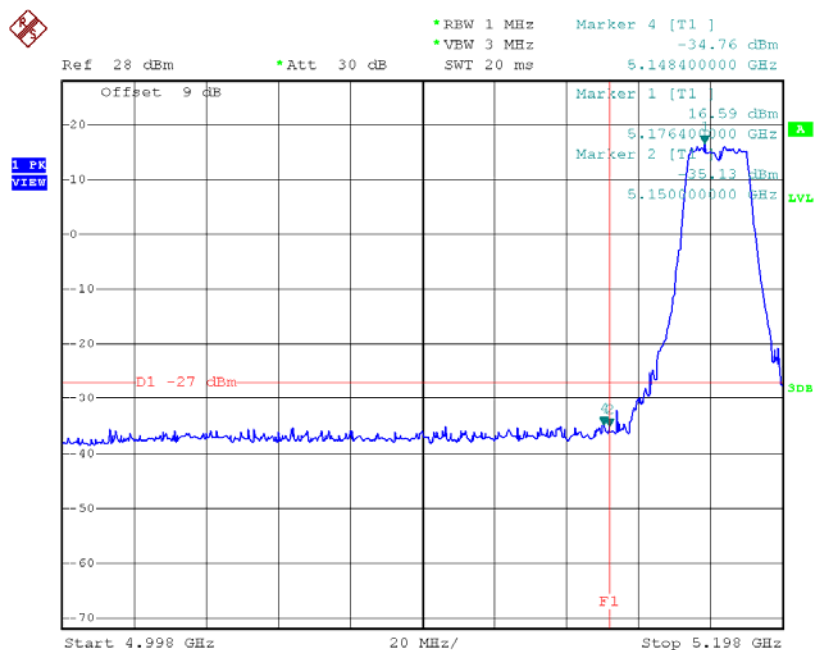
TX modeCH48



Date: 2.APR.2015 17:00:20

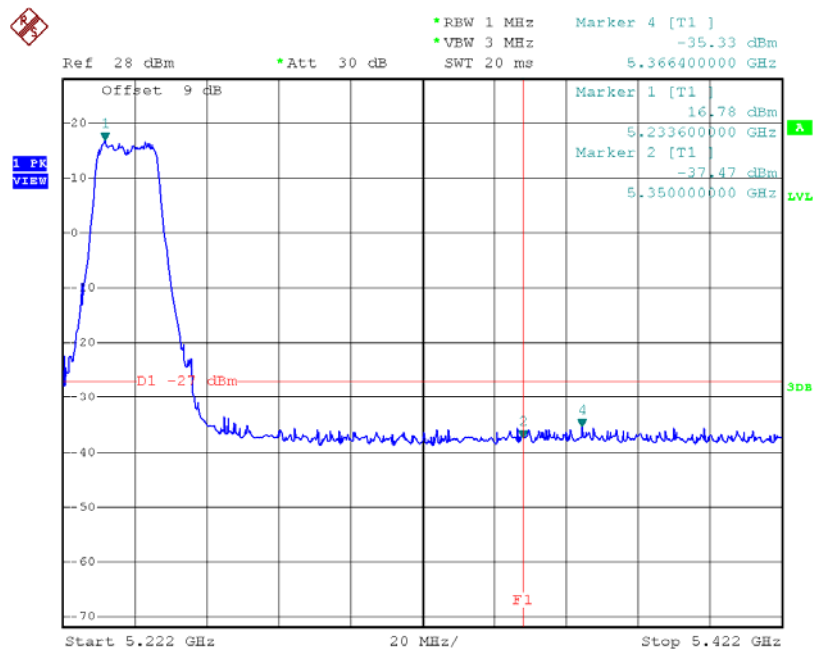
Test Mode:	UNII-1/TX N20 Mode_ANT 2
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TX mode CH36



Date: 2.APR.2015 17:46:13

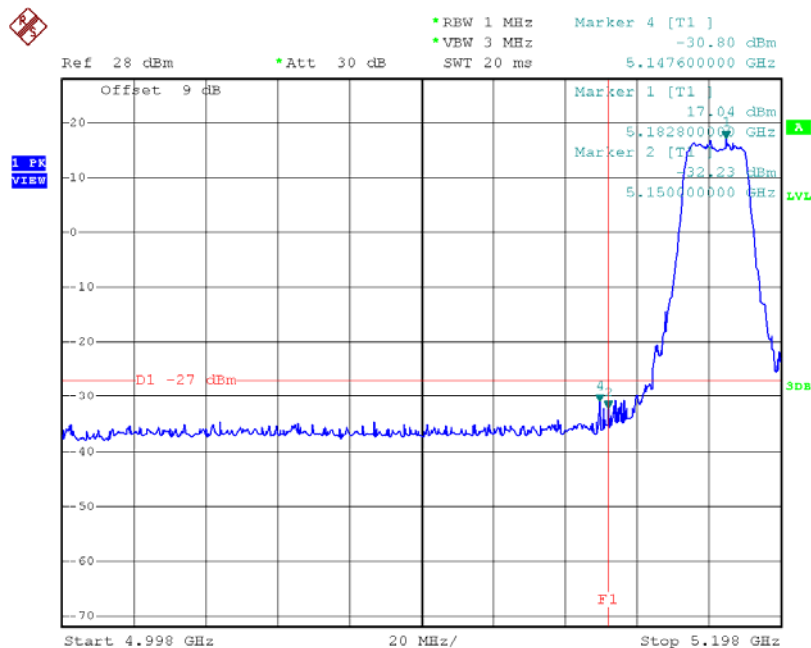
TX mode CH48



Date: 2.APR.2015 17:47:16

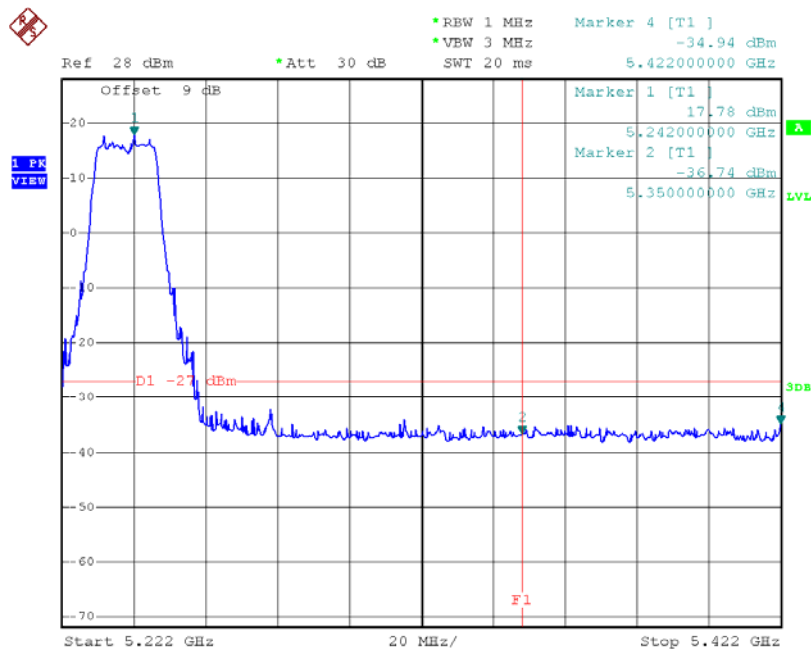
Test Mode:	UNII-1/TX N20 Mode_ANT 3
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TX mode CH36



Date: 2.APR.2015 20:38:53

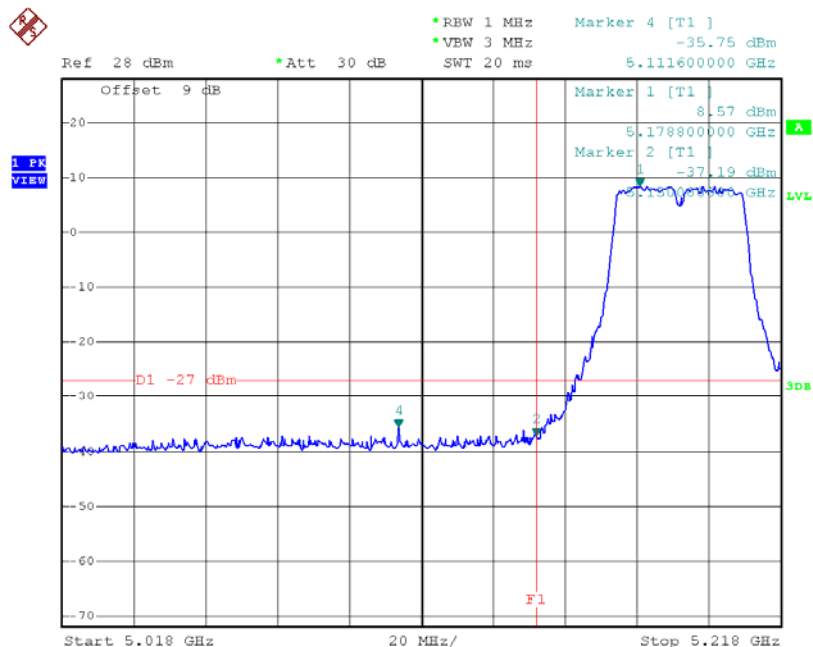
TX mode CH48



Date: 2.APR.2015 20:39:43

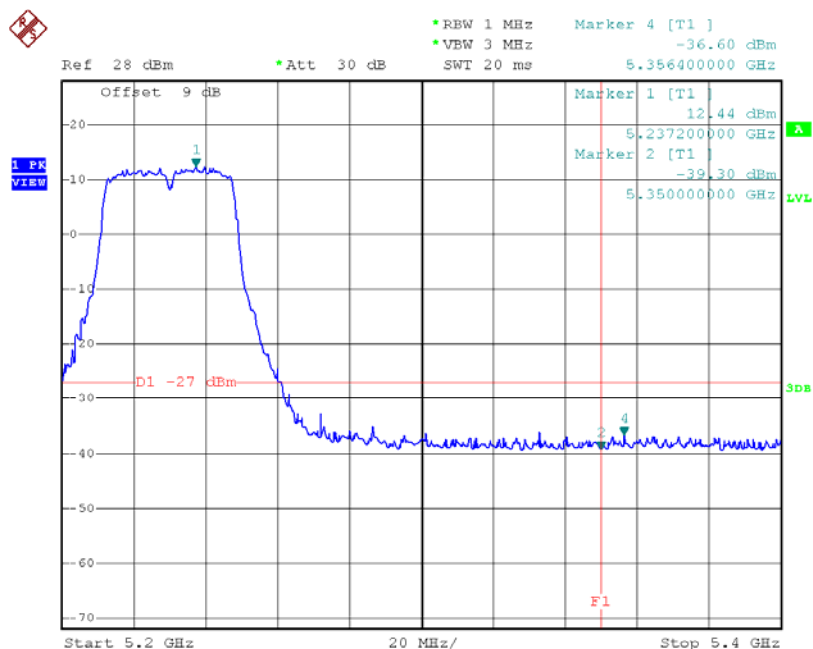
Test Mode: UNII-1/TX N40 Mode_ANT 1

TX mode CH38



Date: 2.APR.2015 17:10:35

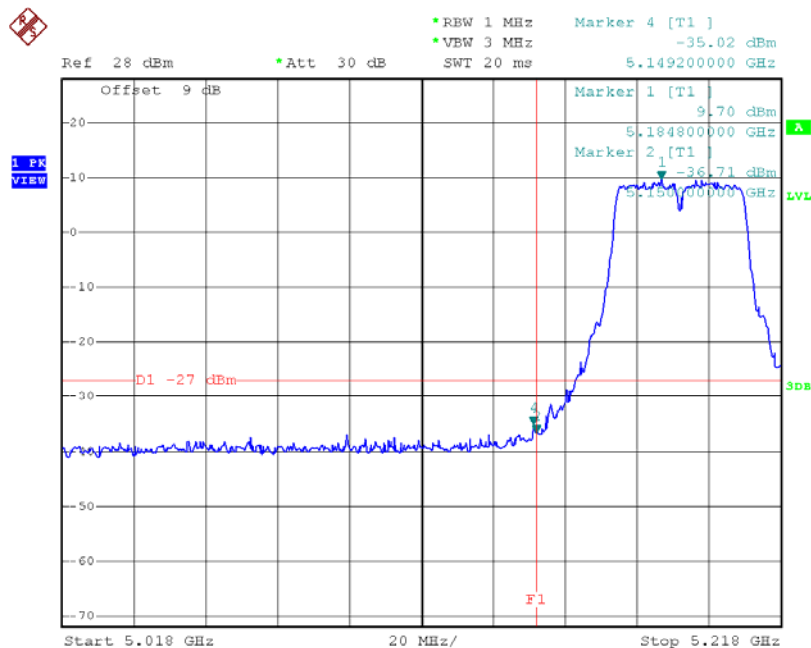
TX modeCH46



Date: 2.APR.2015 17:11:12

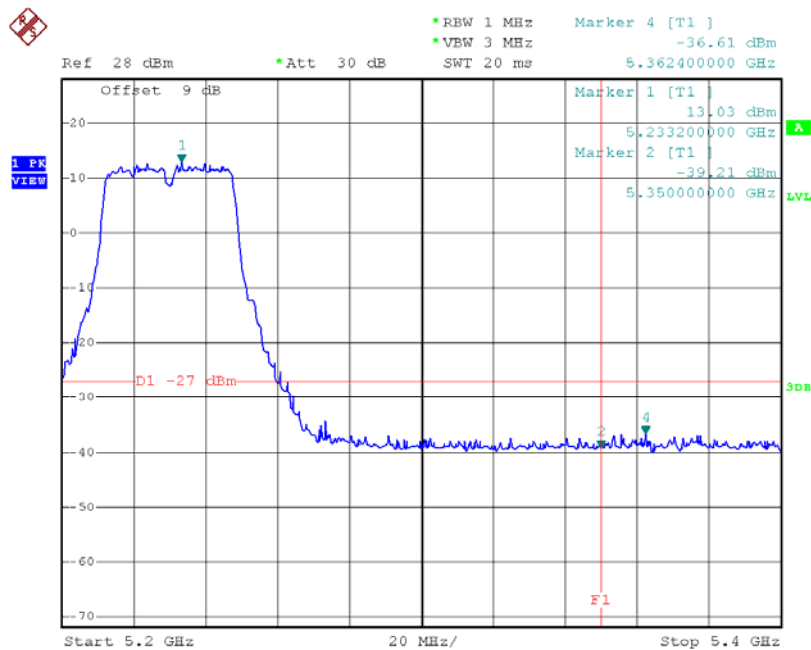
Test Mode:	UNII-1/TX N40 Mode_ANT 2
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TX mode CH38



Date: 2.APR.2015 17:56:11

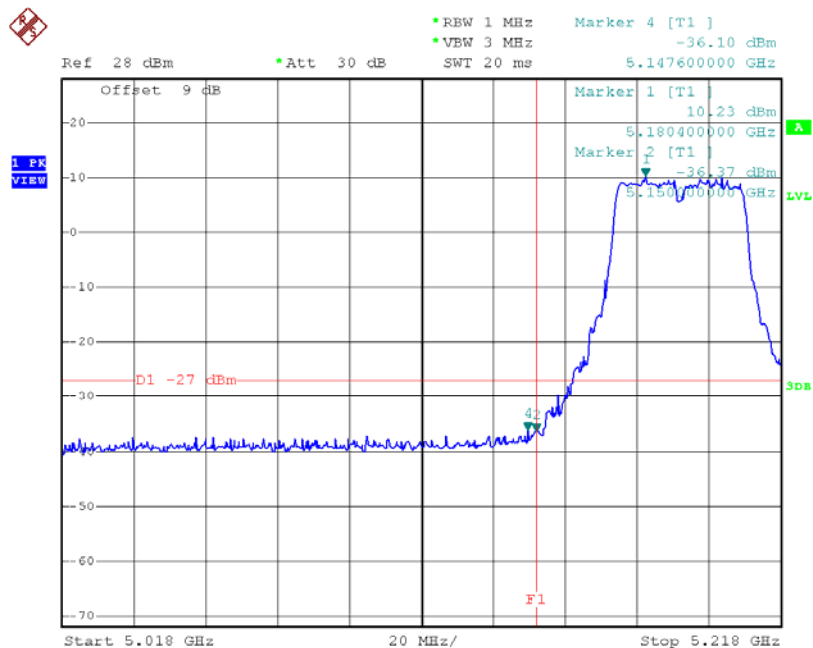
TX mode CH46



Date: 2.APR.2015 17:56:43

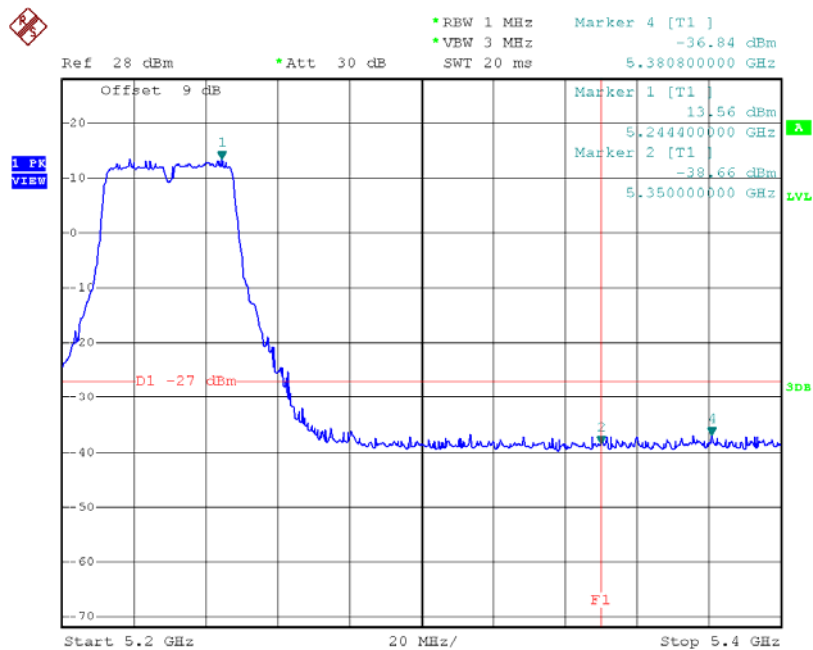
Test Mode:	UNII-1/TX N40 Mode_ANT 3
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TX mode CH38



Date: 2.APR.2015 20:48:51

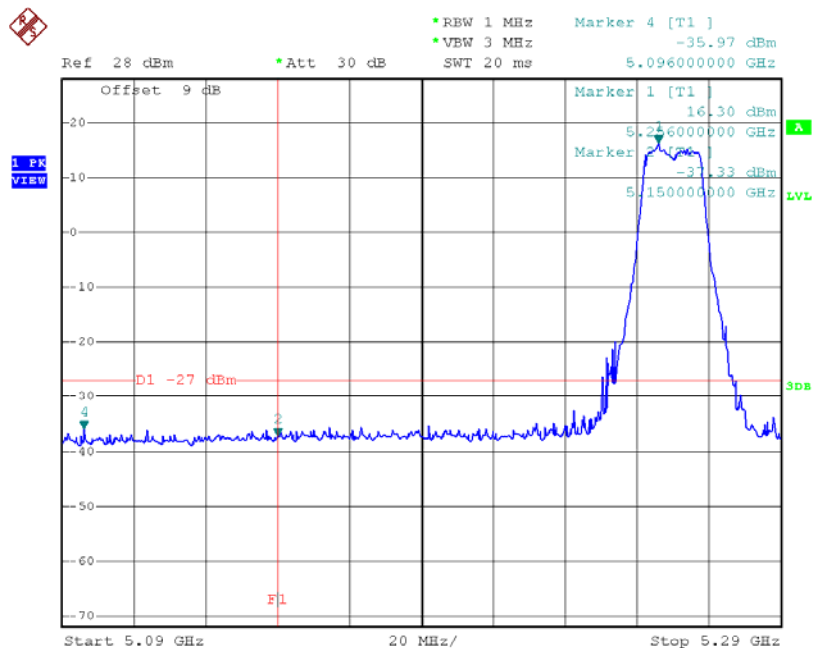
TX mode CH46



Date: 2.APR.2015 20:50:32

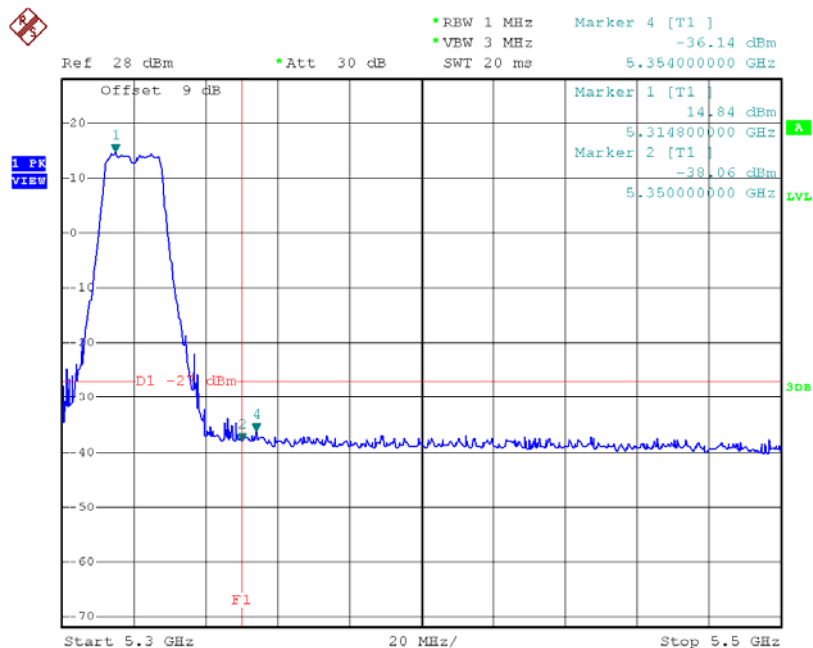
Test Mode:	UNII-2A/TX A Mode_ANT 1
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TX mode CH52



Date: 2.APR.2015 16:38:37

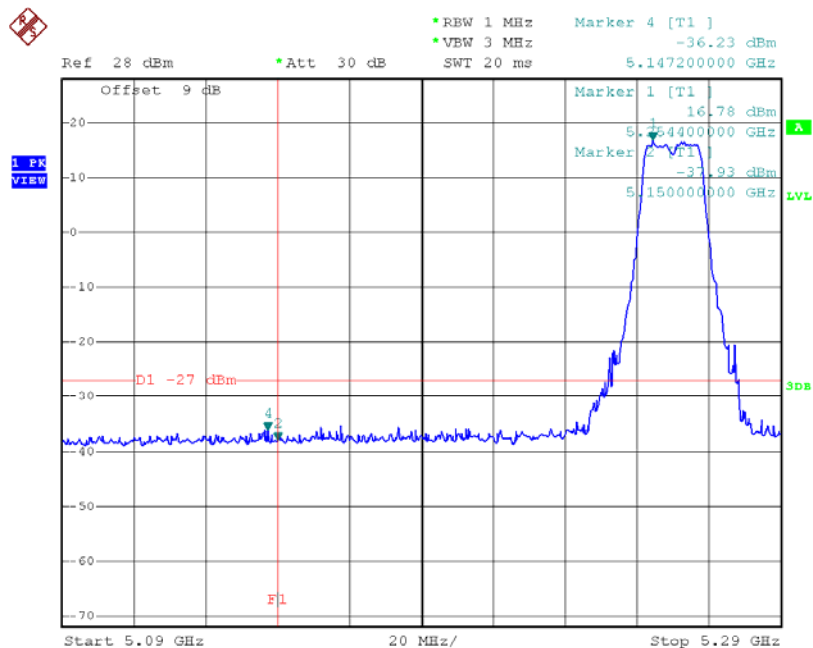
TX mode CH64



Date: 2.APR.2015 16:42:54

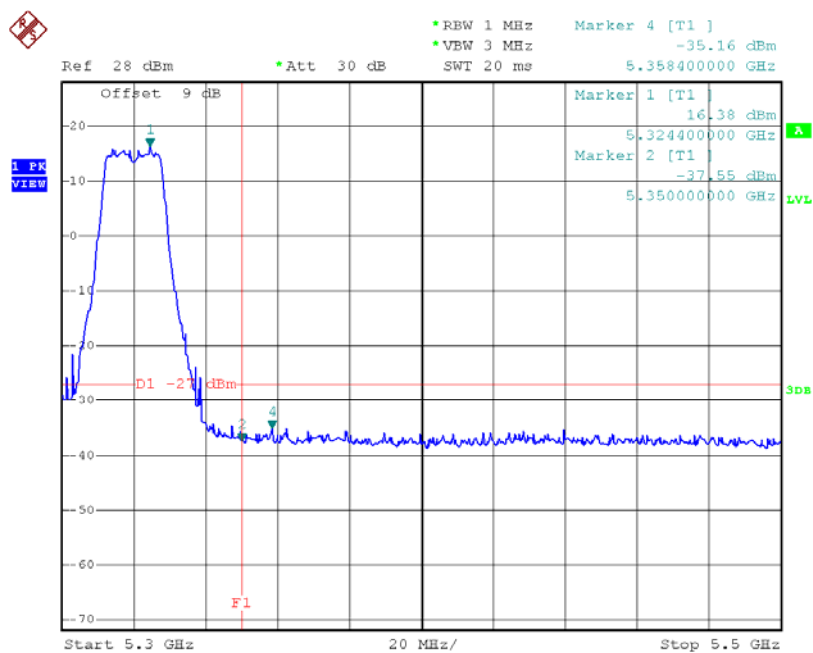
Test Mode:	UNII-2A/TX A Mode_ANT 2
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TX mode CH52



Date: 2.APR.2015 17:32:05

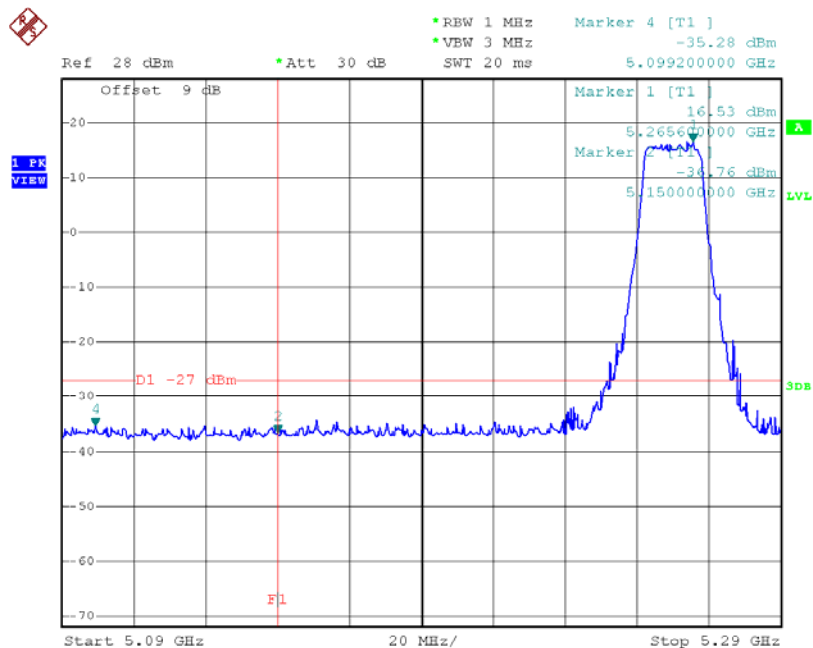
TX mode CH64



Date: 2.APR.2015 17:41:07

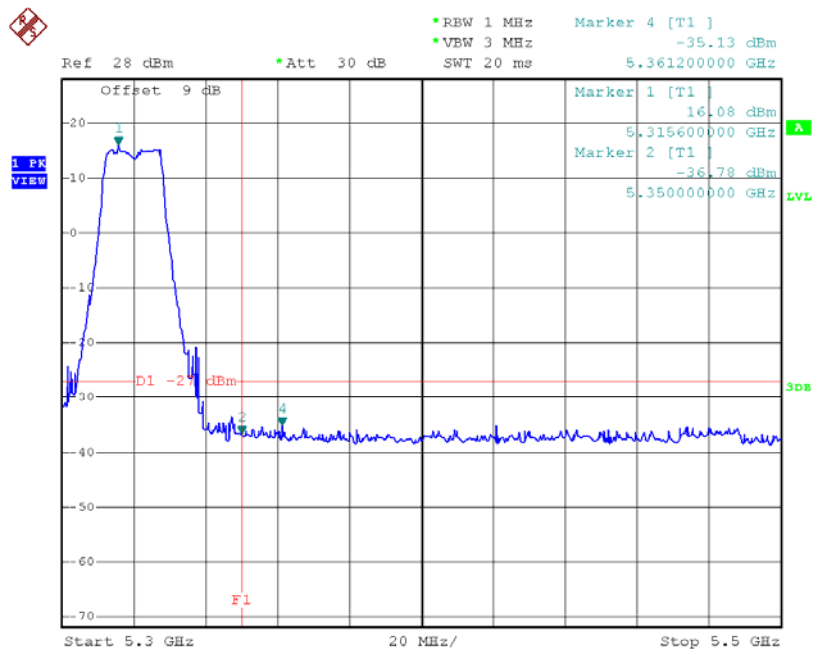
Test Mode:	UNII-2A/TX A Mode_ANT 3
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TX mode CH52



Date: 2.APR.2015 18:22:11

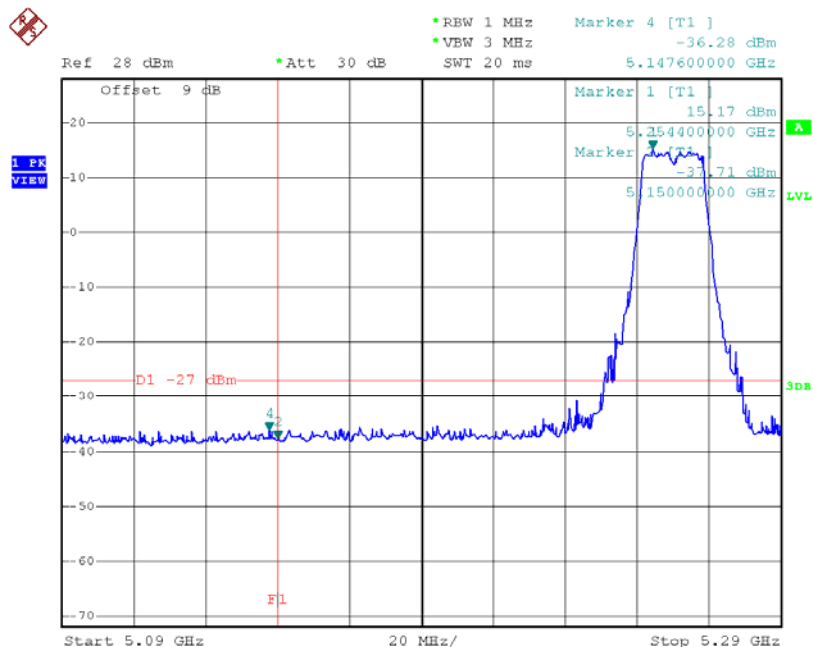
TX mode CH64



Date: 2.APR.2015 18:24:20

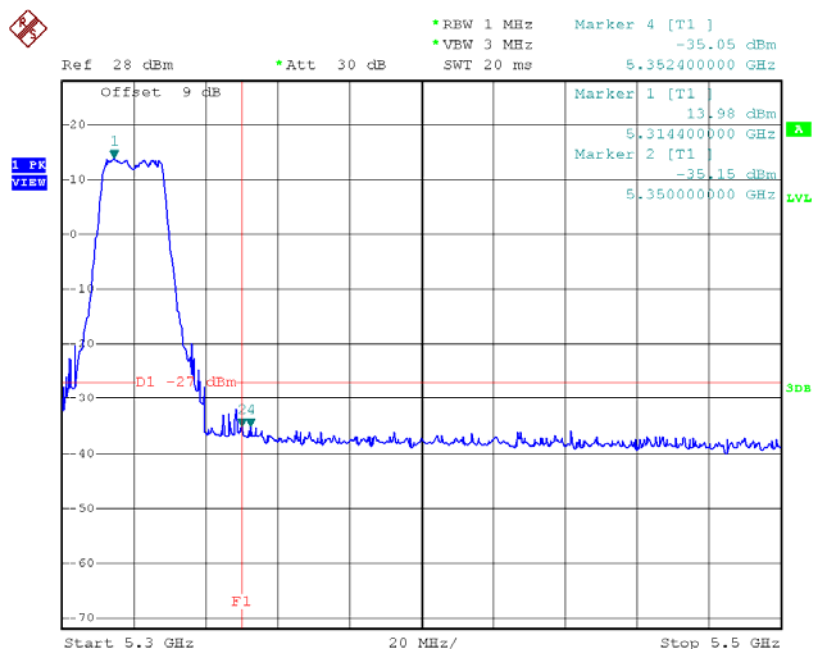
Test Mode: UNII-2A/TX N20 Mode_ANT 1

TX mode CH52



Date: 2.APR.2015 17:00:52

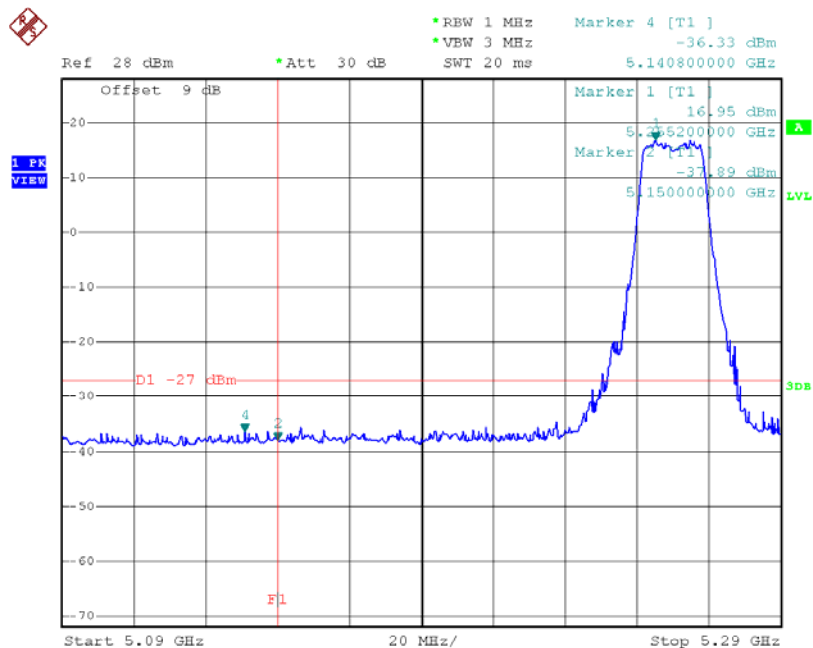
TX modeCH64



Date: 2.APR.2015 17:01:40

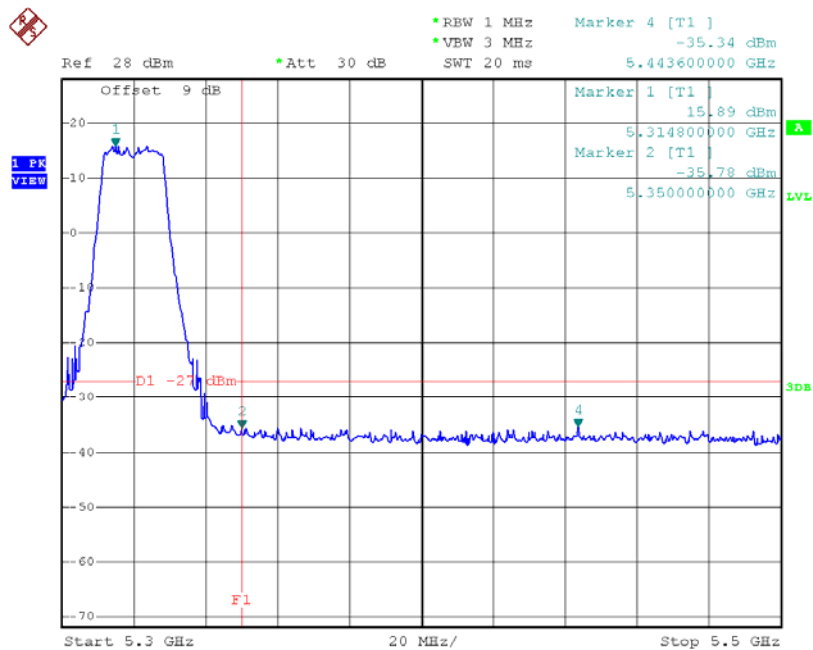
Test Mode: UNII-2A/TX N20 Mode_ANT 2

TX mode CH52



Date: 2.APR.2015 17:47:42

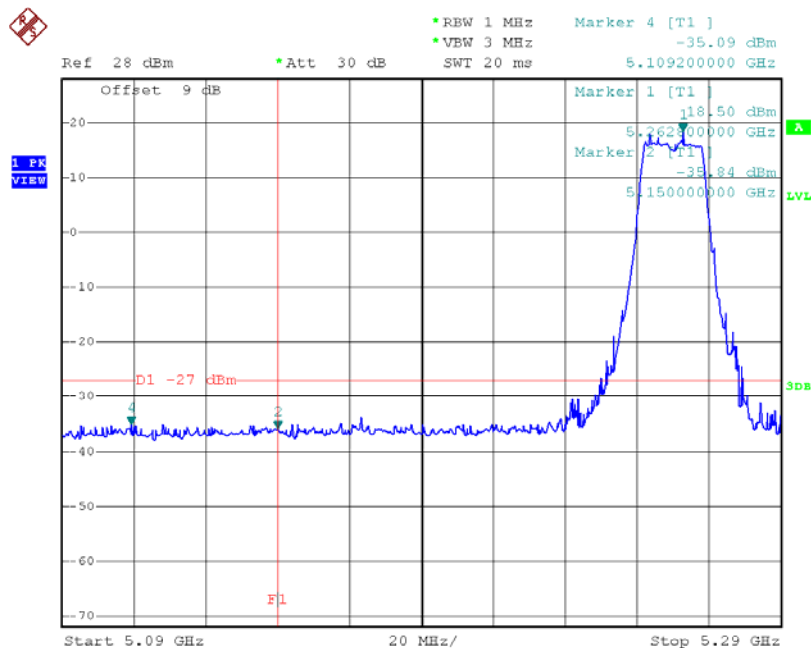
TX mode CH64



Date: 2.APR.2015 17:48:29

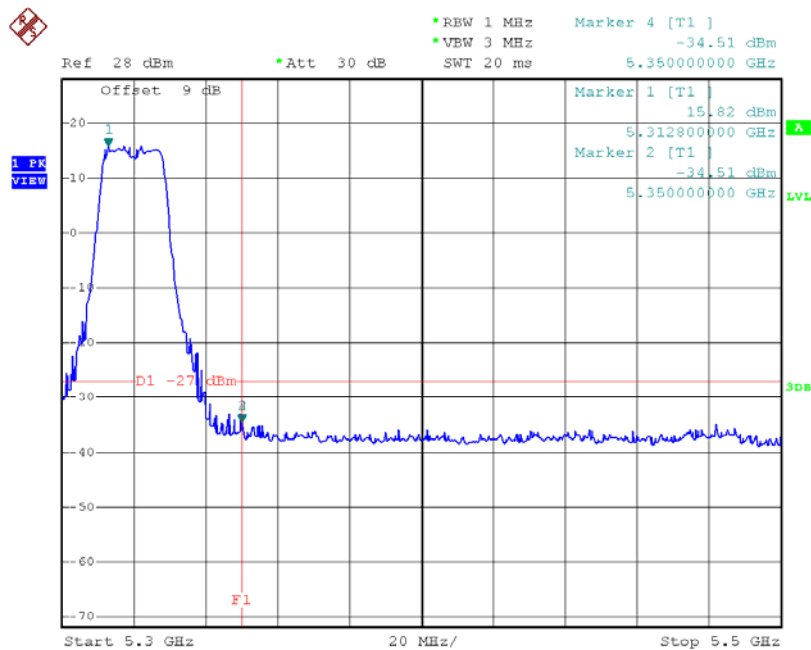
Test Mode: UNII-2A/TX N20 Mode_ANT 3

TX mode CH52



Date: 2.APR.2015 20:40:10

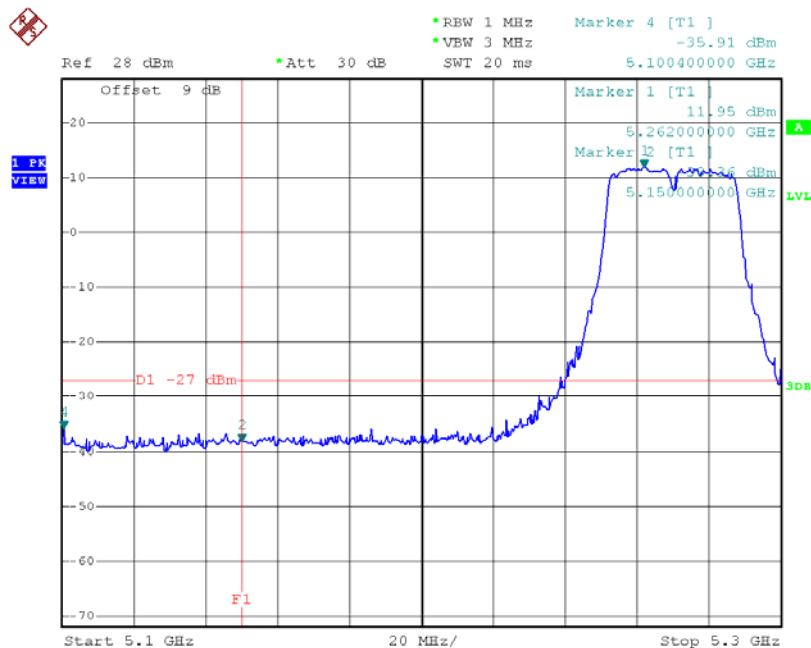
TX modeCH64



Date: 2.APR.2015 20:40:56

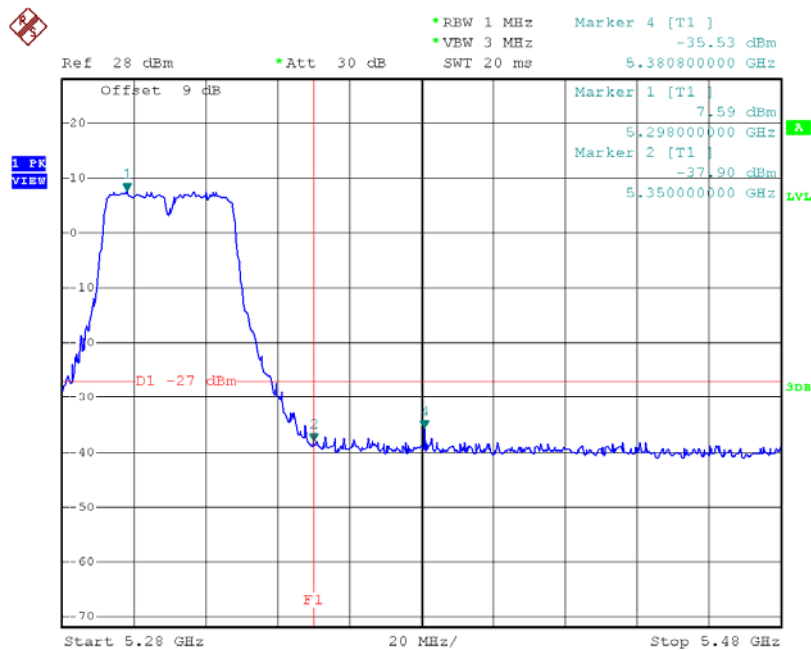
Test Mode:	UNII-2A/TX N40 Mode_ANT 1
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TX mode CH54



Date: 2.APR.2015 17:11:46

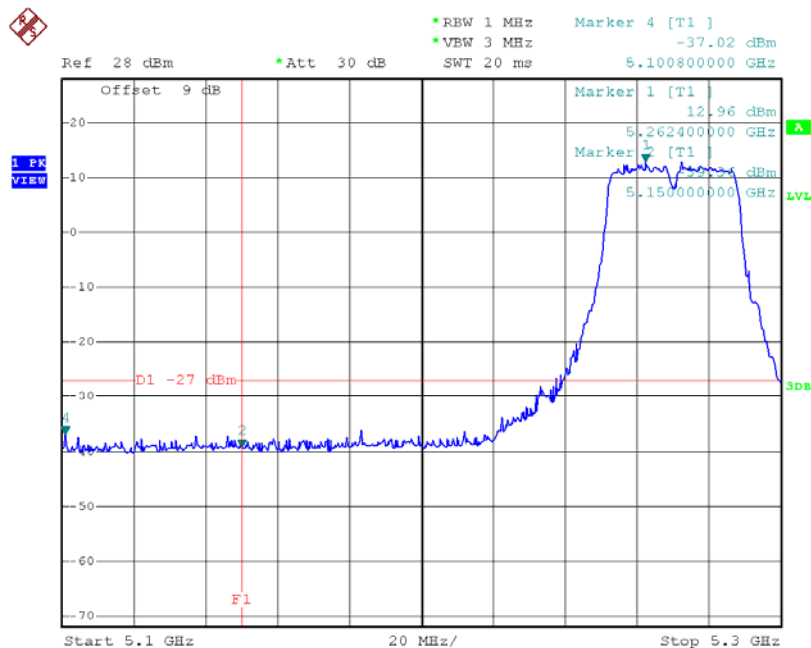
TX mode CH62



Date: 2.APR.2015 17:12:19

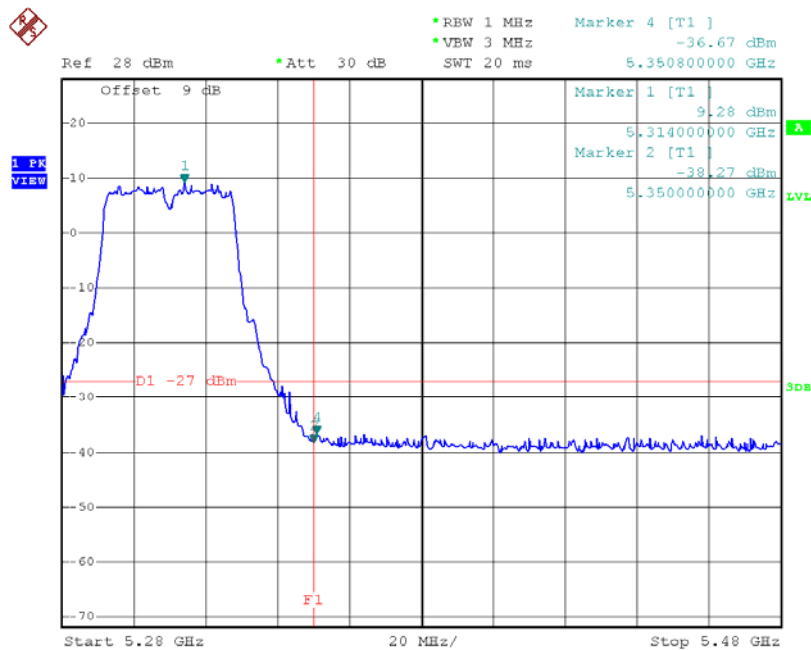
Test Mode: UNII-2A/TX N40 Mode_ANT 2

TX mode CH54



Date: 2.APR.2015 17:57:17

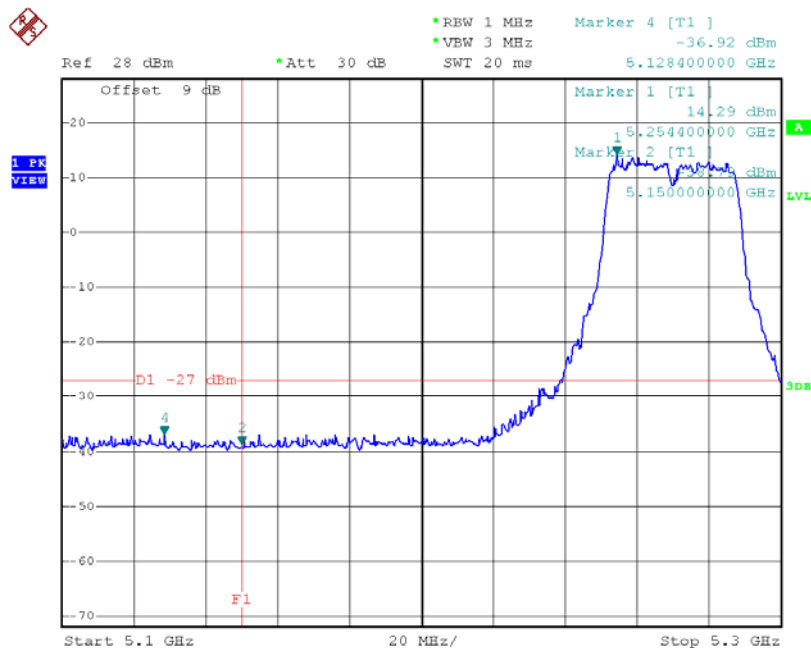
TX mode CH62



Date: 2.APR.2015 17:57:45

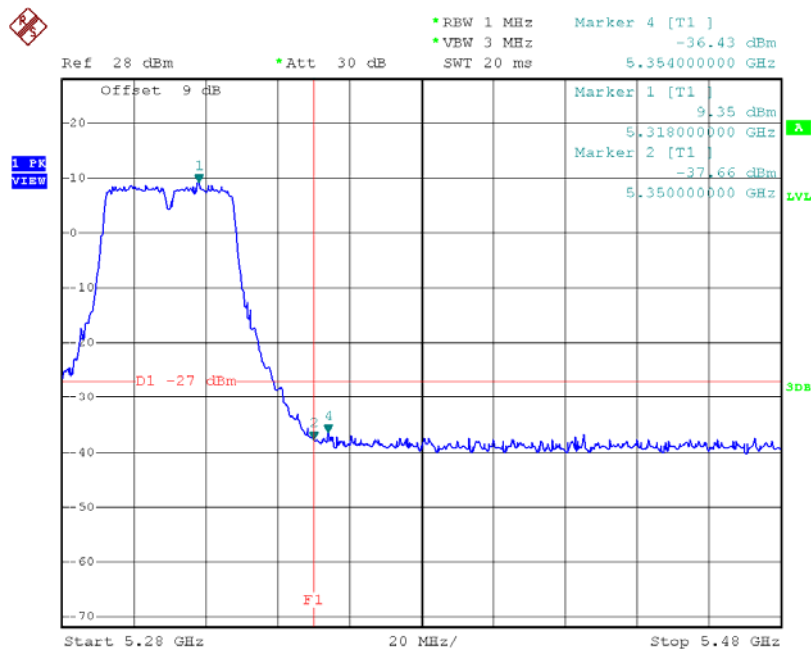
Test Mode:	UNII-2A/TX N40 Mode_ANT 3
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TX mode CH54



Date: 2.APR.2015 20:51:15

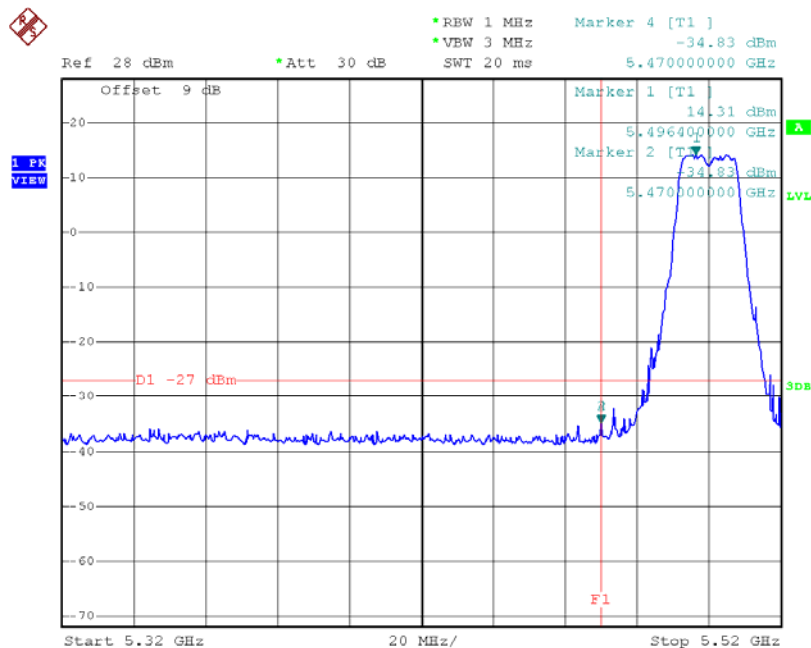
TX mode CH62



Date: 2.APR.2015 20:51:46

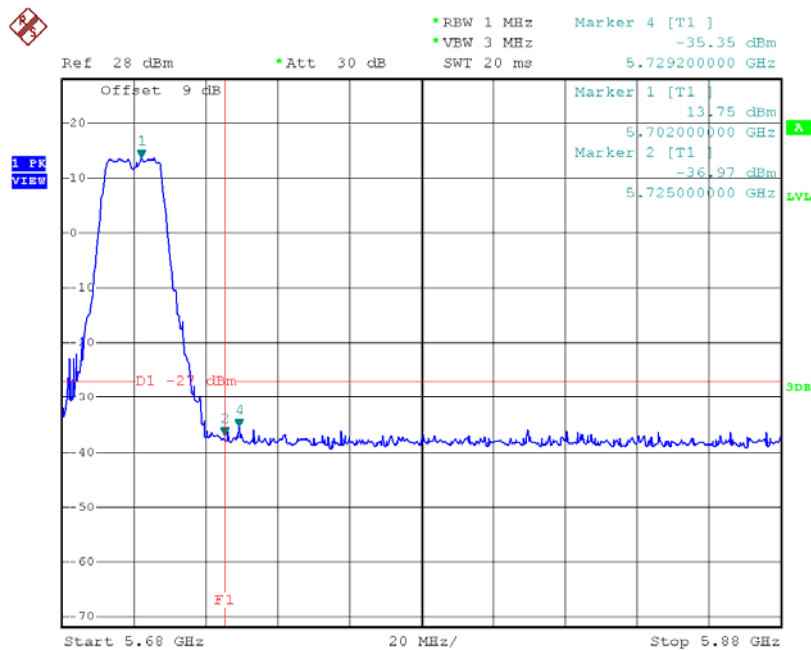
Test Mode:	UNII-2C/TX A Mode_ANT 1
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TX mode CH100



Date: 2.APR.2015 16:43:37

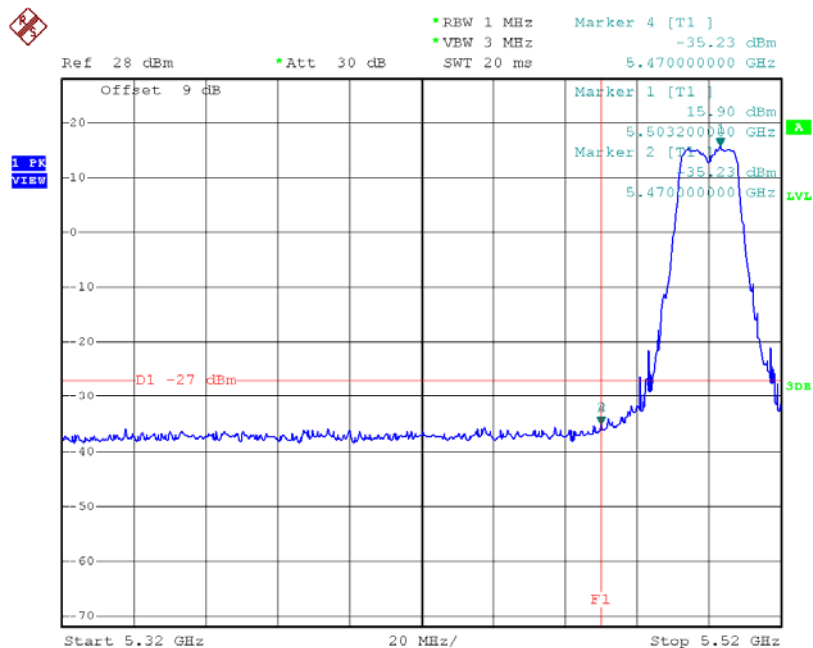
TX mode CH140



Date: 2.APR.2015 16:44:57

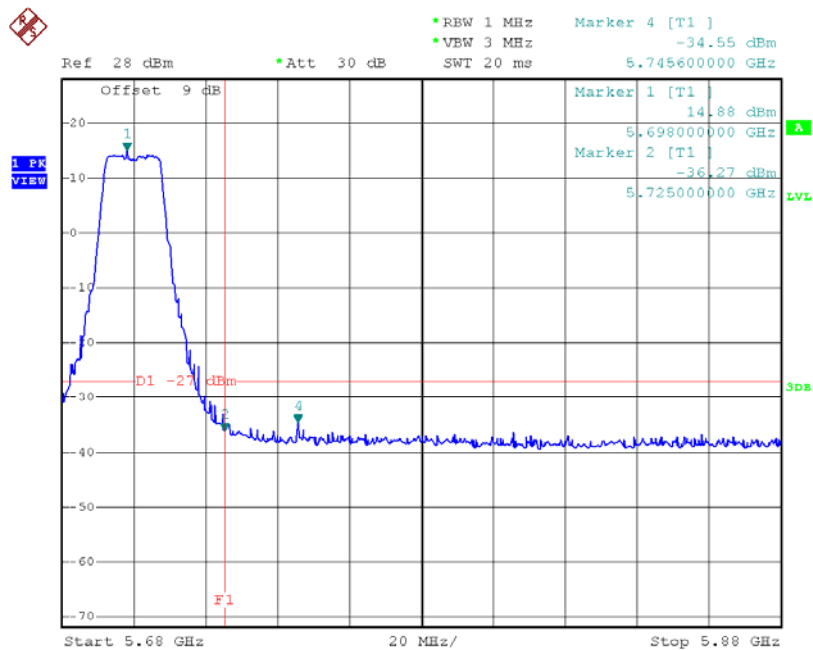
Test Mode:	UNII-2C/TX A Mode_ANT 2
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TX mode CH100



Date: 2.APR.2015 17:41:48

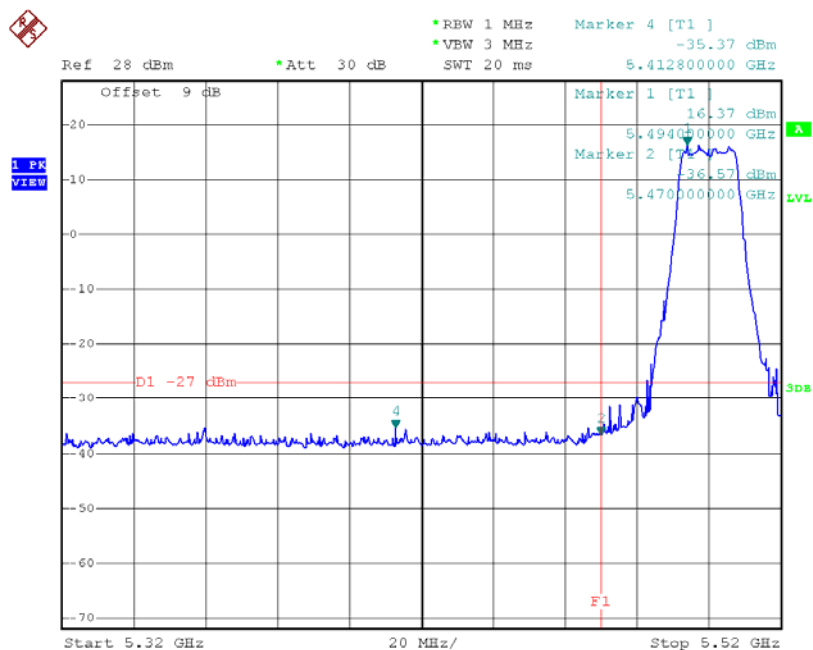
TX mode CH140



Date: 2.APR.2015 17:43:04

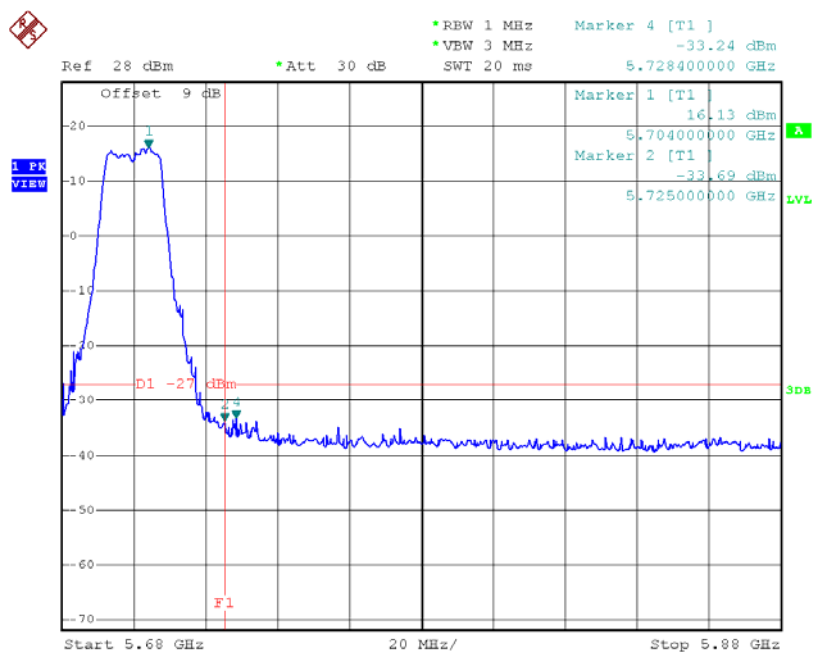
Test Mode:	UNII-2C/TX A Mode_ANT 3
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TX mode CH100



Date: 2.APR.2015 18:25:01

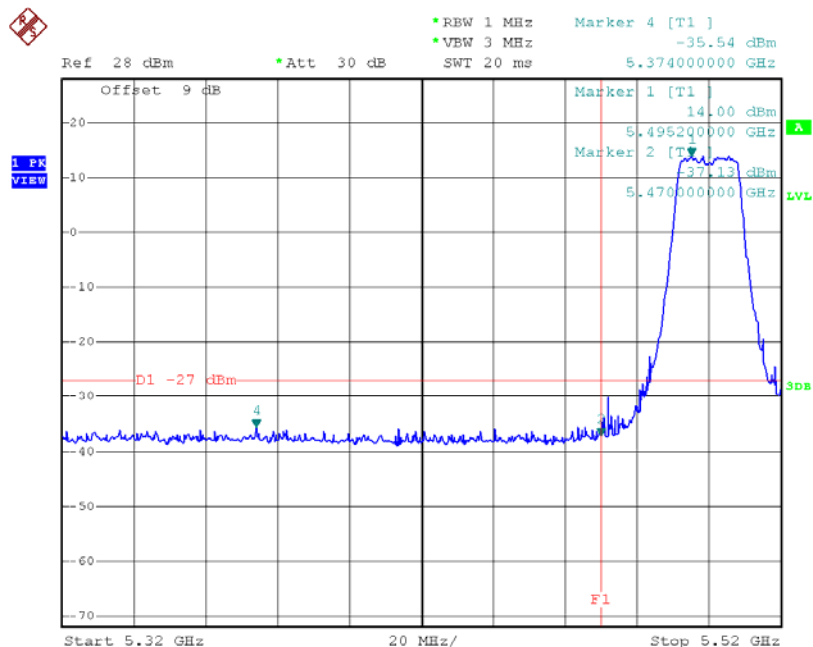
TX mode CH140



Date: 2.APR.2015 20:35:46

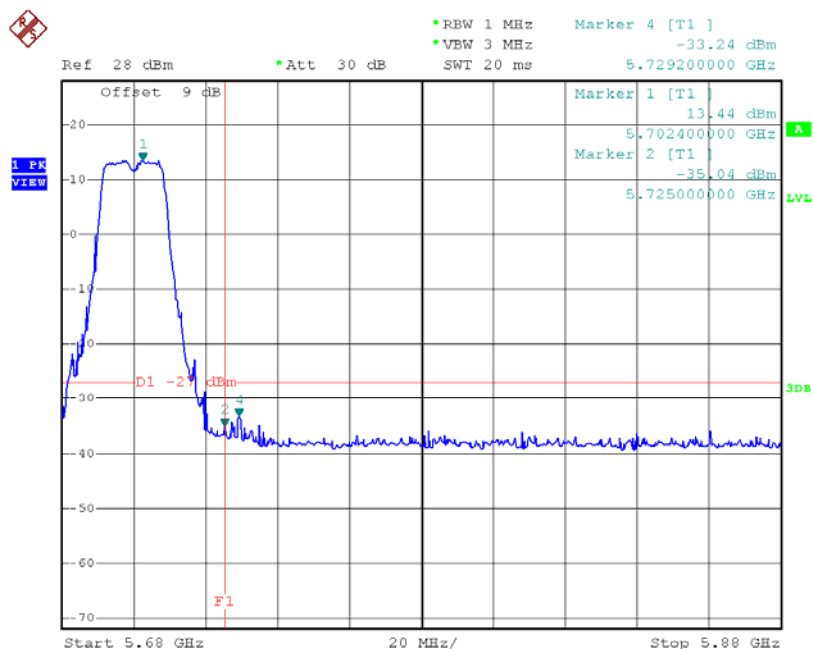
Test Mode: UNII-2C/TX N20 Mode_ANT 1

TX mode CH100



Date: 2.APR.2015 17:02:04

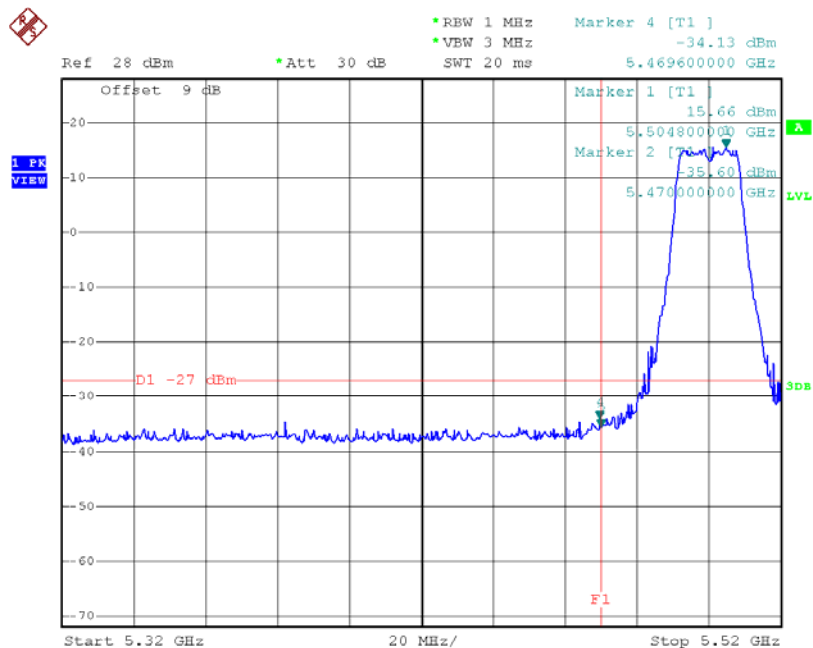
TX mode CH140



Date: 2.APR.2015 17:02:45

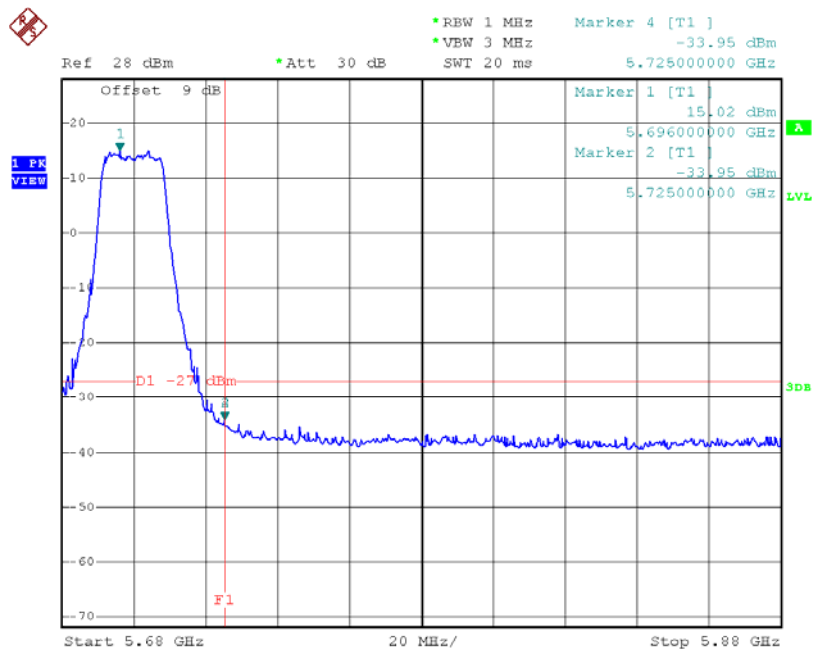
Test Mode: UNII-2C/TX N20 Mode_ANT 2

TX mode CH100



Date: 2.APR.2015 17:48:53

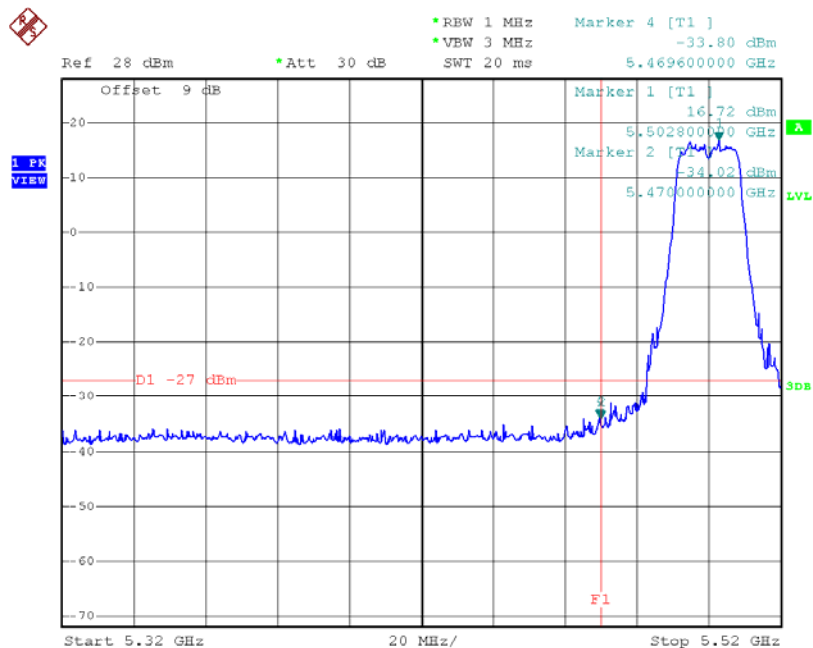
TX mode CH140



Date: 2.APR.2015 17:49:35

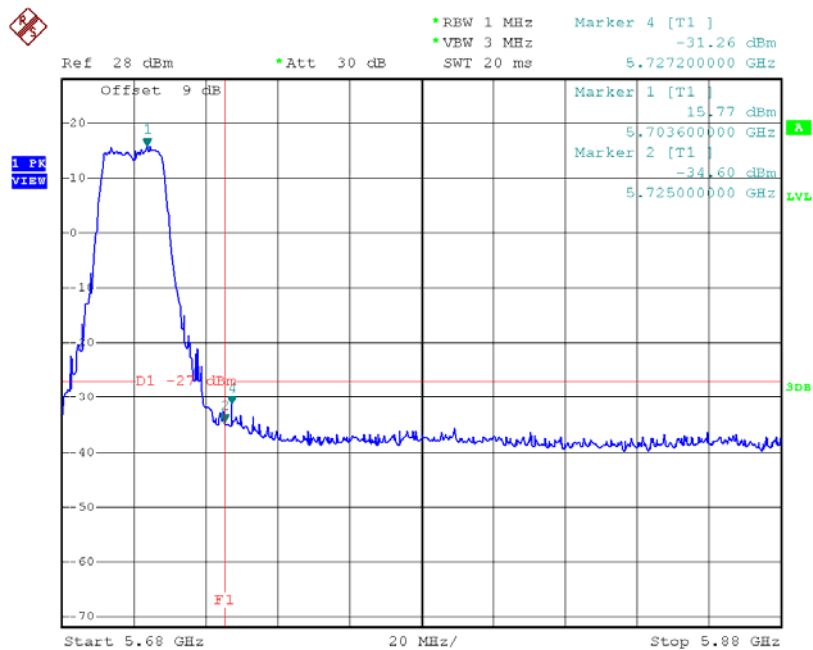
Test Mode: UNII-2C/TX N20 Mode_ANT 3

TX mode CH100



Date: 2.APR.2015 20:41:21

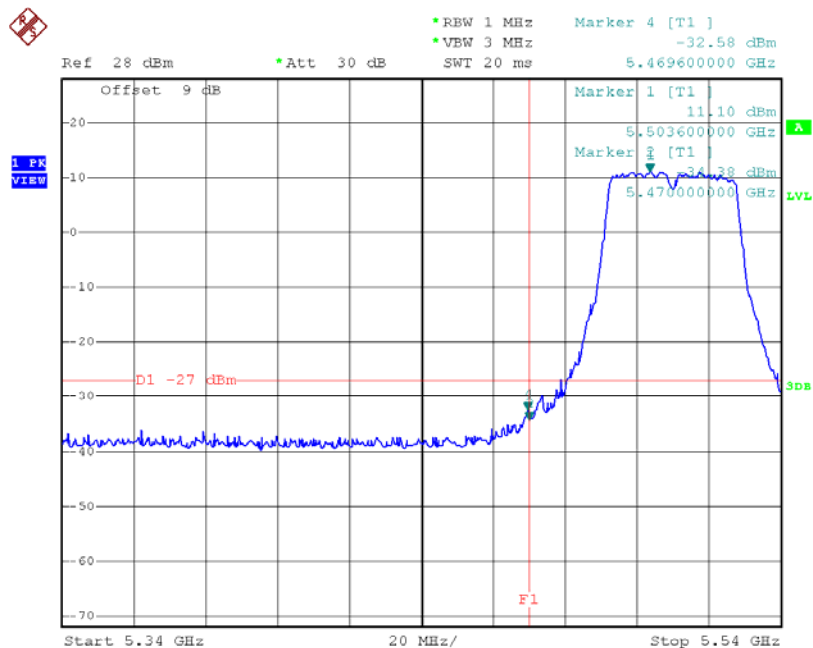
TX mode CH140



Date: 2.APR.2015 20:42:05

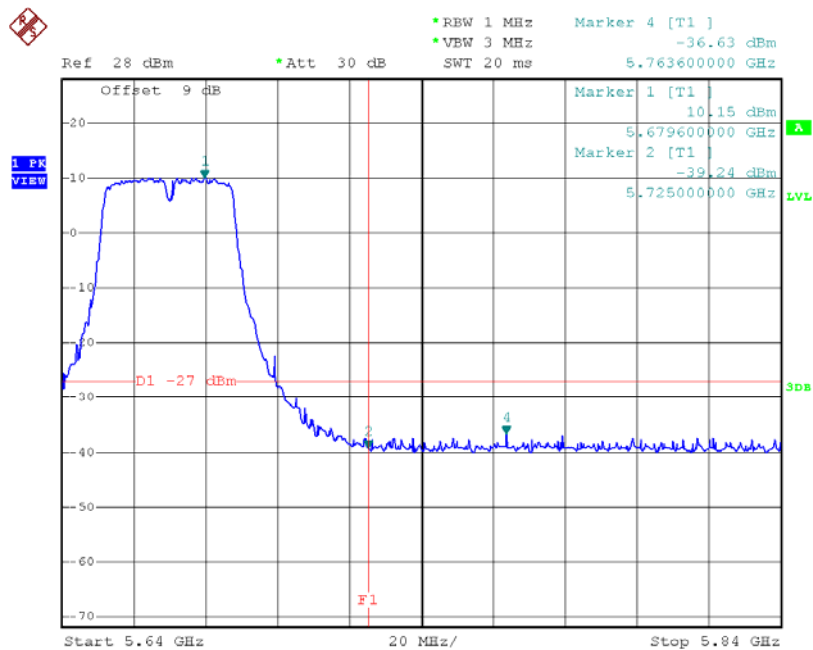
Test Mode:	UNII-2C/TX N40 Mode_ANT 1
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TX mode CH102



Date: 2.APR.2015 17:12:45

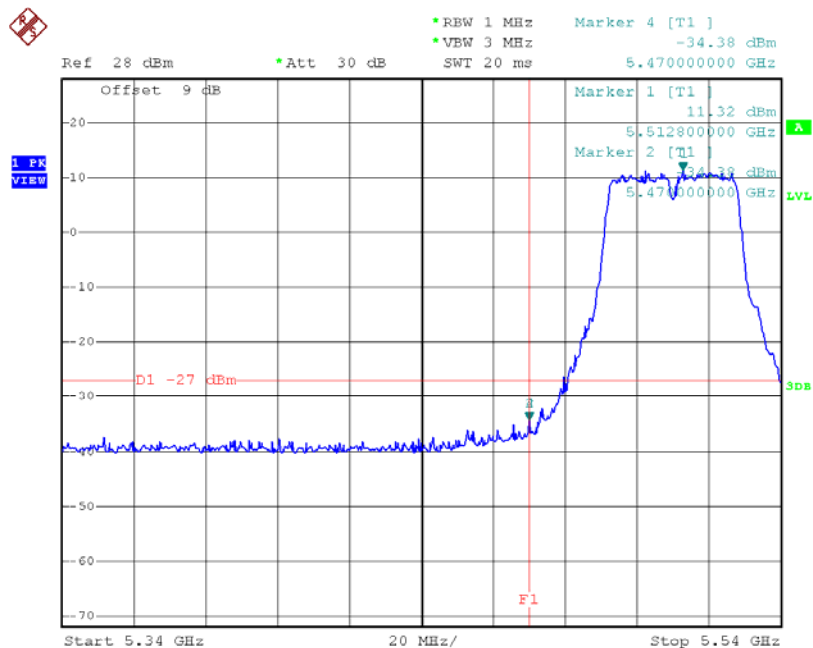
TX mode CH134



Date: 2.APR.2015 17:13:29

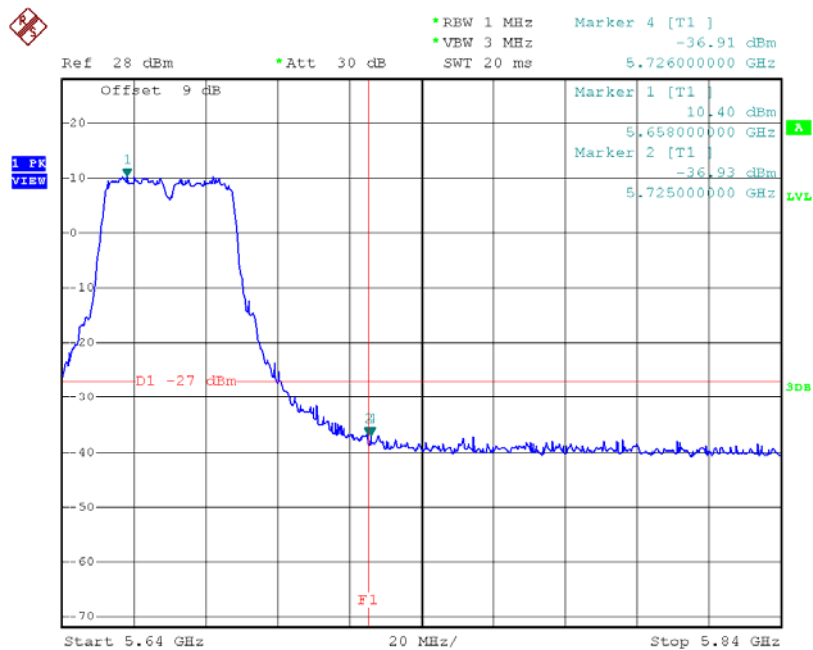
Test Mode: UNII-2C/TX N40 Mode_ANT 2

TX mode CH102



Date: 2.APR.2015 17:58:12

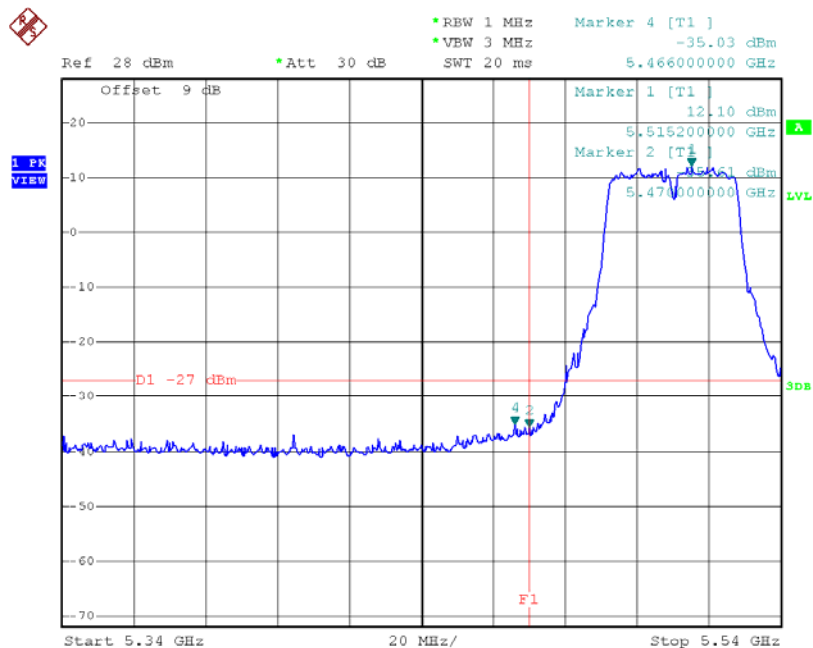
TX mode CH134



Date: 2.APR.2015 17:59:00

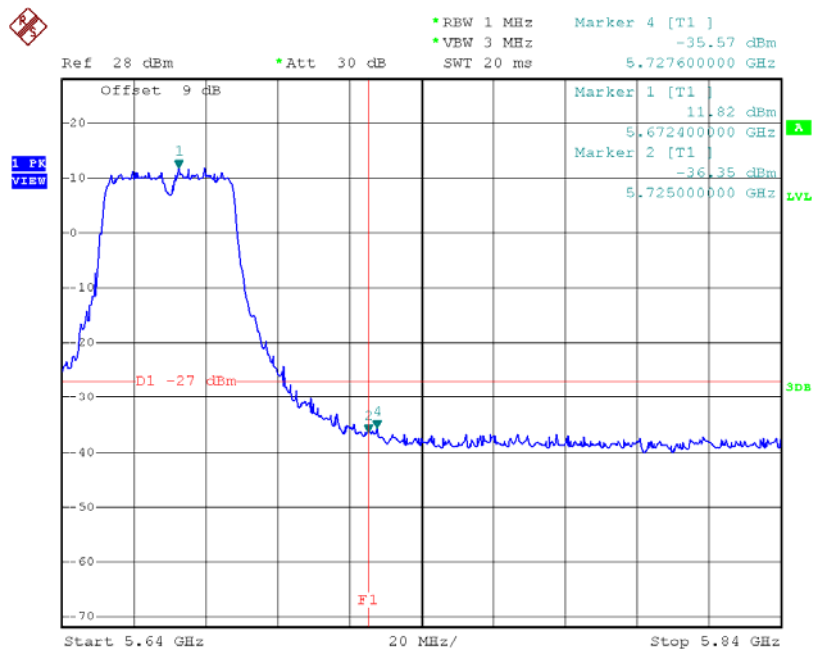
Test Mode: UNII-2C/TX N40 Mode_ANT 3

TX mode CH102



Date: 2.APR.2015 20:52:12

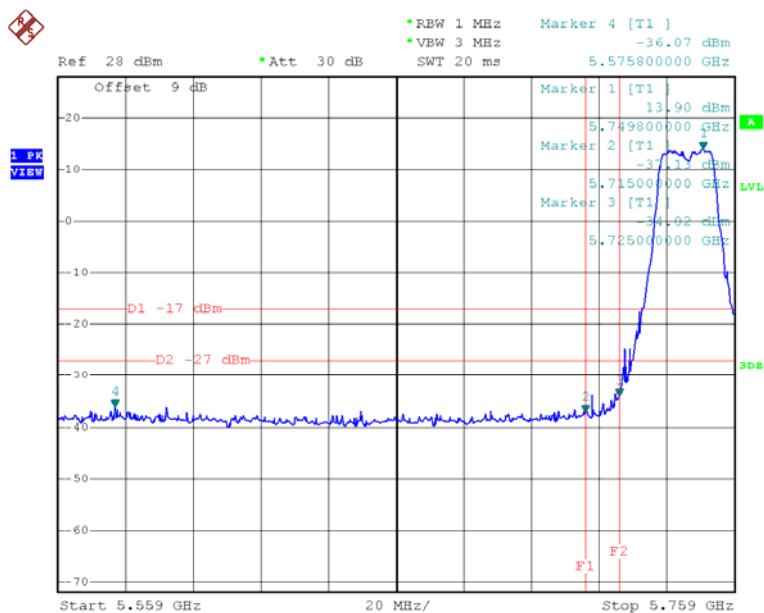
TX mode CH134



Date: 2.APR.2015 20:53:16

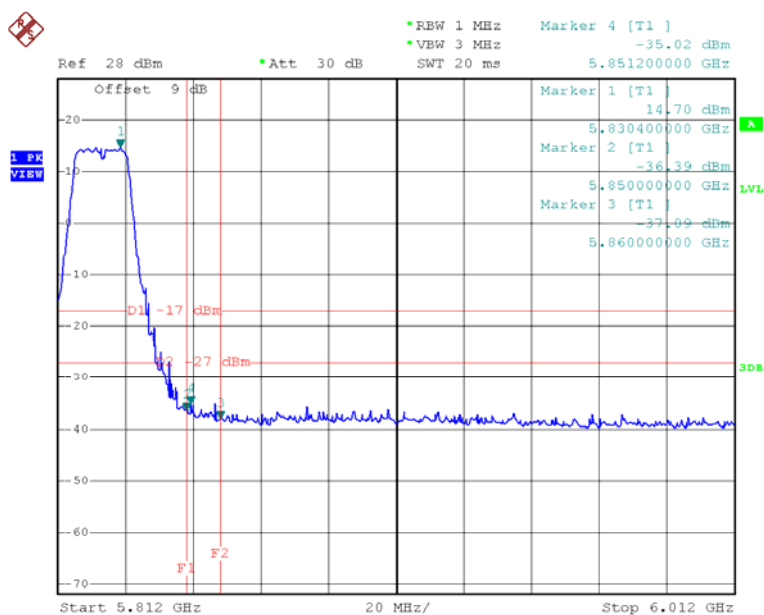
Test Mode: UNII-3/TX A Mode_ANT 1

TX A Mode CH149



Date: 2.APR.2015 16:45:43

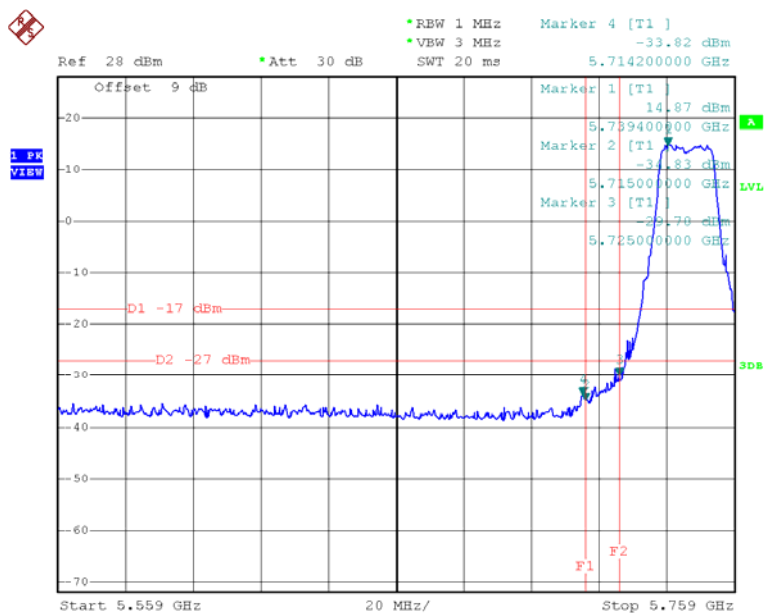
TX A Mode CH165



Date: 2.APR.2015 16:47:13

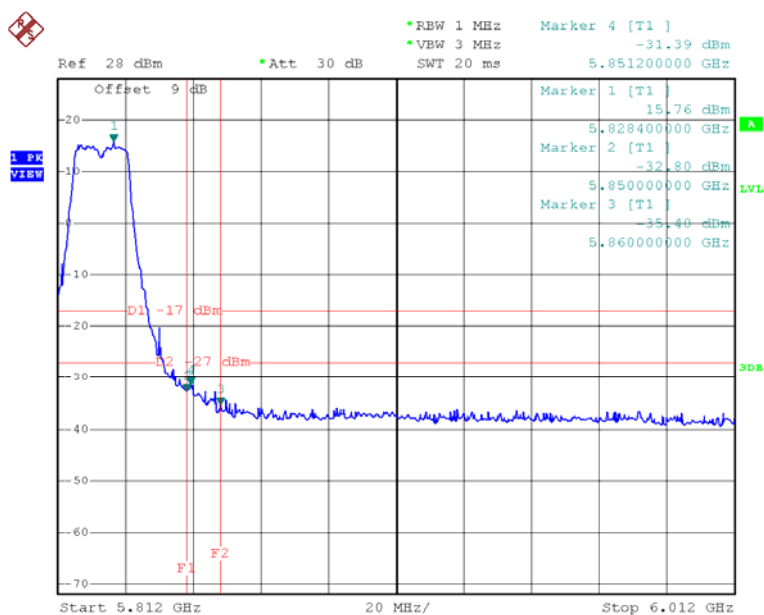
Test Mode: UNII-3/TX A Mode_ANT 2

TX A Mode CH149



Date: 2.APR.2015 17:43:49

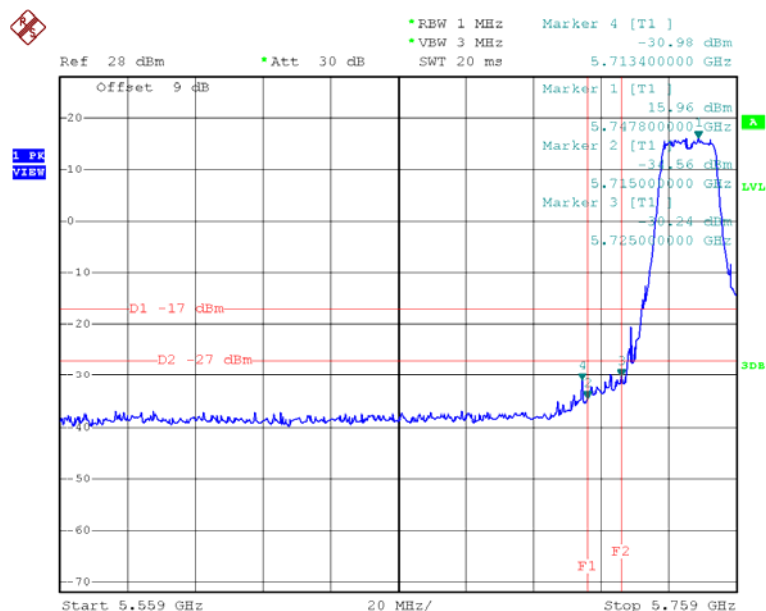
TX A Mode CH165



Date: 2.APR.2015 17:45:13

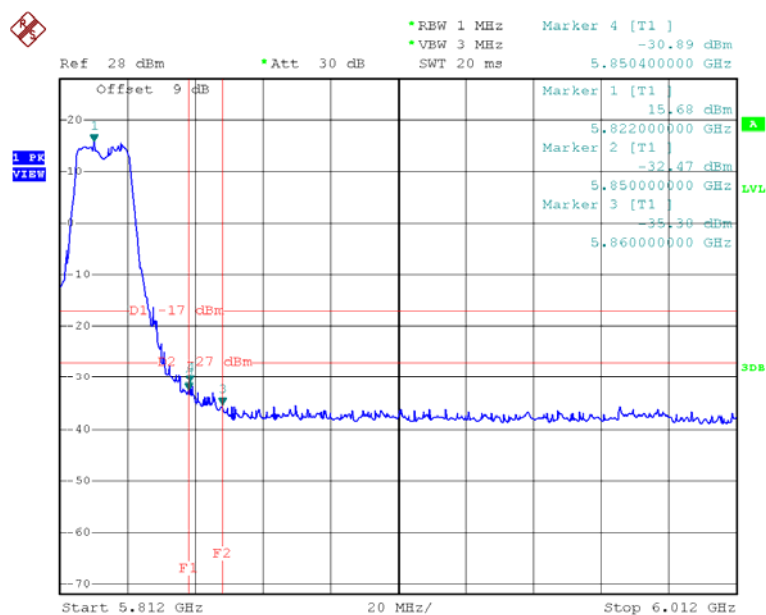
Test Mode: UNII-3/TX A Mode_ANT 3

TX A Mode CH149



Date: 2.APR.2015 20:36:30

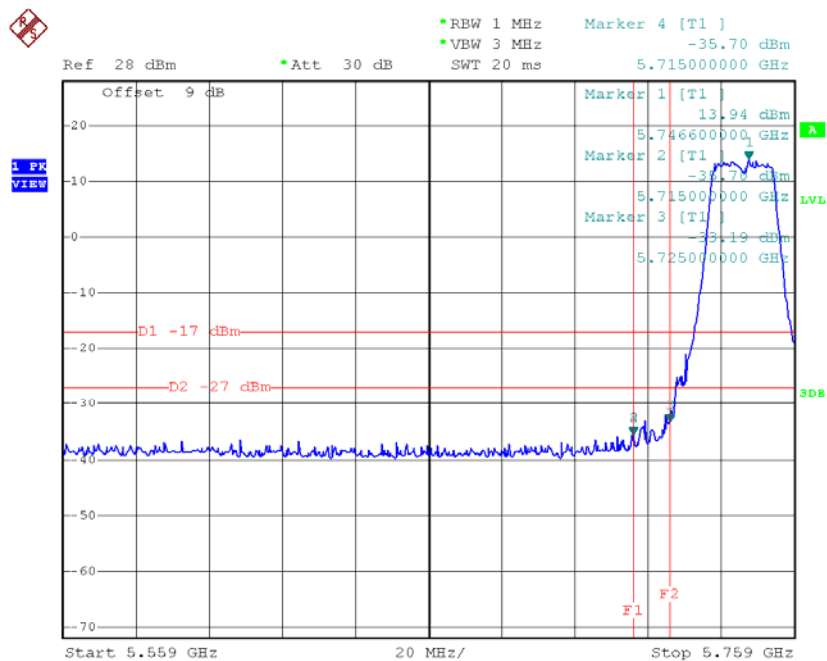
TX A Mode CH165



Date: 2.APR.2015 20:37:51

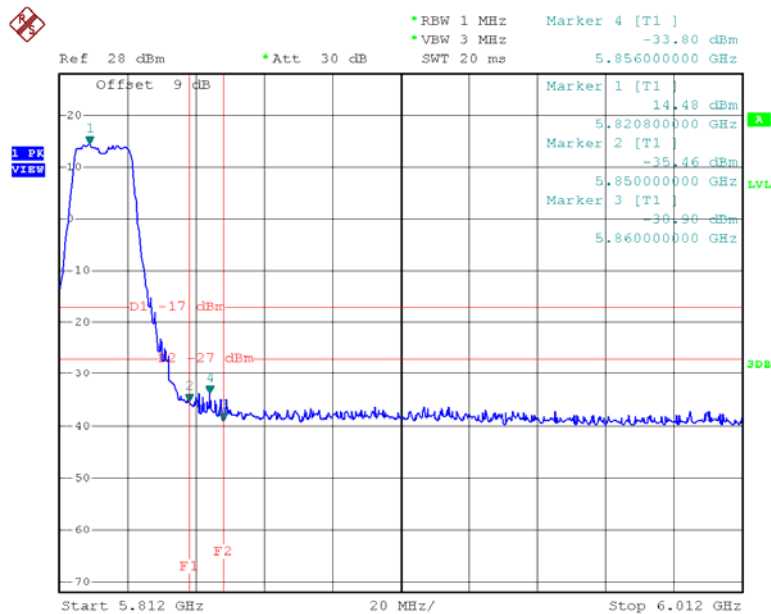
Test Mode: UNII-3/TX N20 Mode_ANT 1

TX HT20 mode CH149



Date: 2.APR.2015 17:03:12

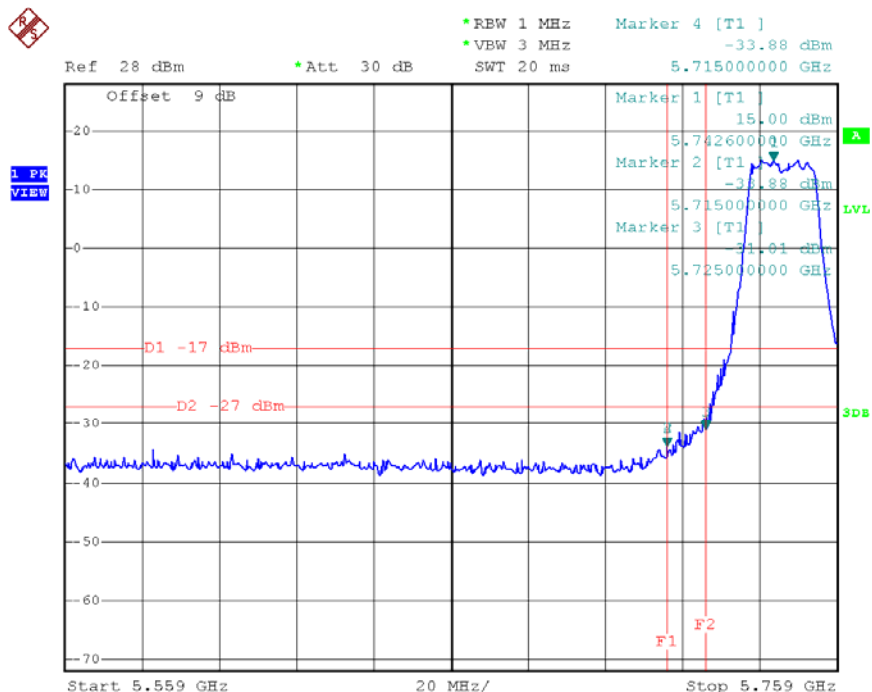
TX HT20 mode CH165



Date: 2.APR.2015 17:04:04

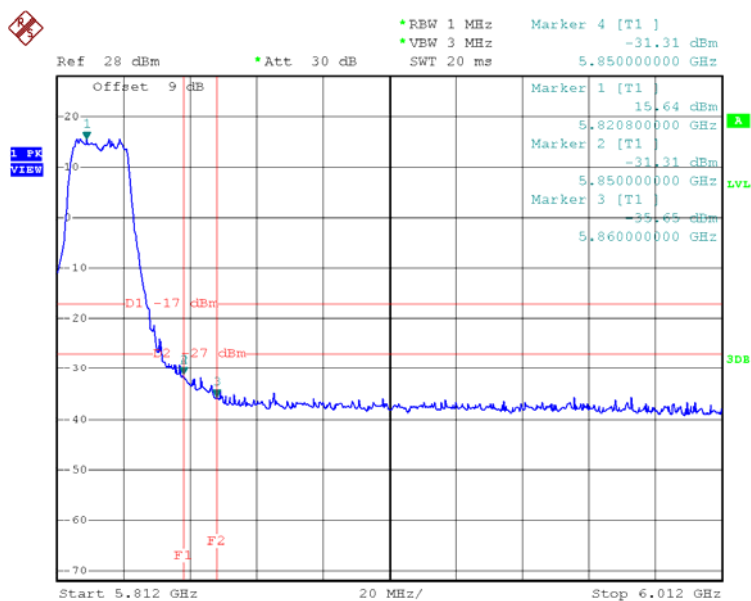
Test Mode: UNII-3/TX N20 Mode_ANT 2

TX HT20 mode CH149



Date: 2.APR.2015 17:49:59

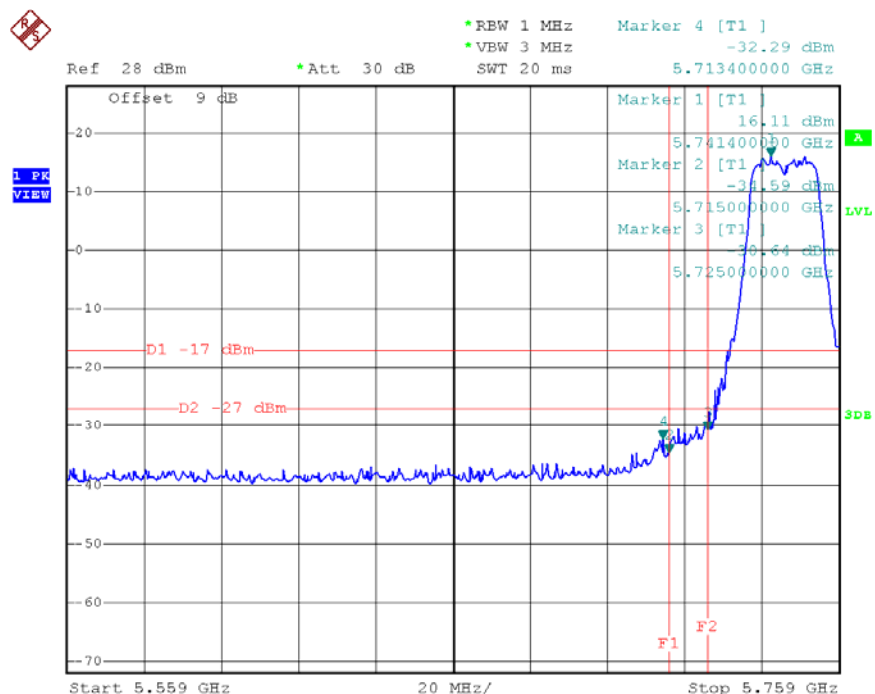
X HT20 mode CH165



Date: 2.APR.2015 17:50:44

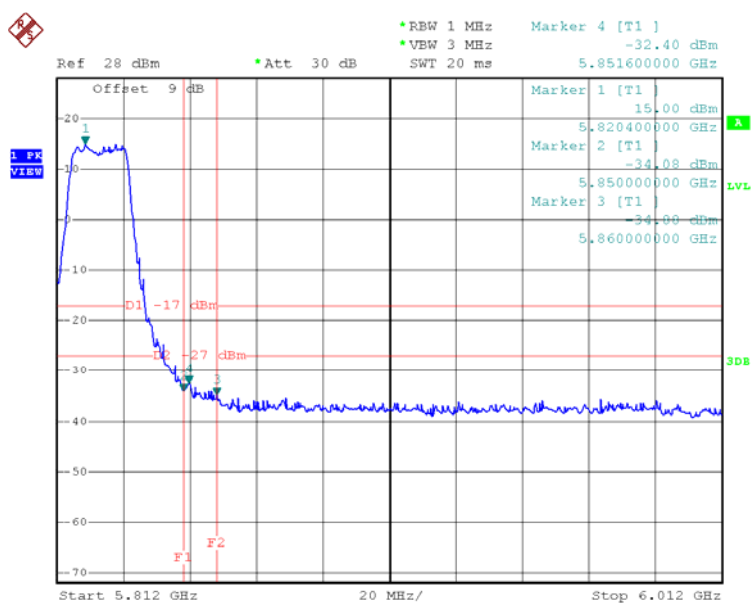
Test Mode: UNII-3/TX N20 Mode_ANT 3

TX HT20 mode CH149



Date: 2.APR.2015 20:42:30

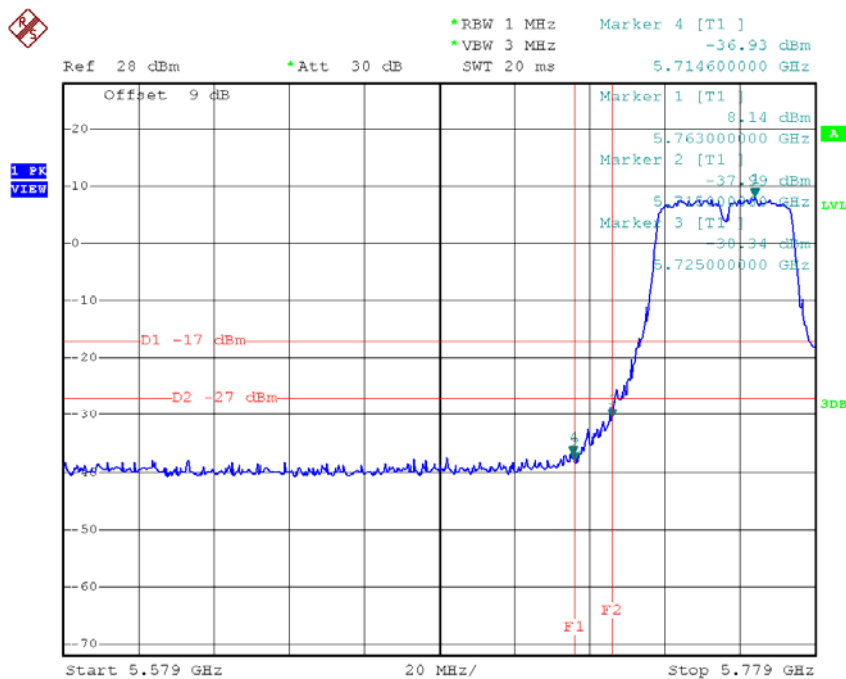
X HT20 mode CH165



Date: 2.APR.2015 20:43:13

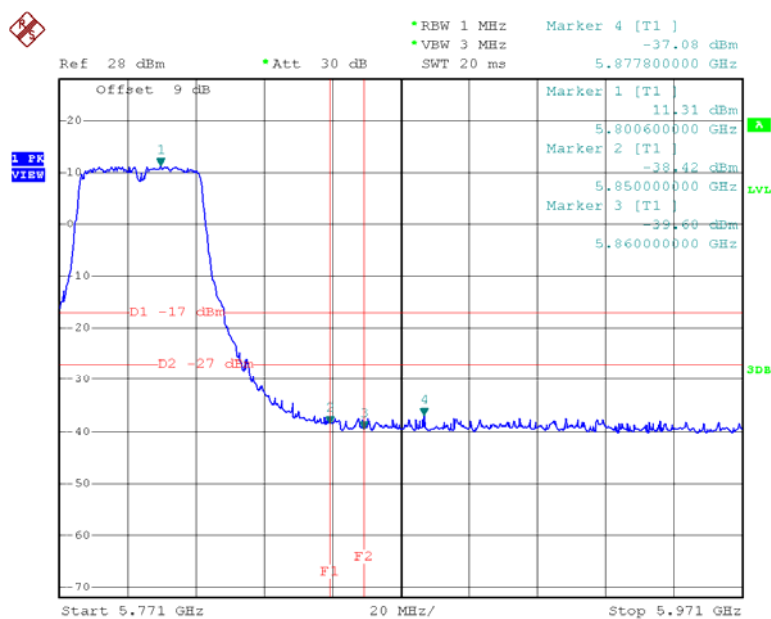
Test Mode: UNII-3/TX N40 Mode_ANT 1

UNII-3/TX HT40 mode CH151



Date: 2.APR.2015 17:13:54

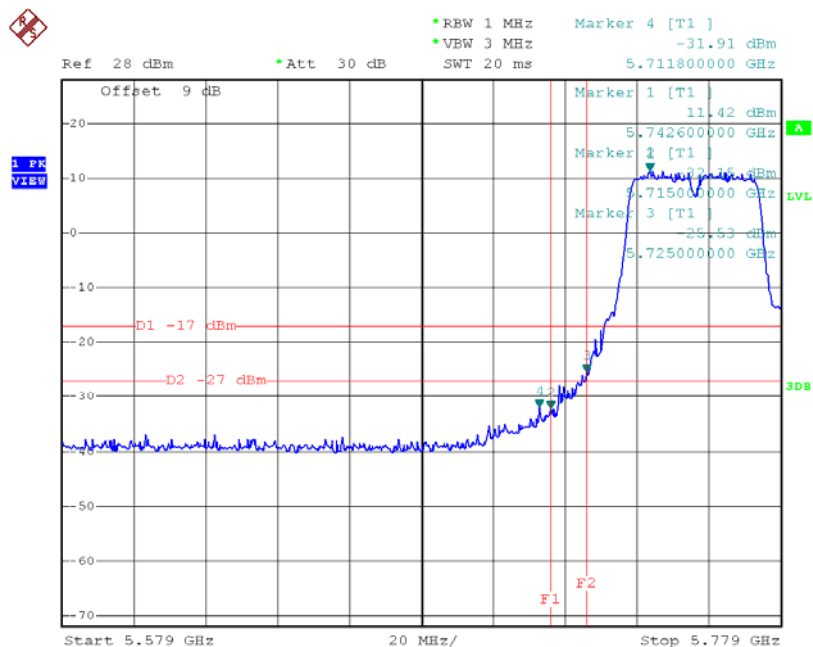
UNII-3/TX HT40 mode CH159



Date: 2.APR.2015 17:14:26

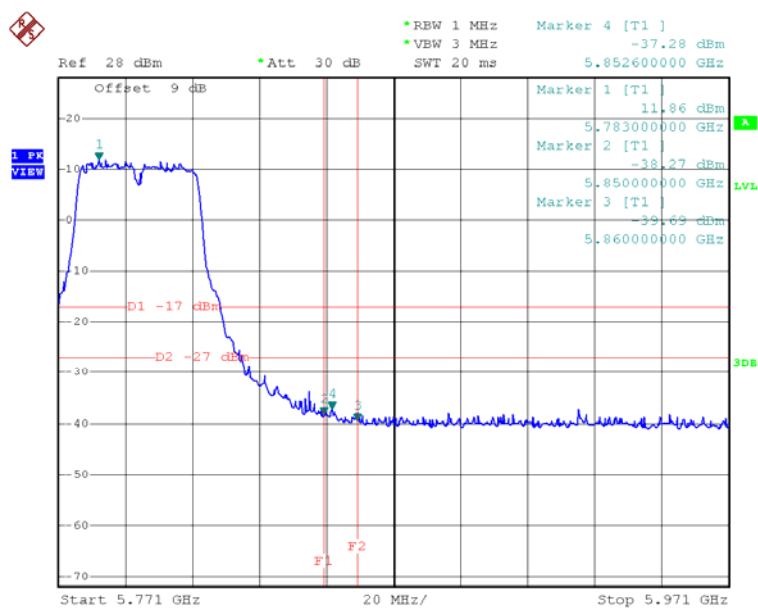
Test Mode: UNII-3/TX N40 Mode_ANT 2

TX HT40 mode CH151



Date: 2.APR.2015 17:59:27

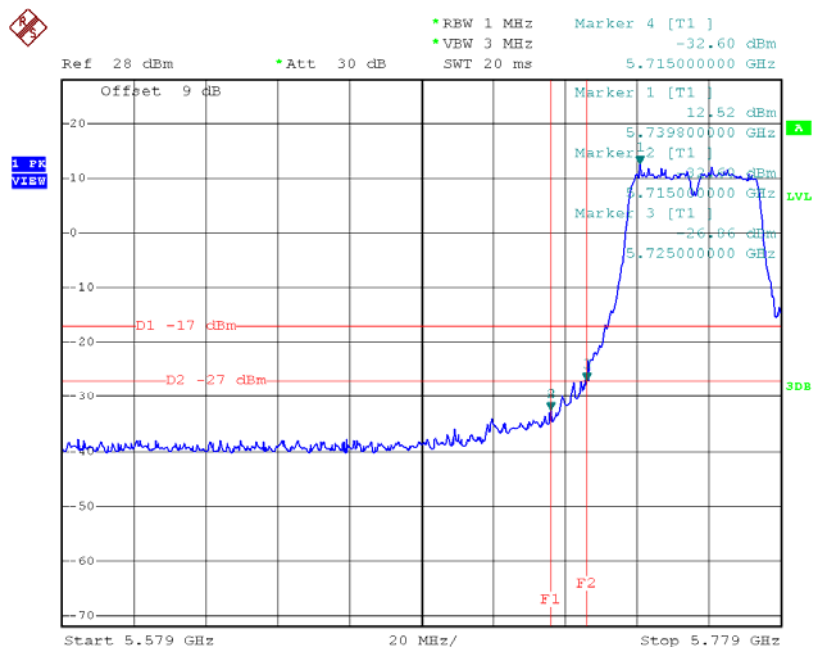
HT40 mode CH159



Date: 2.APR.2015 17:59:53

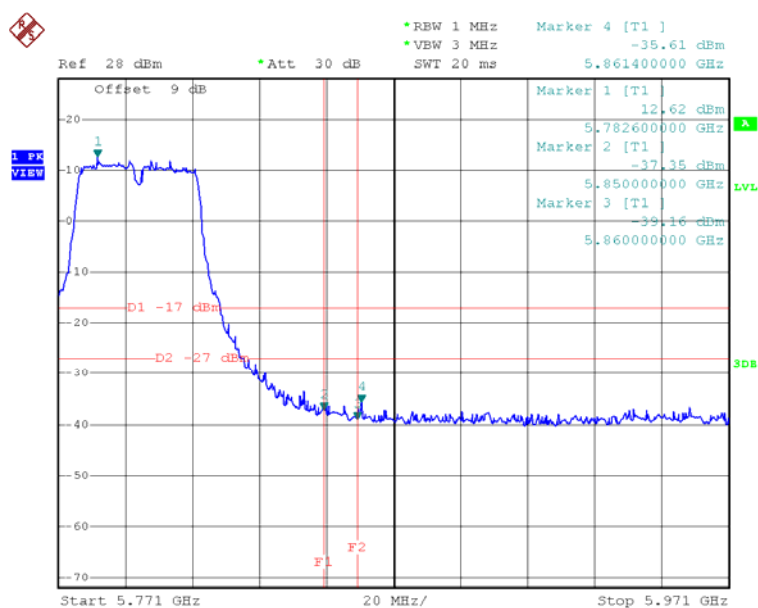
Test Mode: UNII-3/TX N40 Mode_ANT 3

TX HT40 mode CH151



Date: 2.APR.2015 20:53:45

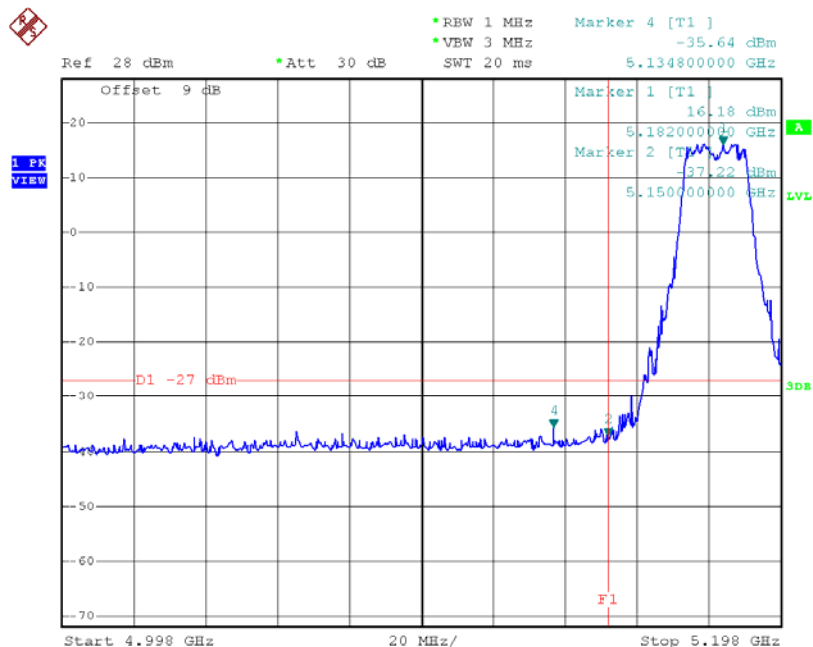
HT40 mode CH159



Date: 2.APR.2015 20:54:15

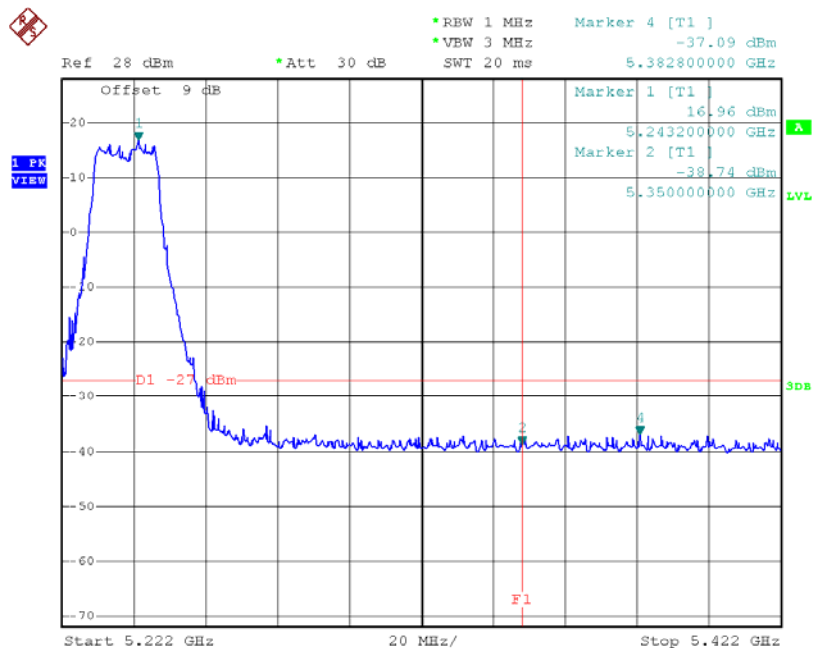
Test Mode: UNII-1/TX AC20 Mode_ANT 1

TX mode CH36



Date: 2.APR.2015 17:04:47

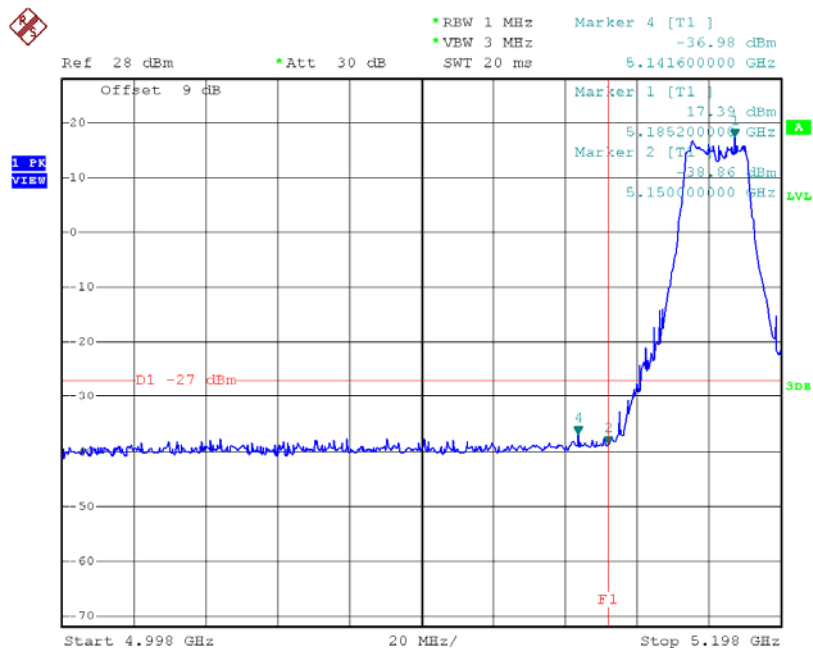
TX mode CH48



Date: 2.APR.2015 17:05:42

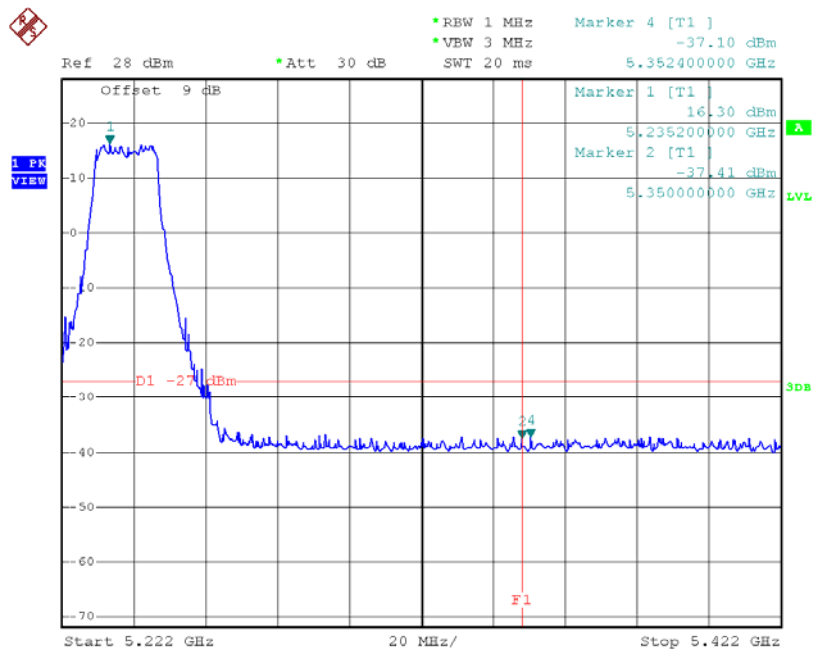
Test Mode: UNII-1/TX AC20 Mode_ANT 2

TX mode CH36



Date: 2.APR.2015 17:51:30

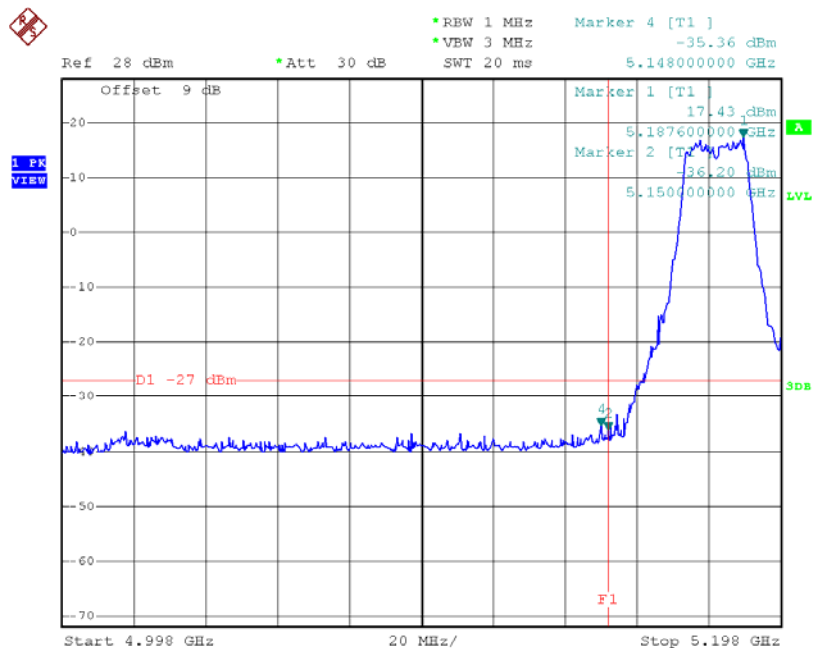
TX mode CH48



Date: 2.APR.2015 17:52:18

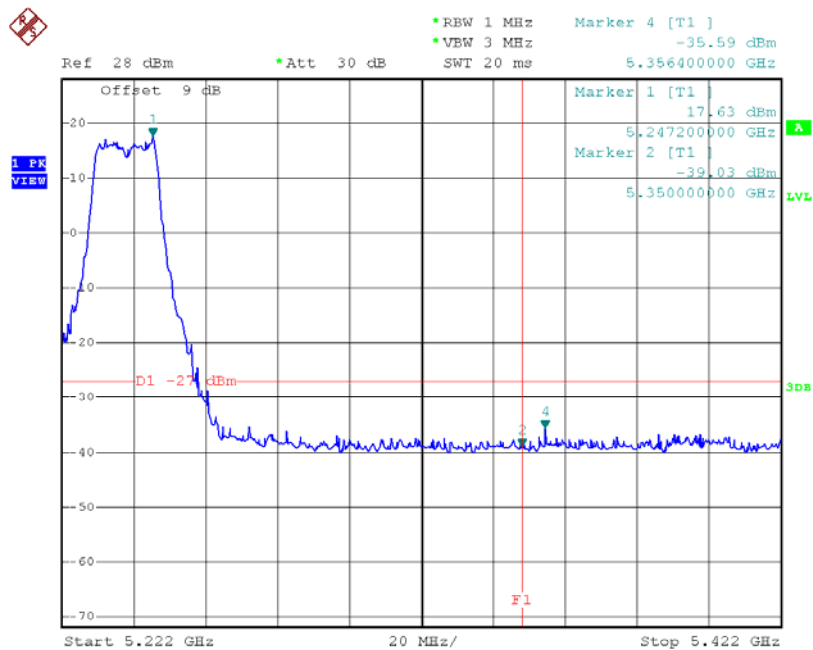
Test Mode: UNII-1/TX AC20 Mode_ANT 3

TX mode CH36



Date: 2.APR.2015 20:43:44

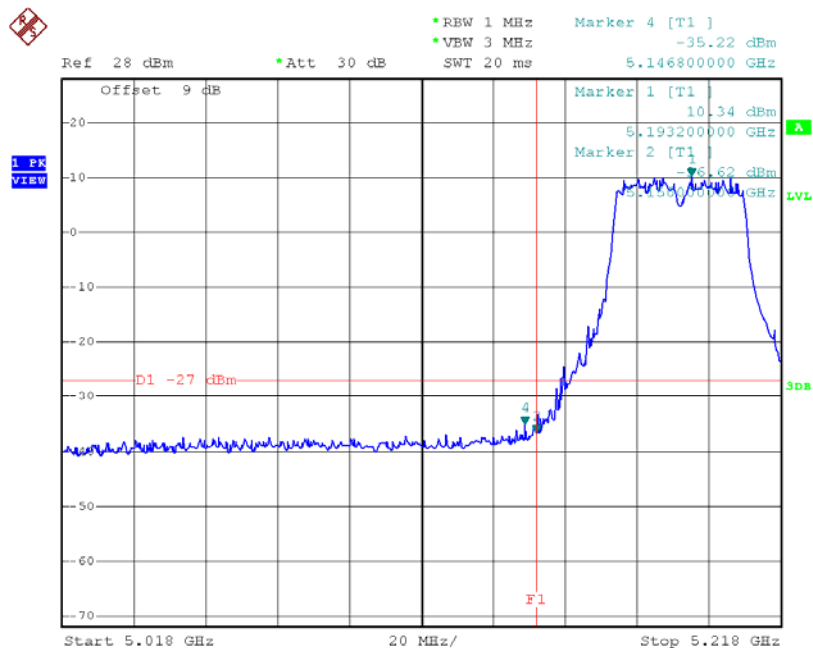
TX mode CH48



Date: 2.APR.2015 20:44:31

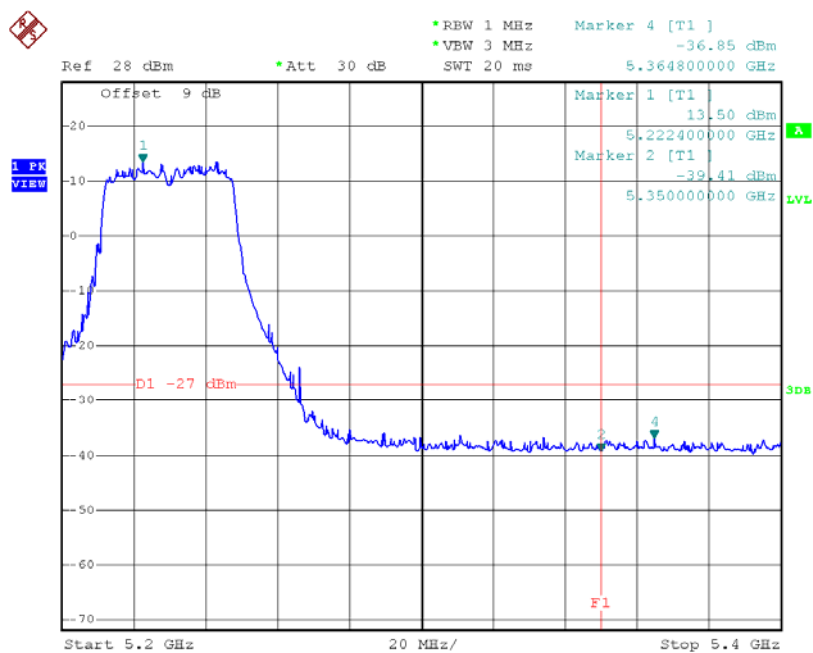
Test Mode: UNII-1/TX AC40 Mode_ANT 1

TX mode CH38



Date: 2.APR.2015 17:15:06

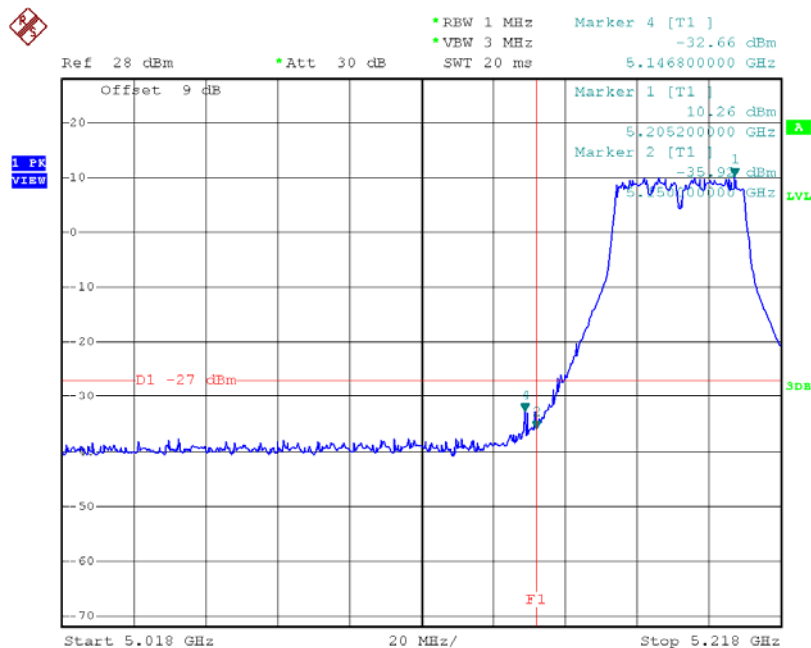
TX mode CH46



Date: 2.APR.2015 17:15:41

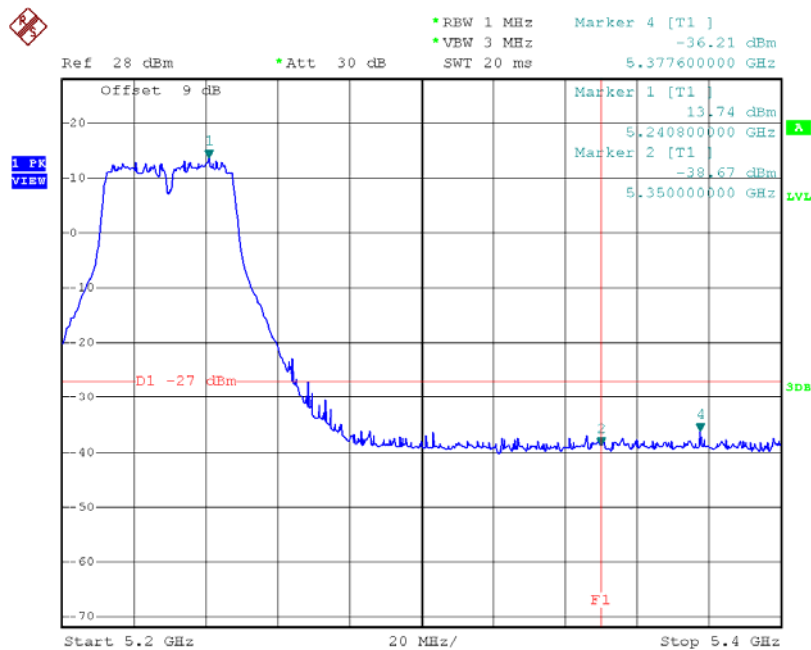
Test Mode: UNII-1/TX AC40 Mode_ANT 2

TX mode CH38



Date: 2.APR.2015 18:00:26

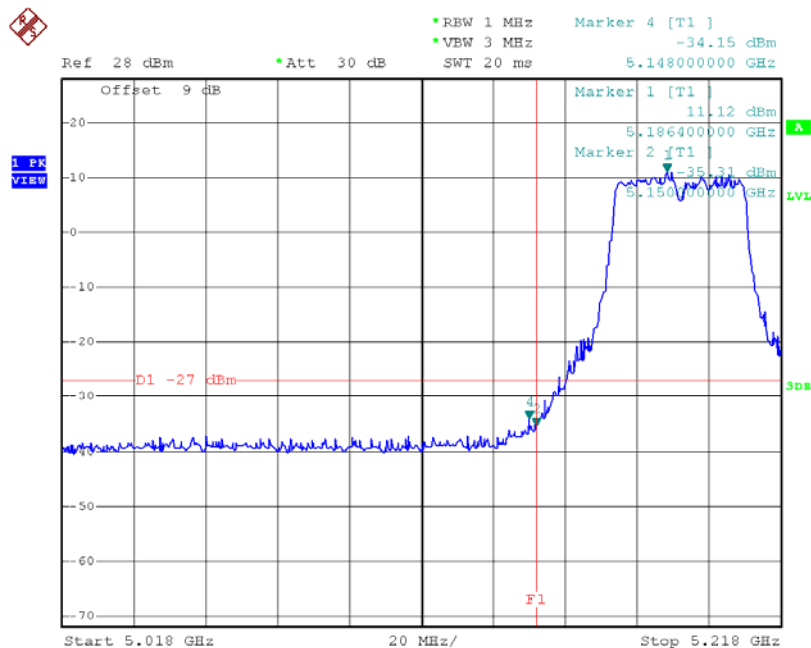
TX mode CH46



Date: 2.APR.2015 18:01:00

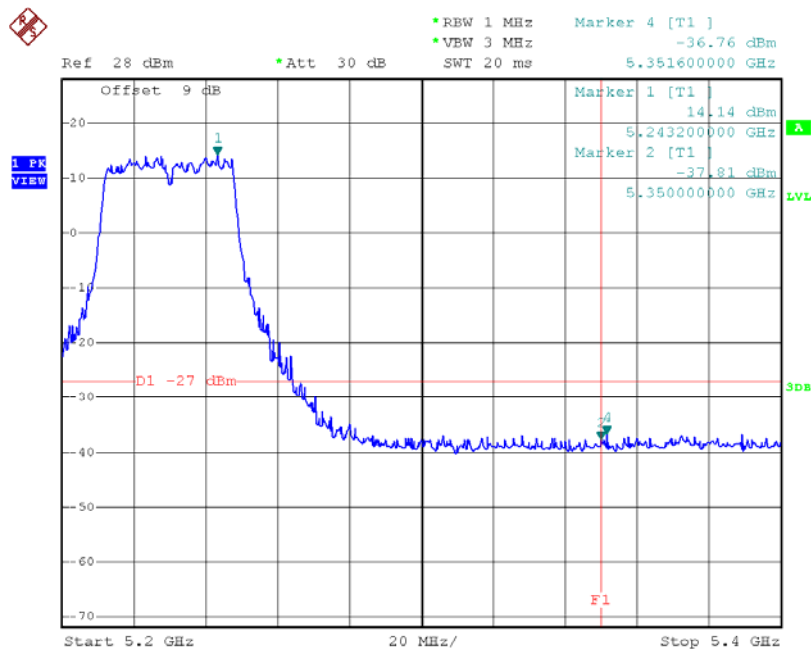
Test Mode:	UNII-1/TX AC40 Mode_ANT 3
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TX mode CH38



Date: 2.APR.2015 20:54:50

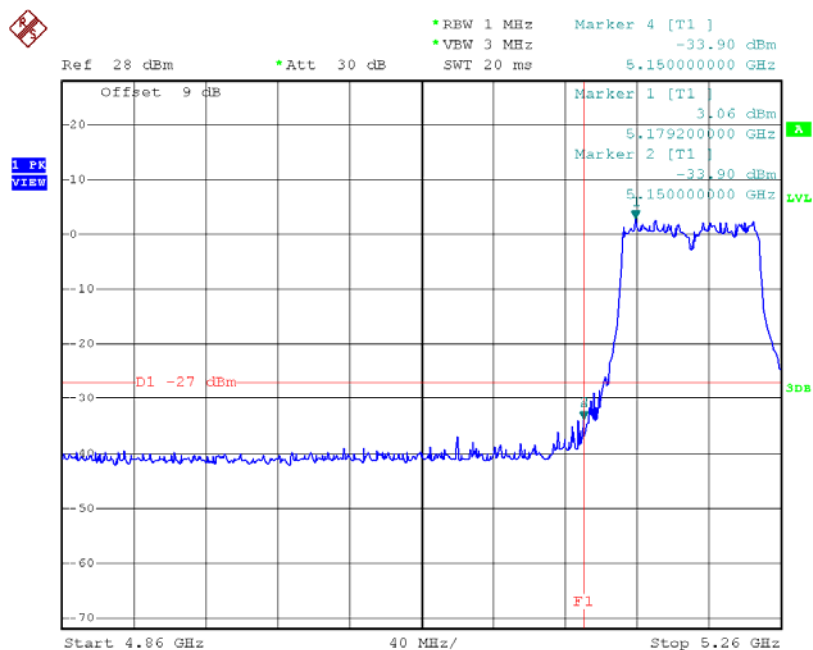
TX mode CH46



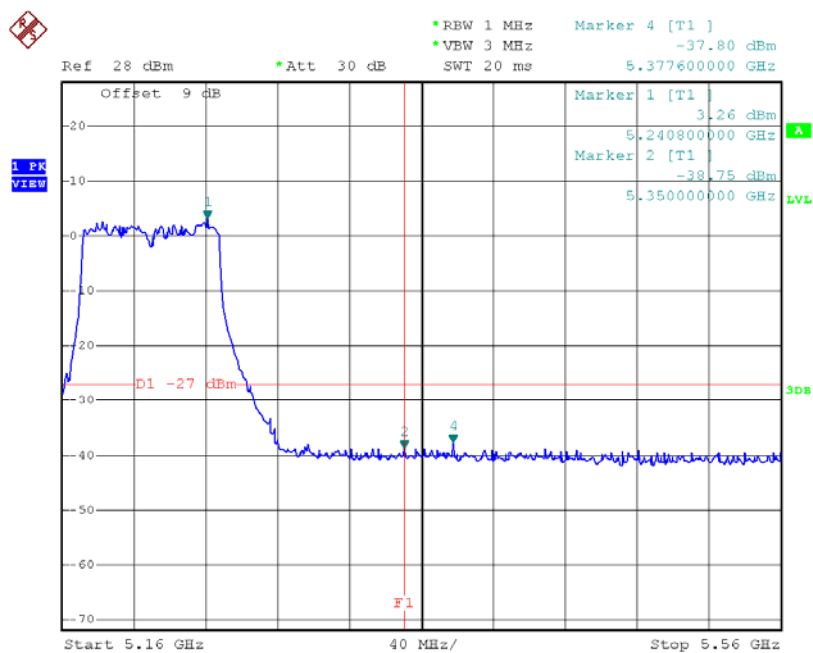
Date: 2.APR.2015 20:55:24

Test Mode: UNII-1/TX AC80 Mode_ANT 1

TX mode CH42



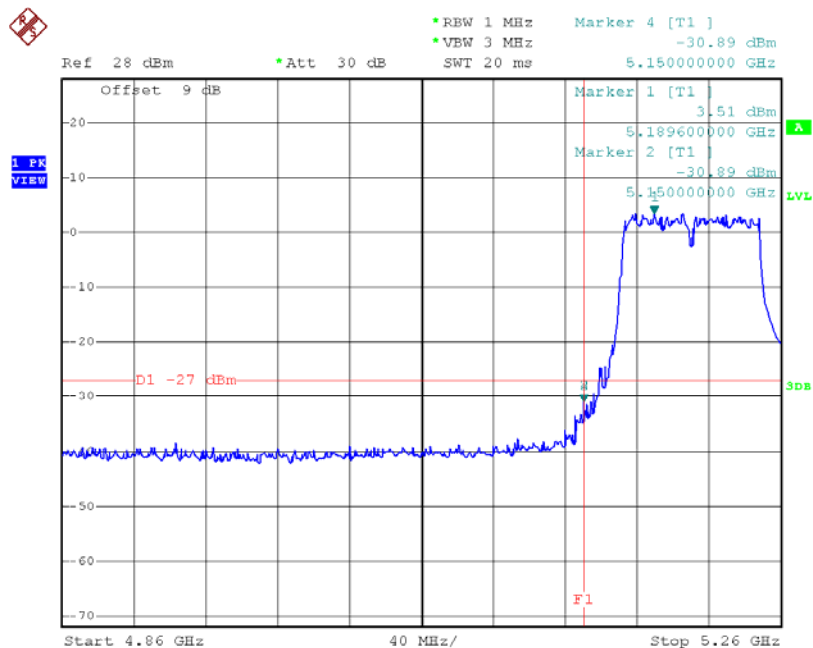
Date: 2.APR.2015 17:20:11



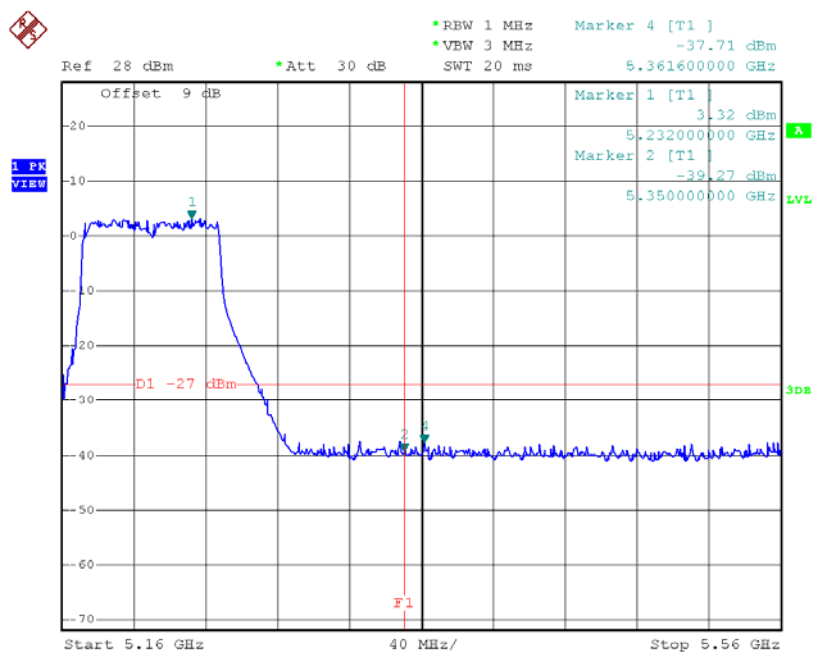
Date: 2.APR.2015 17:20:18

Test Mode:	UNII-1/TX AC80 Mode_ANT 2
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TX mode CH42



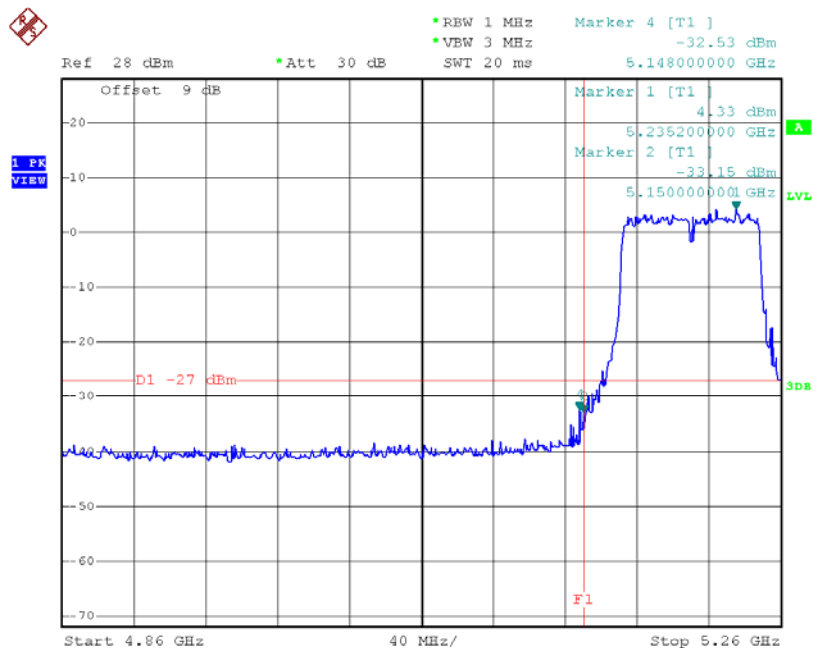
Date: 2.APR.2015 18:07:31



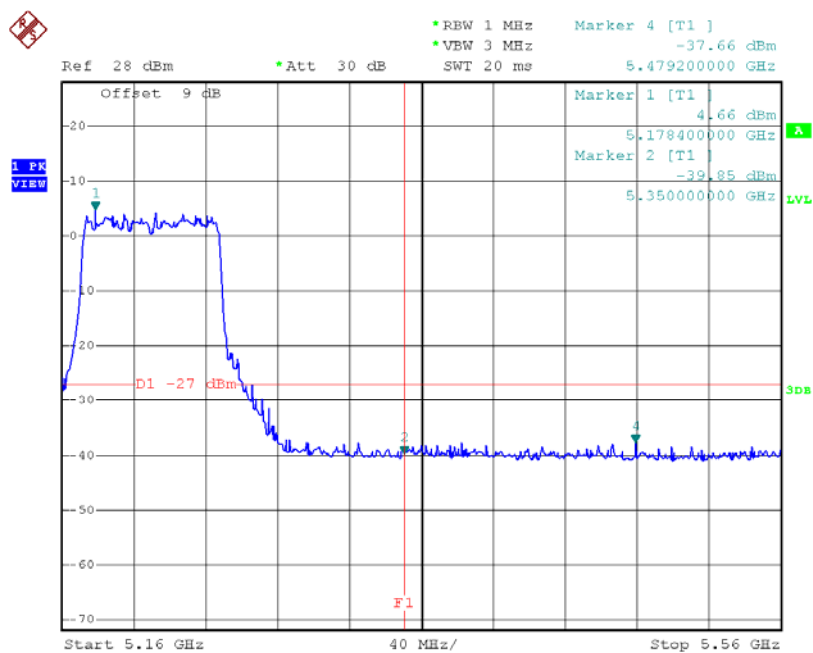
Date: 2.APR.2015 18:07:39

Test Mode: UNII-1/TX AC80 Mode_ANT 3

TX mode CH42



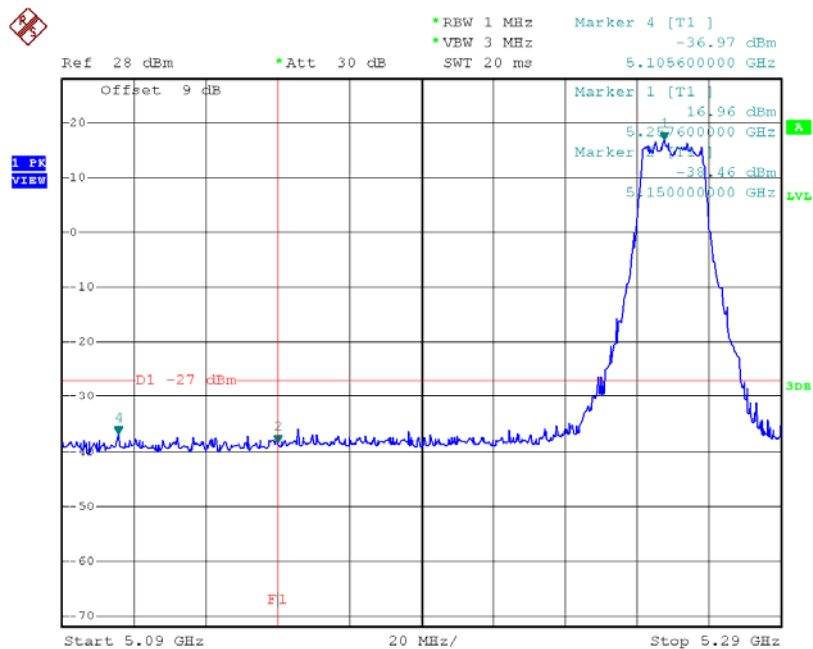
Date: 2.APR.2015 20:59:55



Date: 2.APR.2015 21:00:03

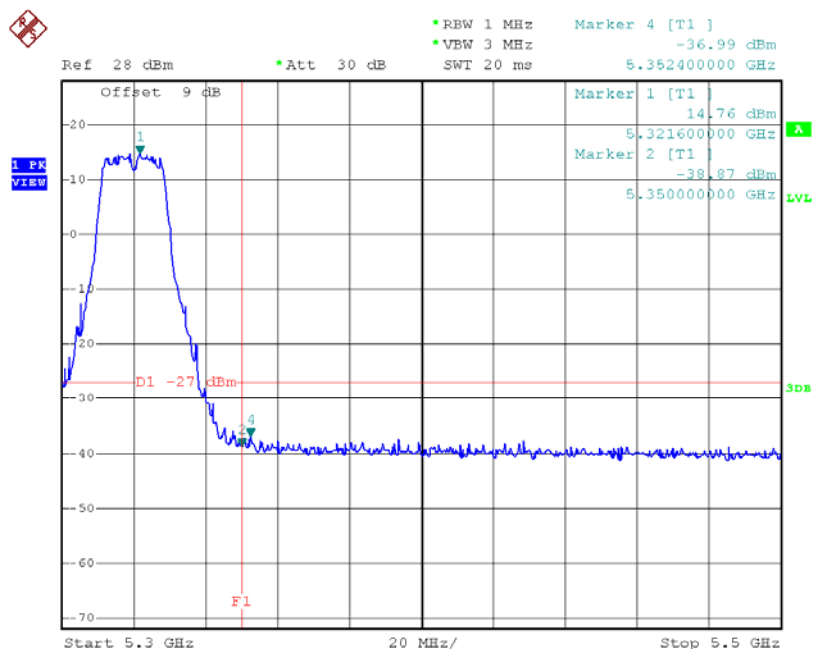
Test Mode: UNII-2A/TX AC20 Mode_ANT 1

TX mode CH52



Date: 2.APR.2015 17:06:08

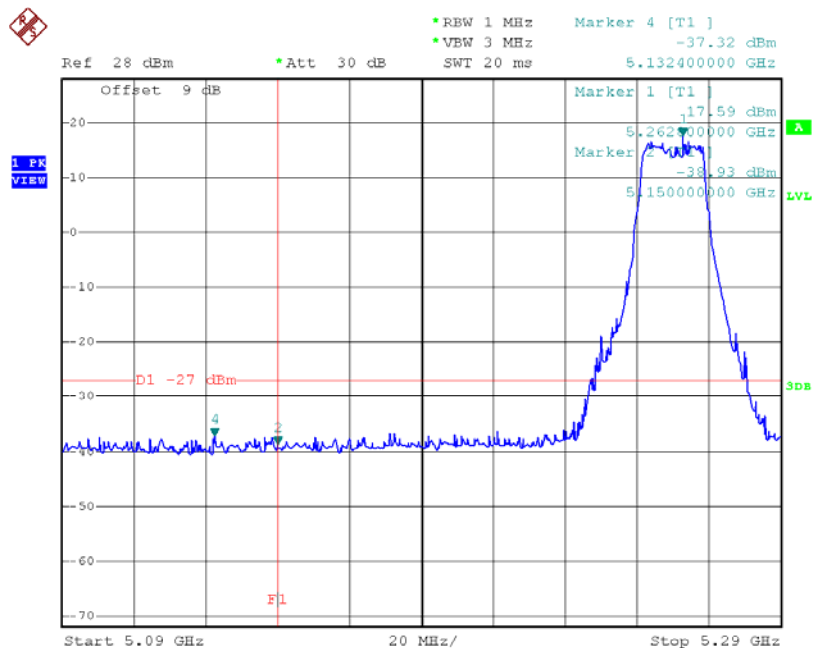
TX mode CH64



Date: 2.APR.2015 17:07:03

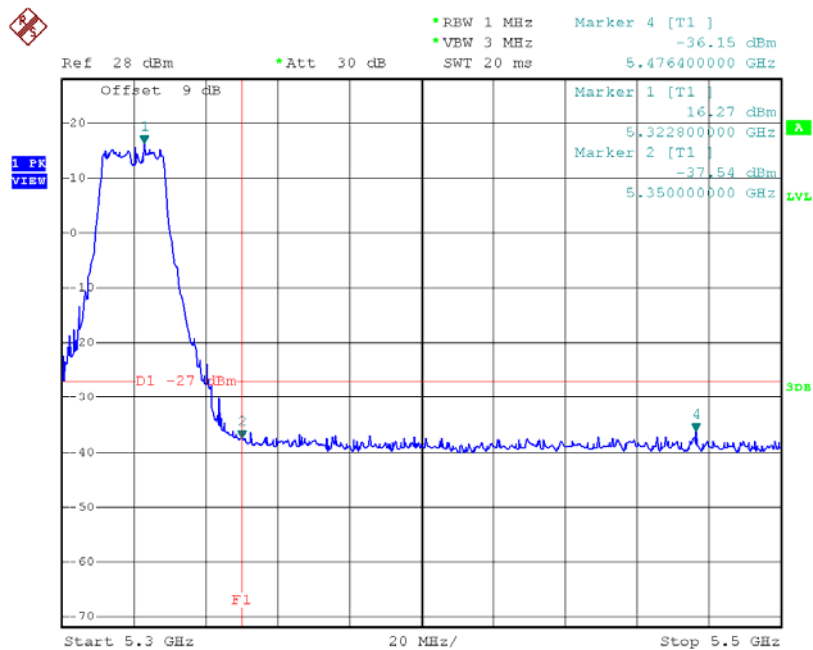
Test Mode:	UNII-2A/TX AC20 Mode_ANT 2
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TX mode CH52



Date: 2.APR.2015 17:52:43

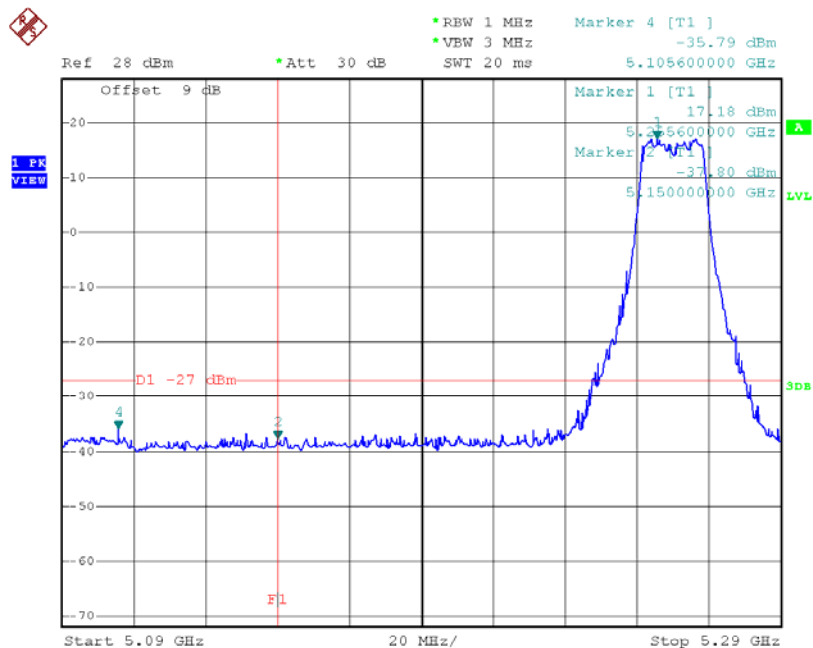
TX mode CH64



Date: 2.APR.2015 17:53:25

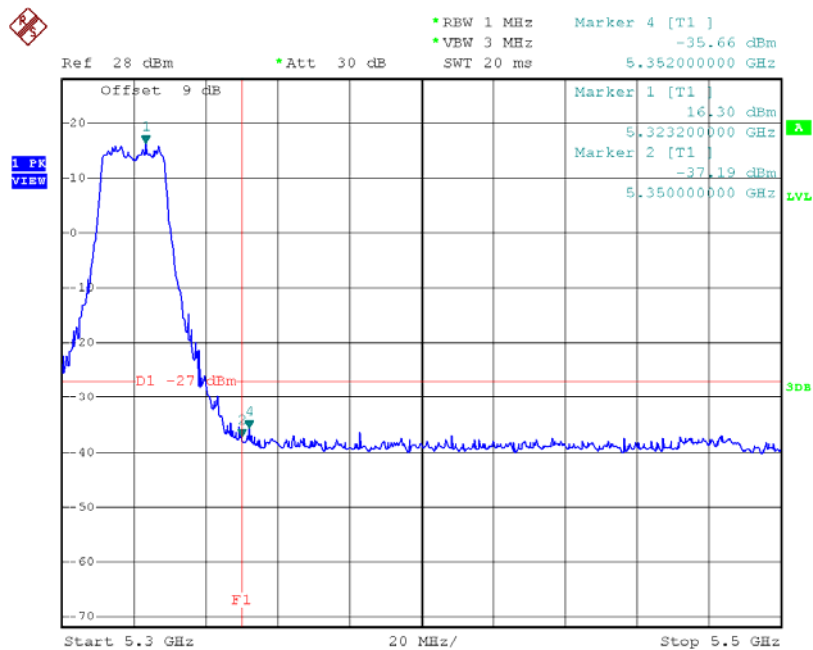
Test Mode: UNII-2A/TX AC20 Mode_ANT 3

TX mode CH52



Date: 2.APR.2015 20:44:59

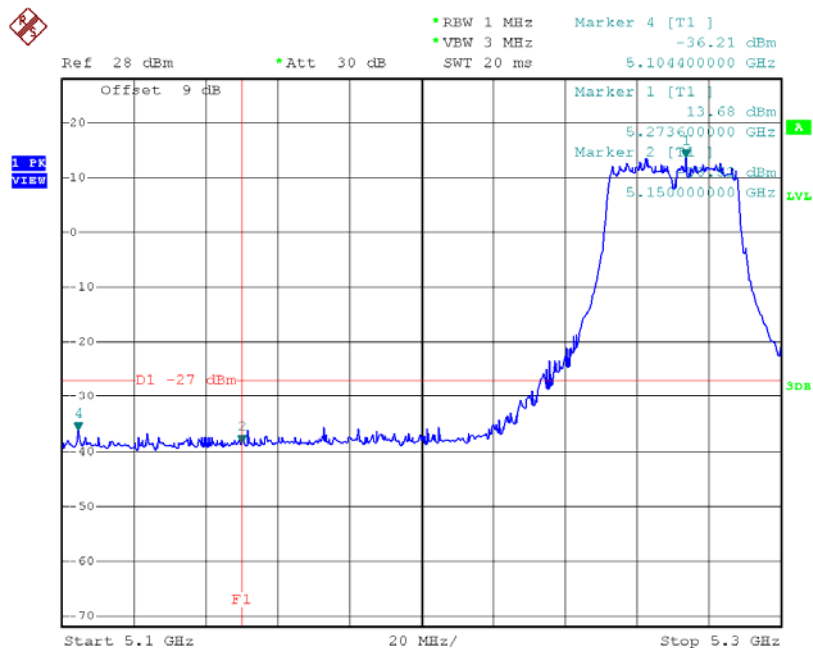
TX mode CH64



Date: 2.APR.2015 20:45:48

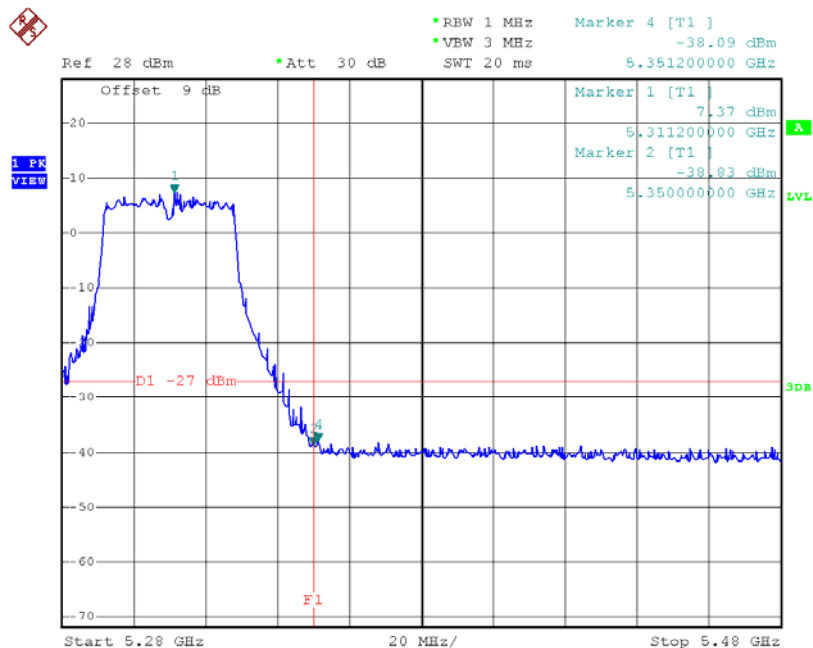
Test Mode:	UNII-2A/TX AC40 Mode_ANT 1
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TX mode CH54



Date: 2.APR.2015 17:16:11

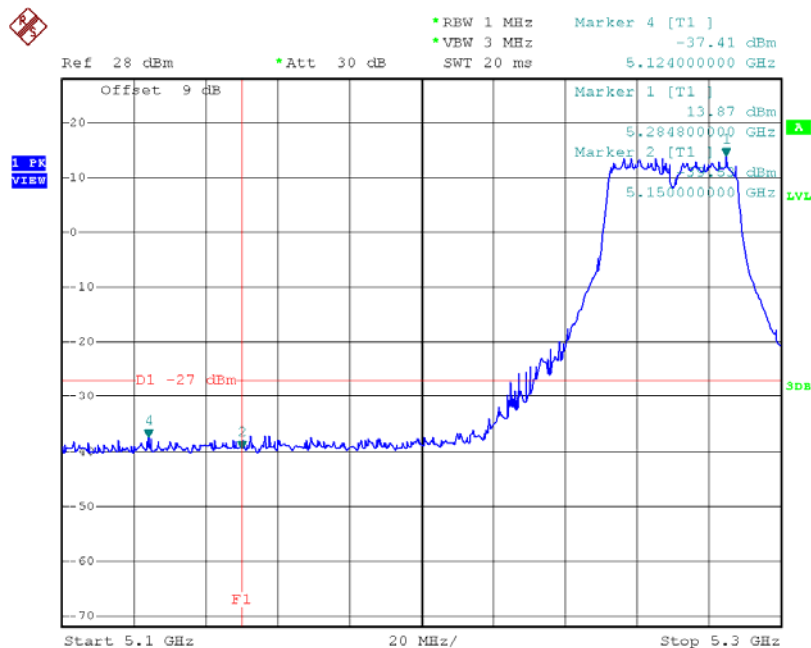
TX mode CH62



Date: 2.APR.2015 17:16:40

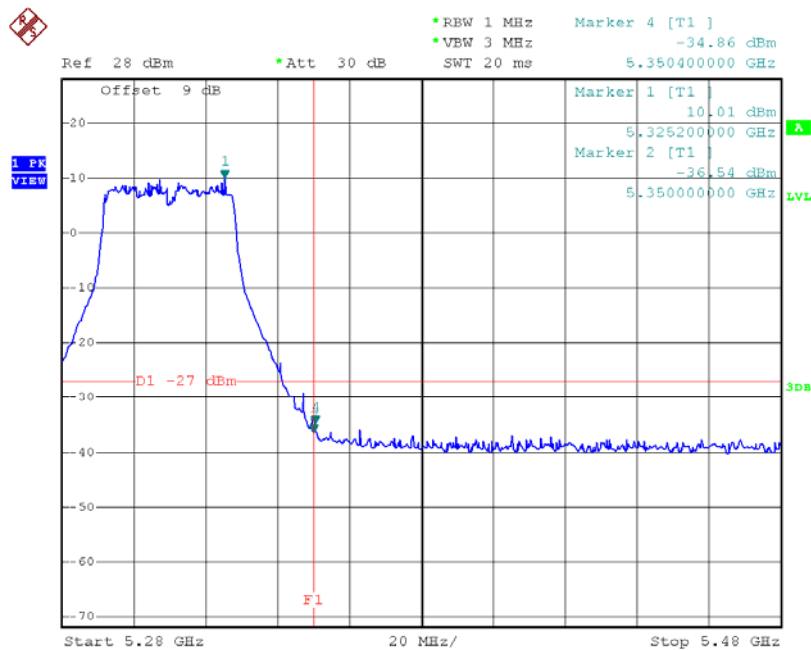
Test Mode:	UNII-2A/TX AC40 Mode_ANT 2
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TX mode CH54



Date: 2.APR.2015 18:01:29

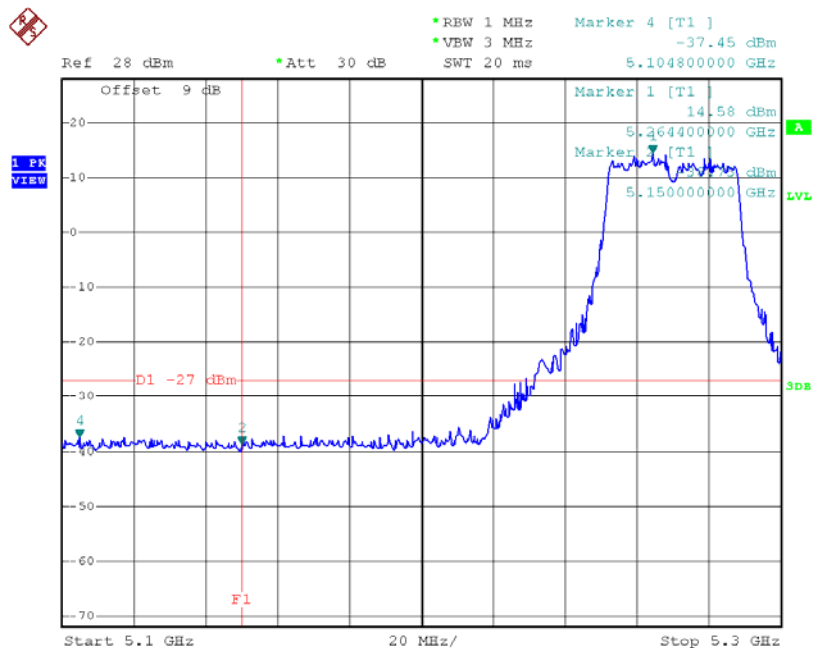
TX mode CH62



Date: 2.APR.2015 18:01:57

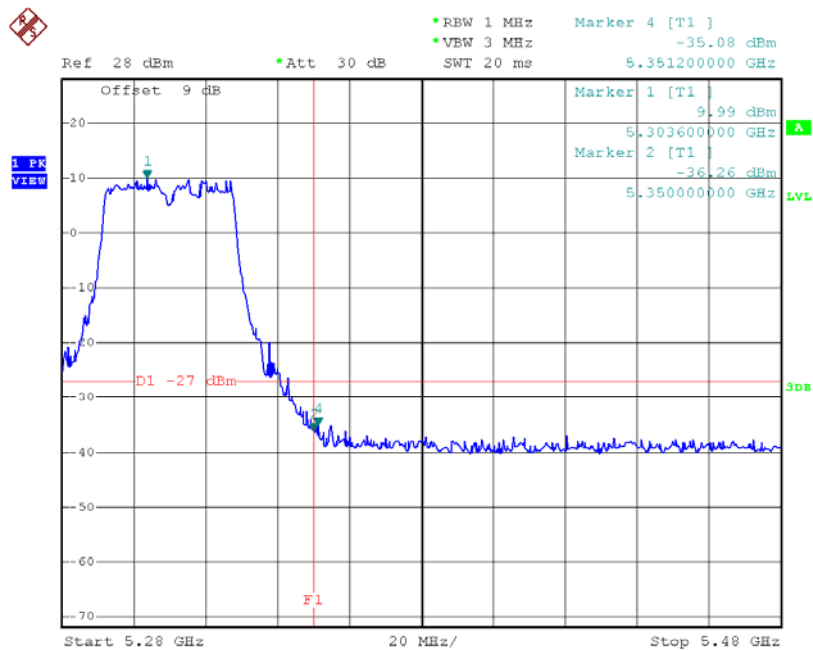
Test Mode:	UNII-2A/TX AC40 Mode_ANT 3
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TX mode CH54



Date: 2.APR.2015 20:56:16

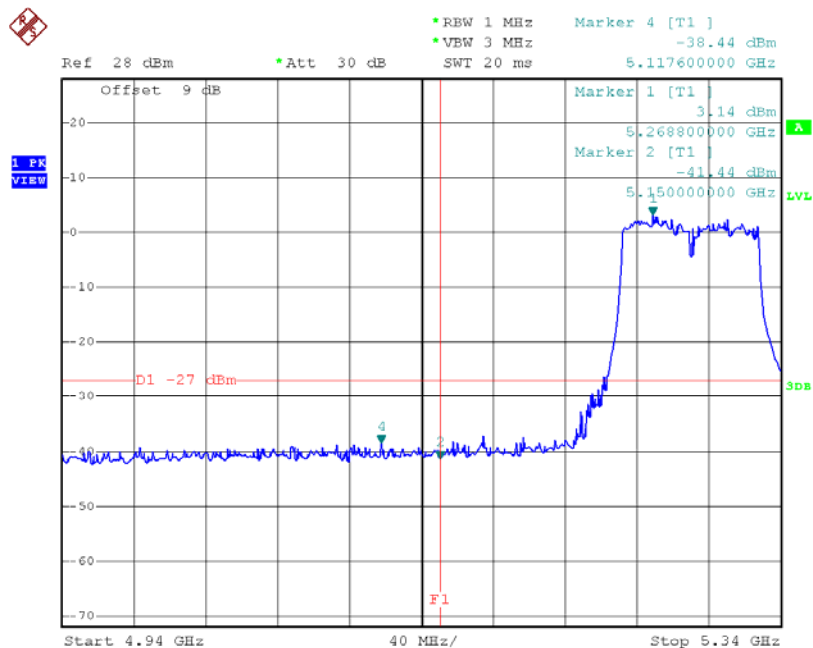
TX mode CH62



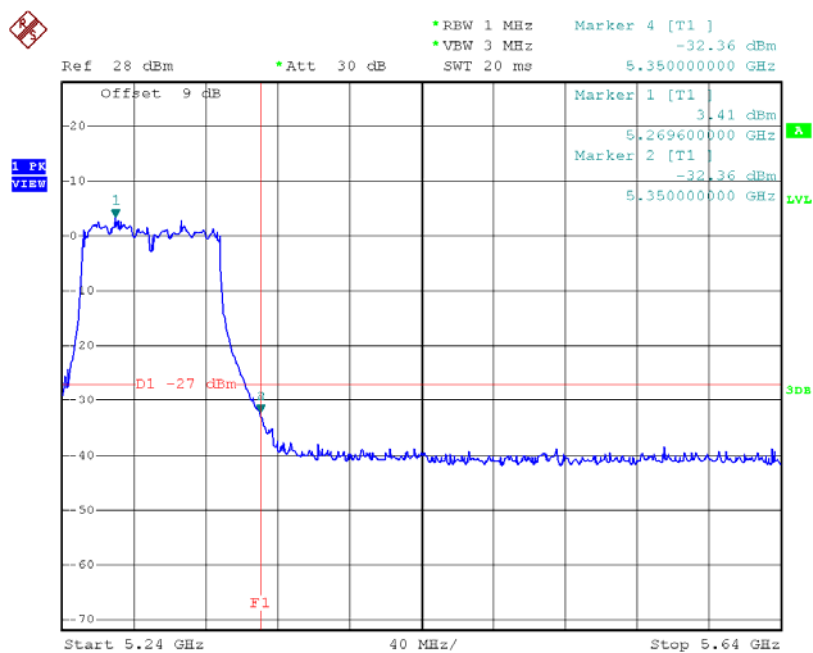
Date: 2.APR.2015 20:56:45

Test Mode: UNII-2A/TX AC80 Mode_ANT 1

TX mode CH58



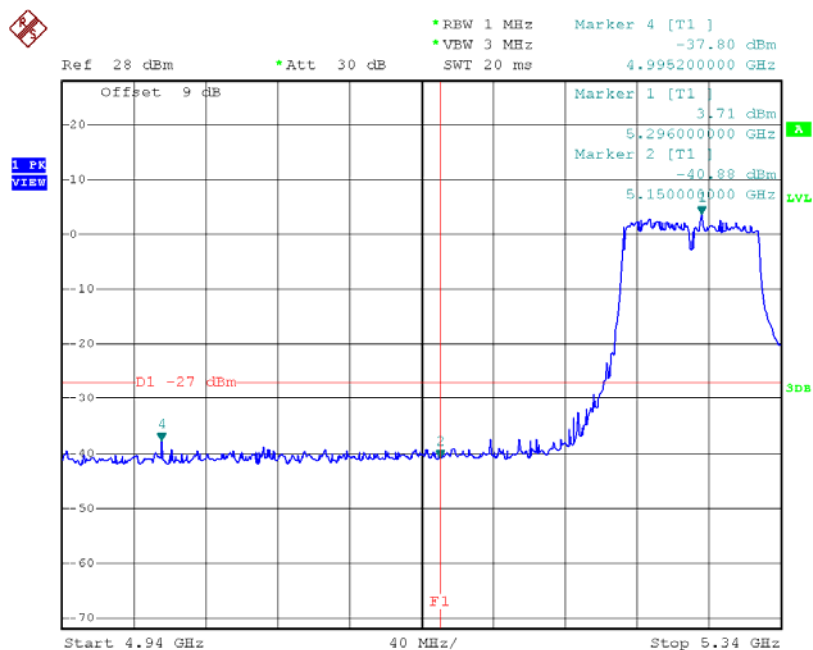
Date: 2.APR.2015 17:21:14



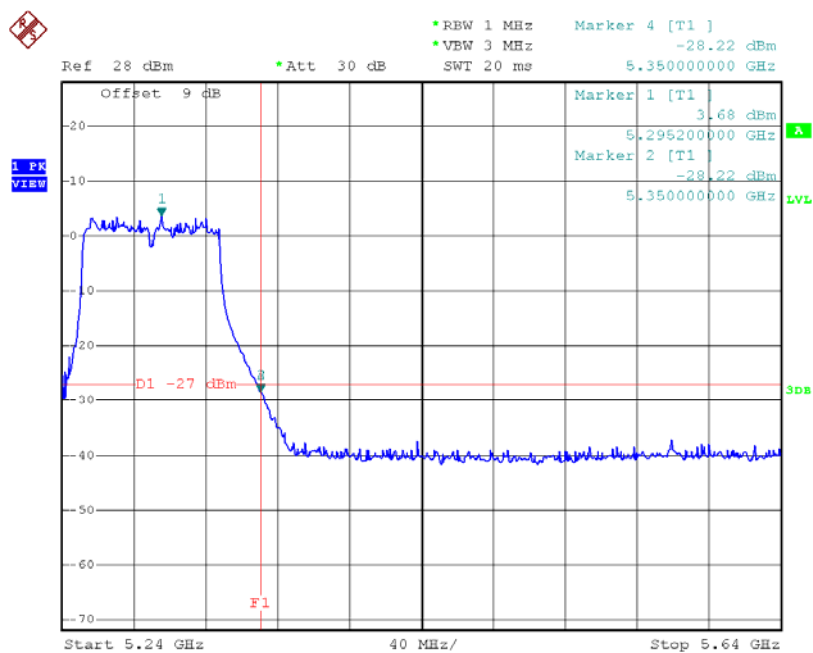
Date: 2.APR.2015 17:21:21

Test Mode: UNII-2A/TX AC80 Mode_ANT 2

TX mode CH58



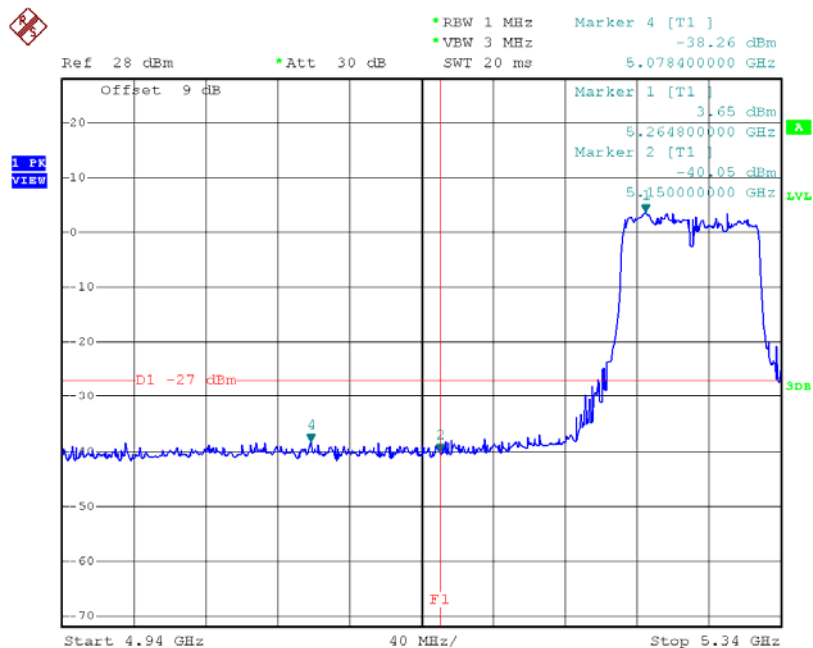
Date: 2.APR.2015 18:08:12



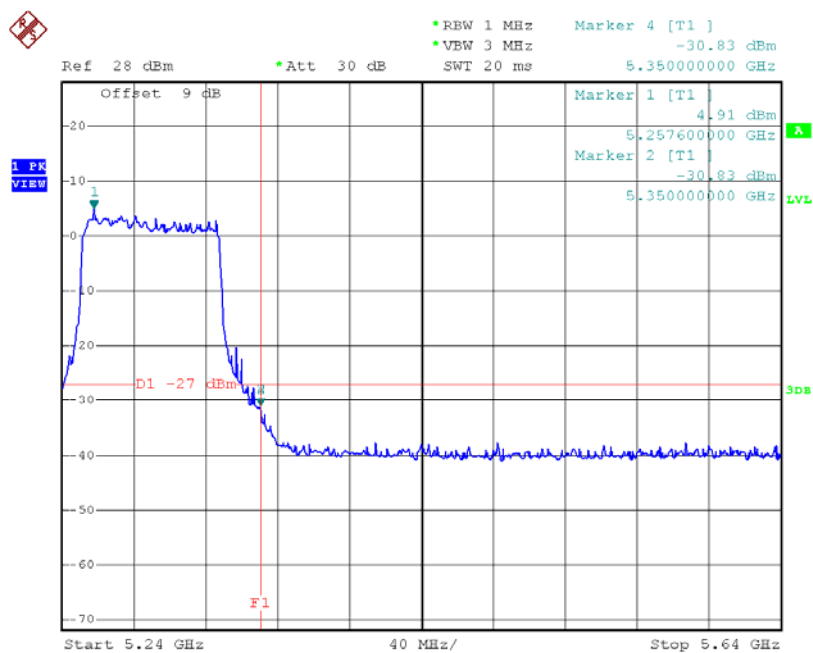
Date: 2.APR.2015 18:08:20

Test Mode: UNII-2A/TX AC80 Mode_ANT 3

TX mode CH58



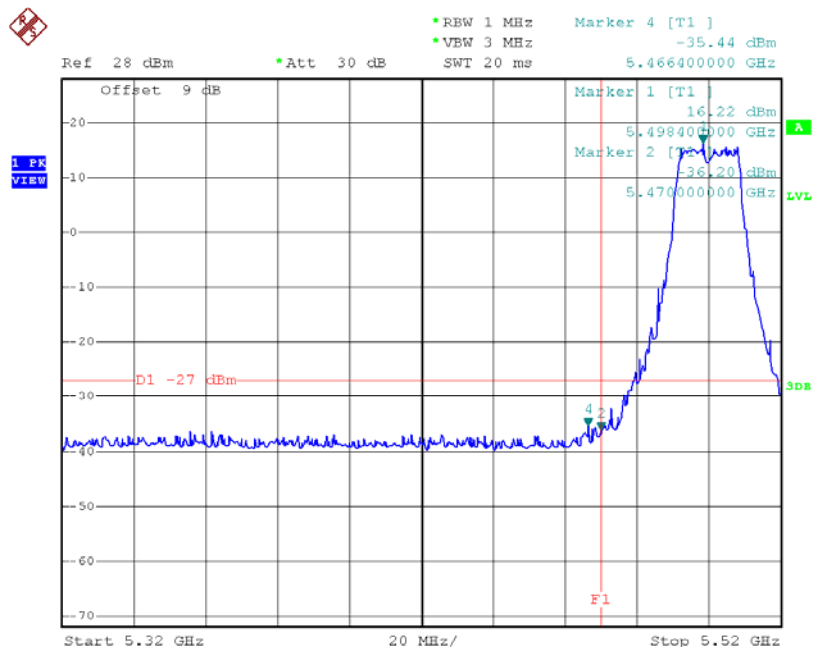
Date: 2.APR.2015 21:01:06



Date: 2.APR.2015 21:01:14

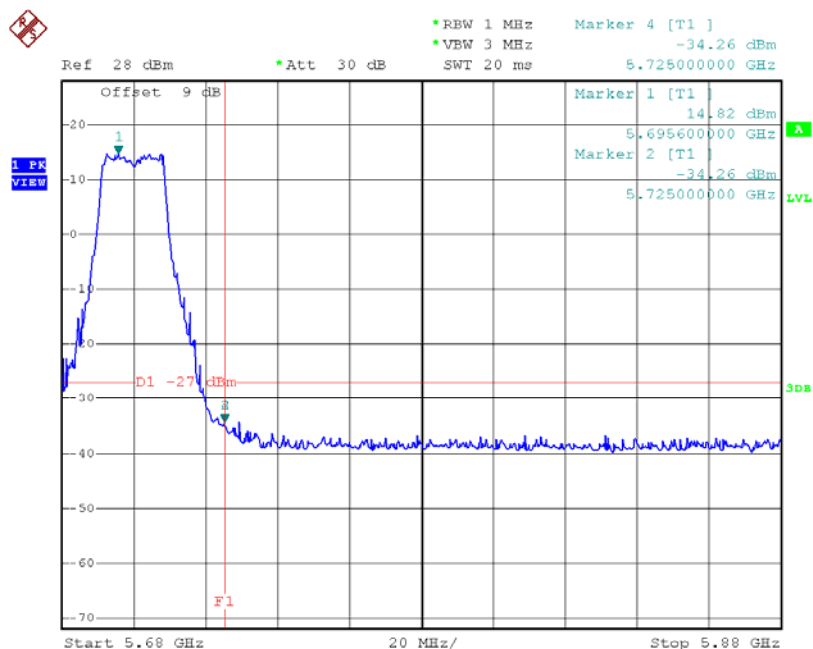
Test Mode: UNII-2C/TX AC20 Mode_ANT 1

TX mode CH100



Date: 2.APR.2015 17:07:27

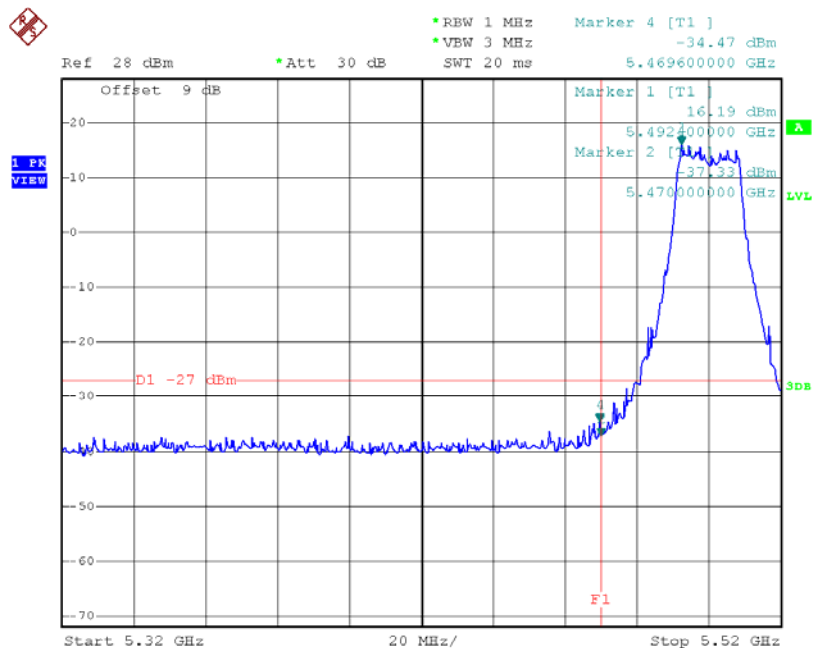
TX mode CH140



Date: 2.APR.2015 17:08:27

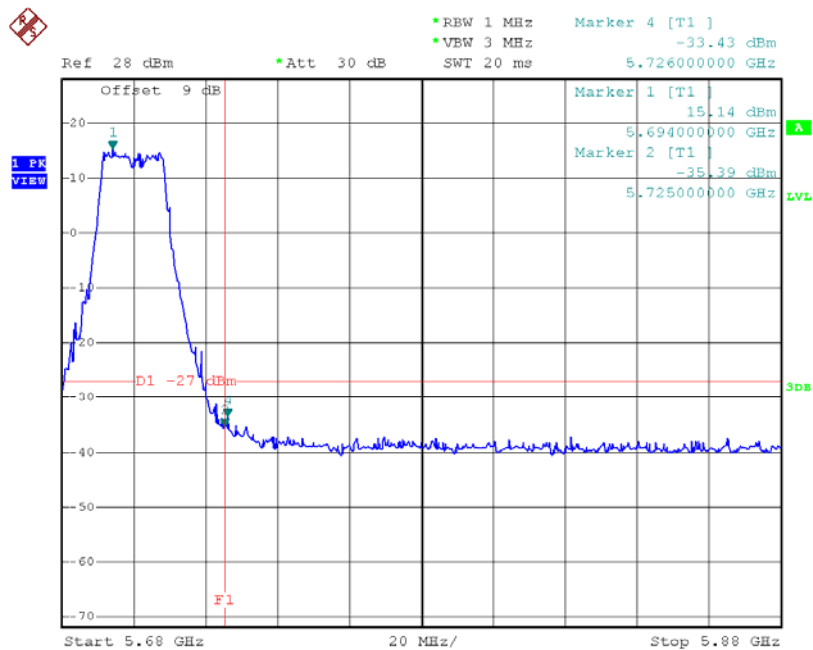
Test Mode:	UNII-2C/TX AC20 Mode_ANT 2
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TX mode CH100



Date: 2.APR.2015 17:53:49

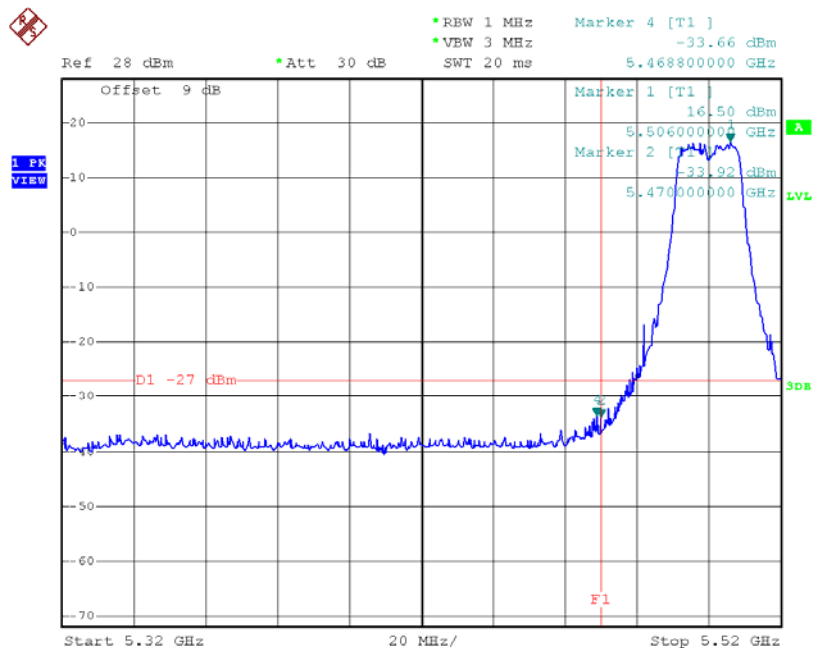
TX mode CH140



Date: 2.APR.2015 17:54:29

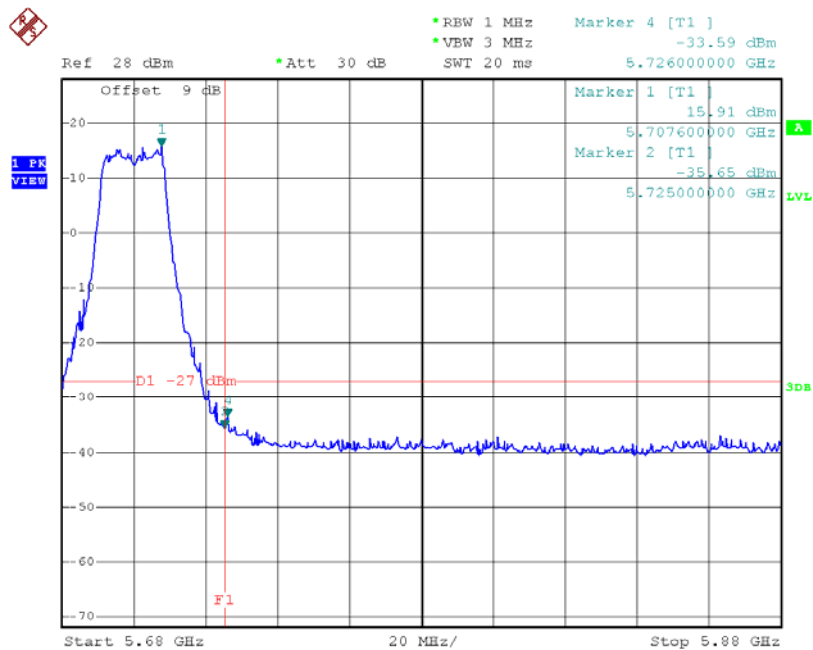
Test Mode:	UNII-2C/TX AC20 Mode_ANT 3
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TX mode CH100



Date: 2.APR.2015 20:46:12

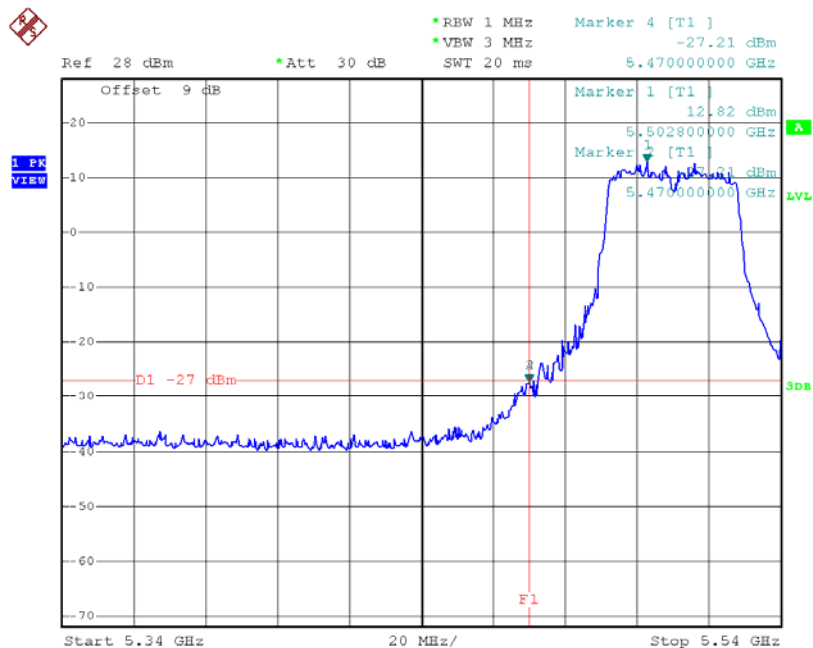
TX mode CH140



Date: 2.APR.2015 20:46:57

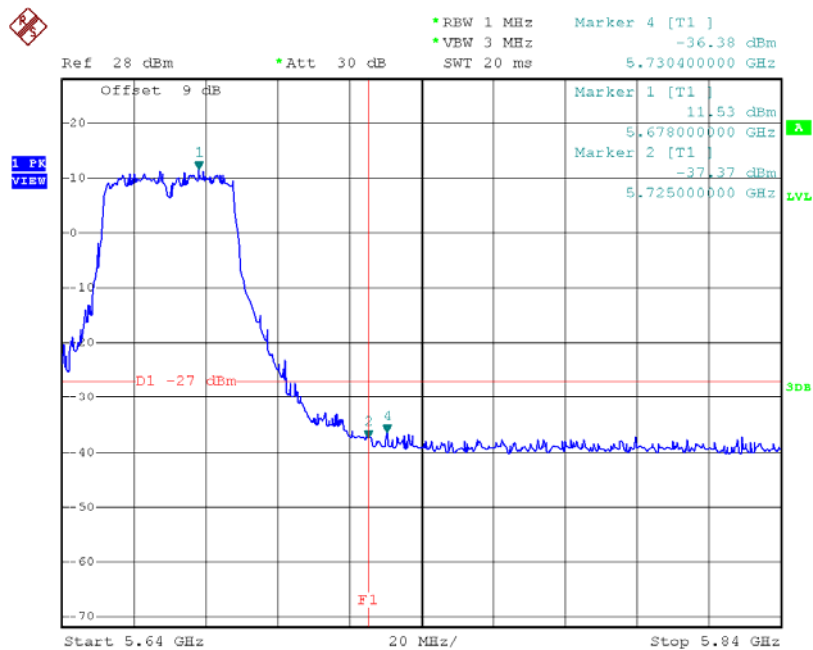
Test Mode:	UNII-2C/TX AC40 Mode_ANT 1
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TX mode CH102



Date: 2.APR.2015 17:17:30

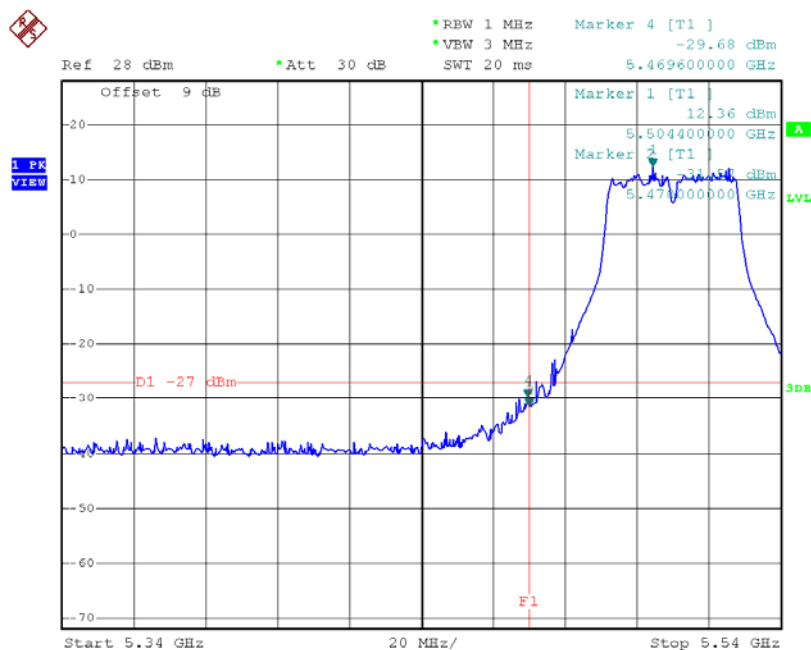
TX mode CH134



Date: 2.APR.2015 17:18:29

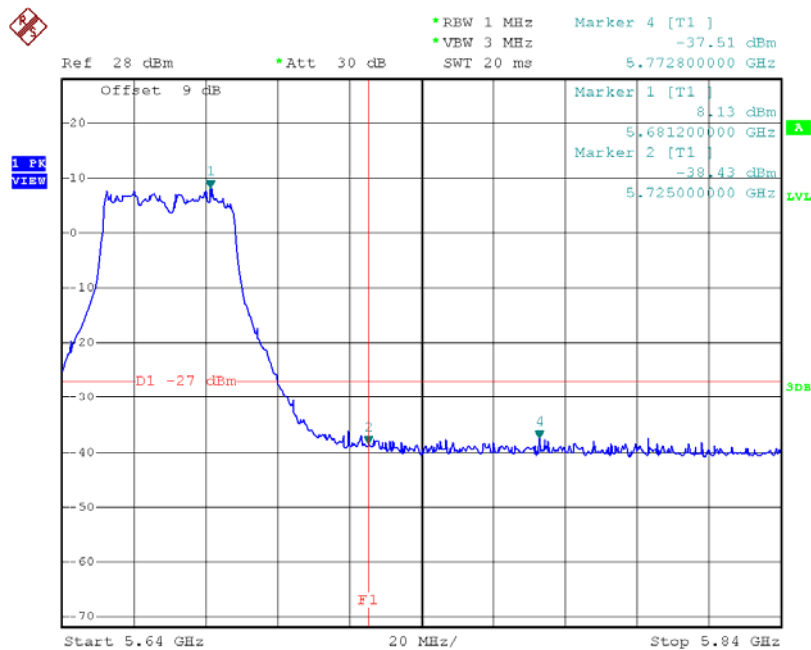
Test Mode:	UNII-2C/TX AC40 Mode_ANT 2
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TX mode CH102



Date: 2.APR.2015 18:02:29

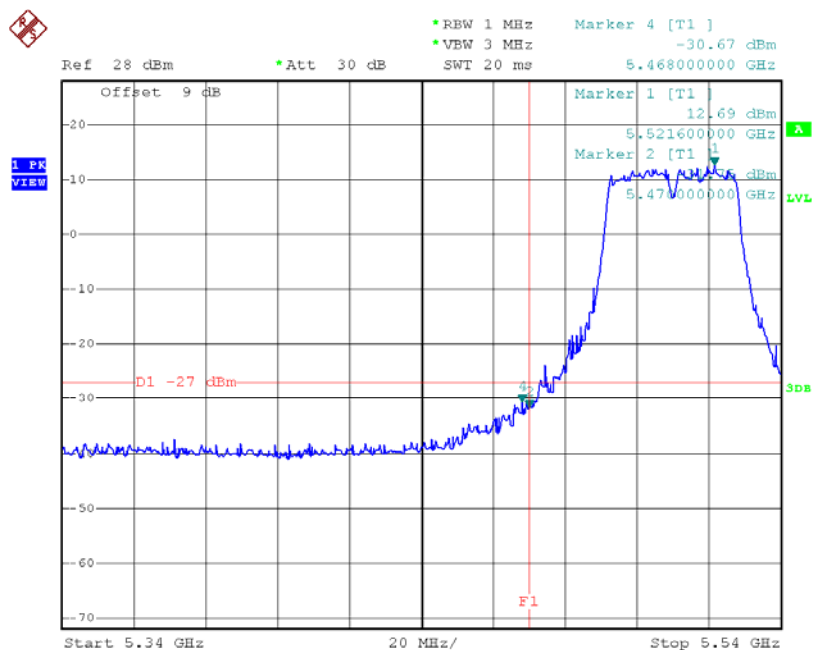
TX mode CH134



Date: 2.APR.2015 18:03:20

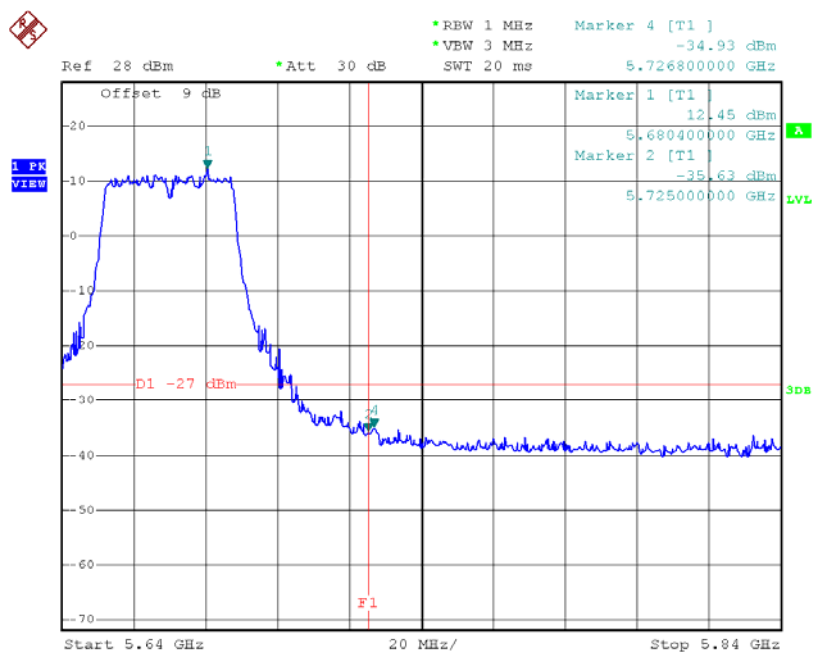
Test Mode:	UNII-2C/TX AC40 Mode_ANT 3
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TX mode CH102



Date: 2.APR.2015 20:57:13

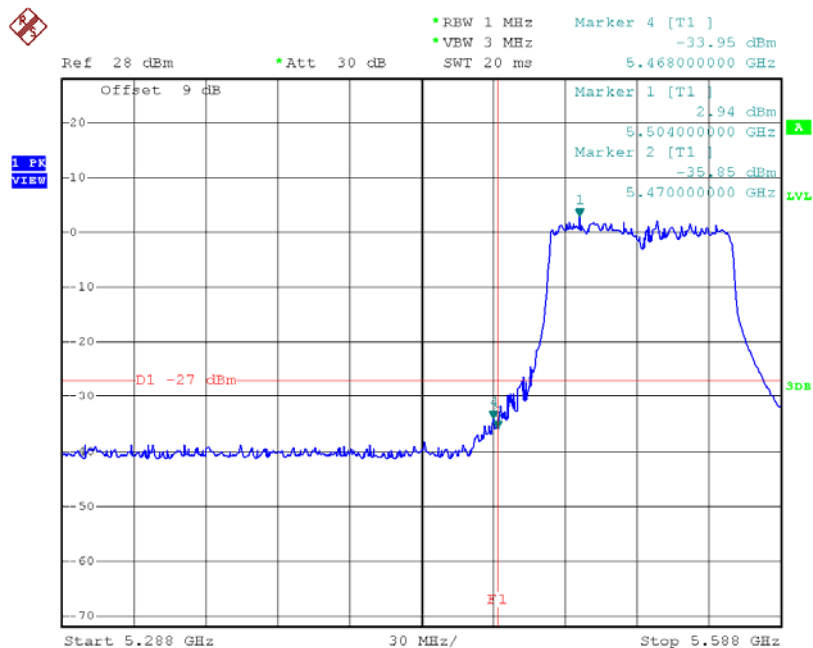
TX mode CH134



Date: 2.APR.2015 20:58:06

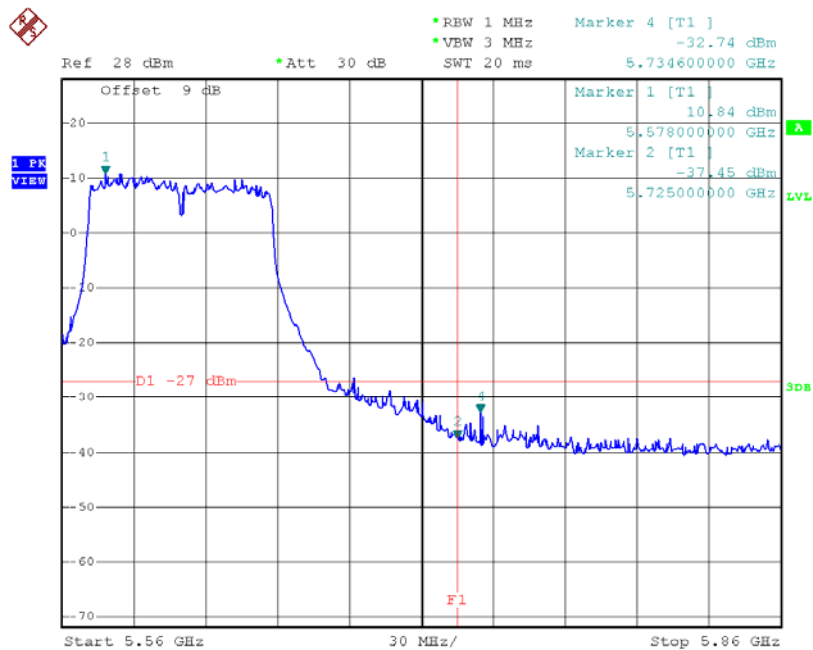
Test Mode:	UNII-2C/TX AC80 Mode_ANT 1
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TX mode CH106



Date: 2.APR.2015 17:21:49

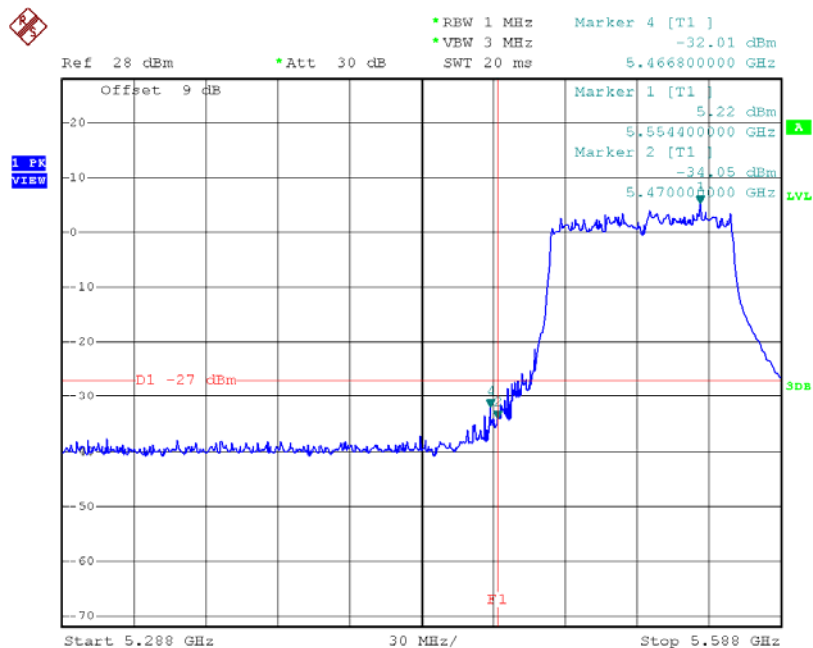
TX mode CH122



Date: 2.APR.2015 17:22:25

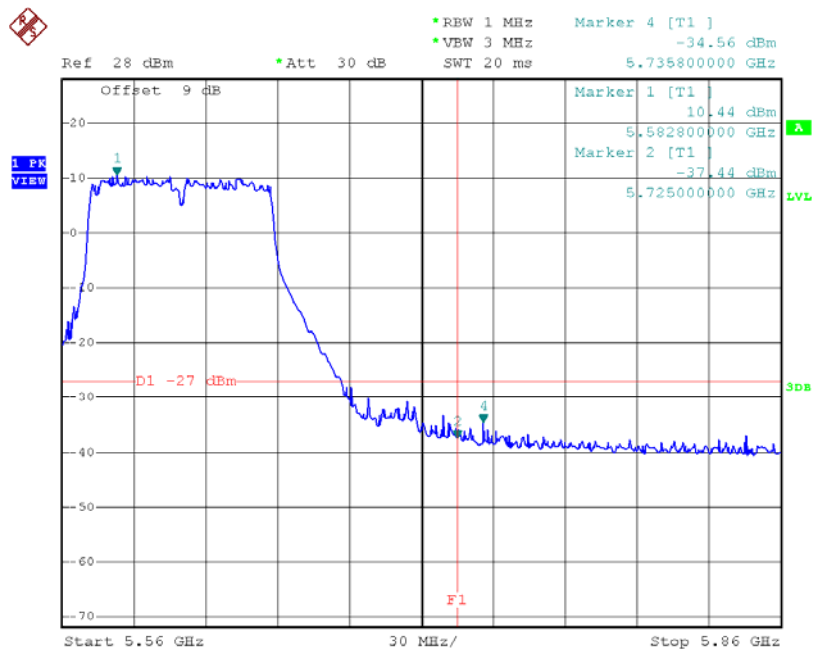
Test Mode:	UNII-2C/TX AC80 Mode_ANT 2
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TX mode CH106



Date: 2.APR.2015 18:09:00

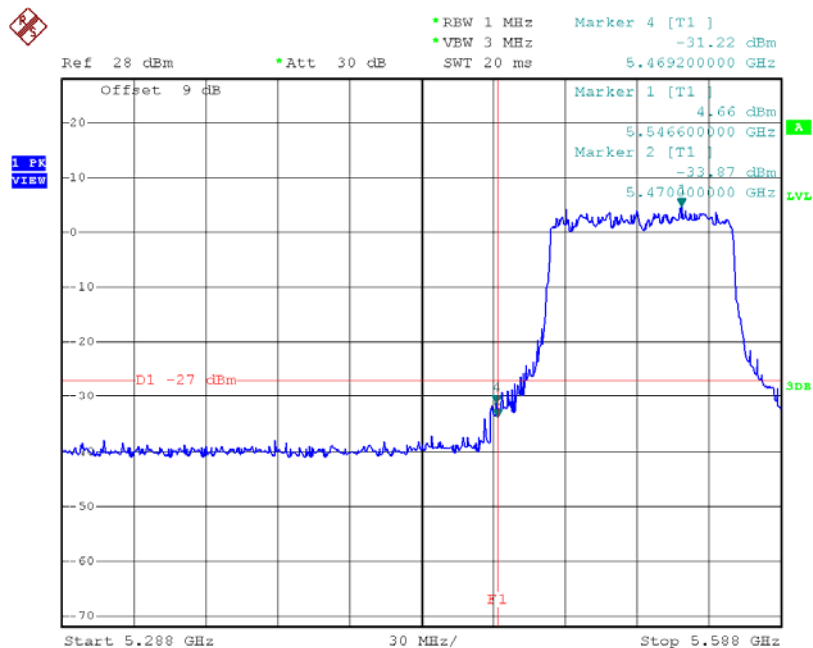
TX mode CH122



Date: 2.APR.2015 18:09:35

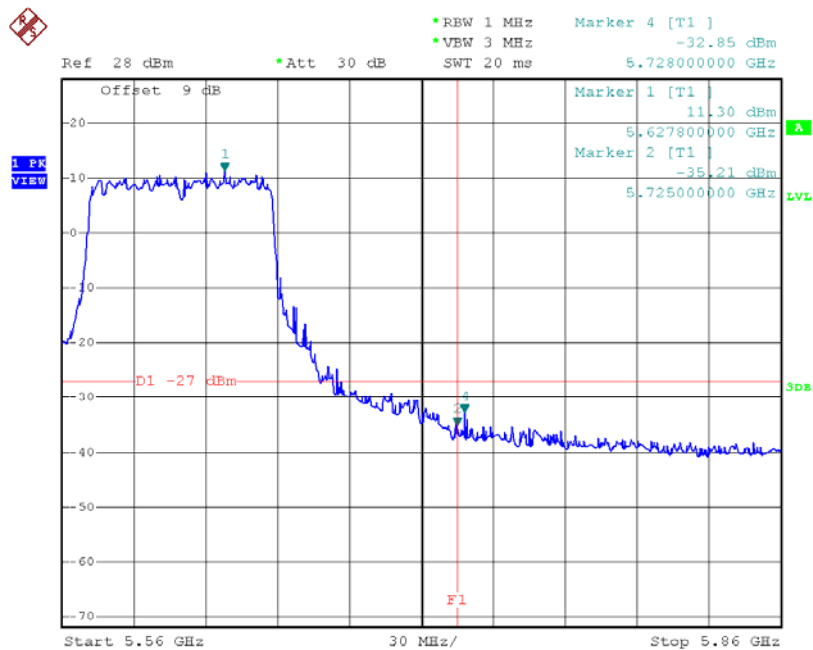
Test Mode: UNII-2C/TX AC80 Mode_ANT 3

TX mode CH106



Date: 2.APR.2015 21:01:47

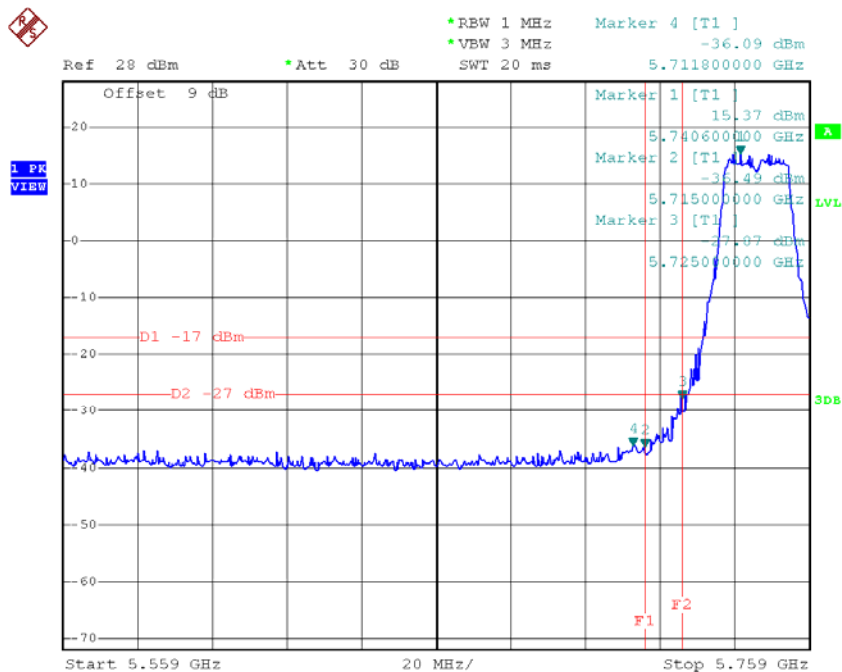
TX mode CH122



Date: 2.APR.2015 21:02:20

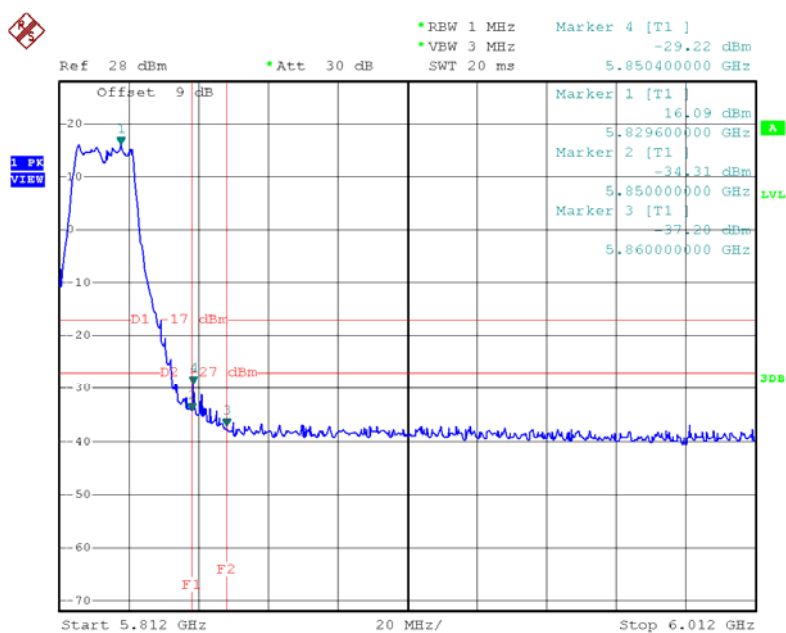
Test Mode: UNII-3/TX AC20 Mode_ANT 1

TX AC HT20 mode CH149



Date: 2.APR.2015 17:08:54

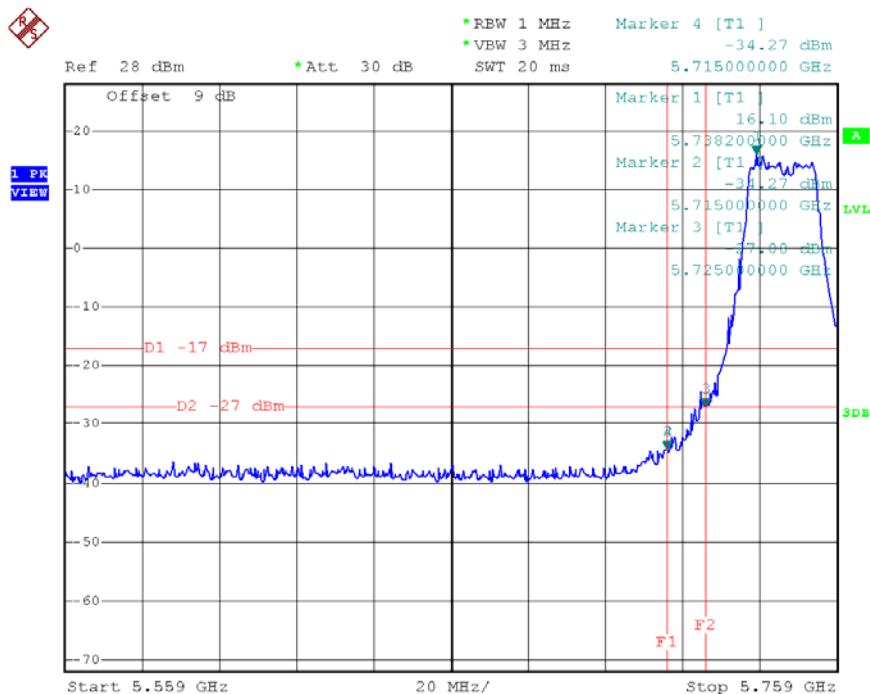
TX AC HT20 mode CH165



Date: 2.APR.2015 17:09:36

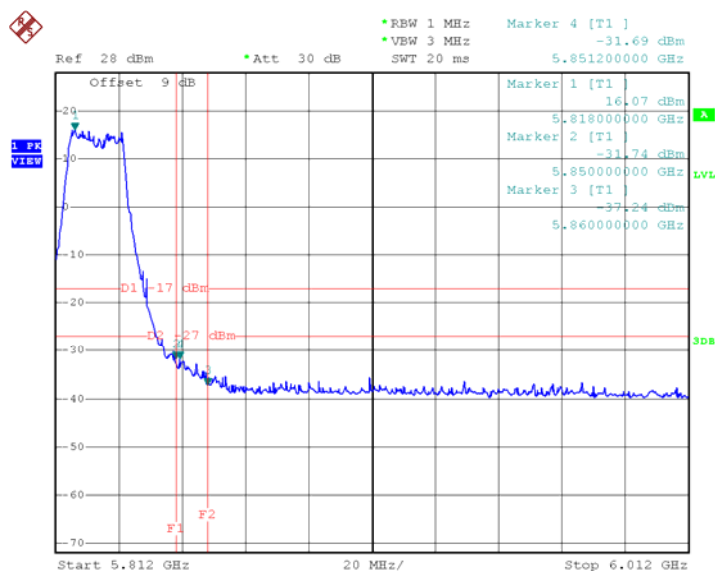
Test Mode: UNII-3/TX AC20 Mode_ANT 2

TX AC HT20 mode CH149



Date: 2.APR.2015 17:54:55

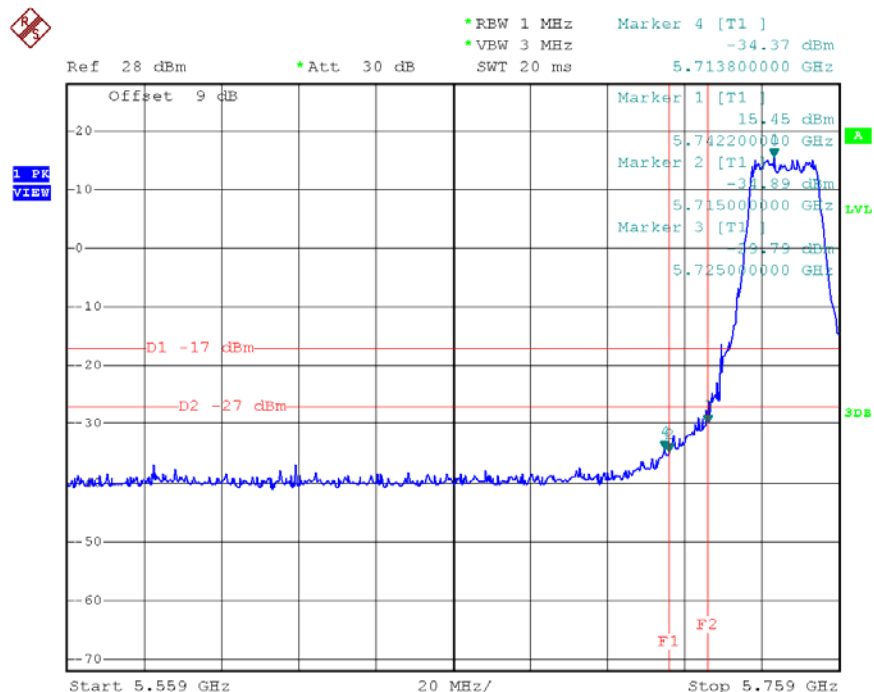
TX AC HT20 mode CH165



Date: 2.APR.2015 17:55:39

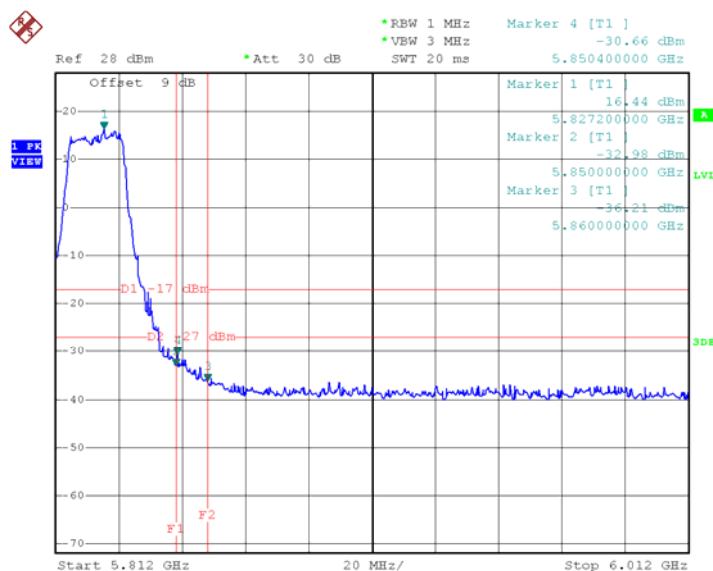
Test Mode: UNII-3/TX AC20 Mode_ANT 3

TX AC HT20 mode CH149



Date: 2.APR.2015 20:47:21

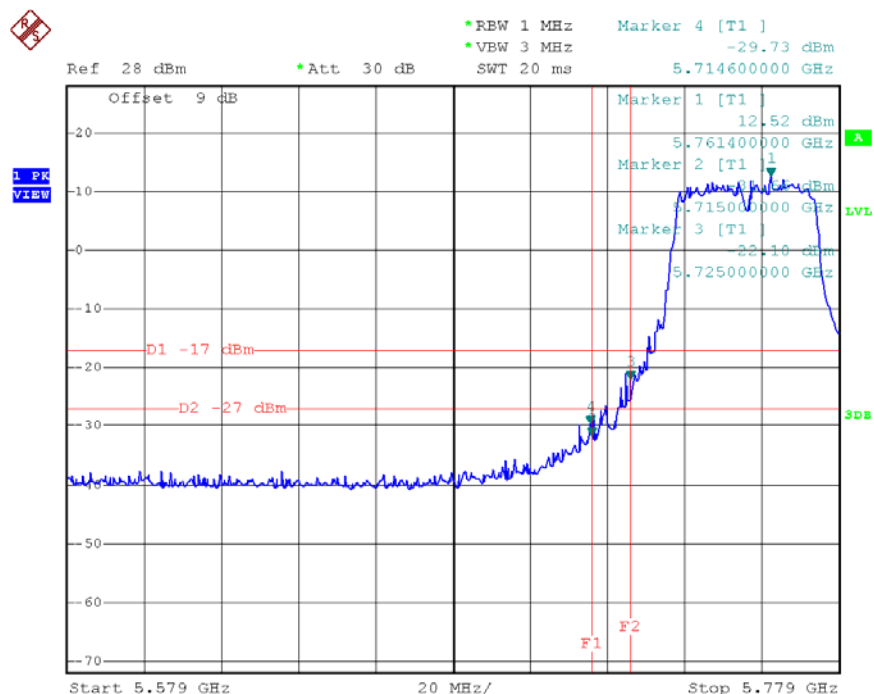
TX AC HT20 mode CH165



Date: 2.APR.2015 20:48:04

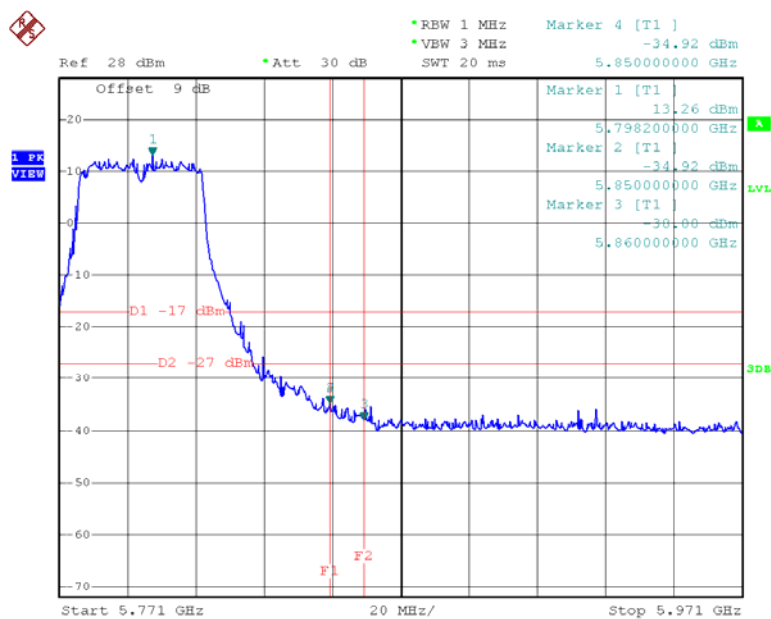
Test Mode: UNII-3/TX AC40 Mode_ANT 1

TX AC HT40 mode CH151



Date: 2.APR.2015 17:19:07

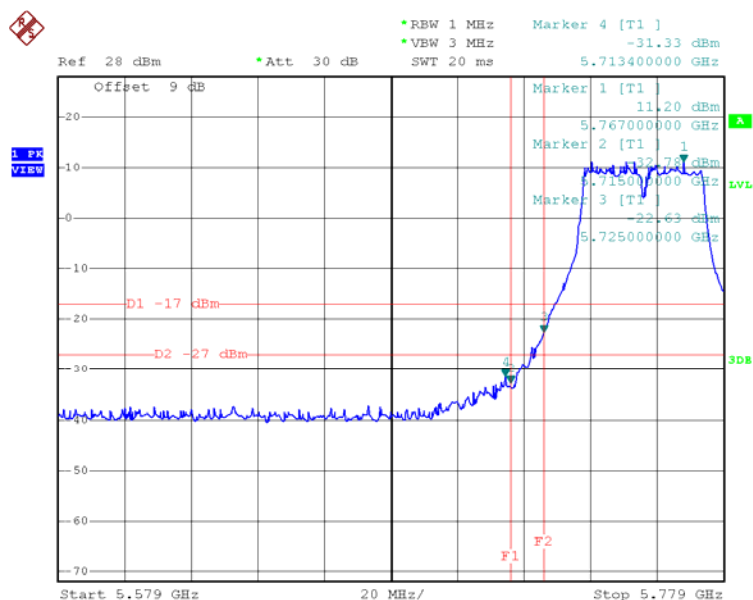
TX AC HT40 mode CH159



Date: 2.APR.2015 17:19:32

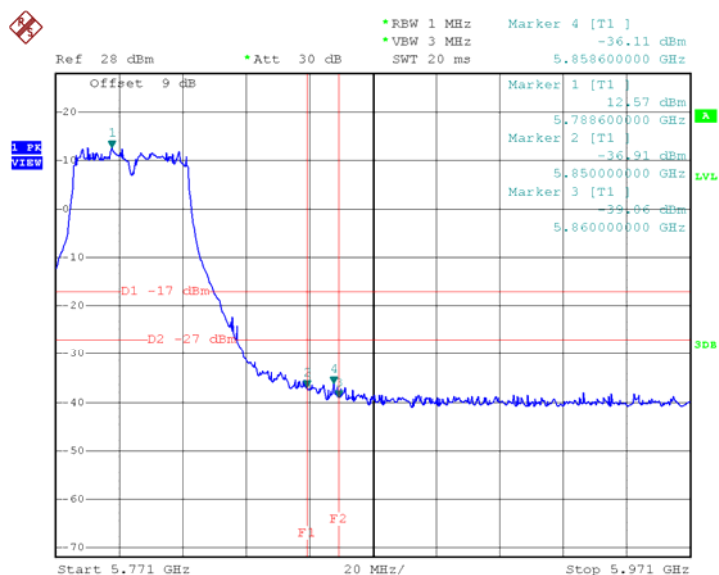
Test Mode: UNII-3/TX AC40 Mode_ANT 2

TX AC HT40 mode CH151



Date: 2.APR.2015 18:05:41

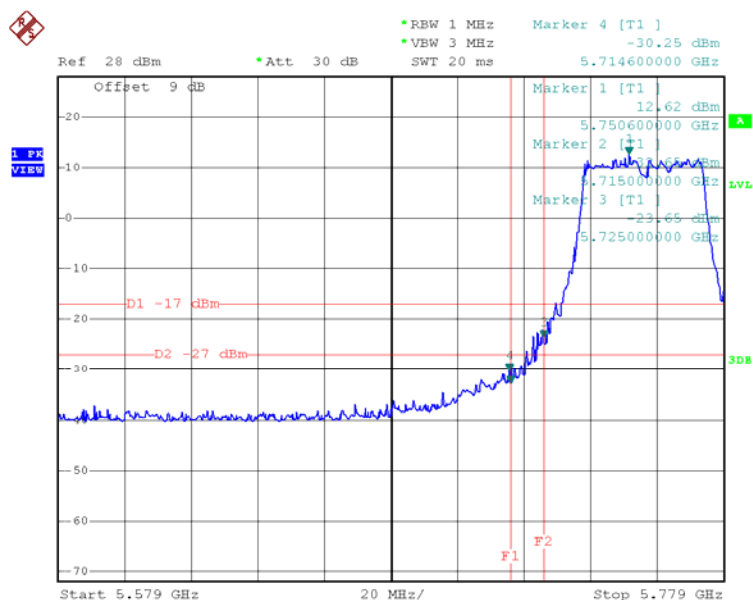
TX AC HT40 mode CH159



Date: 2.APR.2015 18:06:08

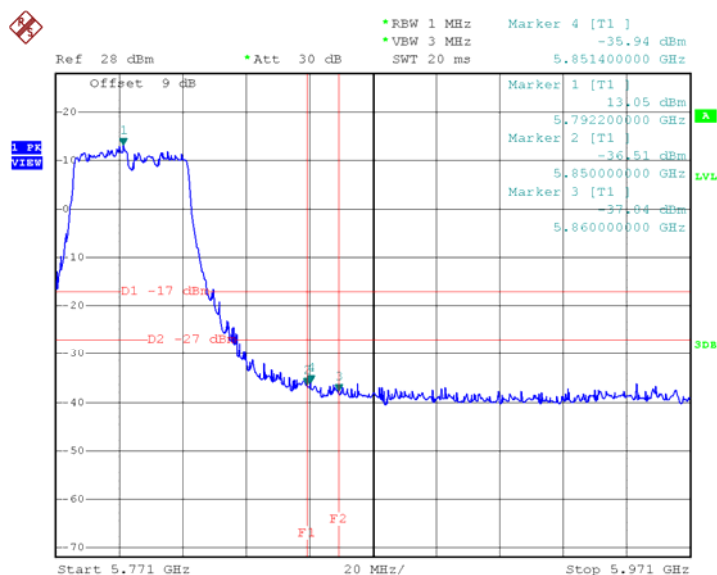
Test Mode: UNII-3/TX AC40 Mode_ANT 3

TX AC HT40 mode CH151



Date: 2.APR.2015 20:58:31

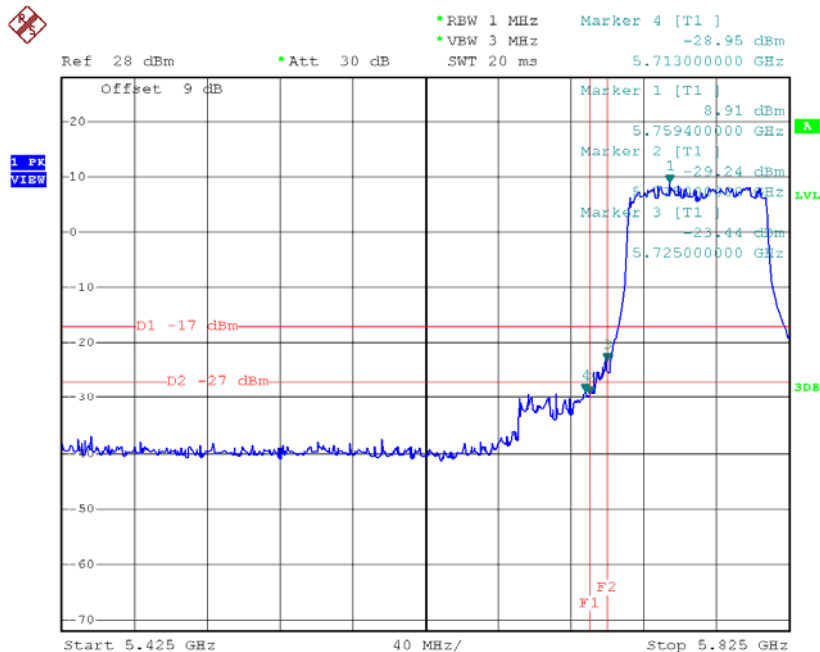
TX AC HT40 mode CH159



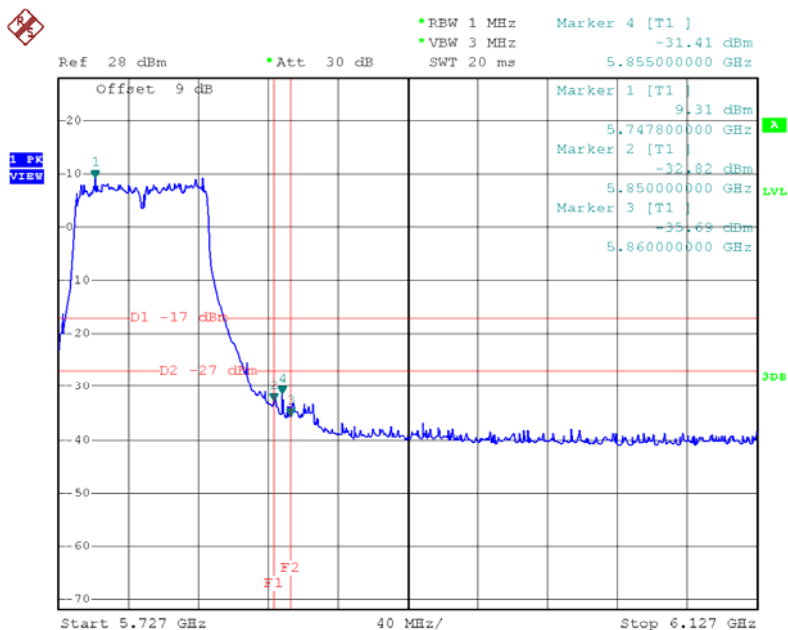
Date: 2.APR.2015 20:59:12

Test Mode: UNII-3/TX AC80 Mode_ANT 1

TX AC HT80 mode CH155



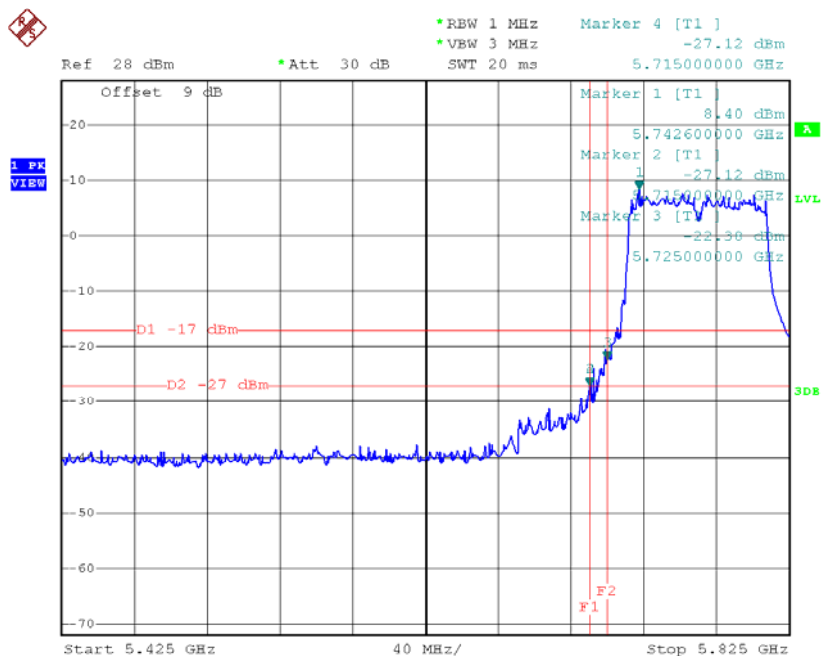
Date: 2.APR.2015 17:24:58



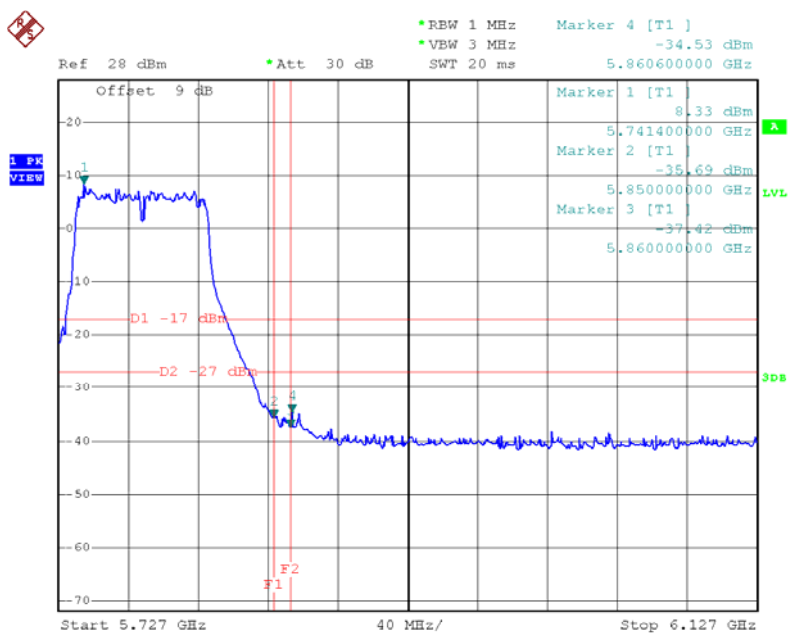
Date: 2.APR.2015 17:25:05

Test Mode: UNII-3/TX AC80 Mode_ANT 2

TX AC HT80 mode CH155



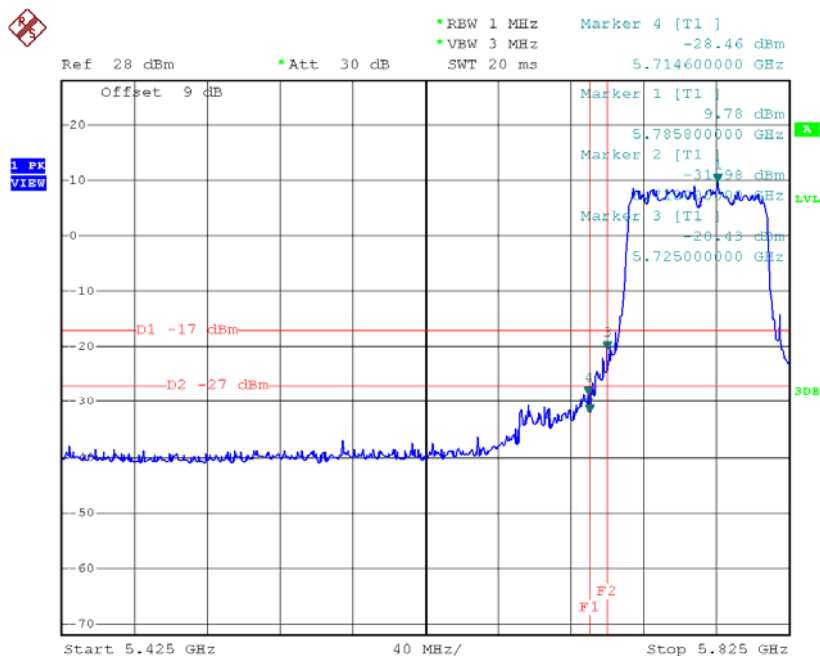
Date: 2.APR.2015 18:14:36



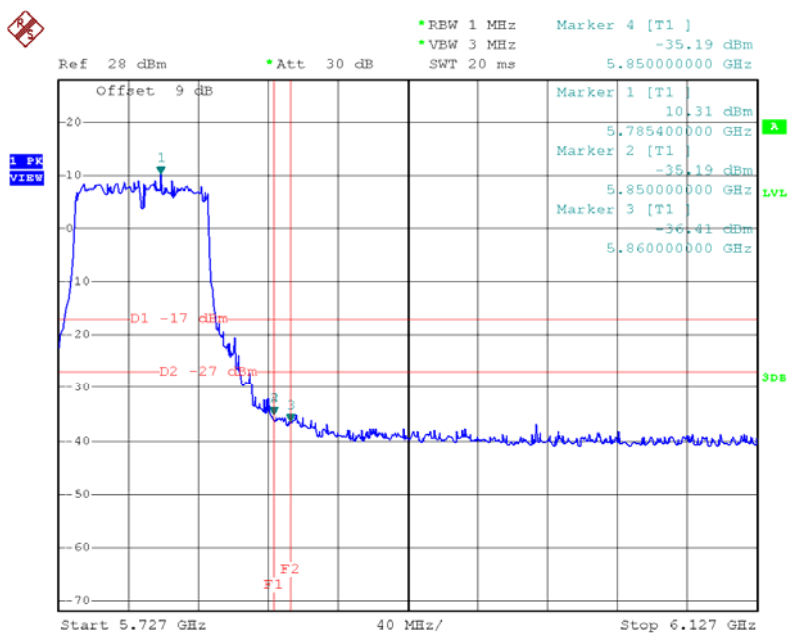
Date: 2.APR.2015 18:14:44

Test Mode: UNII-3/TX AC80 Mode_ANT 3

TX AC HT80 mode CH155



Date: 2.APR.2015 21:02:52



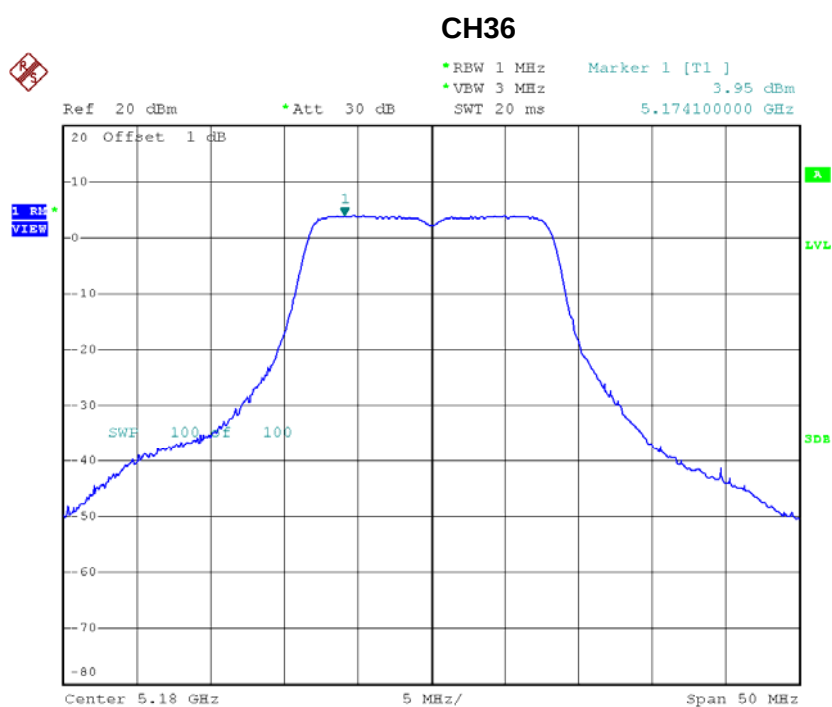
Date: 2.APR.2015 21:03:00

ATTACHMENTH - POWER SPECTRAL DENSITY

For 1TX

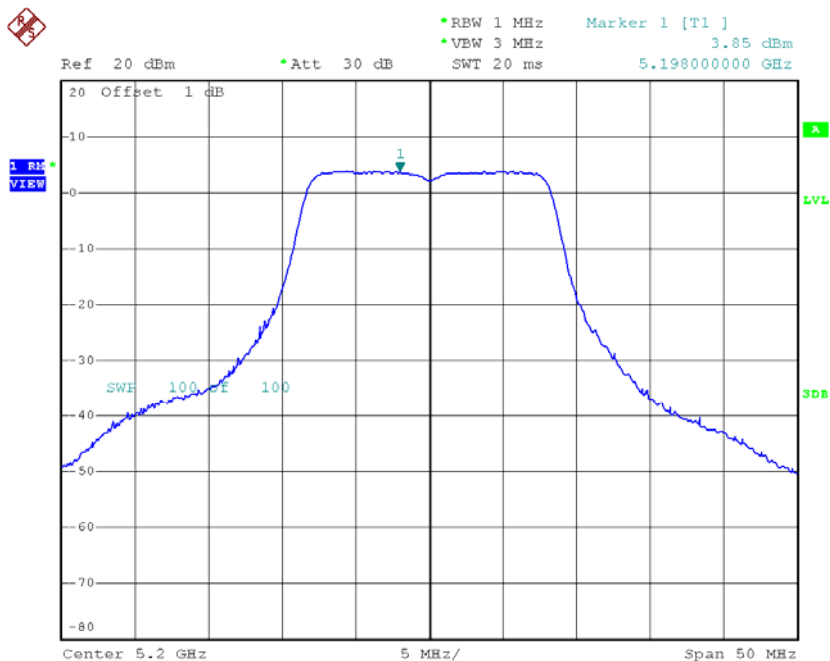
Test Mode: UNII-1/ TX A Mode_CH36/CH40/CH48

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	3.95	0.03	3.98	15.00
CH40	5200	3.85	0.03	3.88	15.00
CH48	5240	3.90	0.03	3.93	15.00



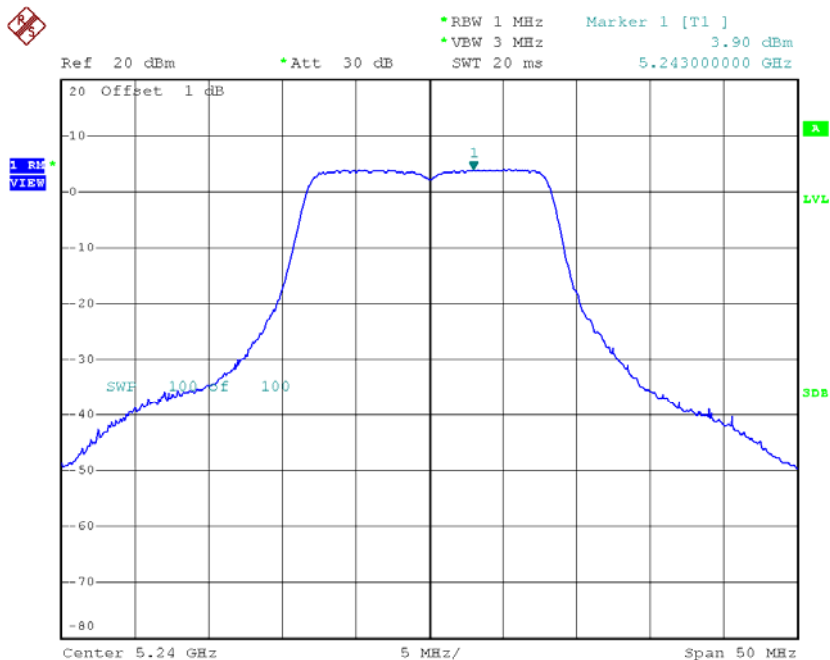
Date: 1.APR.2015 15:21:59

CH40



Date: 1.APR.2015 15:25:44

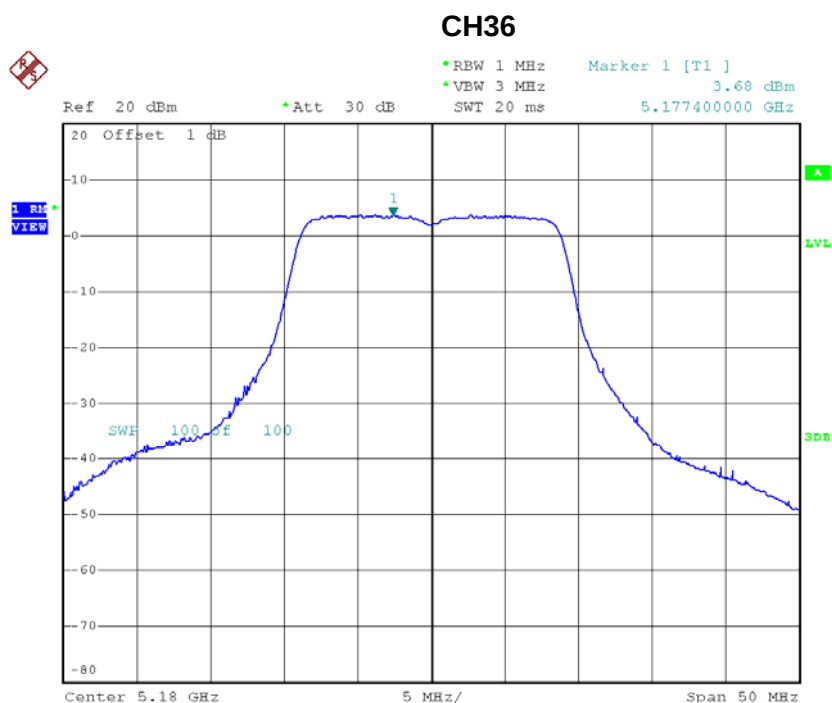
CH48



Date: 1.APR.2015 15:26:38

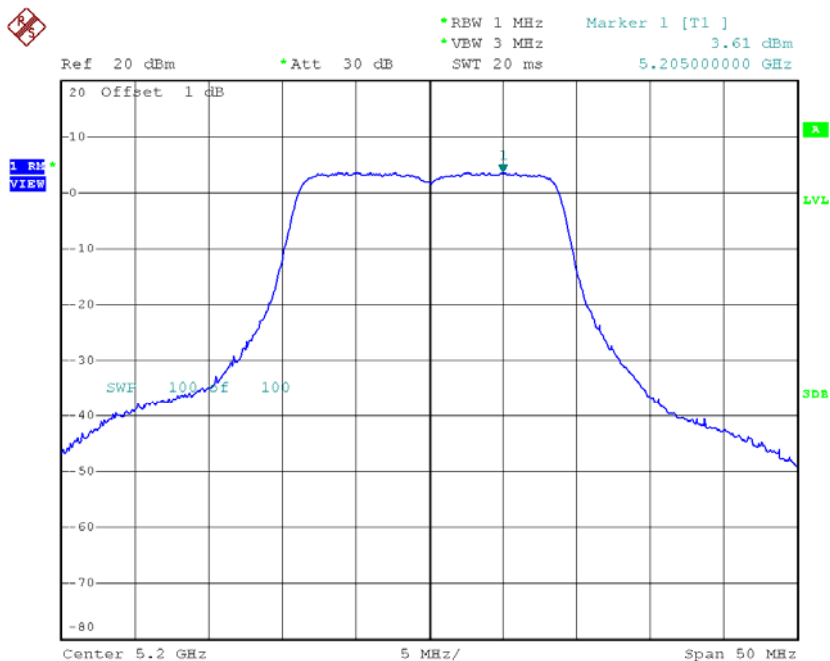
Test Mode: UNII-1/TX N20 Mode_CH36/CH40/CH48

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	3.68	0.10	3.78	15.00
CH40	5200	3.61	0.10	3.71	15.00
CH48	5240	3.69	0.10	3.79	15.00



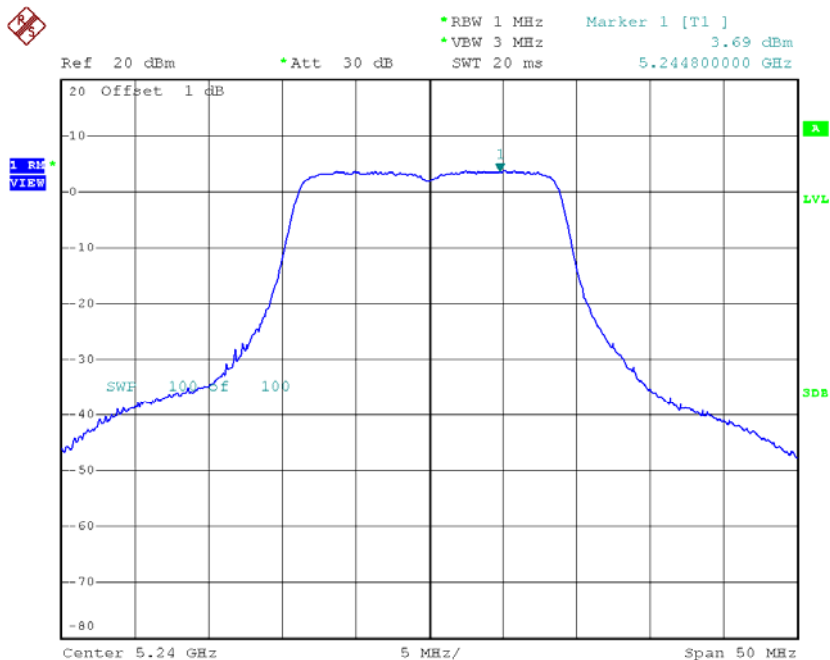
Date: 1.APR.2015 15:49:41

CH40



Date: 1.APR.2015 15:50:49

CH48

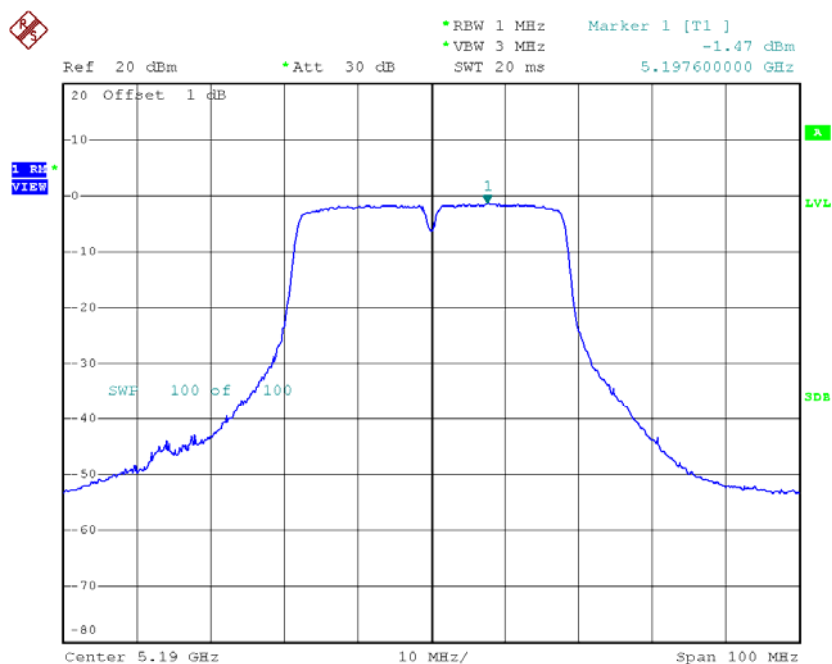


Date: 1.APR.2015 15:51:28

Test Mode: UNII-1/TX N40 Mode_CH38/CH46

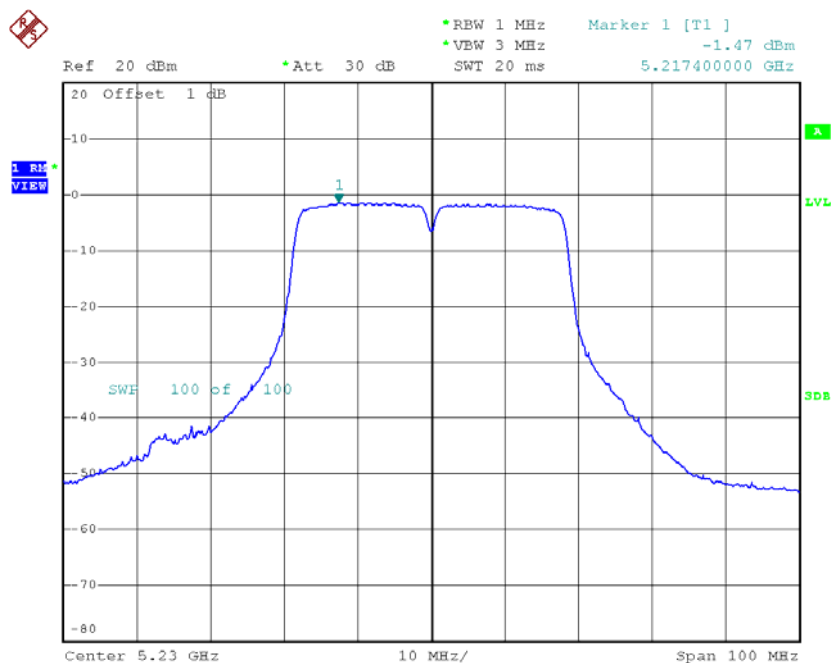
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH38	5190	-1.47	0.13	-1.34	15.00
CH46	5230	-1.47	0.13	-1.34	15.00

CH38



Date: 1.APR.2015 19:38:58

CH46

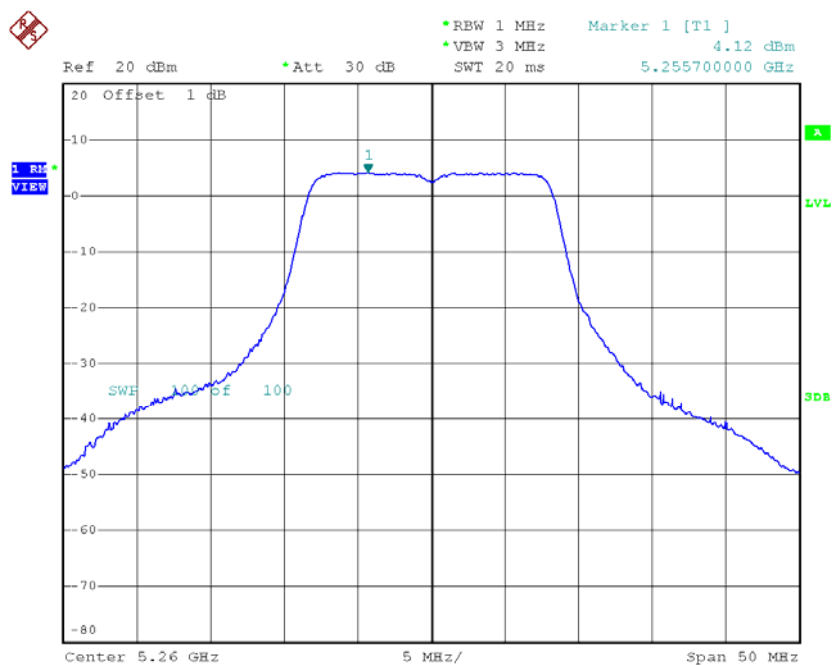


Date: 1.APR.2015 19:41:23

Test Mode: UNII-2A/ TX A Mode_CH52/CH60/CH64

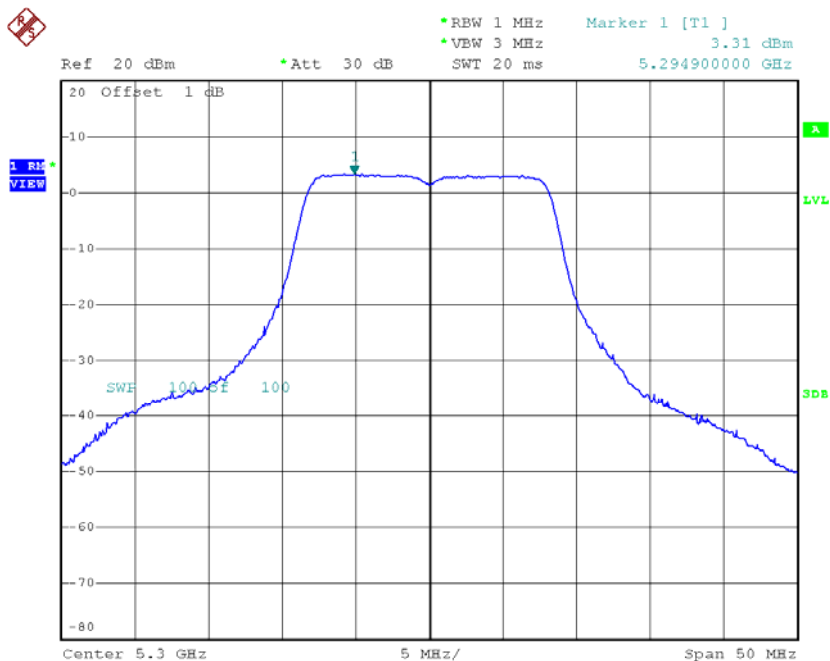
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH52	5260	4.12	0.03	4.15	9.00
CH60	5300	3.31	0.03	3.34	9.00
CH64	5320	3.11	0.03	3.14	9.00

CH52



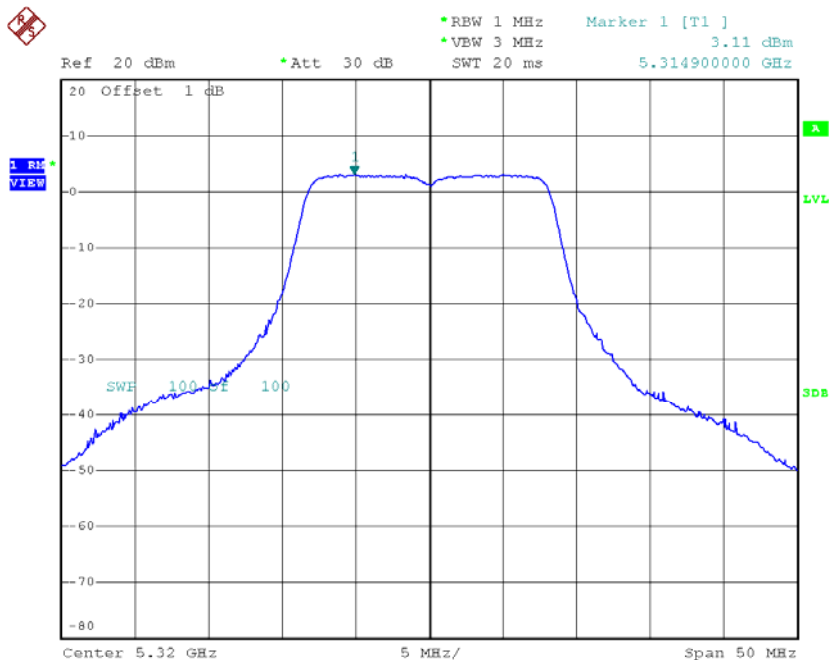
Date: 1.APR.2015 15:27:37

CH60



Date: 1.APR.2015 15:29:35

CH64

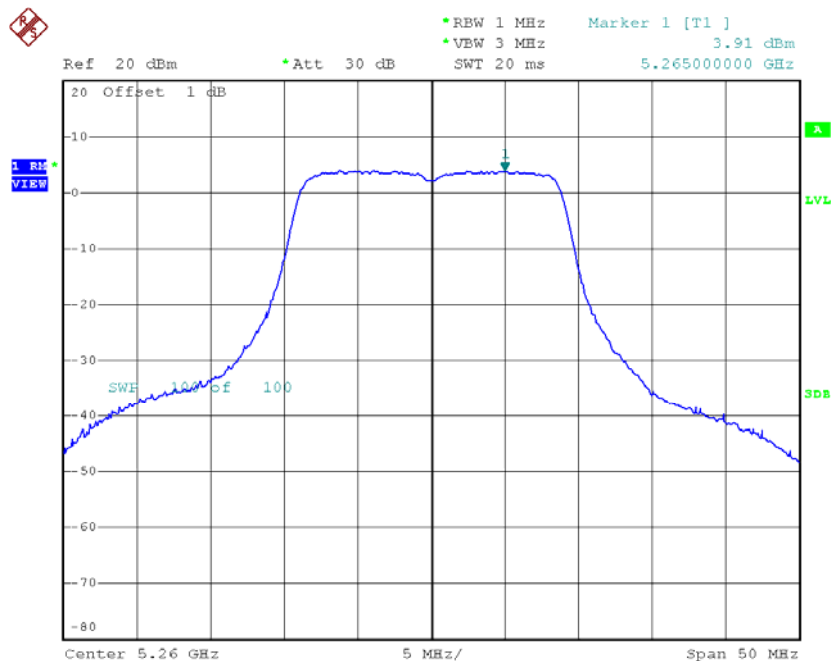


Date: 1.APR.2015 15:30:20

Test Mode: UNII-2A/TX N20 Mode_CH52/CH60/CH64

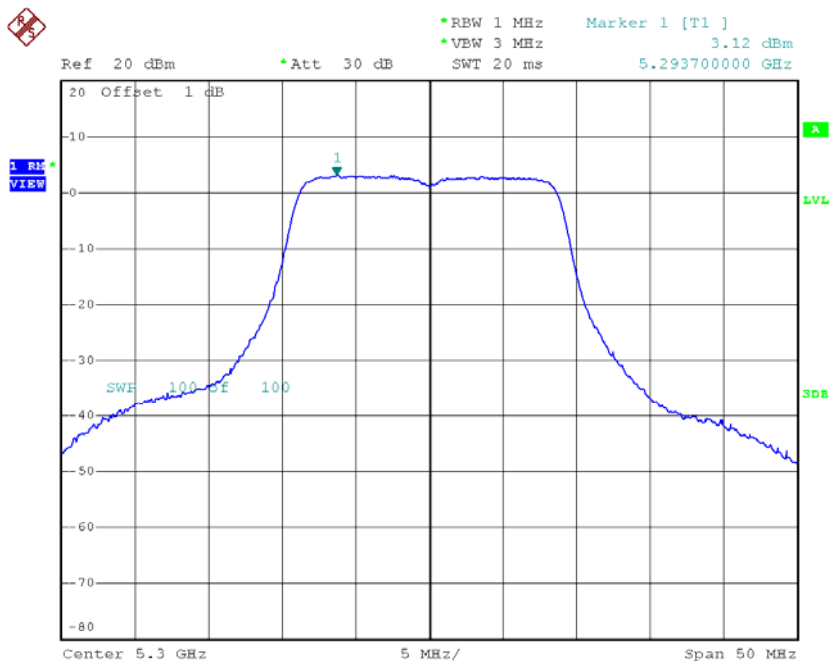
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH52	5260	3.91	0.10	4.01	9.00
CH60	5300	3.12	0.10	3.22	9.00
CH64	5320	2.86	0.10	2.96	9.00

CH52



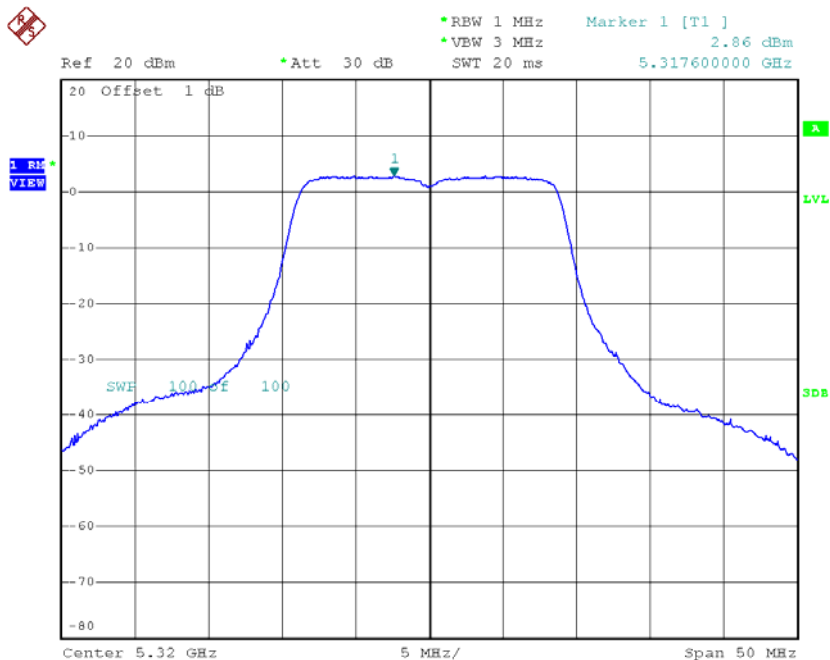
Date: 1.APR.2015 15:52:11

CH60



Date: 1.APR.2015 15:53:09

CH64

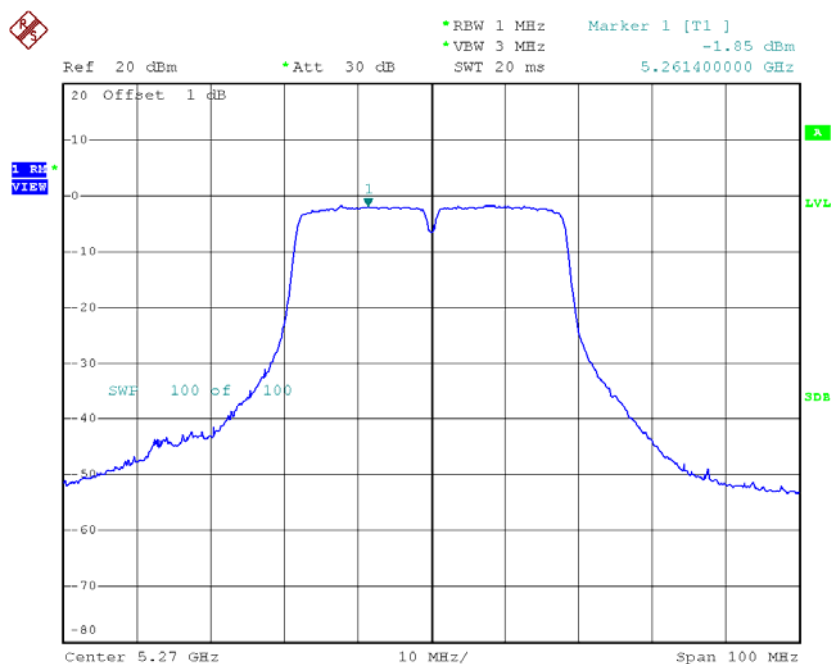


Date: 1.APR.2015 15:53:44

Test Mode: UNII-2A/TX N40 Mode_CH54/CH62

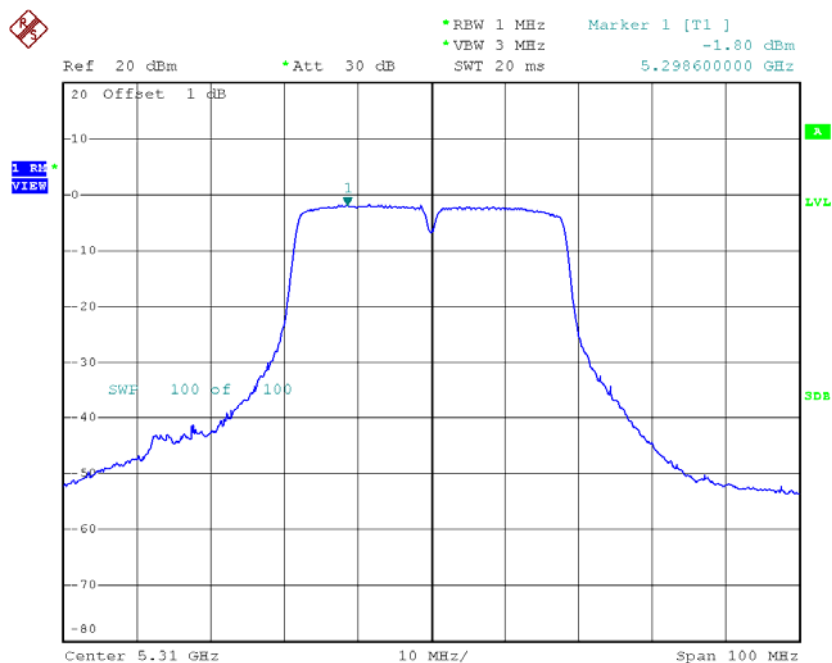
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH54	5270	-1.85	0.13	-1.72	9.00
CH62	5310	-1.80	0.13	-1.67	9.00

CH54



Date: 1.APR.2015 19:42:04

CH62

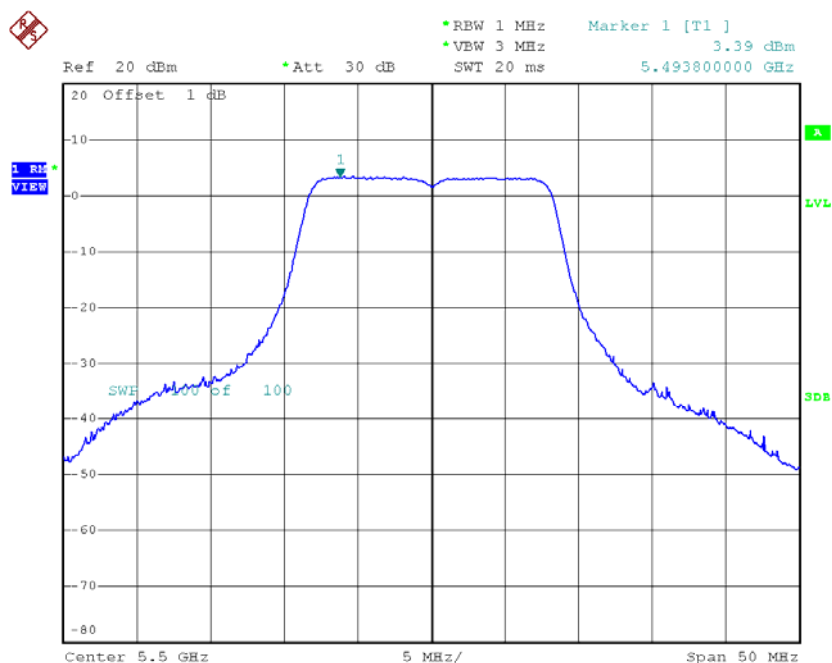


Date: 1.APR.2015 19:42:57

Test Mode: UNII-2C/ TX A Mode_CH100/CH116/CH140

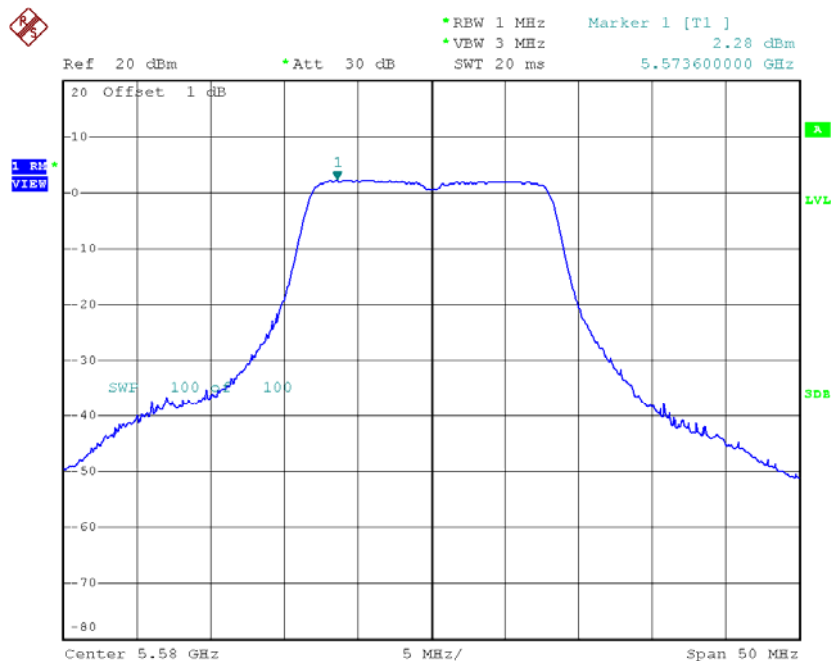
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH100	5500	3.39	0.03	3.42	9.00
CH116	5580	2.28	0.03	2.31	9.00
CH140	5700	2.86	0.03	2.89	9.00

CH100



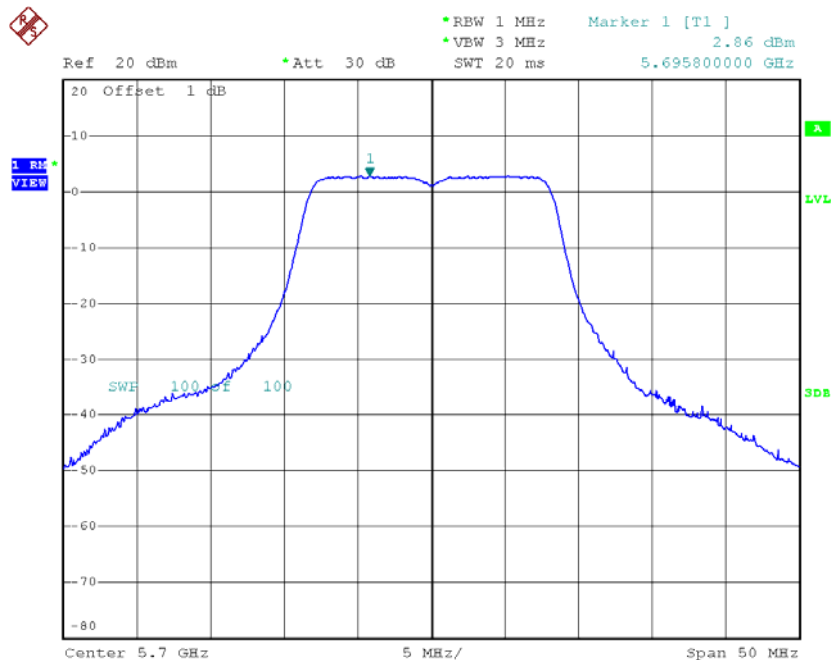
Date: 1.APR.2015 15:31:10

CH116



Date: 1.APR.2015 15:32:45

CH140

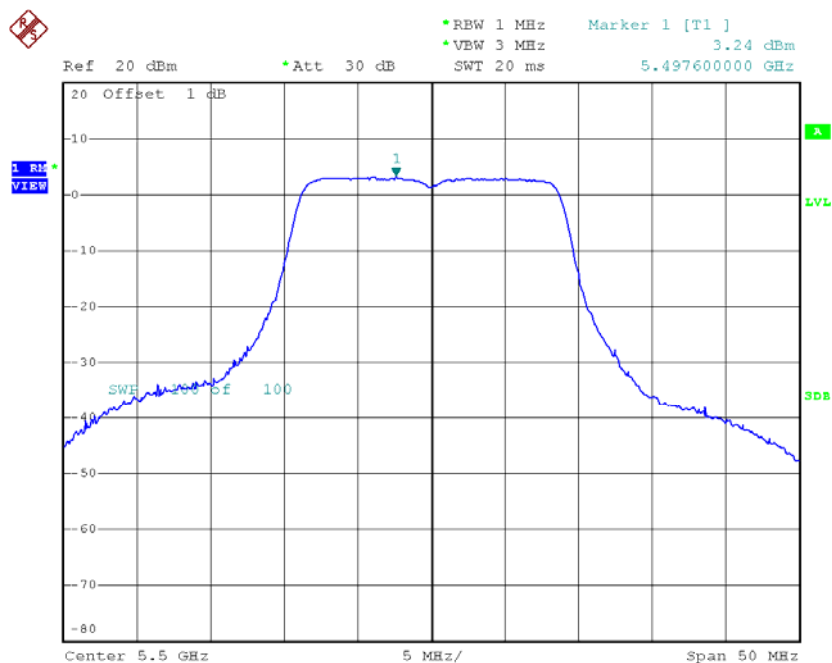


Date: 1.APR.2015 15:33:37

Test Mode: UNII-2C/TX N20 Mode_CH100/CH116/CH140

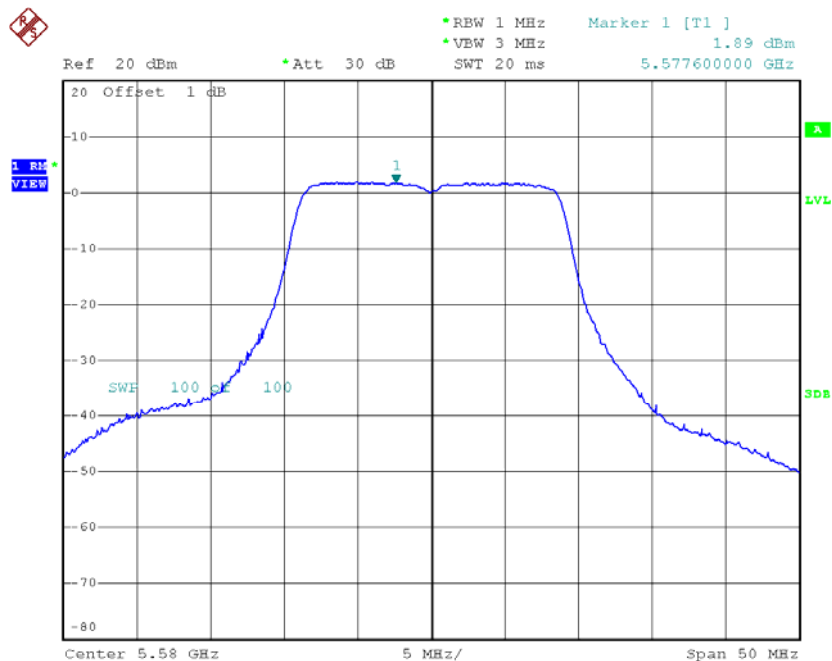
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH100	5500	3.24	0.10	3.34	9.00
CH116	5580	1.89	0.10	1.99	9.00
CH140	5700	2.69	0.10	2.79	9.00

CH100



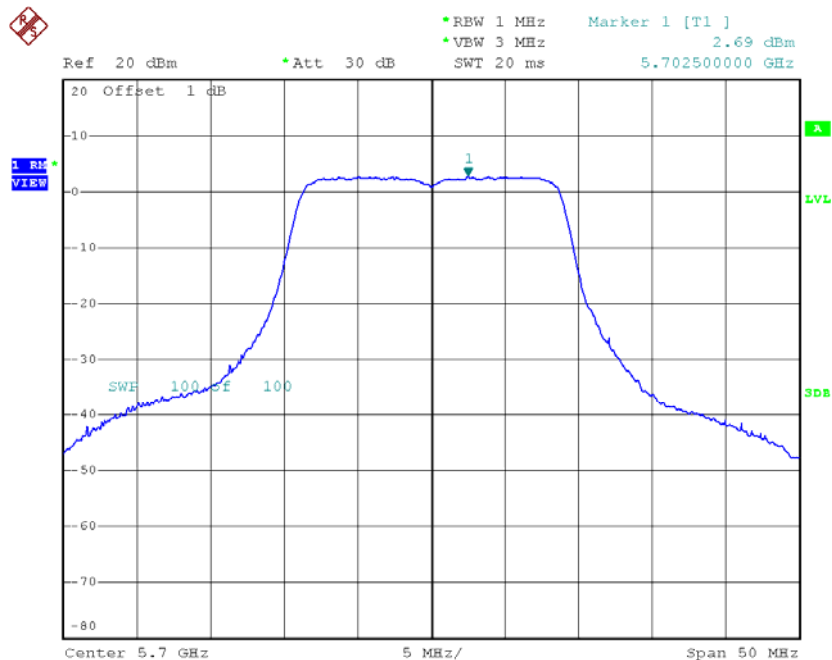
Date: 1.APR.2015 15:54:21

CH116



Date: 1.APR.2015 15:55:21

CH140

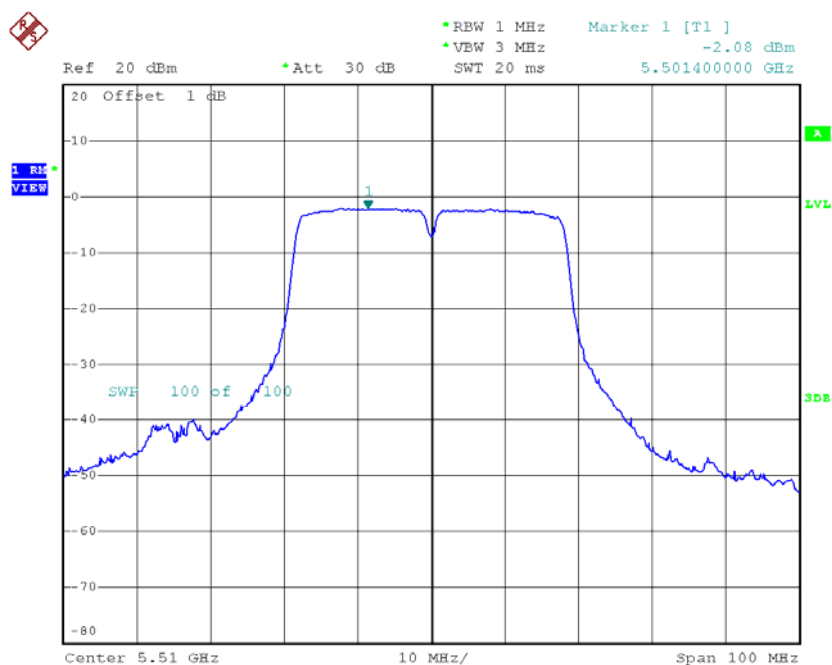


Date: 1.APR.2015 15:55:53

Test Mode: UNII-2C/TX N40 Mode_CH102/CH110/CH134

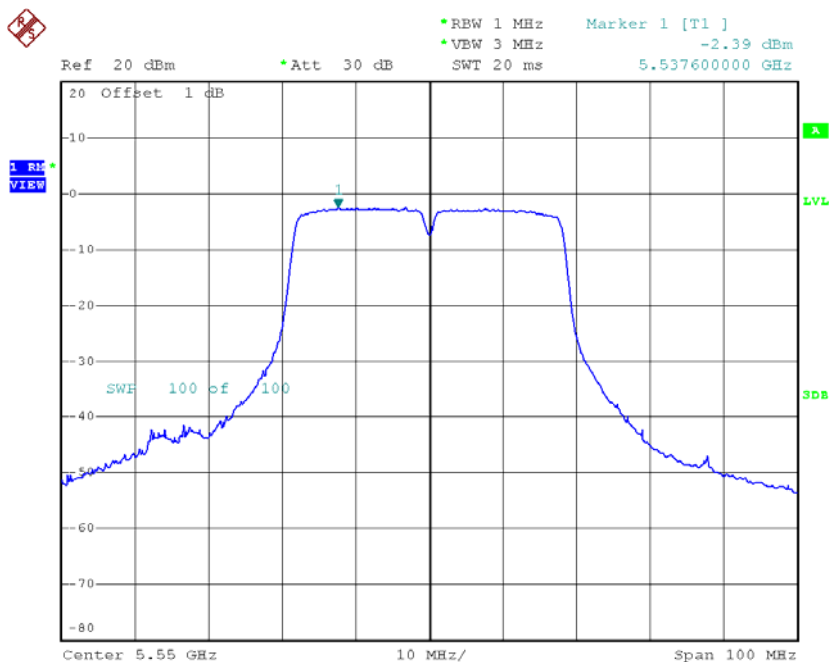
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH102	5510	-2.08	0.13	-1.95	9.00
CH110	5550	-2.39	0.13	-2.26	9.00
CH134	5670	-3.11	0.13	-2.98	9.00

CH102



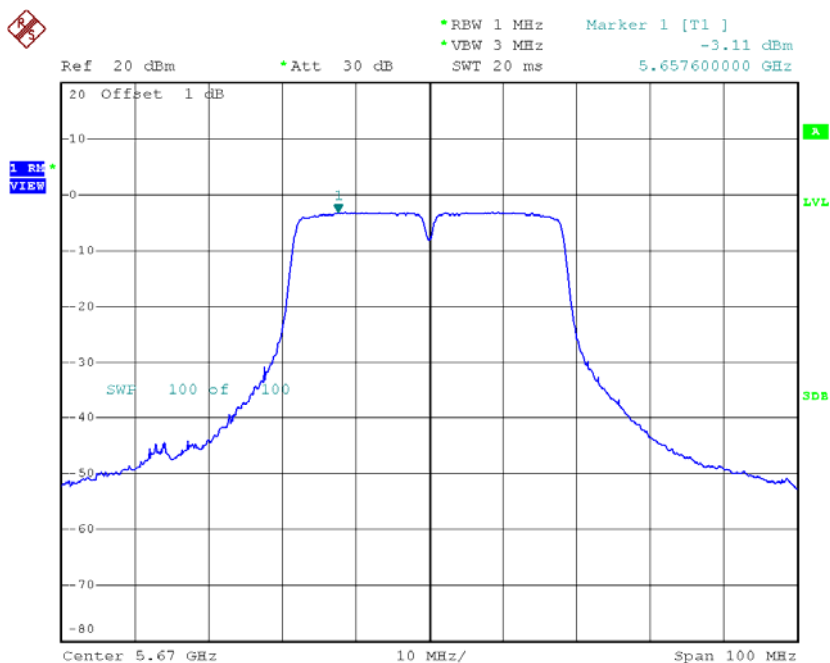
Date: 1.APR.2015 19:43:40

CH110



Date: 1.APR.2015 19:45:22

CH134

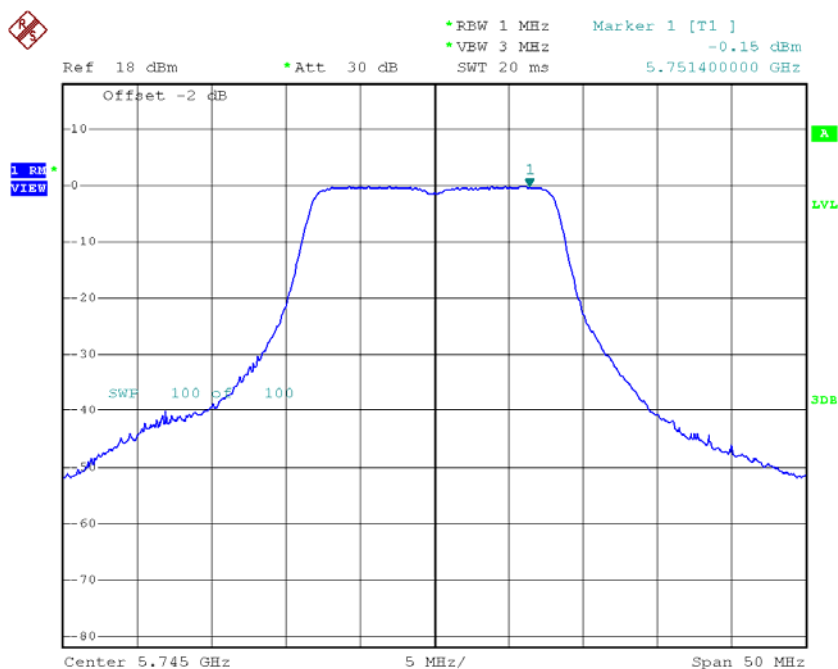


Date: 1.APR.2015 19:45:54

Test Mode: UNII-3/TX A Mode_CH149/CH157/CH165

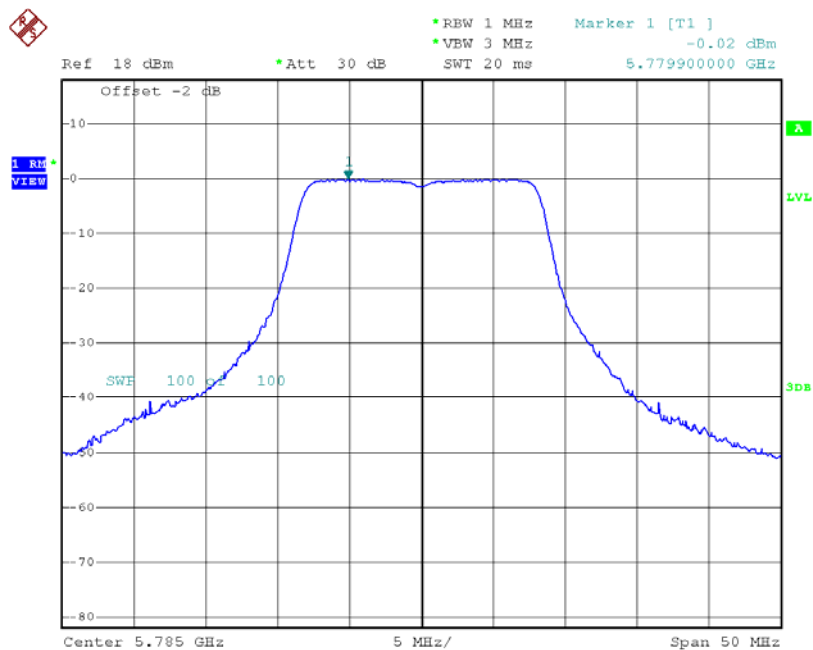
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH149	5745	-0.15	0.03	-0.12	28.00
CH157	5785	-0.02	0.03	0.01	28.00
CH165	5825	0.50	0.03	0.53	28.00

TX CH149



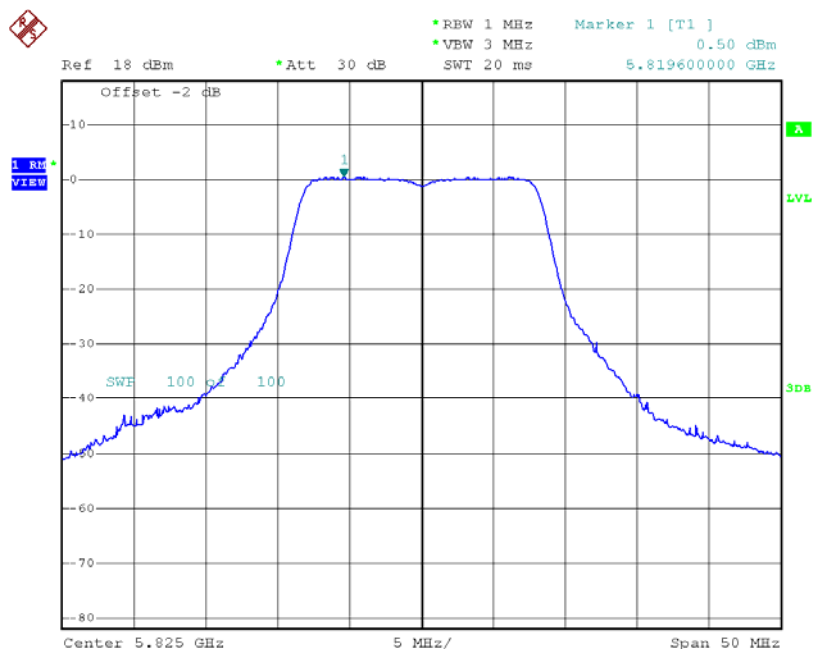
Date: 1.APR.2015 15:37:21

TX CH157



Date: 1.APR.2015 15:43:46

TX CH165

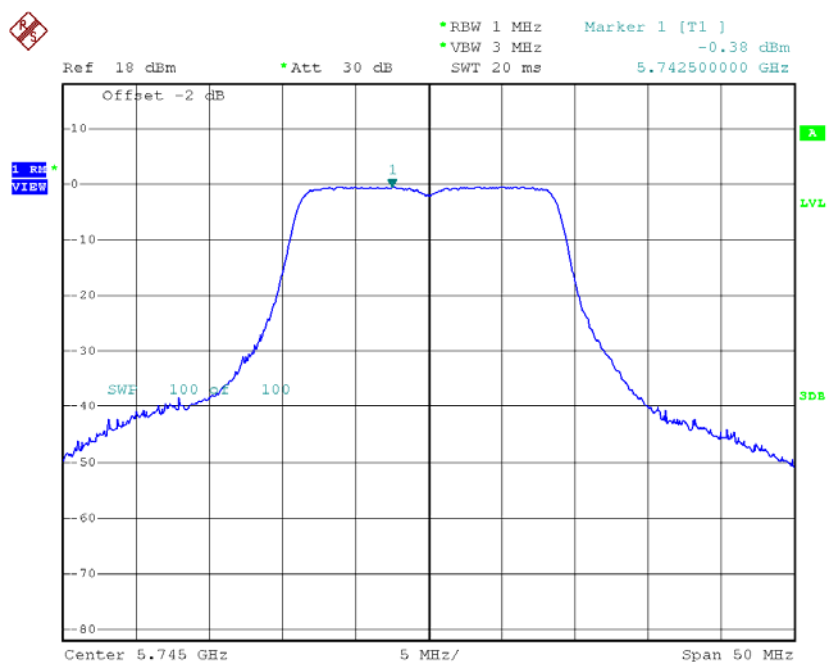


Date: 1.APR.2015 15:47:39

Test Mode: UNII-3/ TX N20 Mode_CH149/CH157/CH165

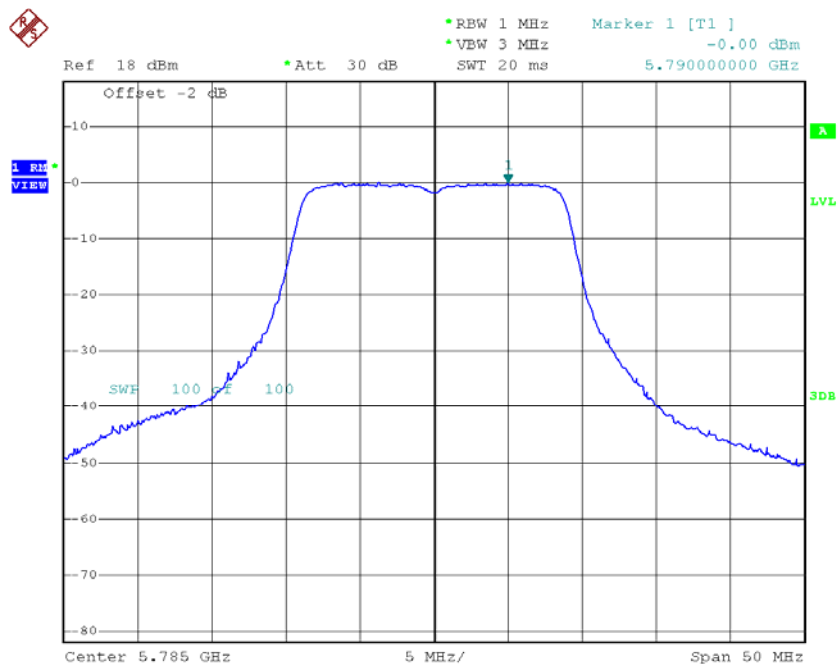
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH149	5745	-0.38	0.10	-0.28	28.00
CH157	5785	0.00	0.10	0.10	28.00
CH165	5825	0.19	0.10	0.29	28.00

TX CH149



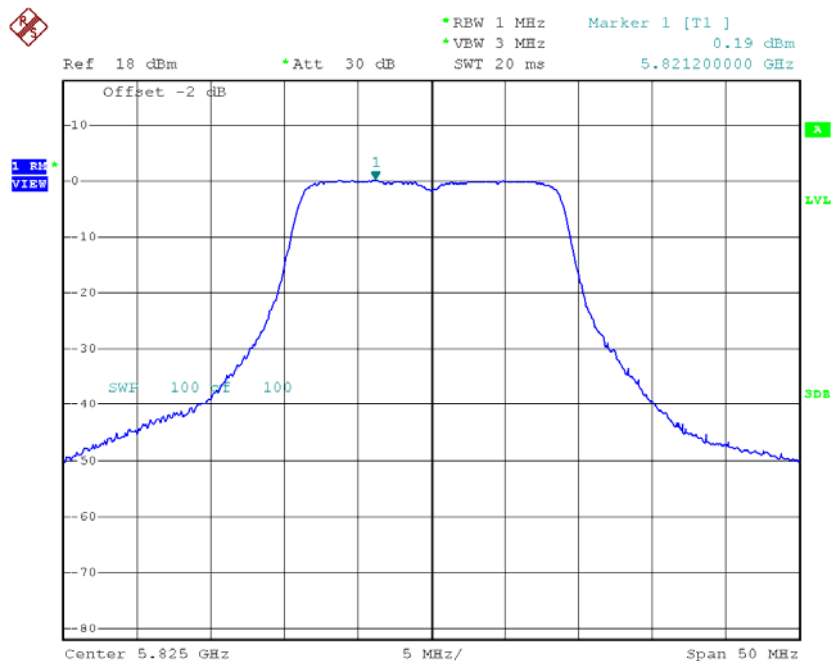
Date: 1.APR.2015 15:56:35

TX CH157



Date: 1.APR.2015 15:57:33

TX CH165

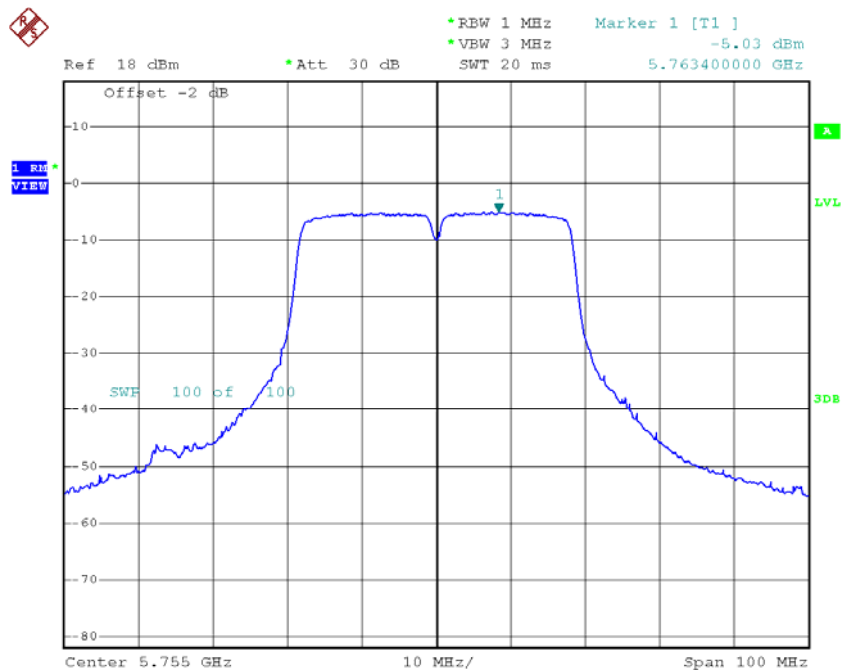


Date: 1.APR.2015 15:58:10

Test Mode: UNII-3/ TX N40 Mode_CH151/CH159

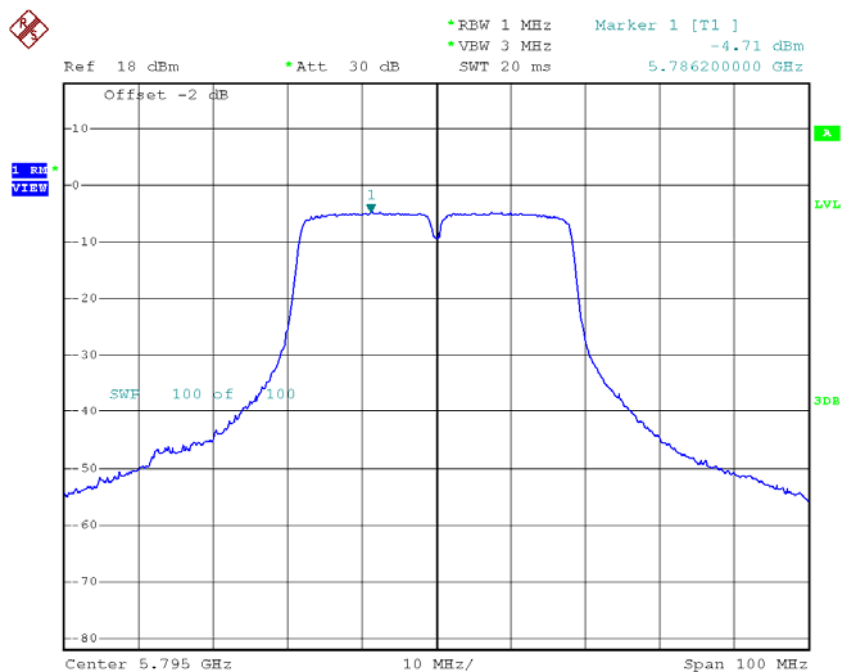
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH151	5755	-5.03	0.13	-4.90	28.00
CH159	5795	-4.71	0.13	-4.58	28.00

TX CH151



Date: 1.APR.2015 19:46:36

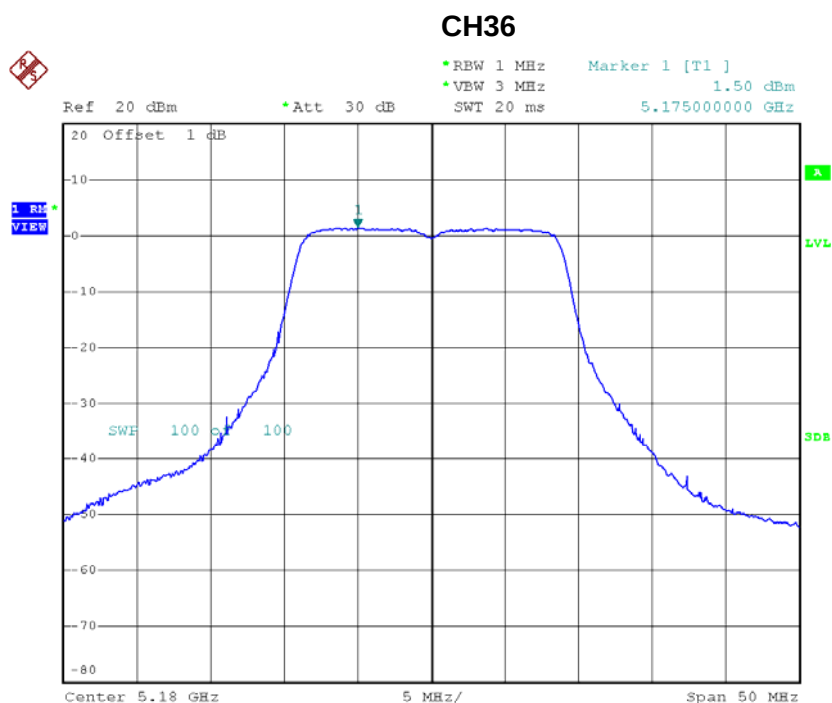
TX CH159



Date: 1.APR.2015 19:47:38

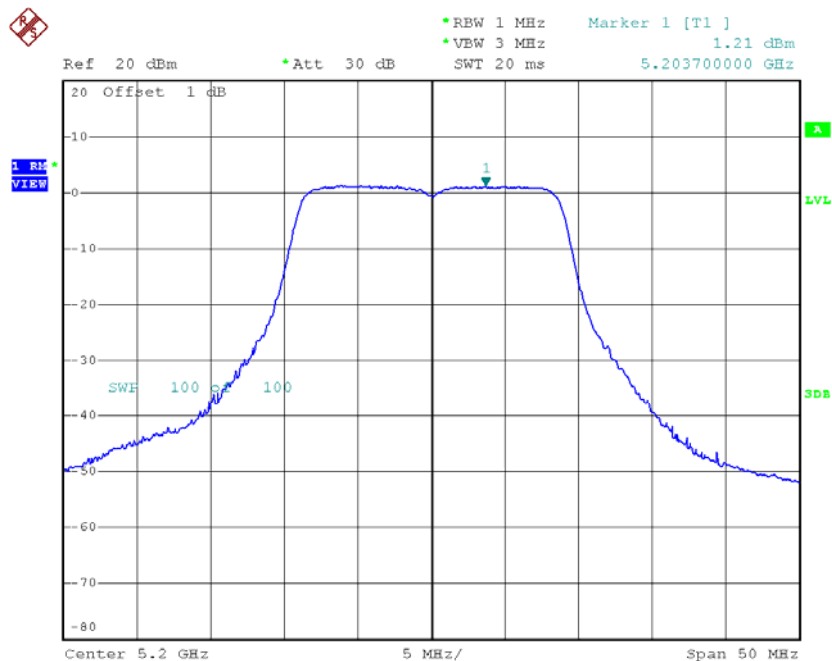
Test Mode: UNII-1/TX AC20 Mode_CH36/CH40/CH48

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	1.50	0.16	1.66	15.00
CH40	5200	1.21	0.16	1.37	15.00
CH48	5240	1.52	0.16	1.68	15.00



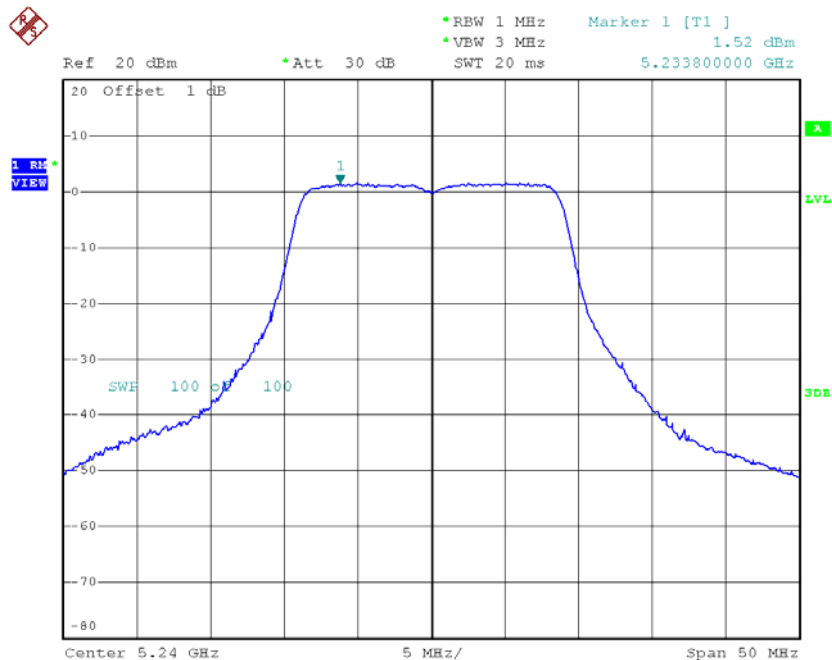
Date: 1.APR.2015 16:00:01

CH40



Date: 1.APR.2015 16:01:06

CH48

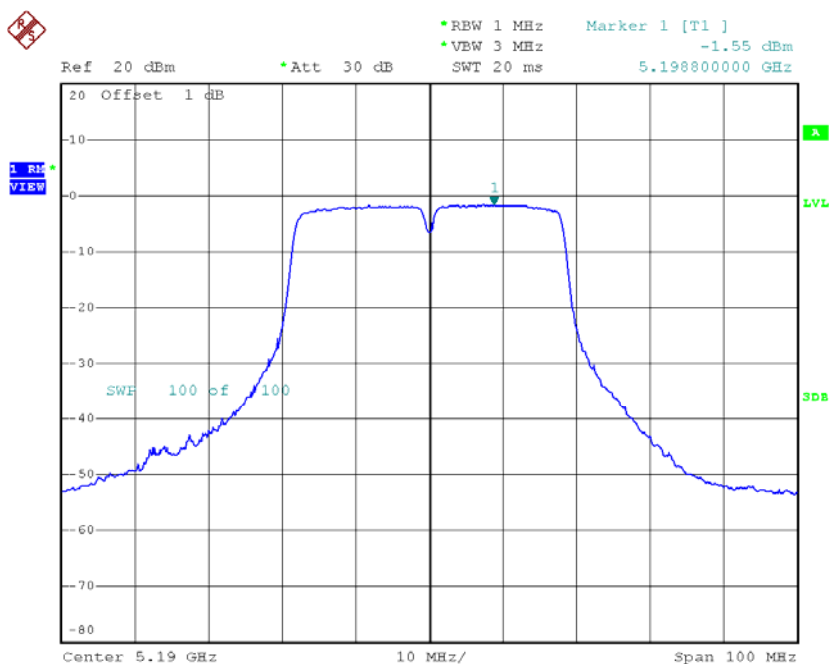


Date: 1.APR.2015 16:02:52

Test Mode: UNII-1/TX AC40 Mode_CH38/CH46

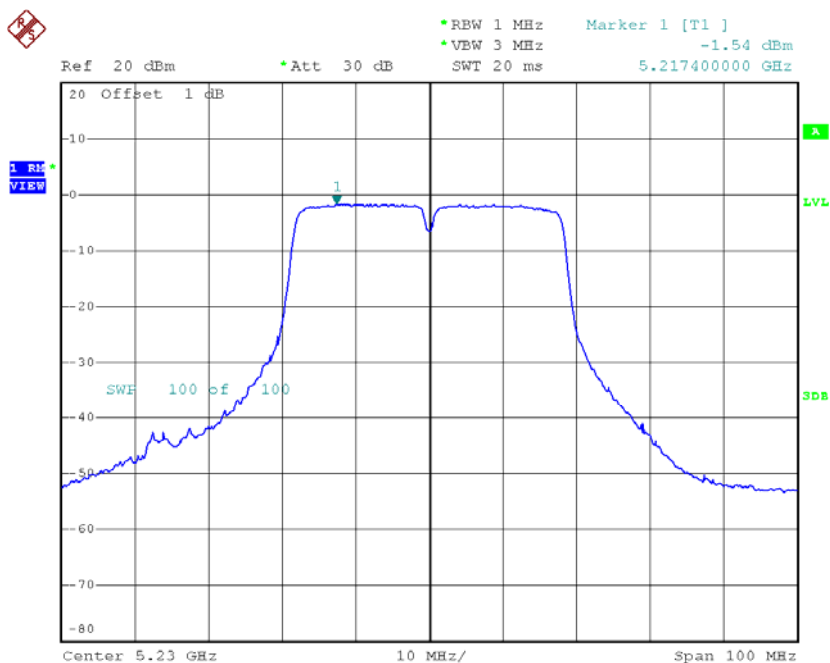
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH38	5190	-1.55	0.19	-1.36	15.00
CH46	5230	-1.54	0.19	-1.35	15.00

CH38



Date: 1.APR.2015 19:48:33

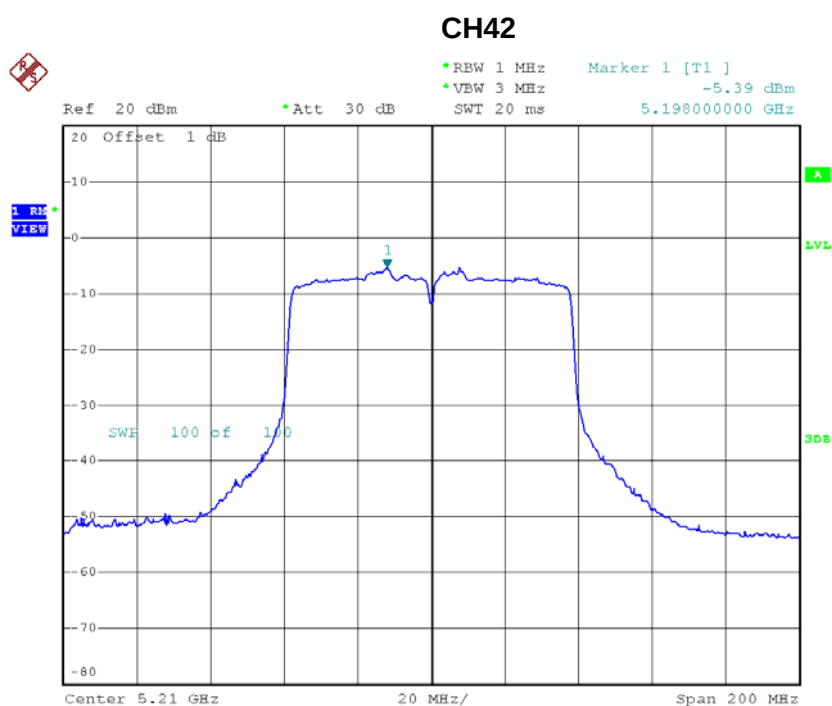
CH46



Date: 1.APR.2015 19:49:30

Test Mode: UNII-1/TX AC80 Mode_CH42

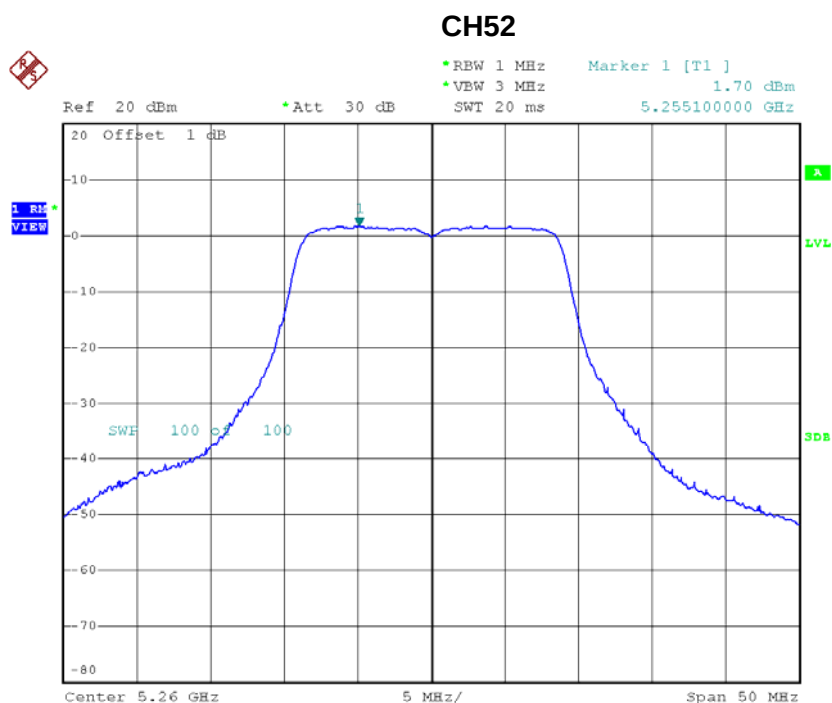
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH42	5210	-5.39	0.48	-4.91	15.00



Date: 1.APR.2015 20:01:42

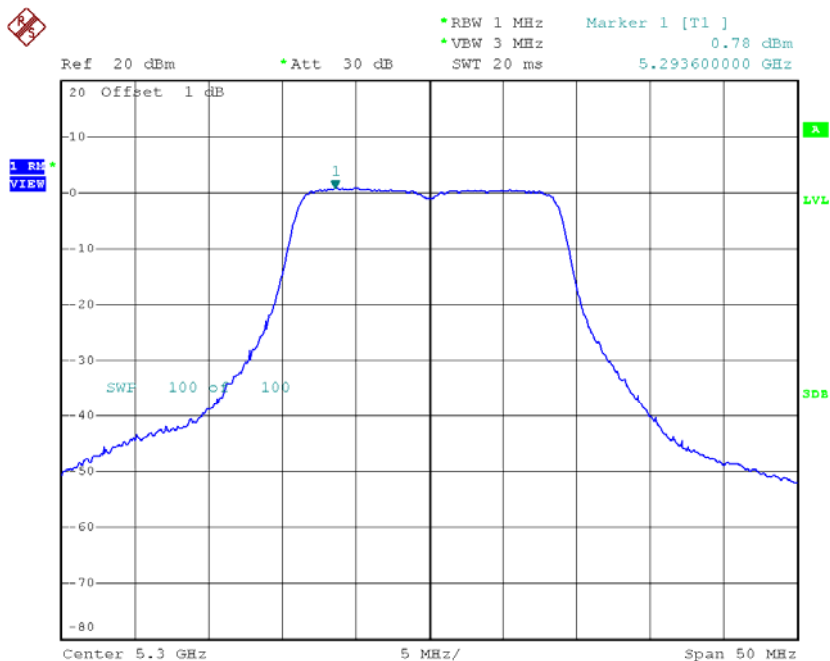
Test Mode: UNII-2A/TX AC20 Mode_CH52/CH60/CH64

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH52	5260	1.70	0.16	1.86	9.00
CH60	5300	0.78	0.16	0.94	9.00
CH64	5320	0.65	0.16	0.81	9.00



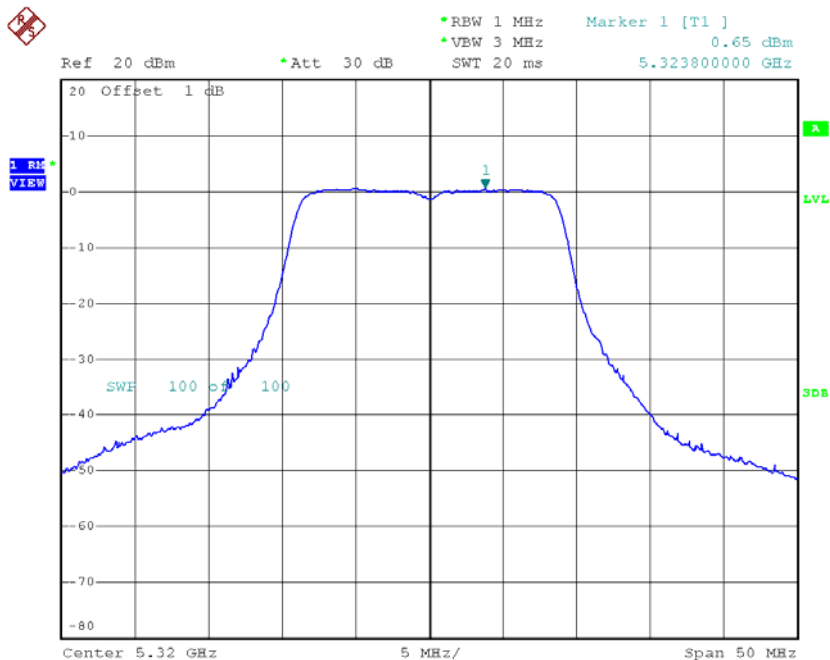
Date: 1.APR.2015 16:04:03

CH60



Date: 1.APR.2015 16:05:01

CH64

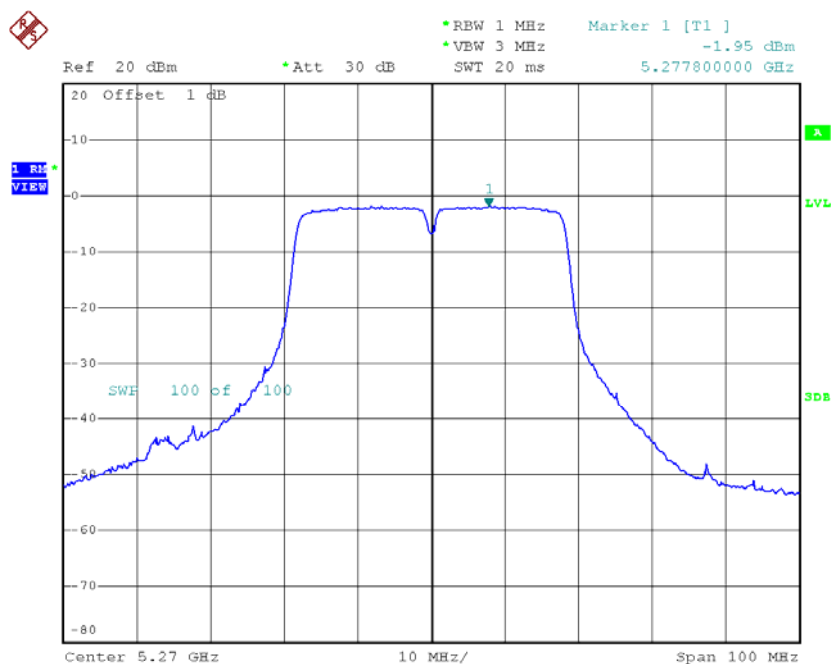


Date: 1.APR.2015 16:06:44

Test Mode: UNII-2A/TX AC40 Mode_CH54/CH62

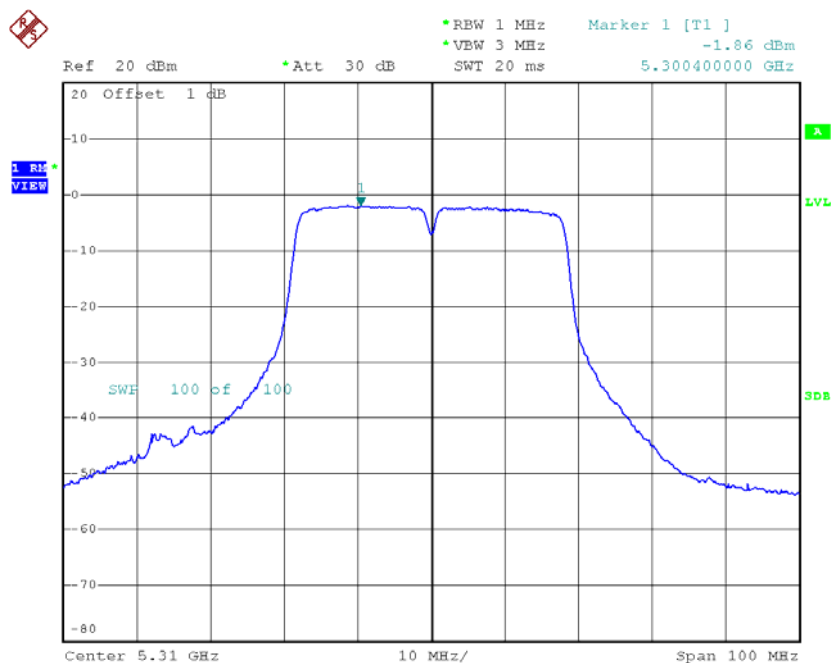
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH54	5270	-1.95	0.19	-1.76	9.00
CH62	5310	-1.86	0.19	-1.67	9.00

CH54



Date: 1.APR.2015 19:50:08

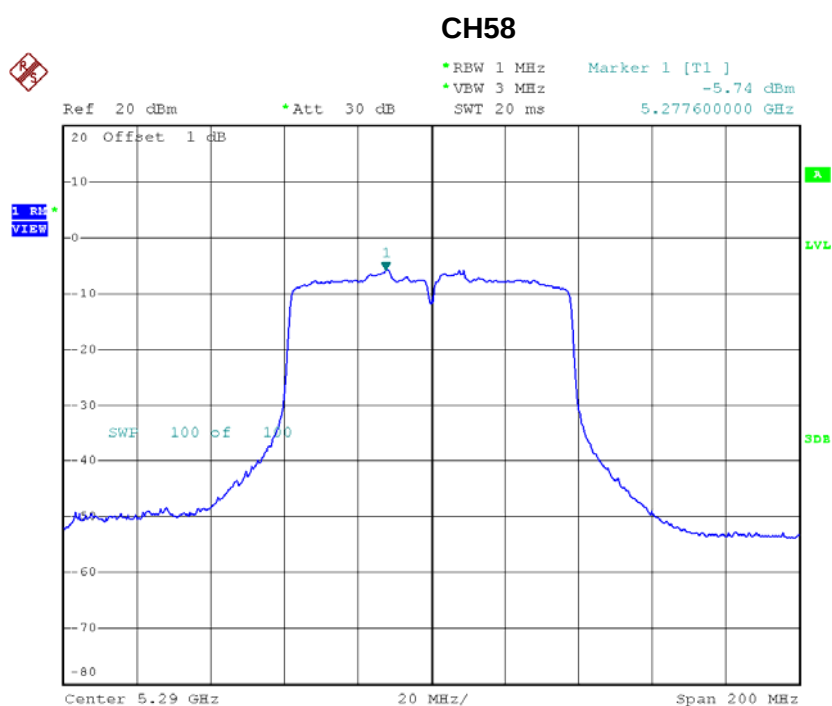
CH62



Date: 1.APR.2015 19:50:57

Test Mode: UNII-2A/TX AC80 Mode_CH58

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH58	5290	-5.74	0.48	-5.26	9.00

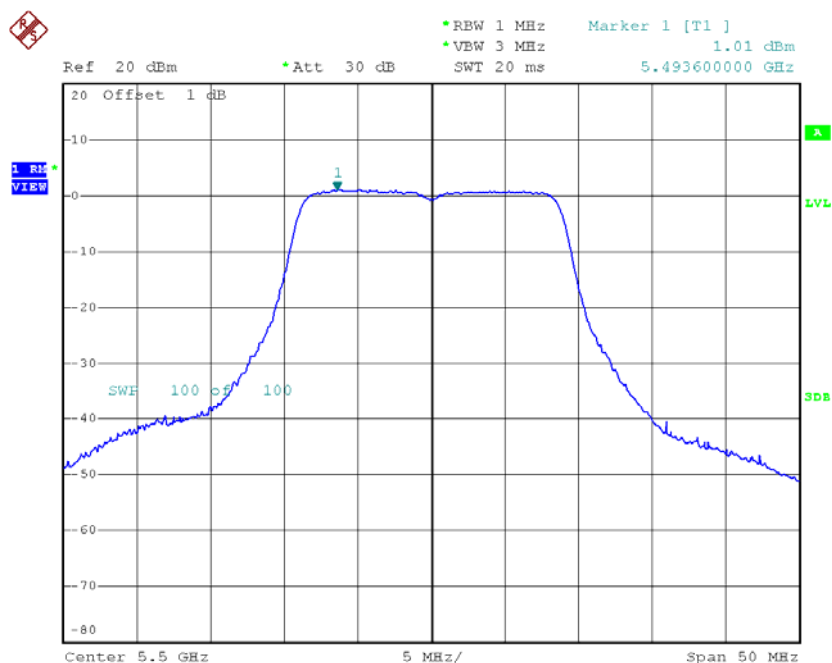


Date: 1.APR.2015 20:02:59

Test Mode: UNII-2C/TX AC20 Mode_CH100/CH116/CH140

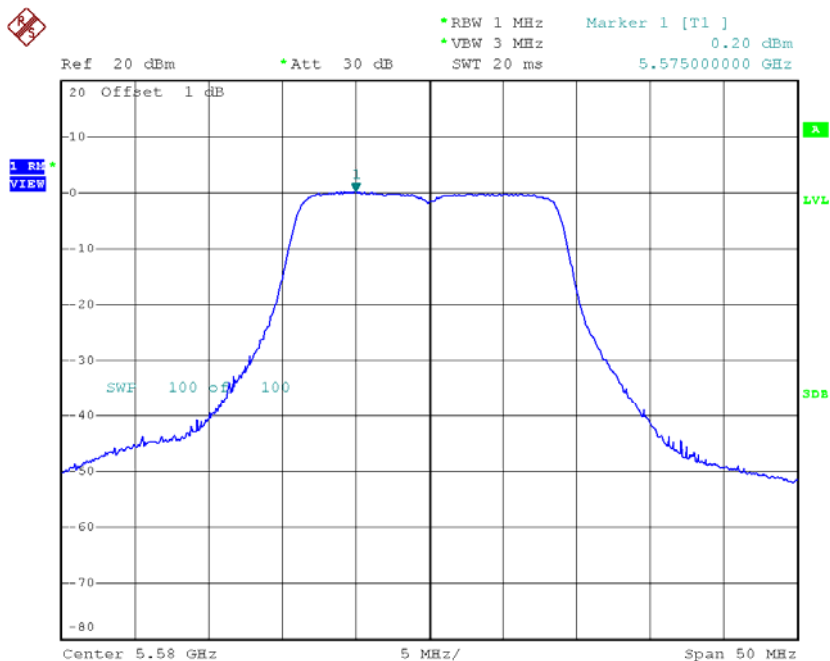
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH100	5500	1.01	0.16	1.17	9.00
CH116	5580	0.20	0.16	0.36	9.00
CH140	5700	0.42	0.16	0.58	9.00

CH100



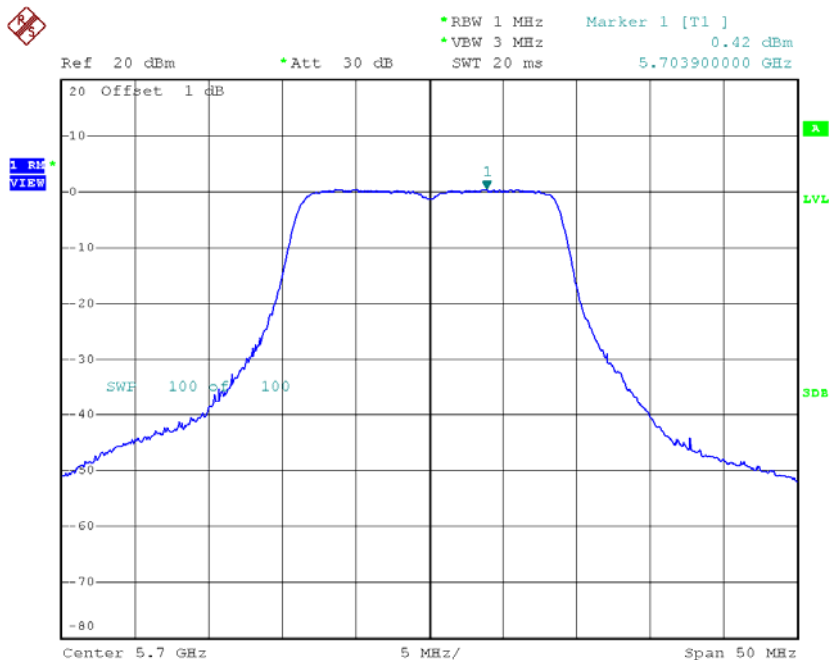
Date: 1.APR.2015 16:08:27

CH116



Date: 1.APR.2015 16:09:27

CH140

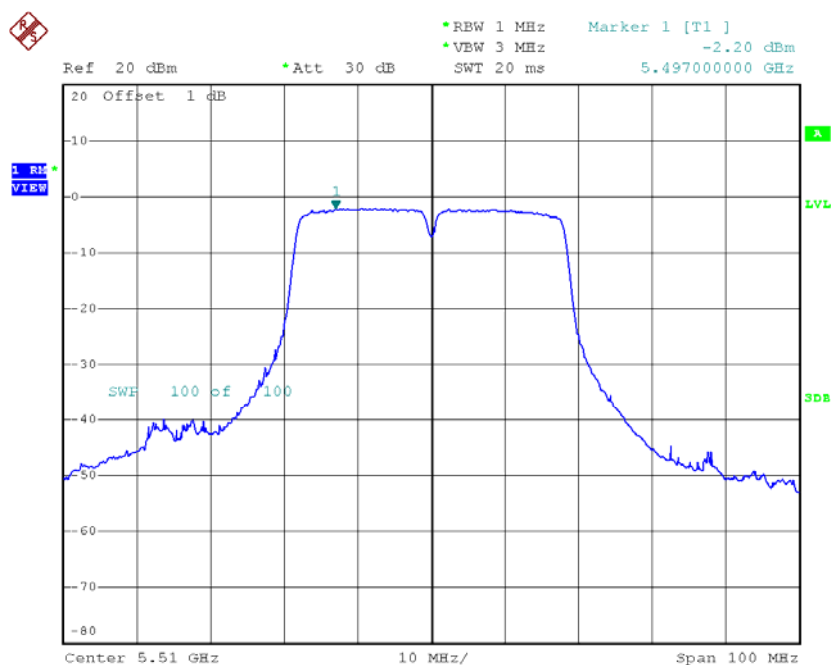


Date: 1.APR.2015 16:09:59

Test Mode: UNII-2C/TX AC40 Mode_CH102/CH110/CH134

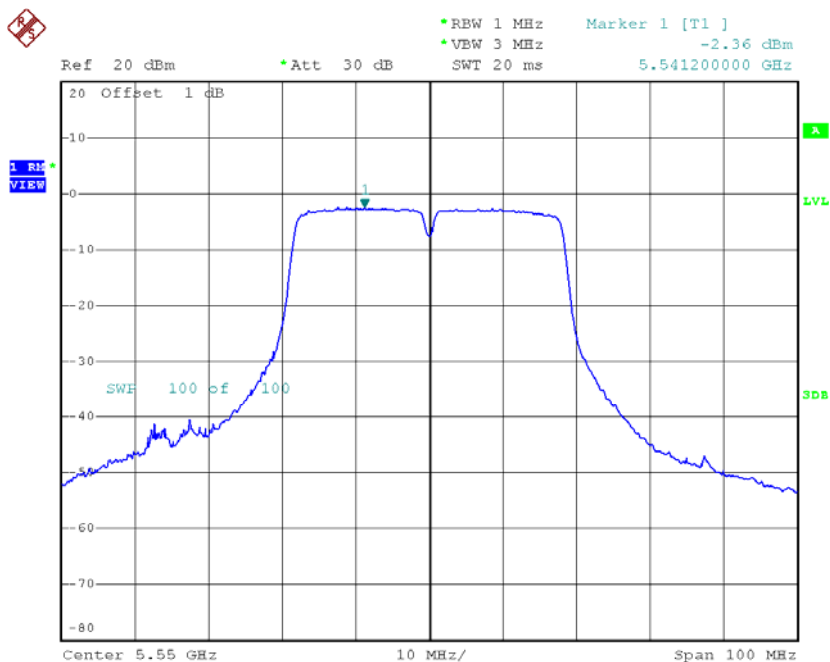
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH102	5510	-2.20	0.19	-2.01	9.00
CH110	5550	-2.36	0.19	-2.17	9.00
CH134	5670	-4.17	0.19	-3.98	9.00

CH102



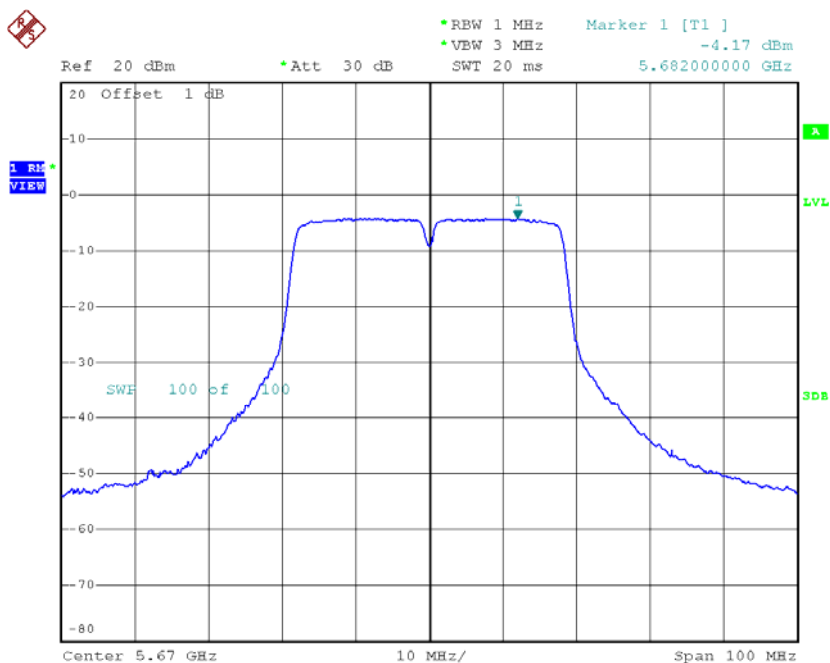
Date: 1.APR.2015 19:51:37

CH110



Date: 1.APR.2015 19:57:29

CH134

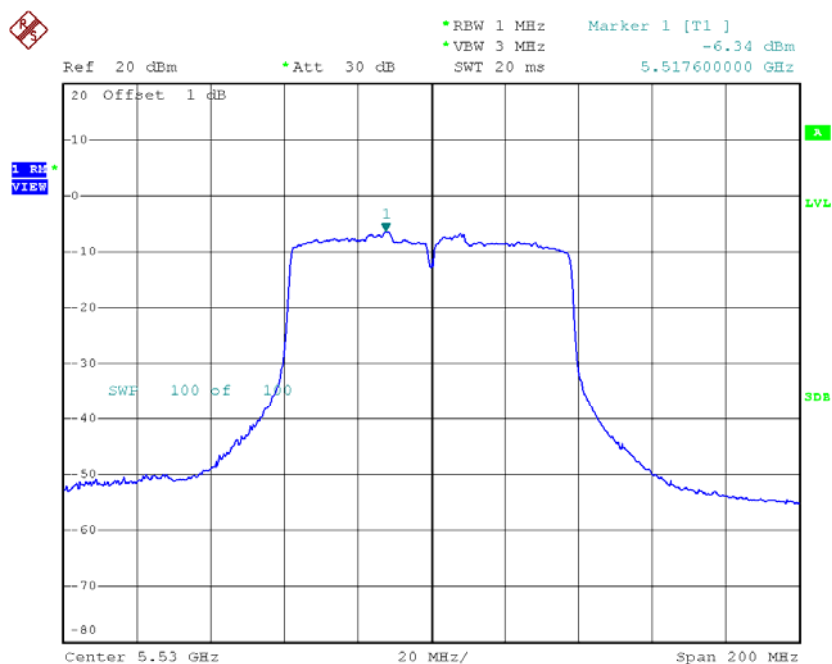


Date: 1.APR.2015 19:58:03

Test Mode: UNII-2C/TX AC80 Mode_CH106/CH122

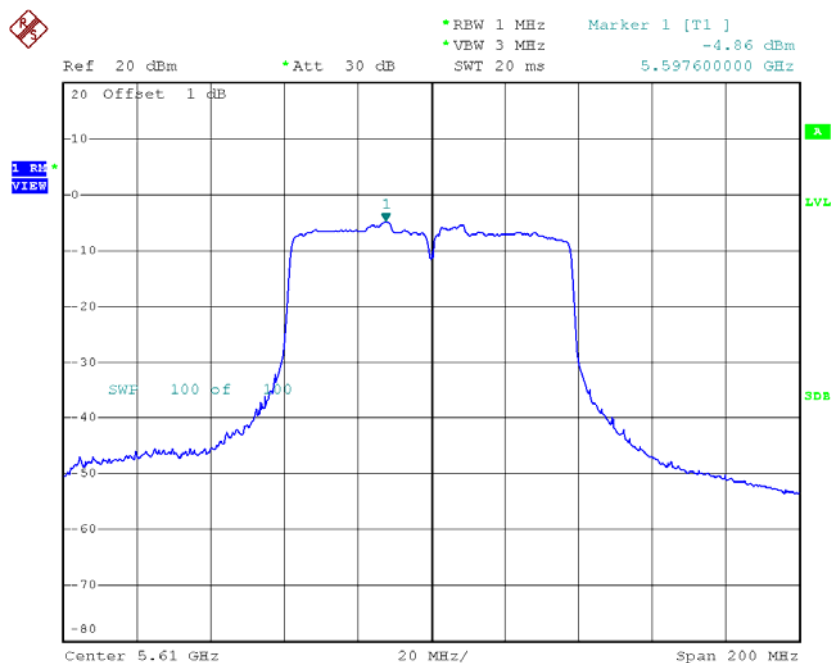
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH106	5530	-6.34	0.48	-5.86	9.00
CH122	5610	-4.86	0.48	-4.38	9.00

CH106



Date: 1.APR.2015 20:04:13

CH122

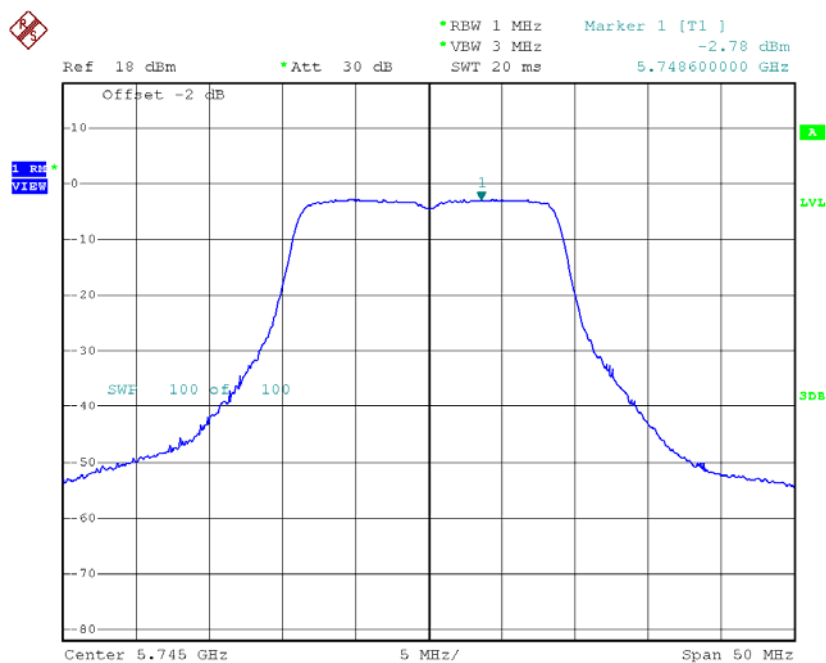


Date: 1.APR.2015 20:05:22

Test Mode: UNII-3/ TX AC20 Mode_CH149/CH157/CH165

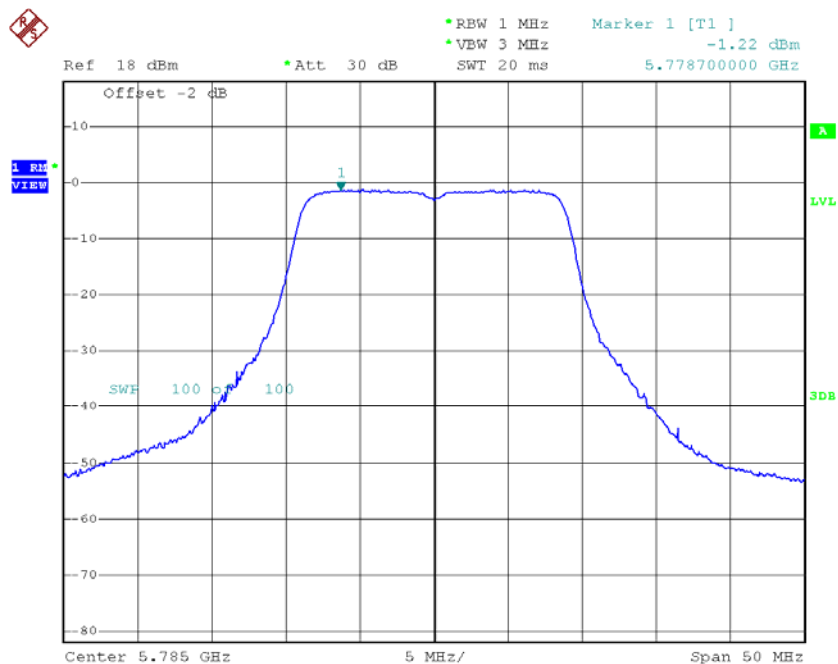
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH149	5745	-2.78	0.16	-2.62	28.00
CH157	5785	-1.22	0.16	-1.06	28.00
CH165	5825	-0.96	0.16	-0.80	28.00

TX CH149



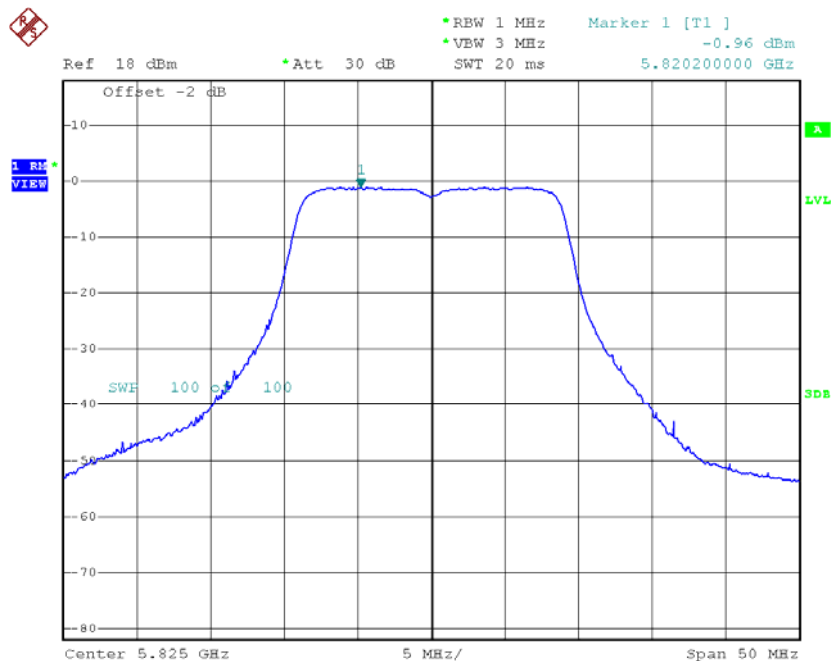
Date: 1.APR.2015 16:10:48

TX CH157



Date: 1.APR.2015 19:37:08

TX CH165



Date: 1.APR.2015 19:37:59