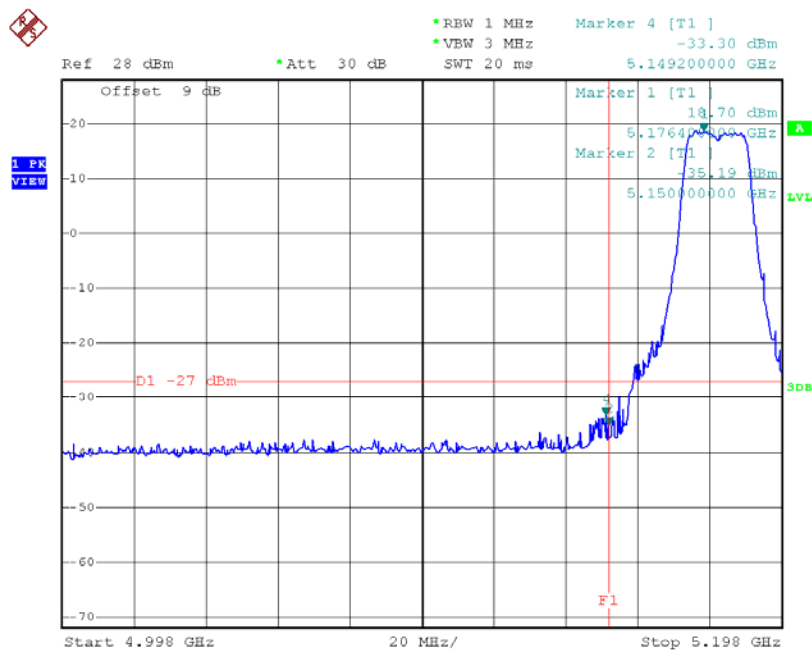


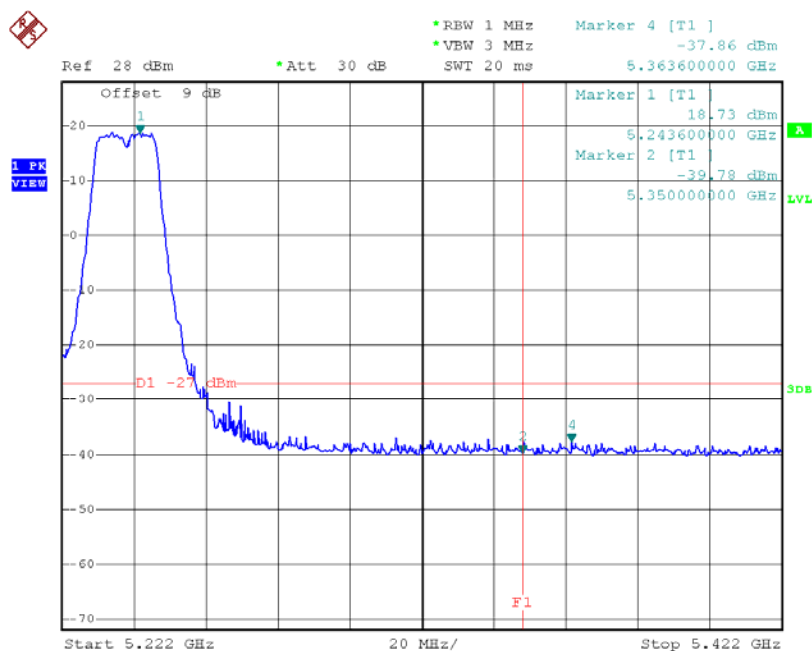
Test Mode:	UNII-1/TX AC20 Mode
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TX mode CH36



Date: 3.APR.2015 15:57:36

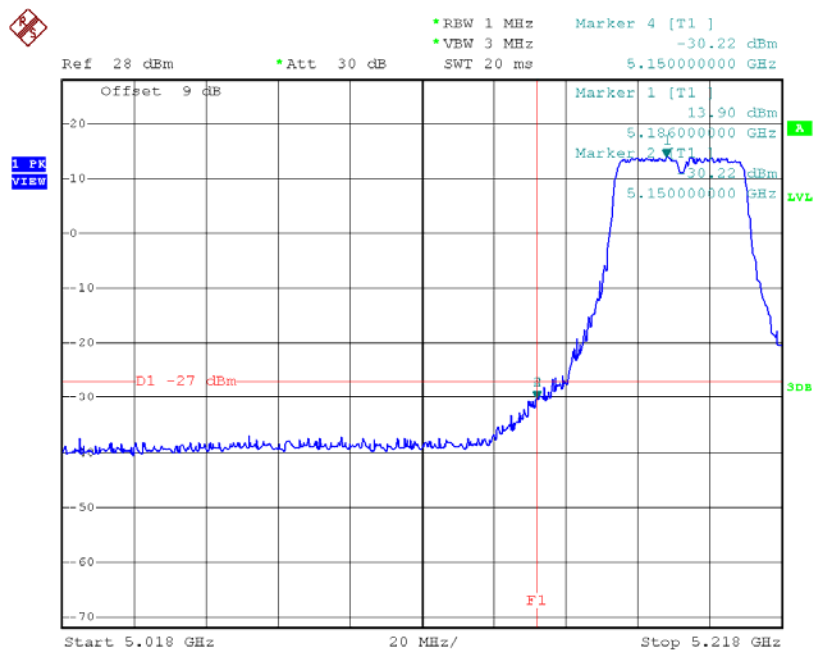
TX mode CH48



Date: 3.APR.2015 15:59:15

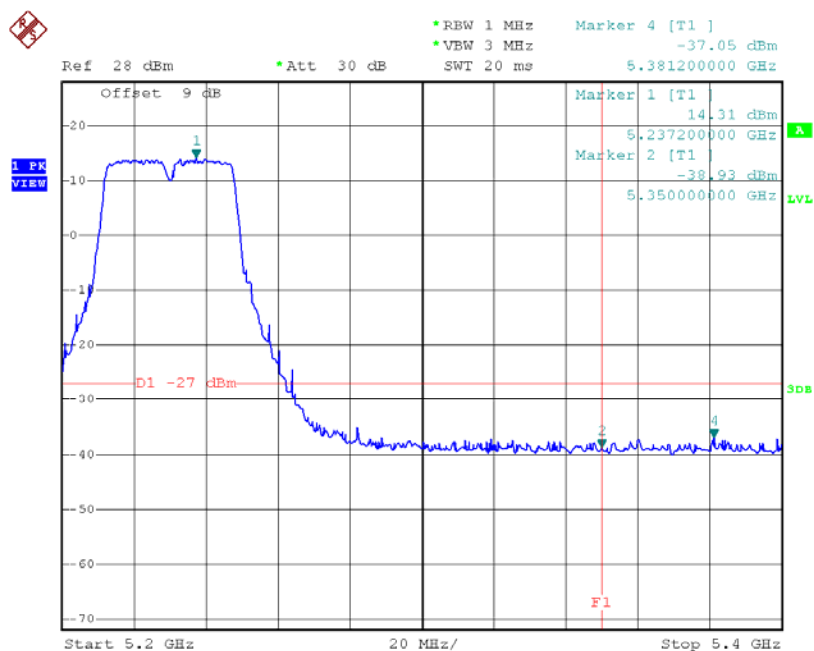
Test Mode:	UNII-1/TX AC40 Mode
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TX mode CH38



Date: 3.APR.2015 16:27:52

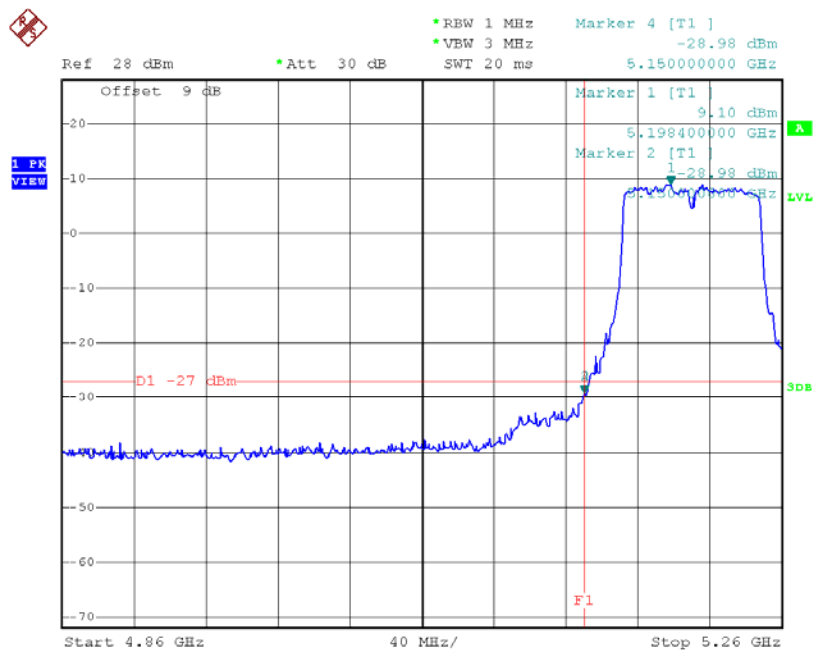
TX mode CH46



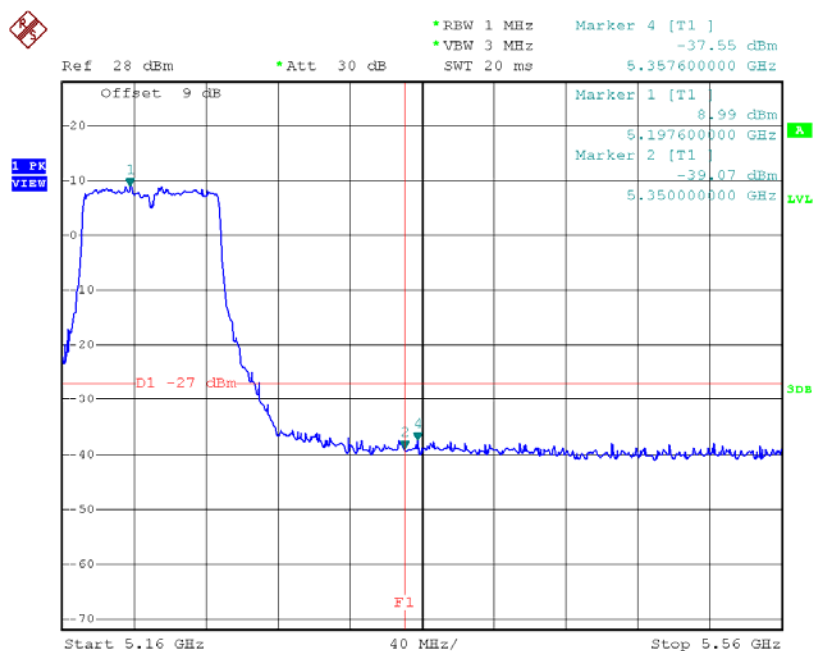
Date: 3.APR.2015 16:28:46

Test Mode: UNII-1/TX AC80 Mode

TX mode CH42



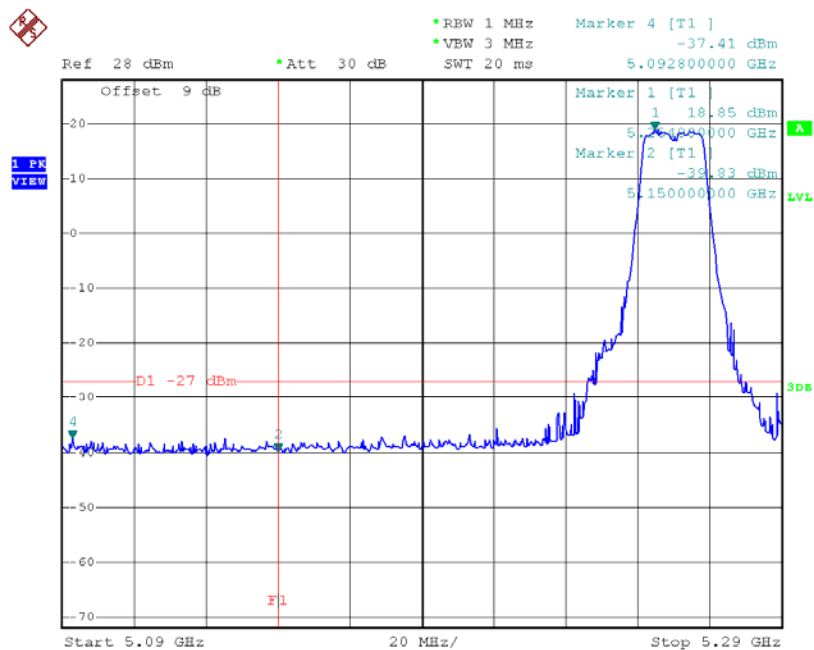
Date: 3.APR.2015 16:35:13



Date: 3.APR.2015 16:35:20

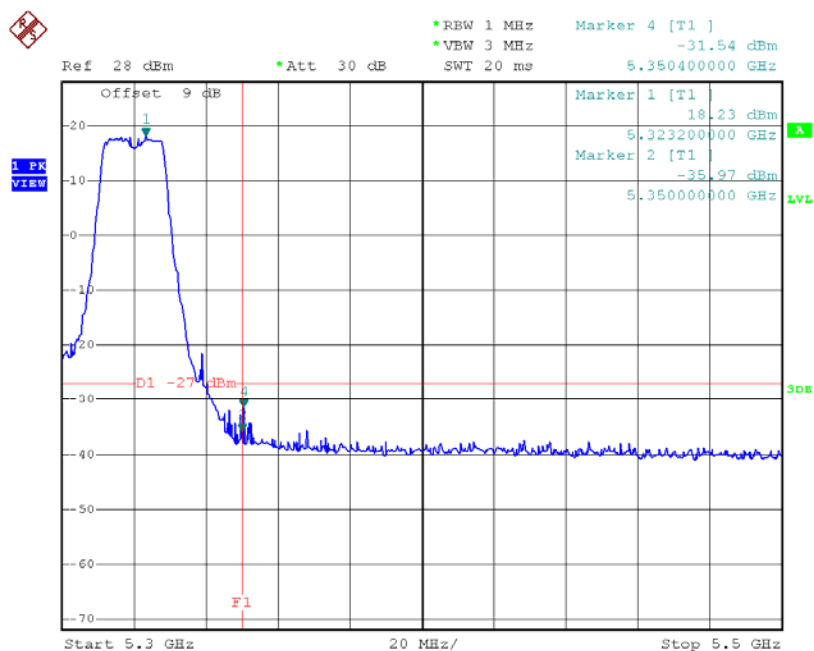
Test Mode:	UNII-2A/TX AC20 Mode
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TX mode CH52



Date: 3.APR.2015 16:00:11

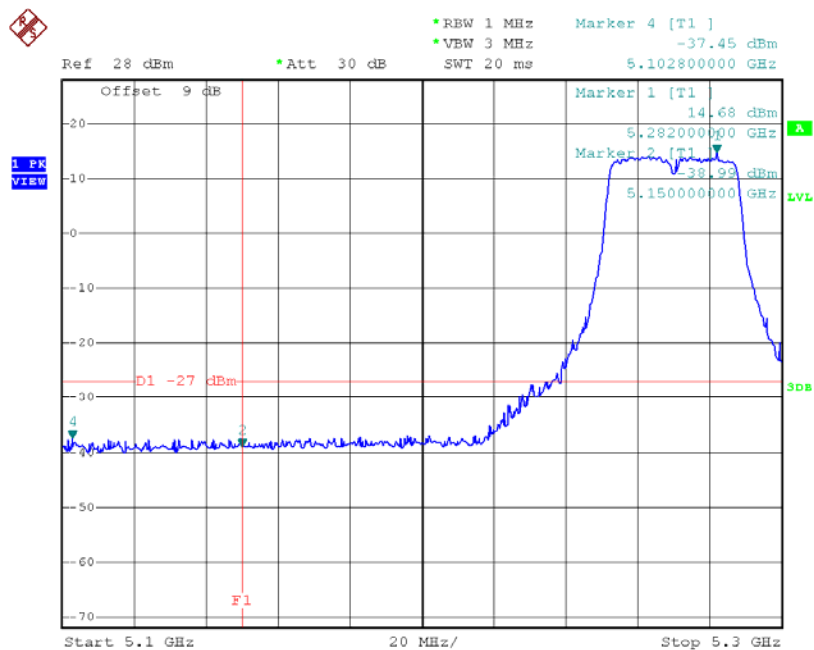
TX mode CH64



Date: 3.APR.2015 16:01:20

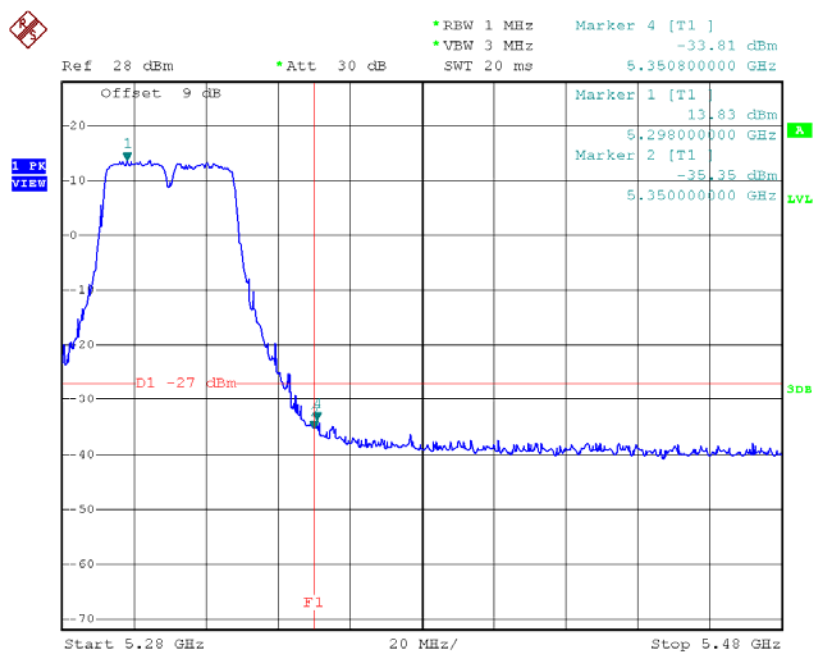
Test Mode: UNII-2A/TX AC40 Mode

TX mode CH54



Date: 3.APR.2015 16:29:36

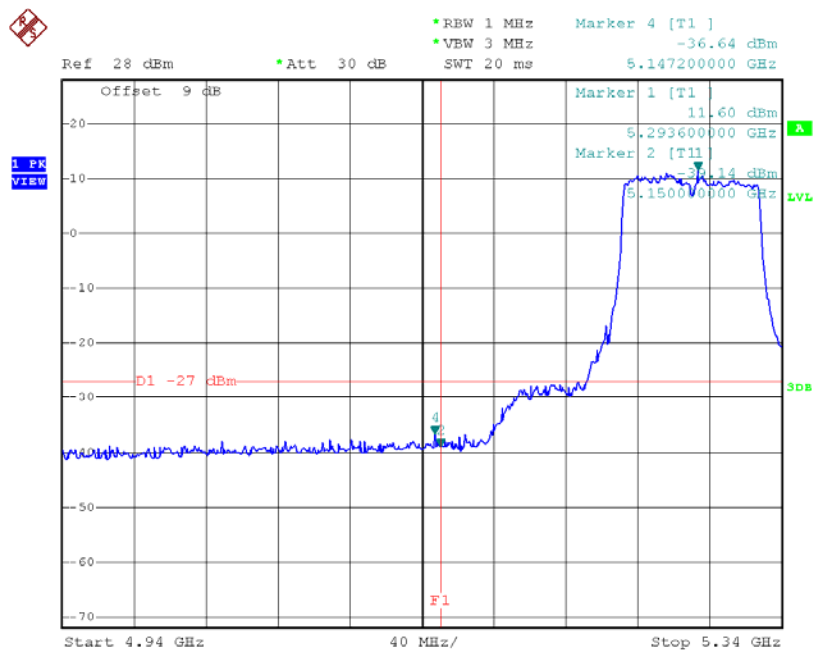
TX mode CH62



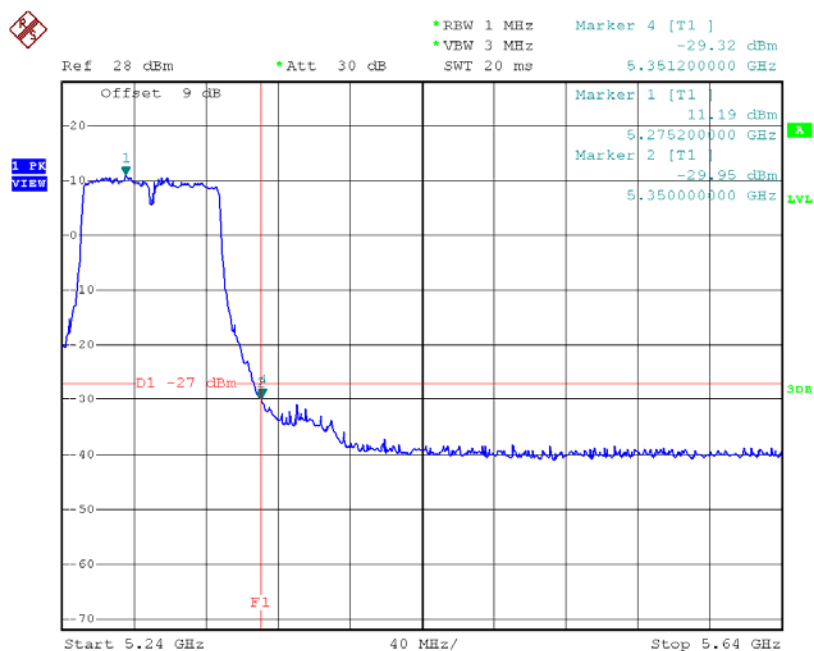
Date: 3.APR.2015 16:30:18

Test Mode: UNII-2A/TX AC80 Mode

TX mode CH58



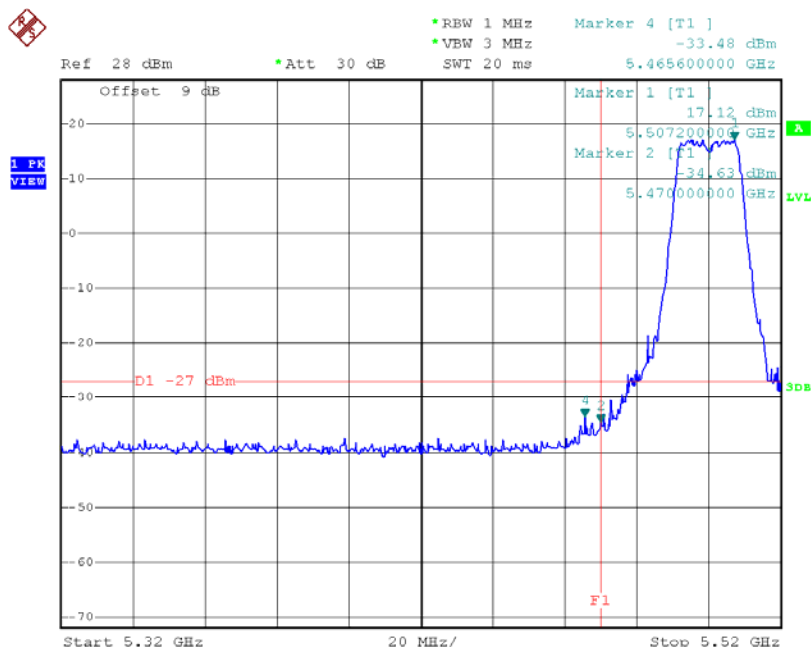
Date: 3.APR.2015 16:36:14



Date: 3.APR.2015 16:39:43

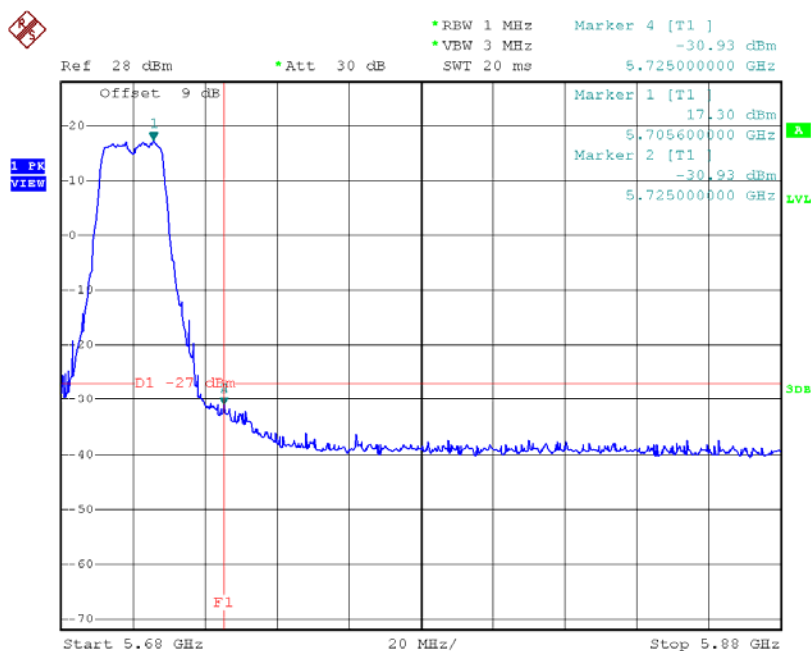
Test Mode:	UNII-2C/TX AC20 Mode
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TX mode CH100



Date: 3.APR.2015 16:02:15

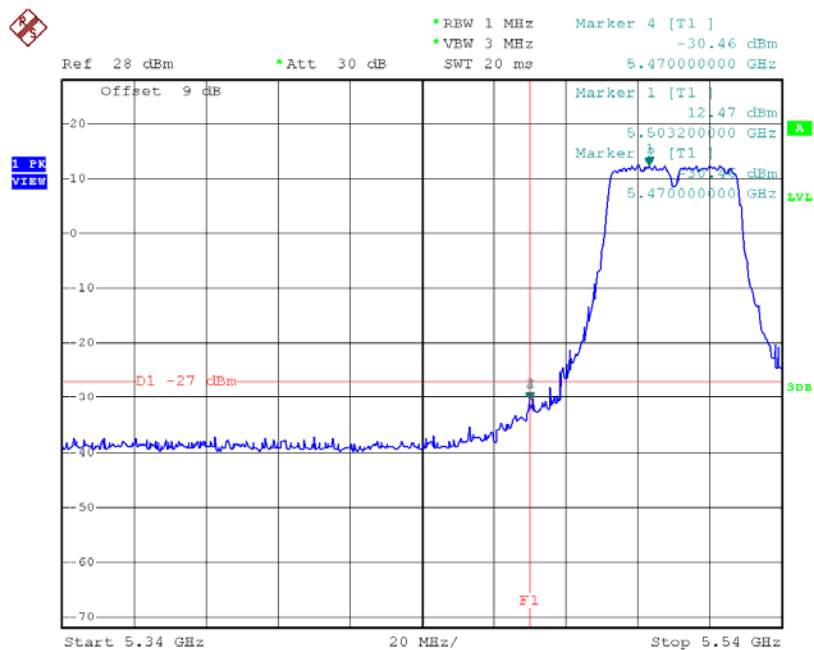
TX mode CH140



Date: 3.APR.2015 16:03:29

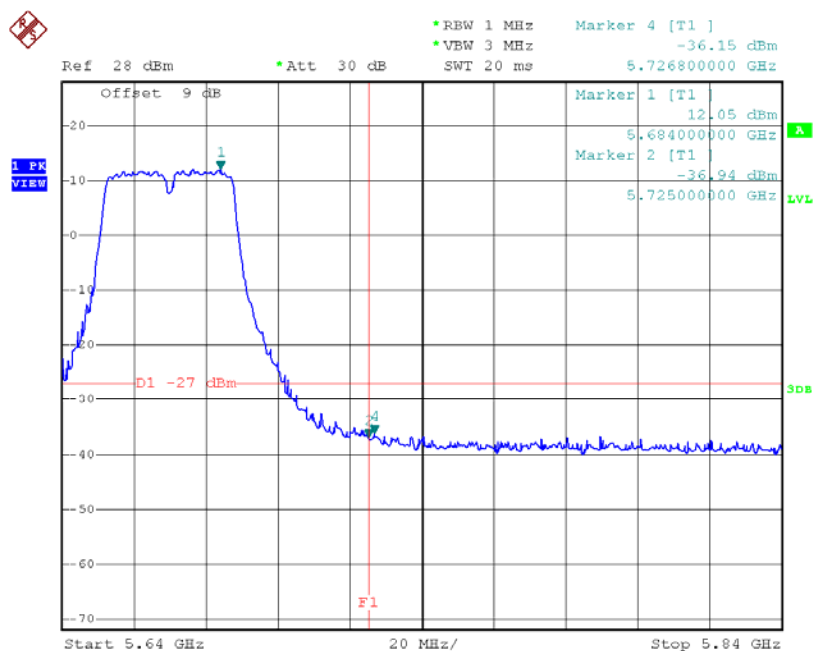
Test Mode:	UNII-2C/TX AC40 Mode
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TX mode CH102



Date: 3.APR.2015 16:30:56

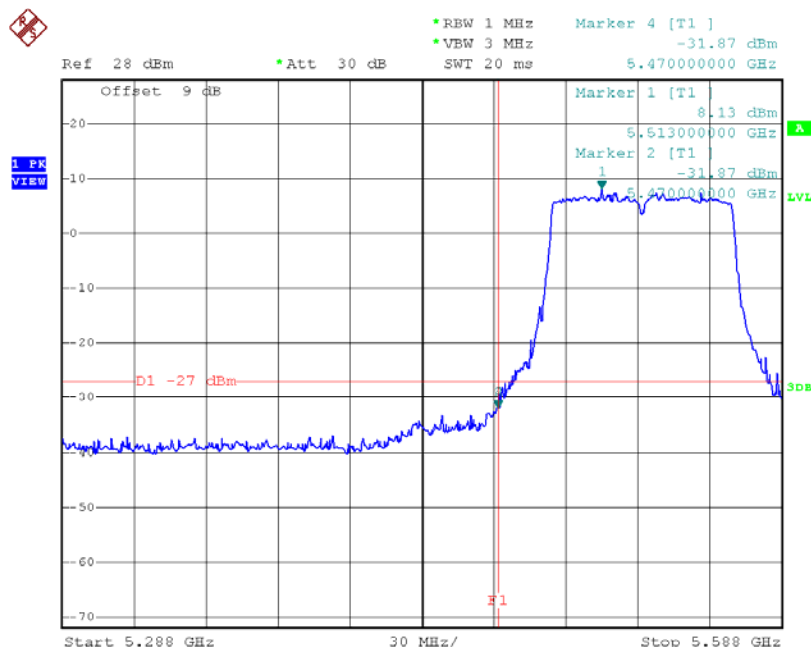
TX mode CH134



Date: 3.APR.2015 16:32:20

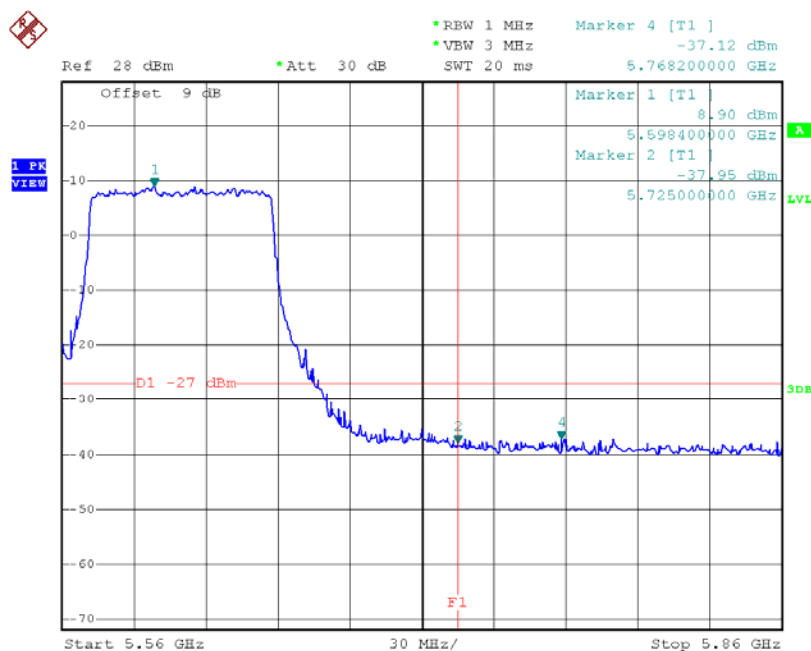
Test Mode:	UNII-2C/TX AC80 Mode
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TX mode CH106



Date: 3.APR.2015 16:42:20

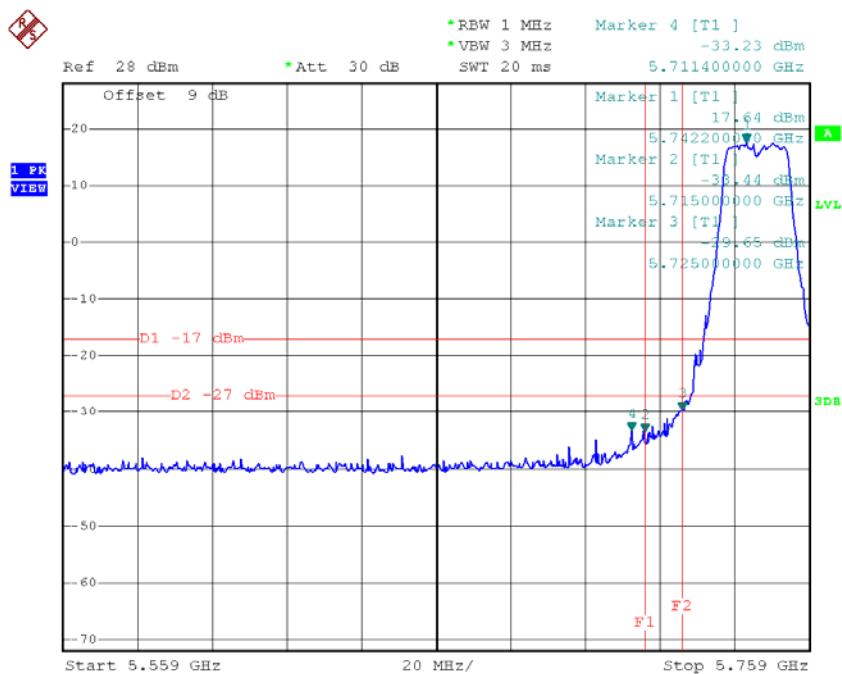
TX mode CH122



Date: 3.APR.2015 16:43:19

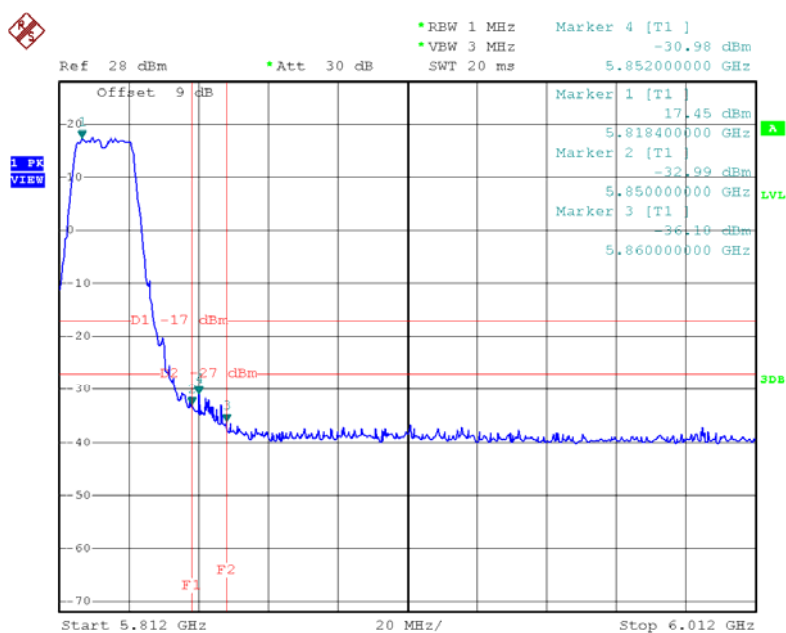
Test Mode: UNII-3/TX AC20 Mode

TX AC HT20 mode CH149



Date: 3.APR.2015 16:04:26

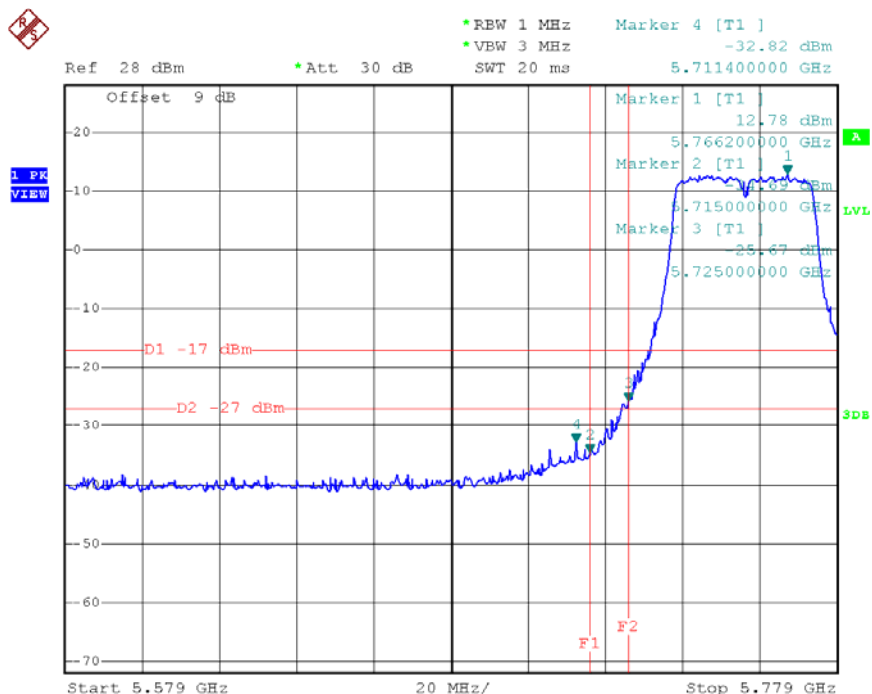
TX AC HT20 mode CH165



Date: 3.APR.2015 16:05:37

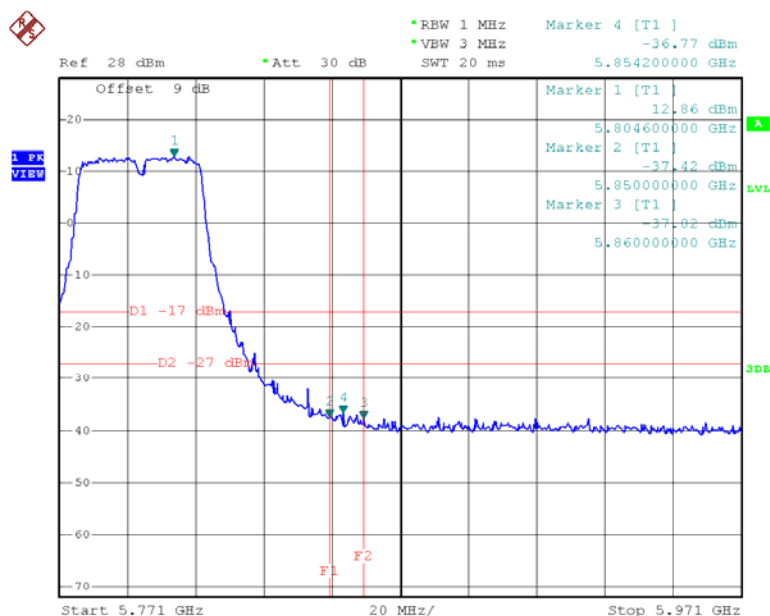
Test Mode: UNII-3/TX AC40 Mode

TX AC HT40 mode CH151



Date: 3.APR.2015 16:33:12

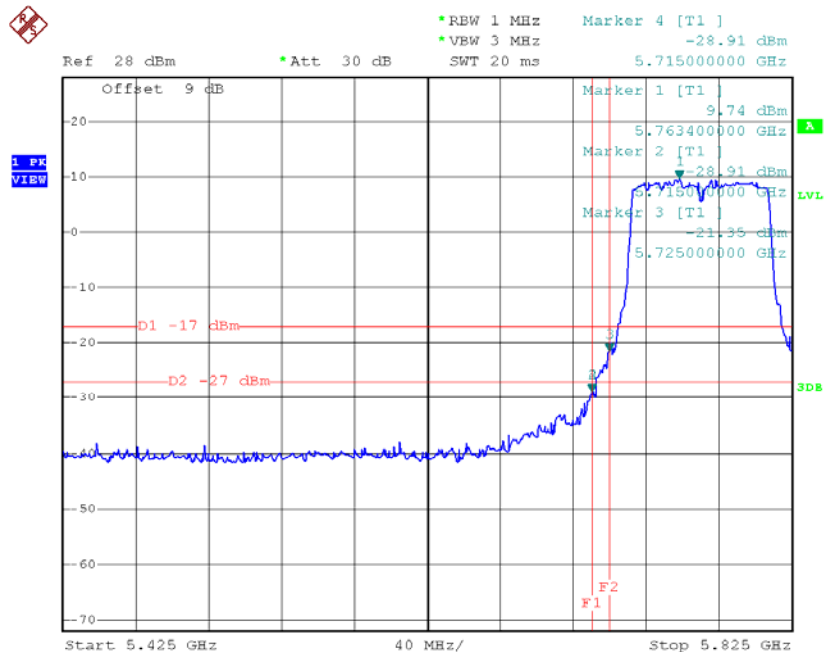
TX AC HT40 mode CH159



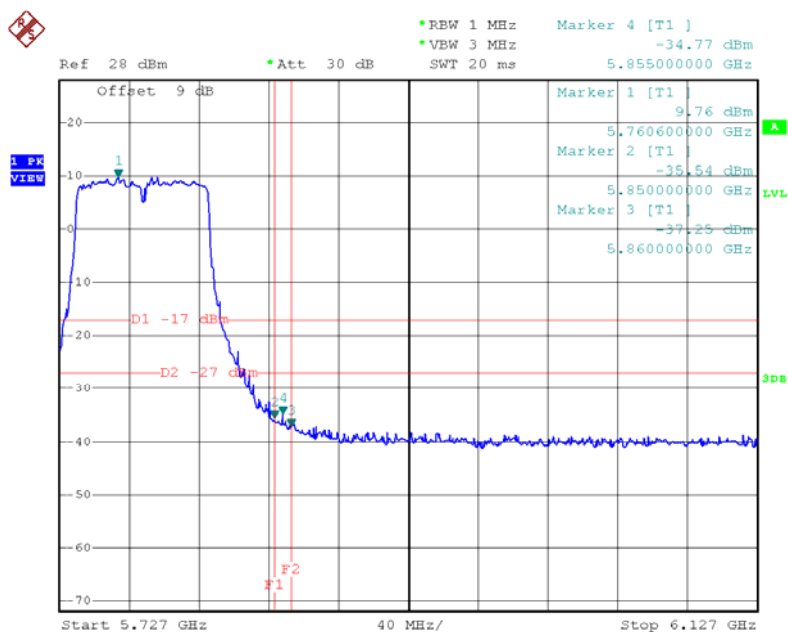
Date: 3.APR.2015 16:33:53

Test Mode: UNII-3/TX AC80 Mode

TX AC HT80 mode CH155



Date: 3.APR.2015 16:44:18

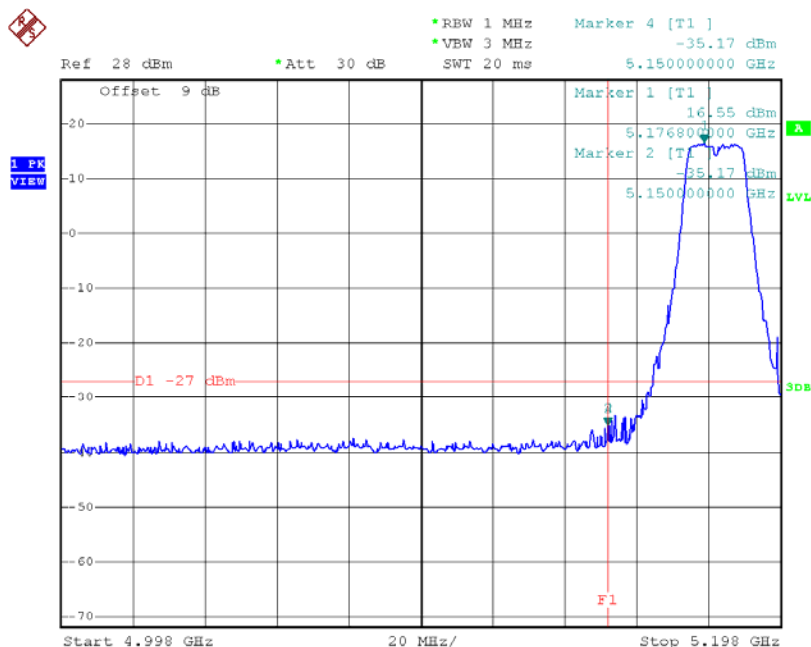


Date: 3.APR.2015 16:44:25

For 2TX

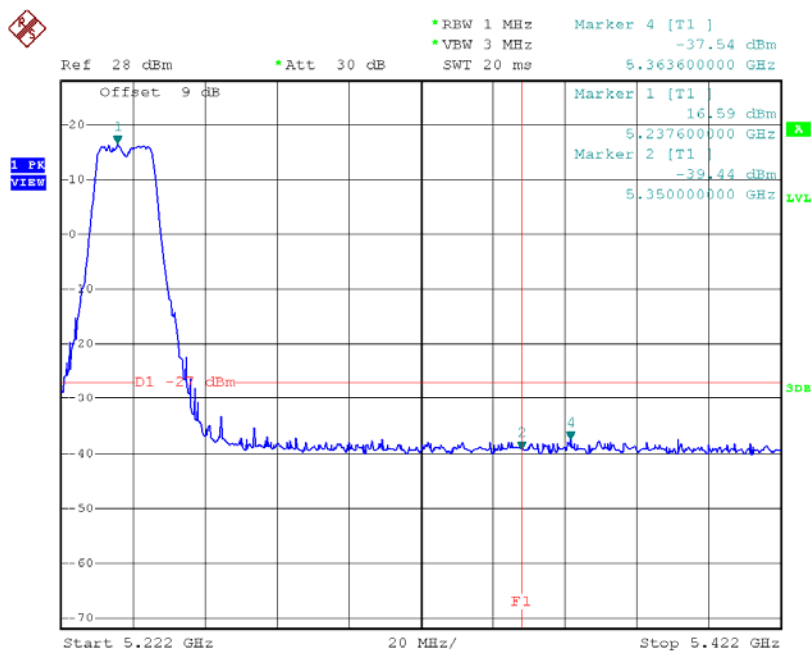
Test Mode: UNII-1/TX A Mode_ANT 1

TX mode CH36



Date: 3.APR.2015 17:37:21

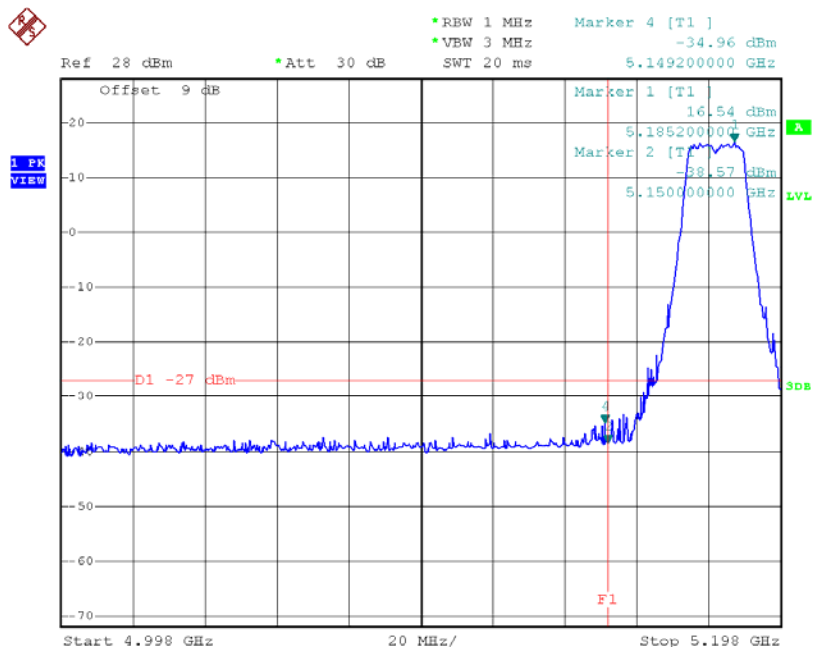
TX modeCH48



Date: 3.APR.2015 17:39:01

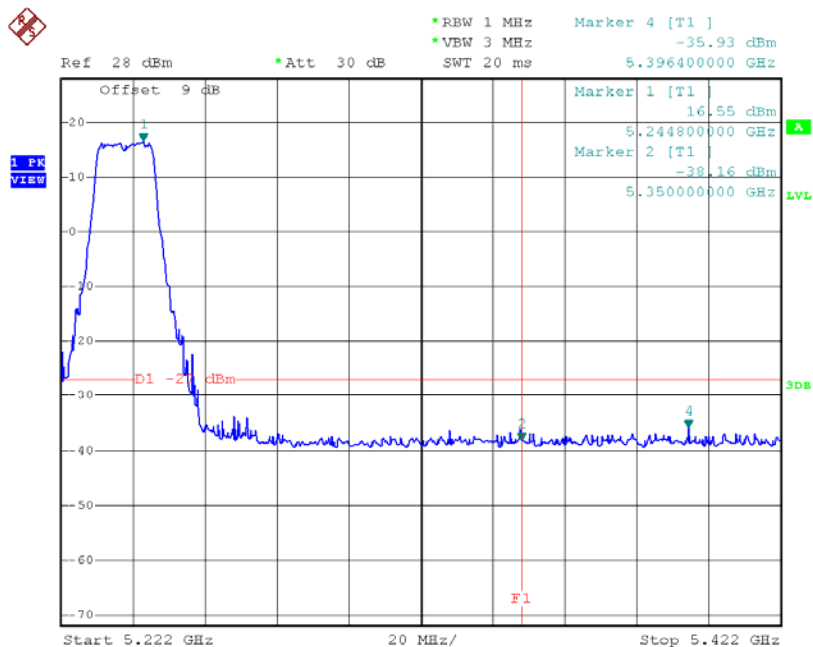
Test Mode: UNII-1/TX A Mode_ANT 2

TX mode CH36



Date: 3.APR.2015 16:54:43

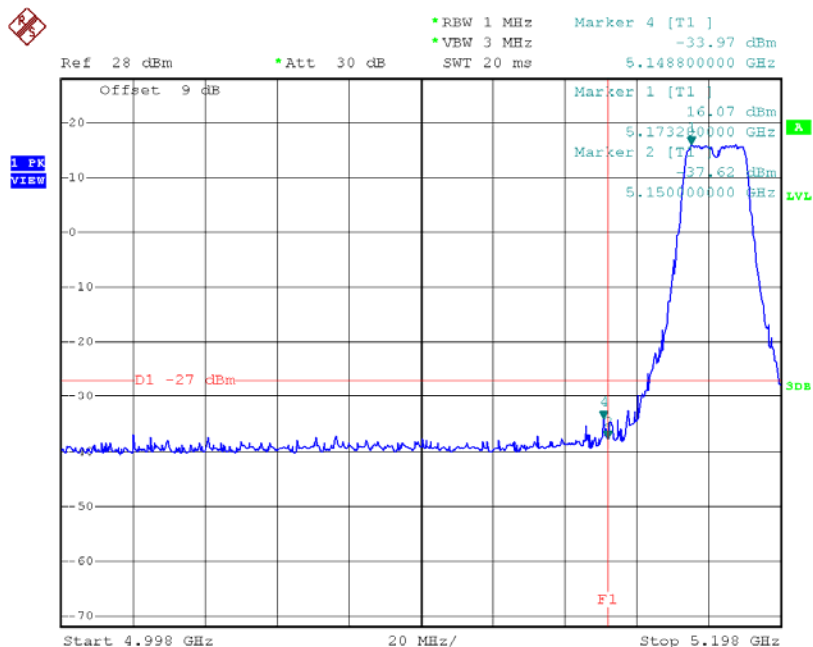
TX modeCH48



Date: 3.APR.2015 16:59:26

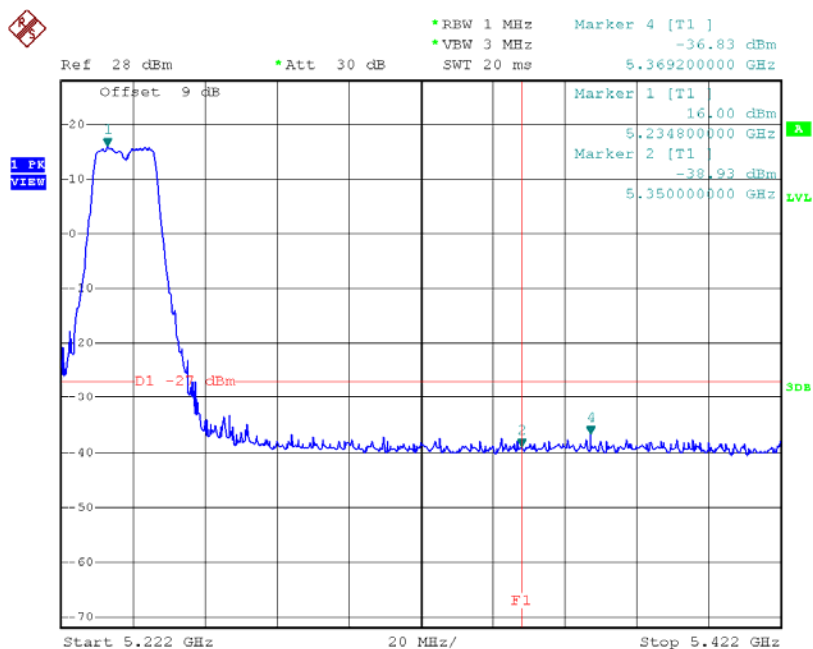
Test Mode: UNII-1/TX N20 Mode_ANT 1

TX mode CH36



Date: 3.APR.2015 17:48:47

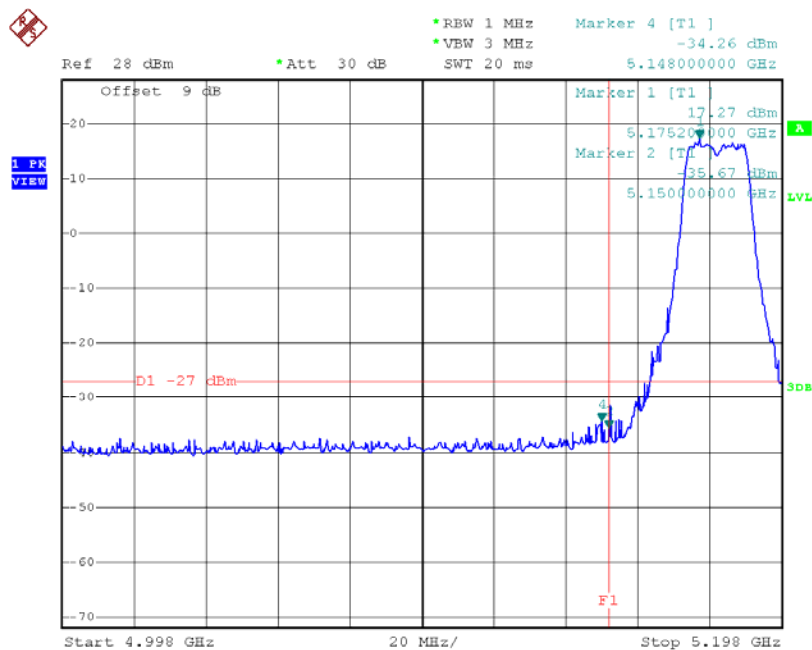
TX modeCH48



Date: 3.APR.2015 17:49:30

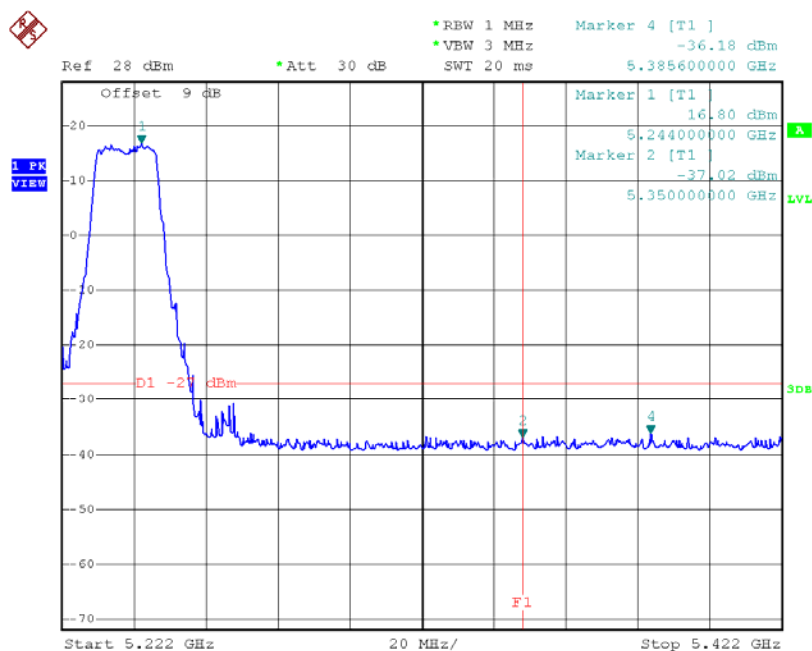
Test Mode:	UNII-1/TX N20 Mode_ANT 2
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TX mode CH36



Date: 3.APR.2015 17:07:03

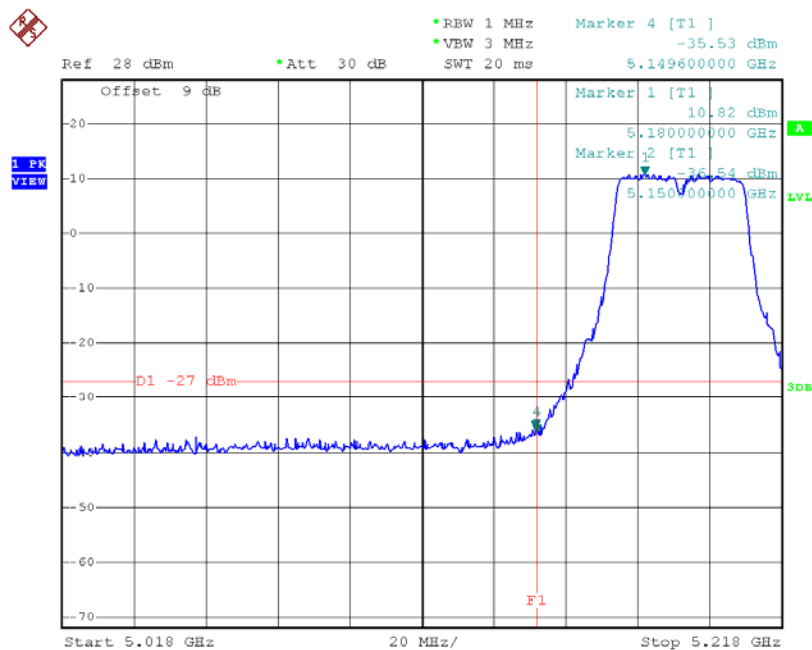
TX mode CH48



Date: 3.APR.2015 17:08:08

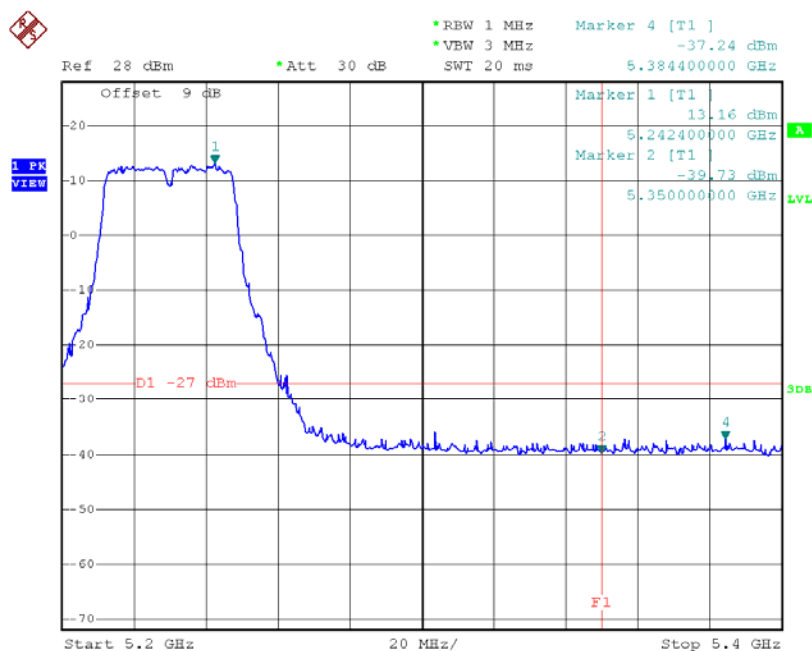
Test Mode:	UNII-1/TX N40 Mode_ANT 1
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TX mode CH38



Date: 3.APR.2015 17:59:03

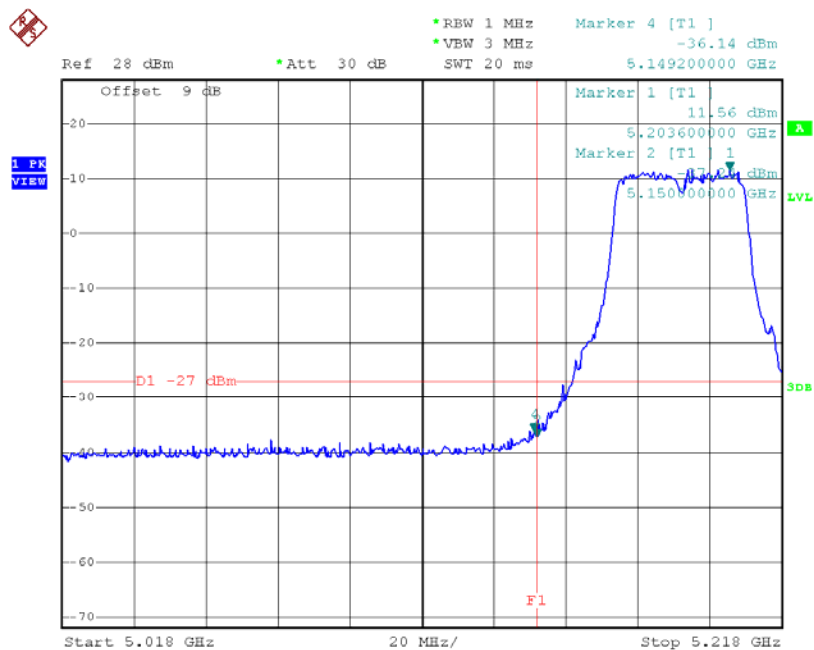
TX mode CH46



Date: 3.APR.2015 17:59:30

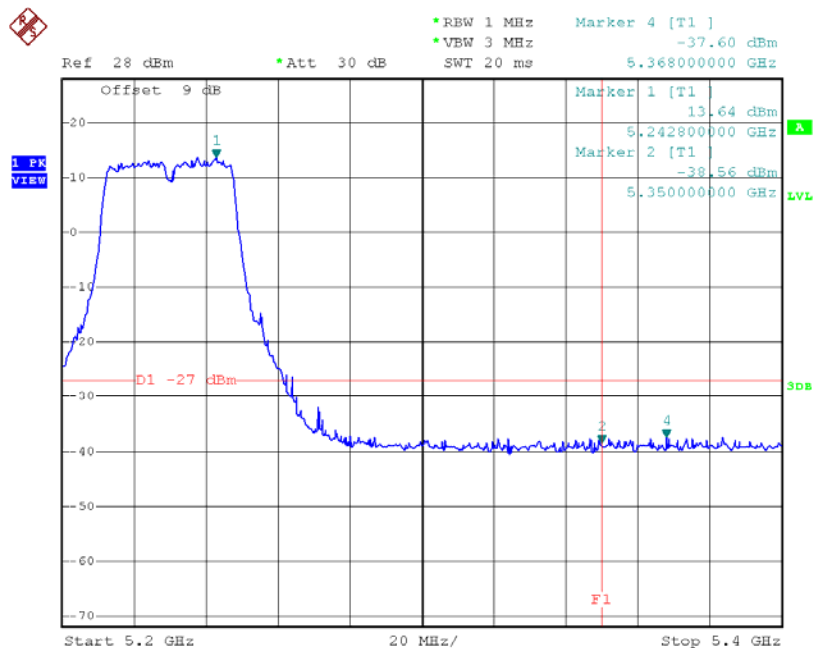
Test Mode:	UNII-1/TX N40 Mode_ANT 2
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TX mode CH38



Date: 3.APR.2015 17:17:59

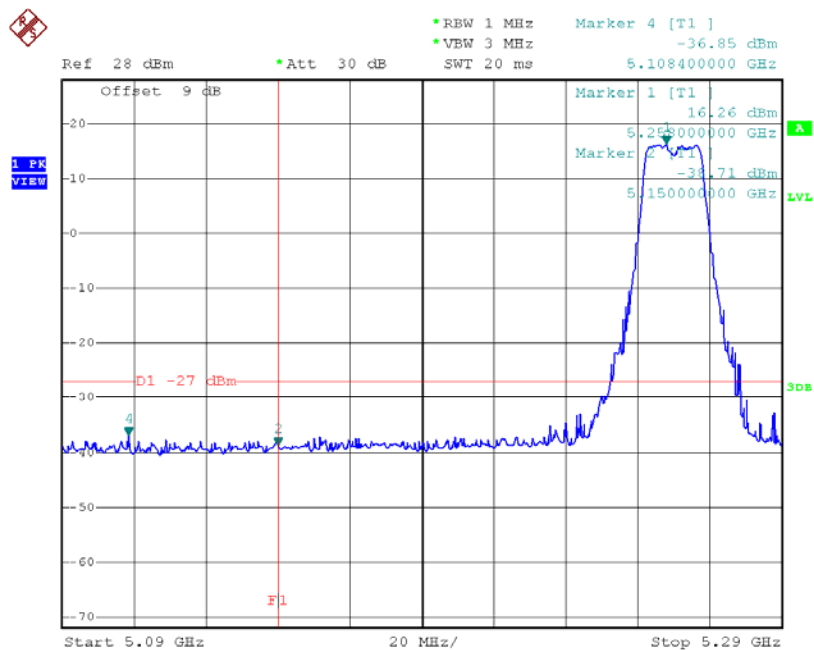
TX mode CH46



Date: 3.APR.2015 17:18:44

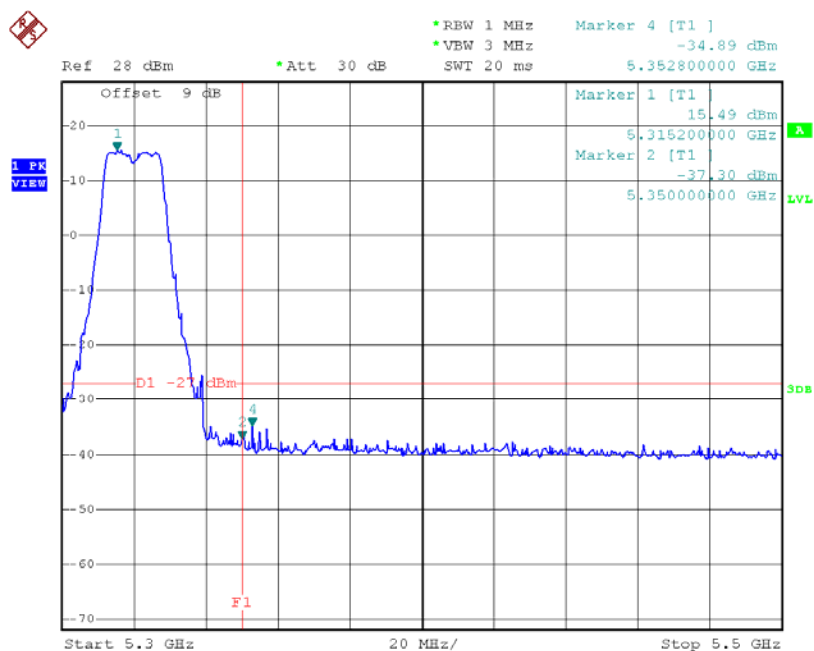
Test Mode:	UNII-2A/TX A Mode_ANT 1
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TX mode CH52



Date: 3.APR.2015 17:40:06

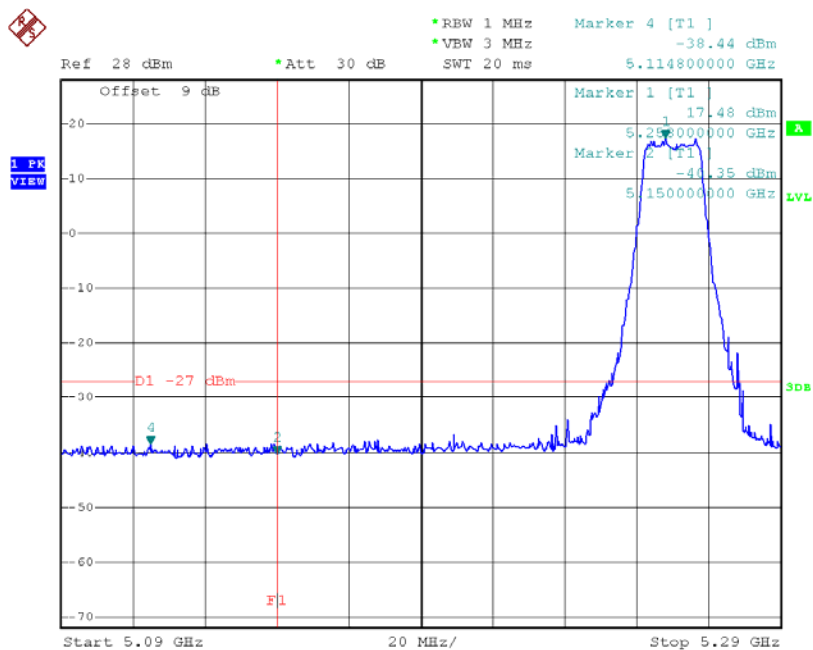
TX modeCH64



Date: 3.APR.2015 17:43:11

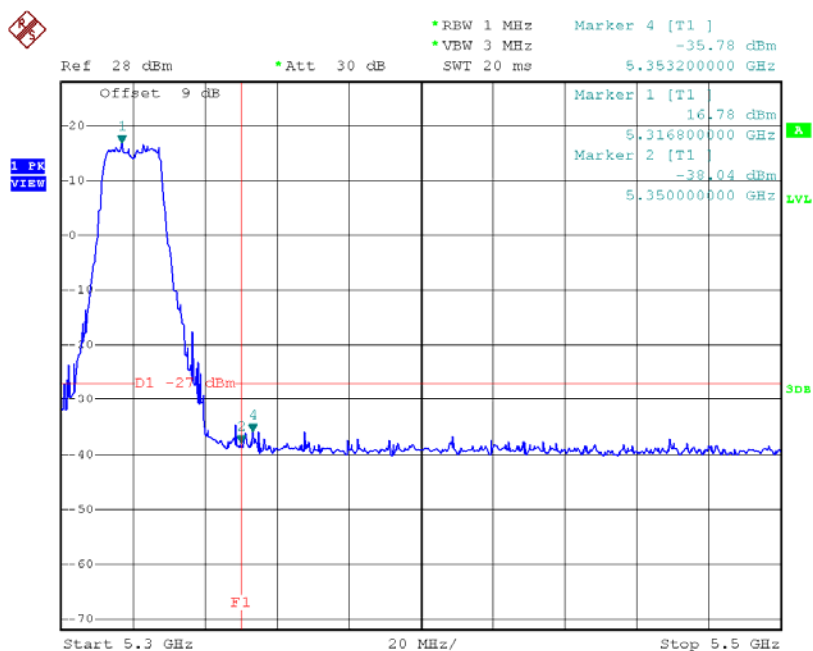
Test Mode:	UNII-2A/TX A Mode_ANT 2
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TX mode CH52



Date: 3.APR.2015 17:00:12

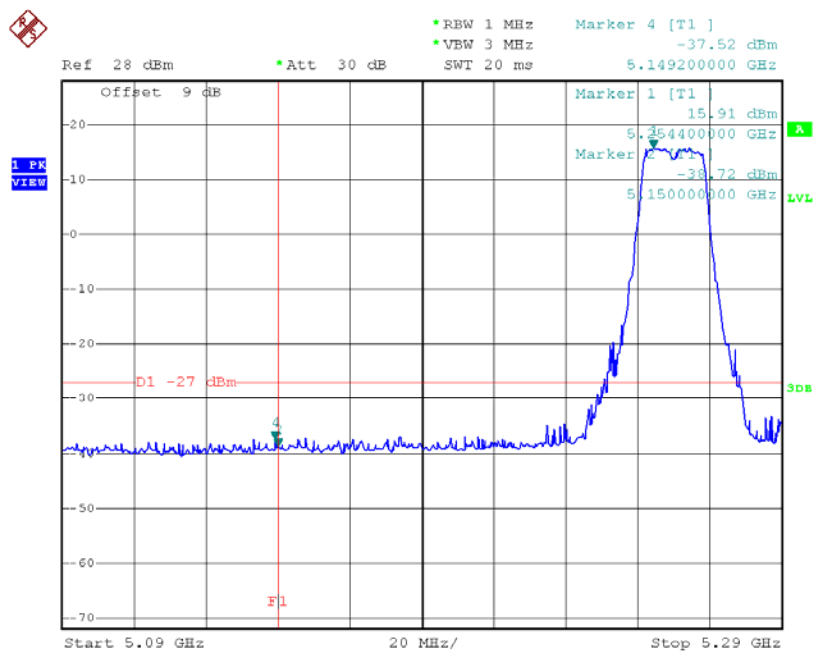
TX mode CH64



Date: 3.APR.2015 17:01:38

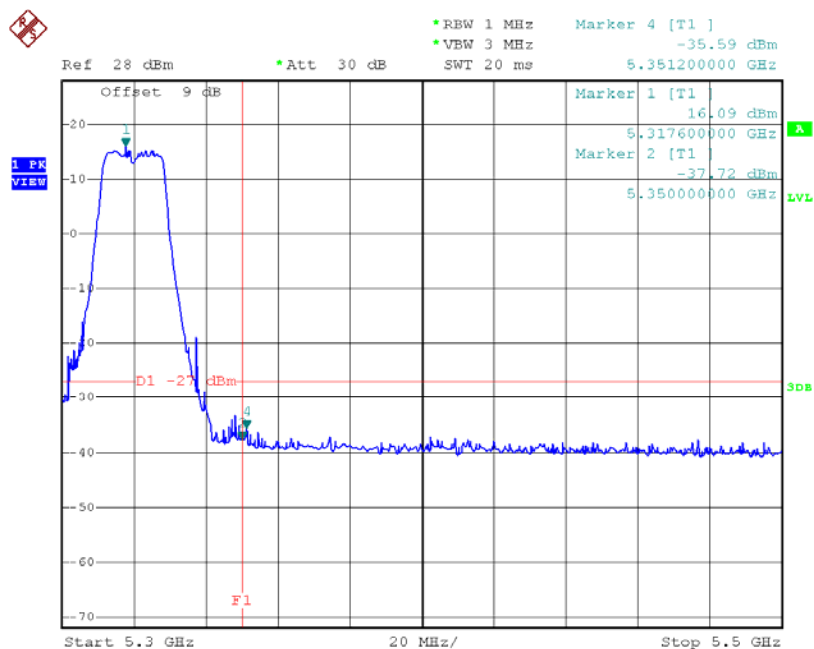
Test Mode: UNII-2A/TX N20 Mode_ANT 1

TX mode CH52



Date: 3.APR.2015 17:50:03

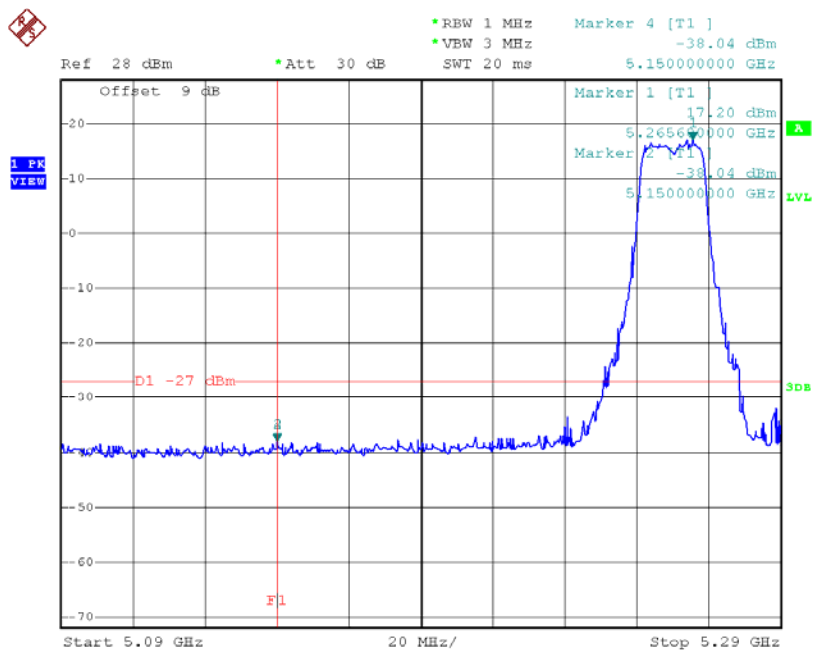
TX mode CH64



Date: 3.APR.2015 17:50:47

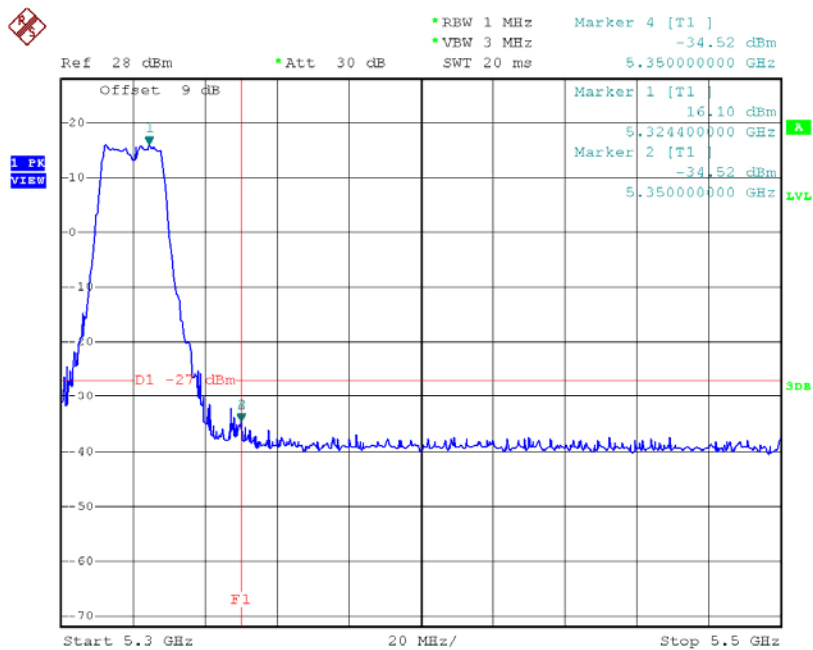
Test Mode: UNII-2A/TX N20 Mode_ANT 2

TX mode CH52



Date: 3.APR.2015 17:08:38

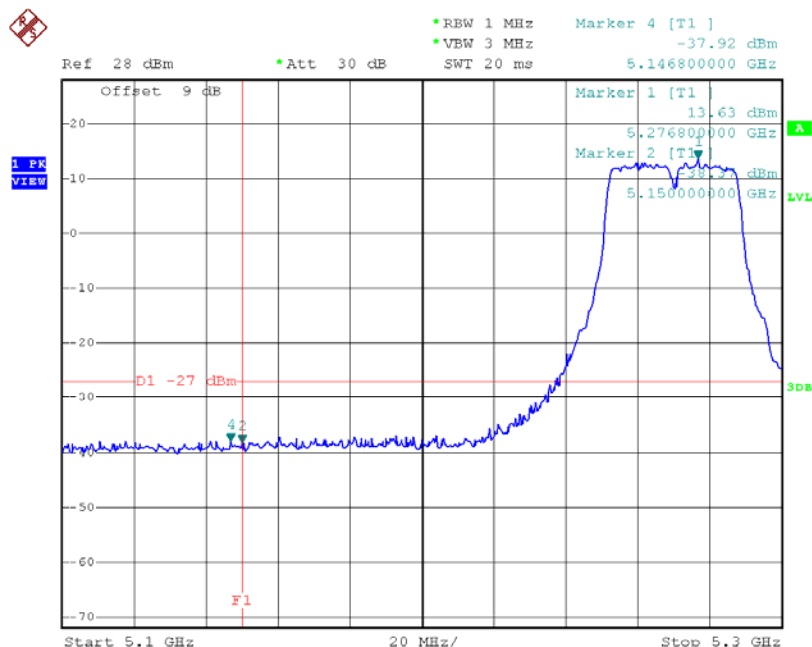
TX modeCH64



Date: 3.APR.2015 17:09:29

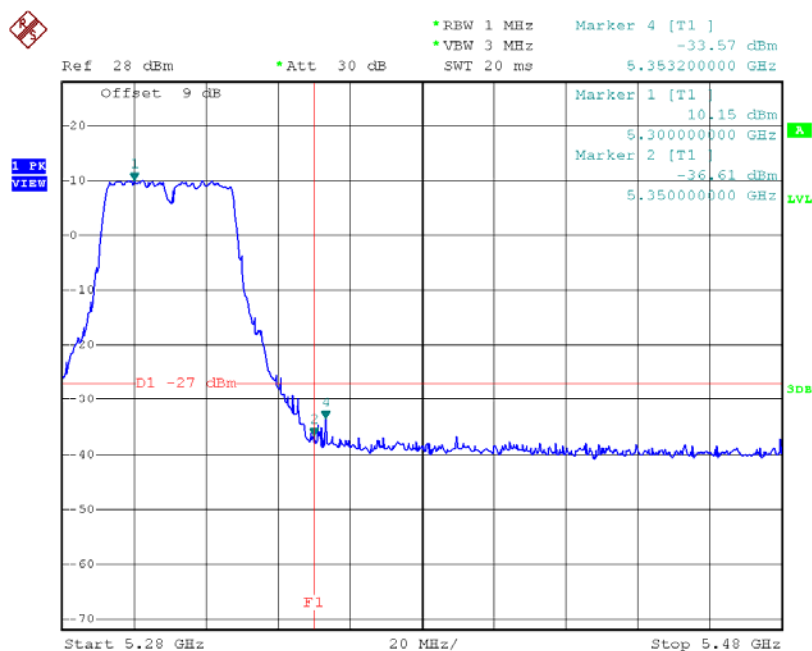
Test Mode:	UNII-2A/TX N40 Mode_ANT 1
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TX mode CH54



Date: 3.APR.2015 18:00:03

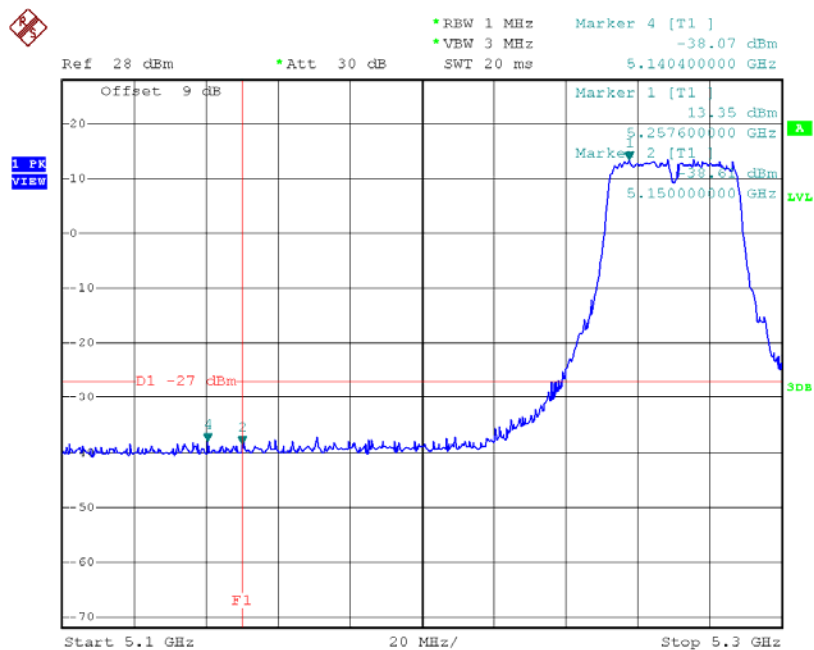
TX mode CH62



Date: 3.APR.2015 18:00:28

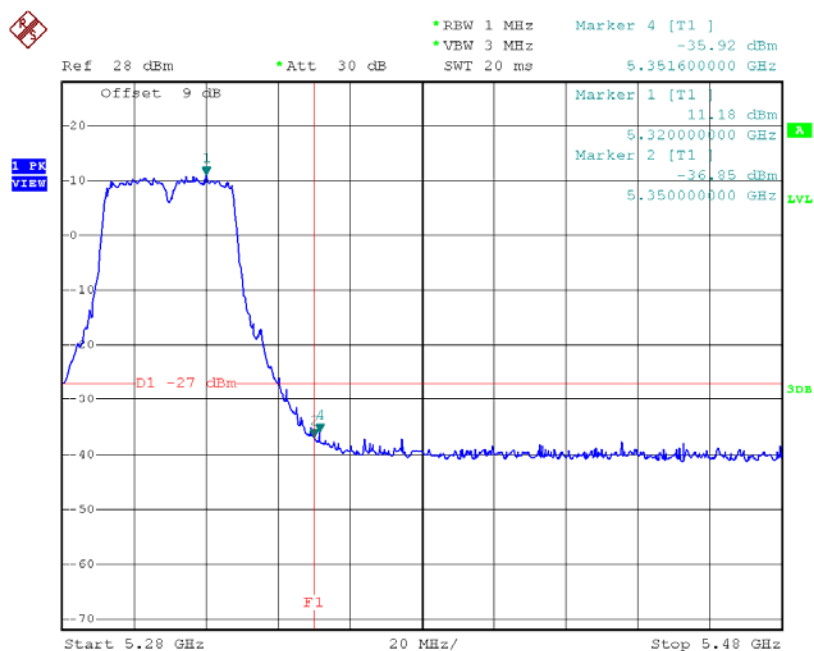
Test Mode:	UNII-2A/TX N40 Mode_ANT 2
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TX mode CH54



Date: 3.APR.2015 17:19:18

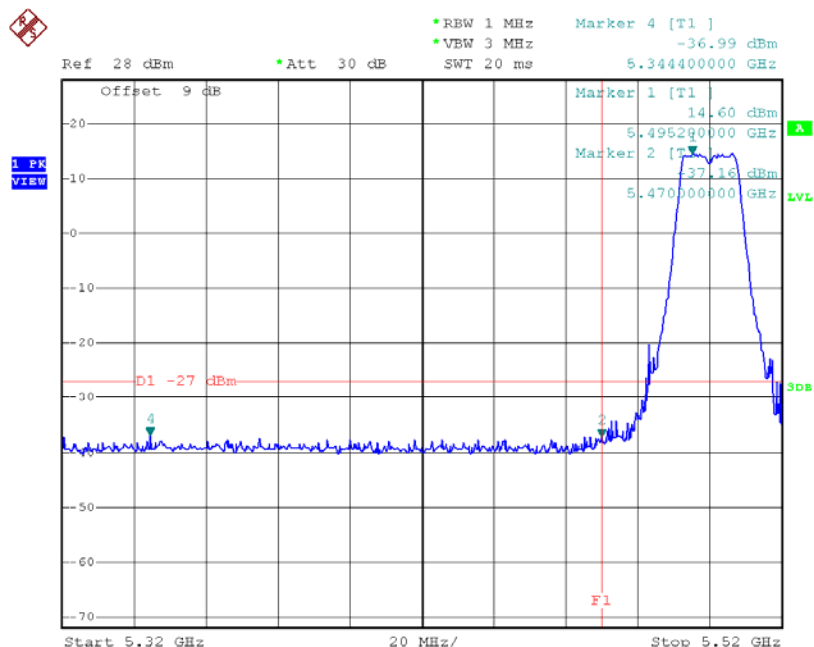
TX mode CH62



Date: 3.APR.2015 17:19:50

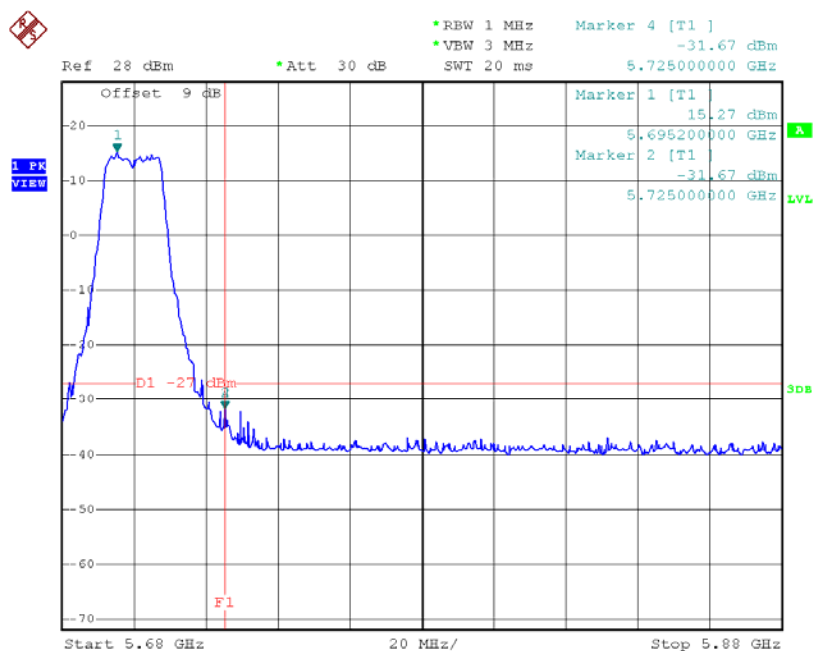
Test Mode:	UNII-2C/TX A Mode_ANT 1
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TX mode CH100



Date: 3.APR.2015 17:43:52

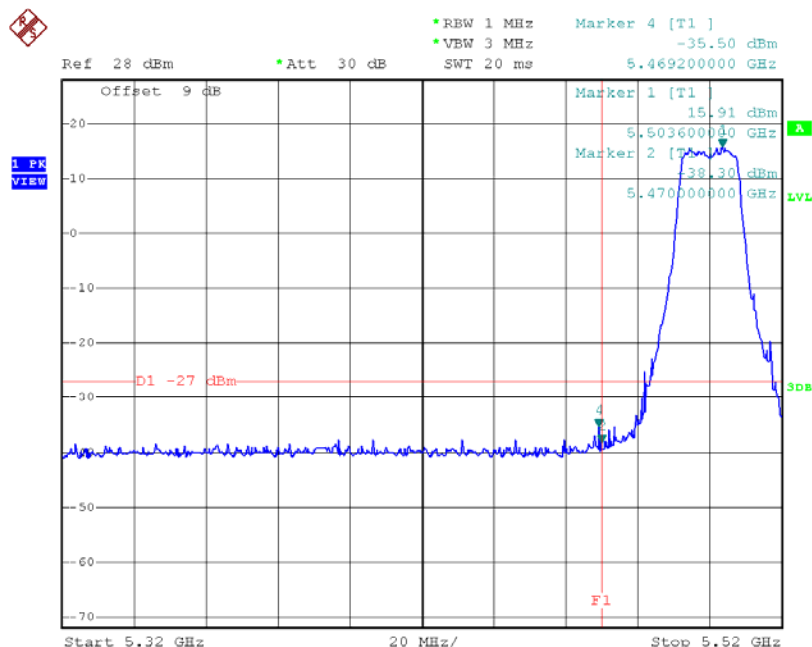
TX mode CH140



Date: 3.APR.2015 17:45:33

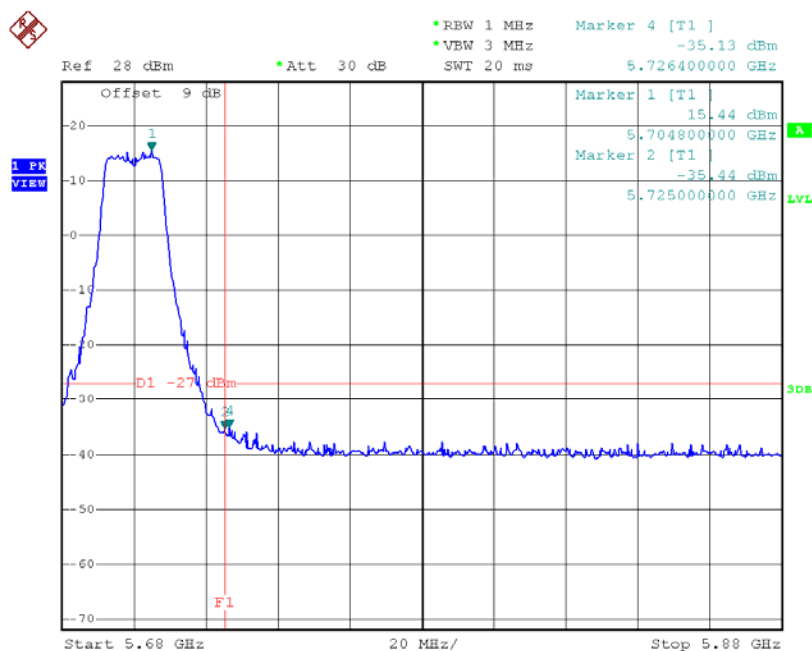
Test Mode:	UNII-2C/TX A Mode_ANT 2
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TX mode CH100



Date: 3.APR.2015 17:02:24

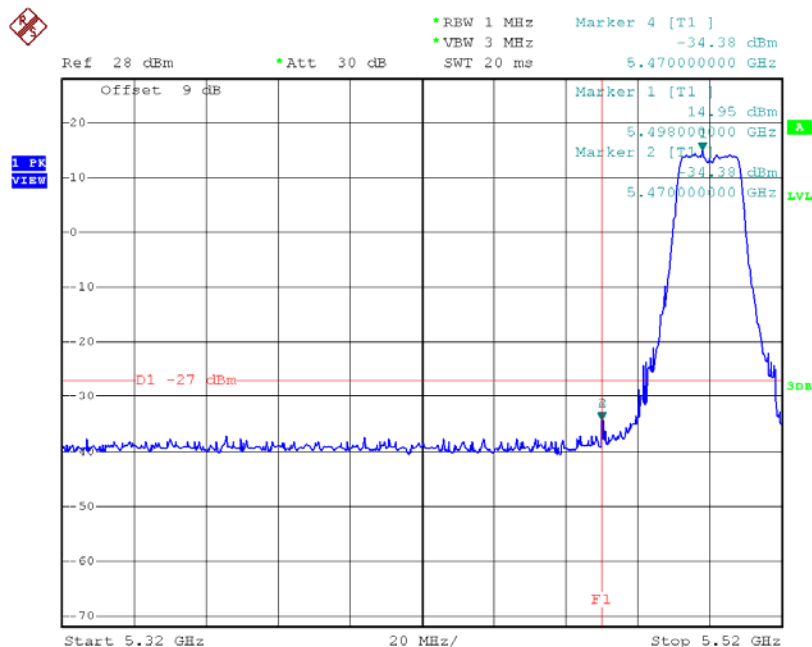
TX mode CH140



Date: 3.APR.2015 17:03:46

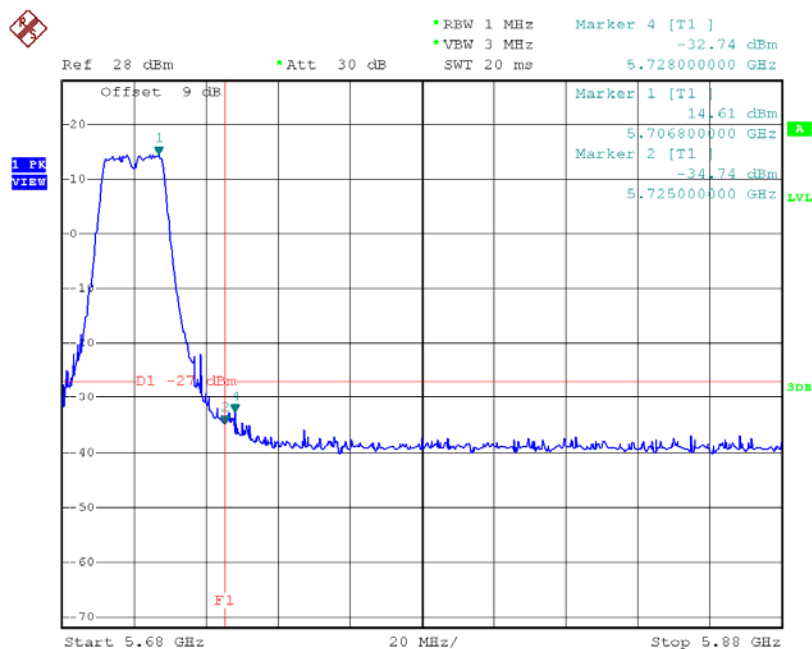
Test Mode: UNII-2C/TX N20 Mode_ANT 1

TX mode CH100



Date: 3.APR.2015 17:51:11

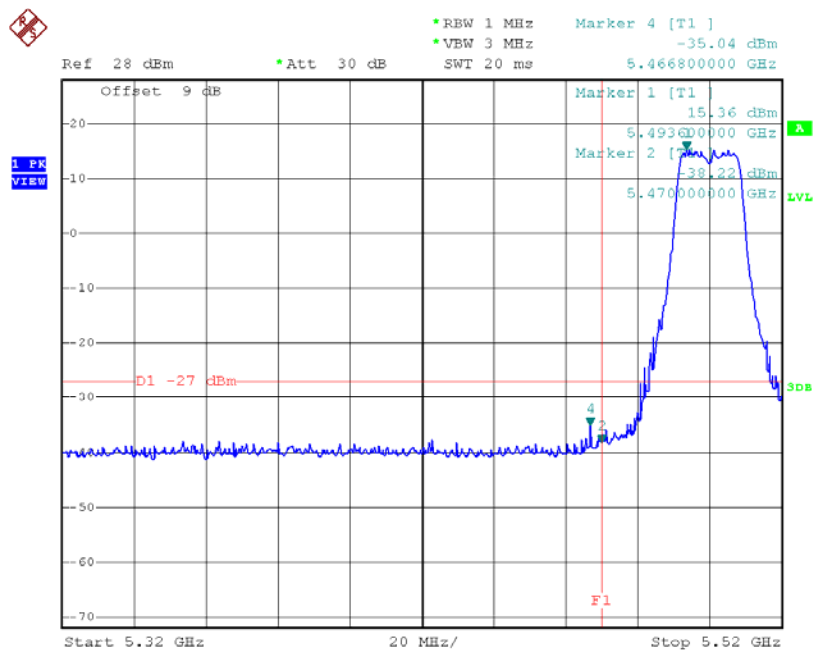
TX mode CH140



Date: 3.APR.2015 17:51:54

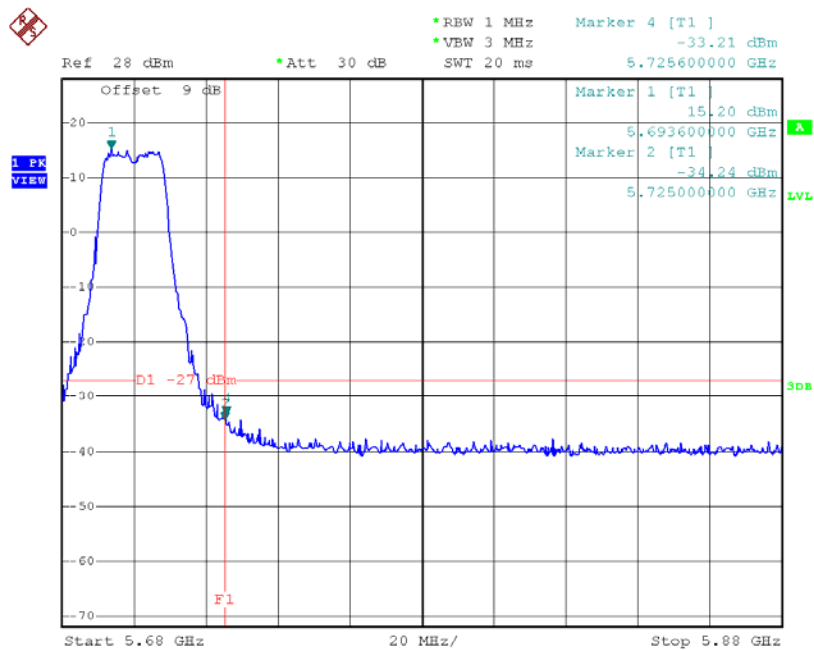
Test Mode:	UNII-2C/TX N20 Mode_ANT 2
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TX mode CH100



Date: 3.APR.2015 17:10:00

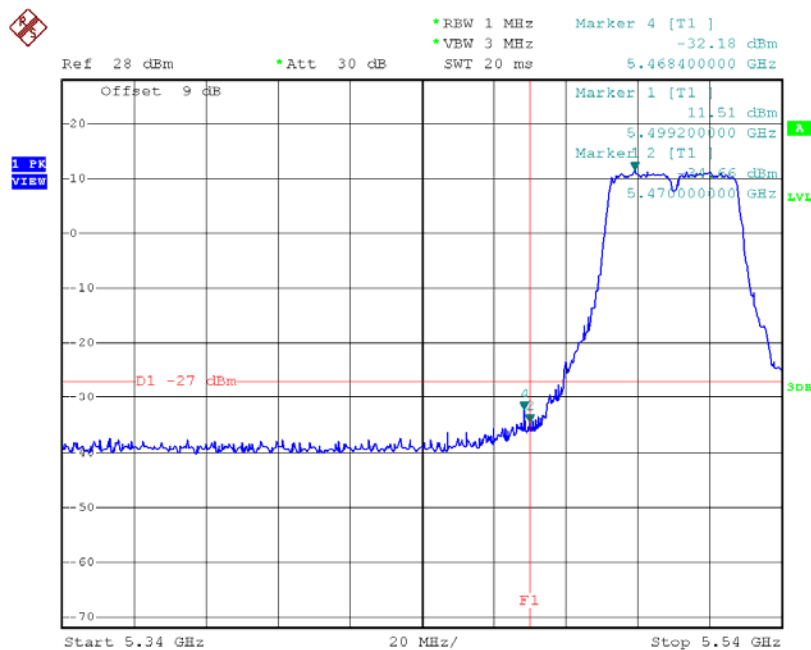
TX mode CH140



Date: 3.APR.2015 17:10:49

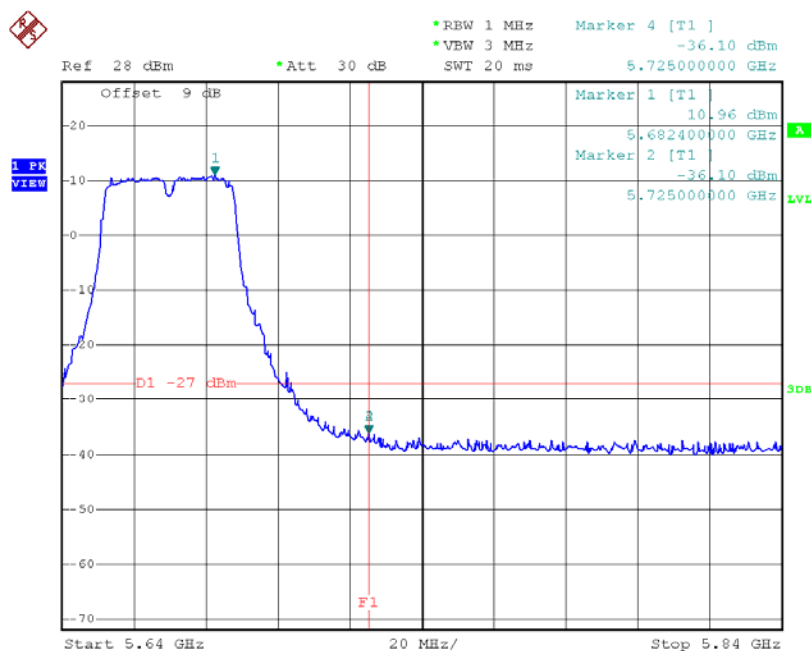
Test Mode:	UNII-2C/TX N40 Mode_ANT 1
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TX mode CH102



Date: 3.APR.2015 18:00:53

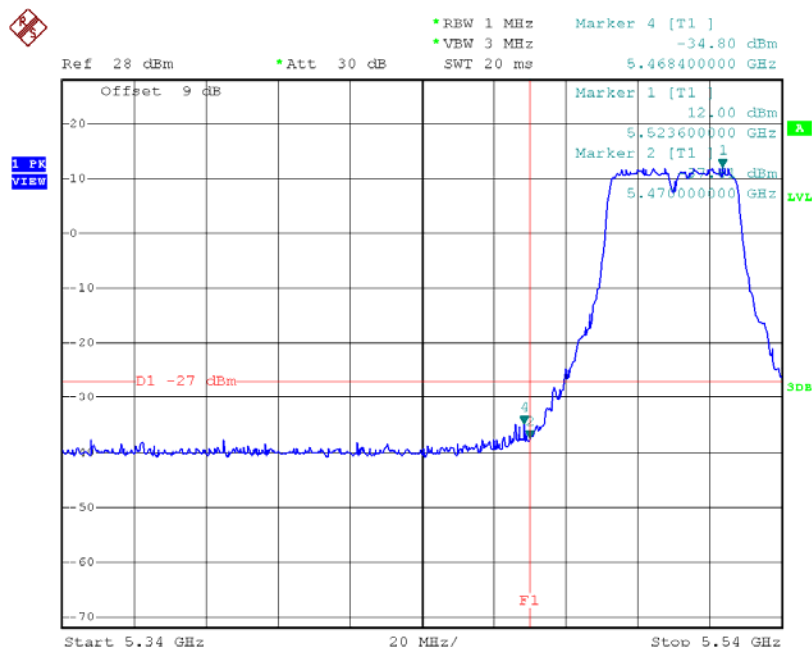
TX mode CH134



Date: 3.APR.2015 18:01:40

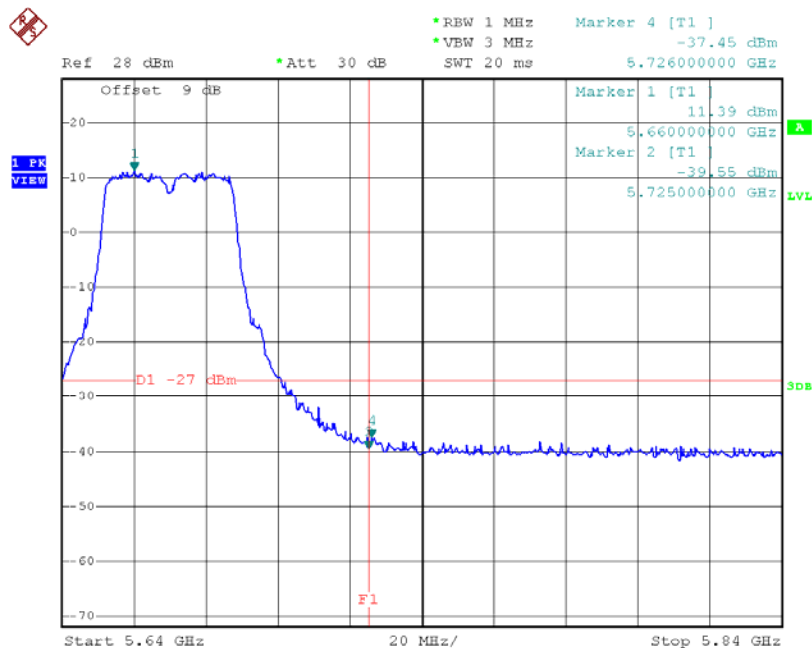
Test Mode:	UNII-2C/TX N40 Mode_ANT 2
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TX mode CH102



Date: 3.APR.2015 17:20:18

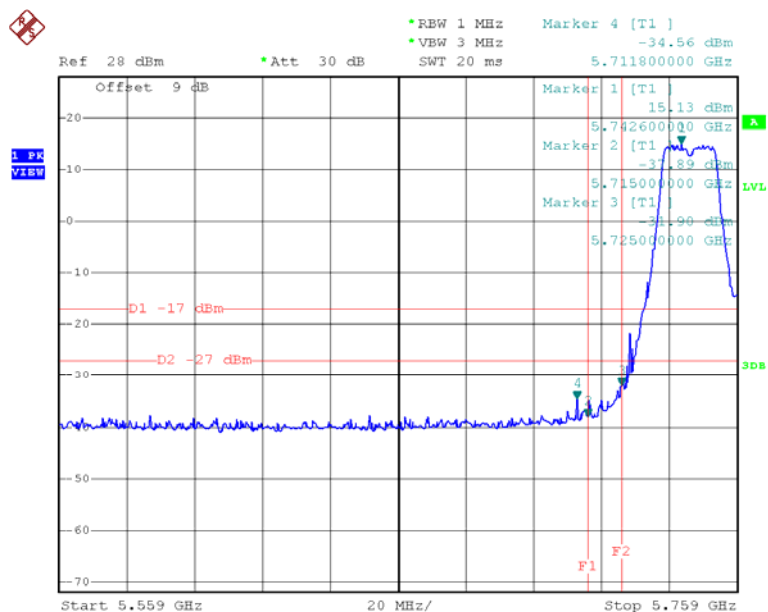
TX mode CH134



Date: 3.APR.2015 17:21:02

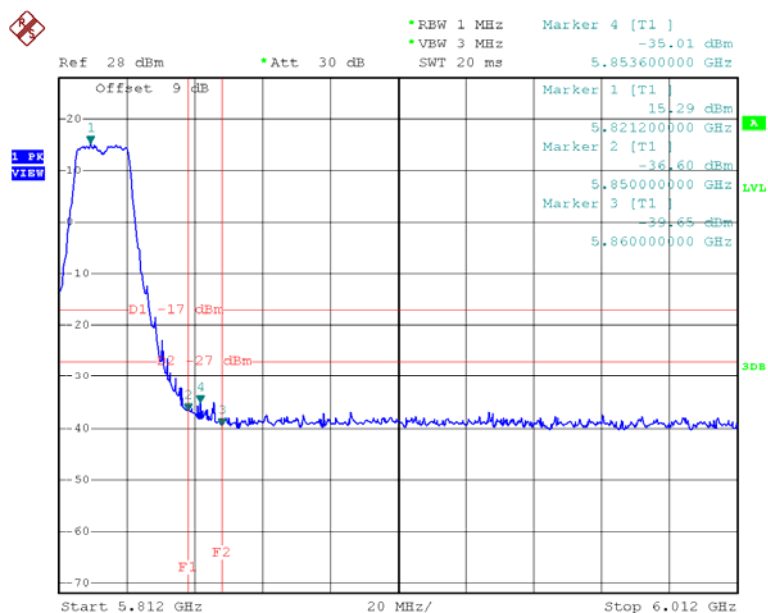
Test Mode: UNII-3/TX A Mode_ANT 1

TX A Mode CH149



Date: 3.APR.2015 17:46:17

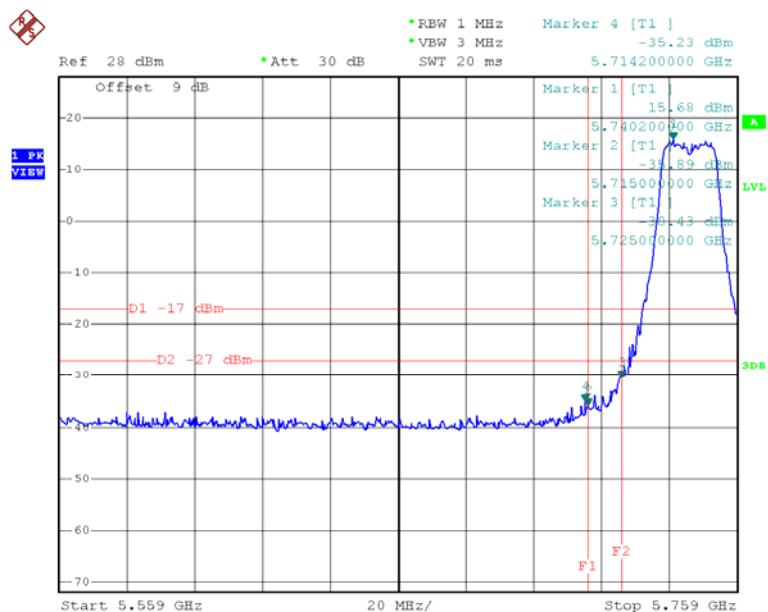
TX A Mode CH165



Date: 3.APR.2015 17:47:46

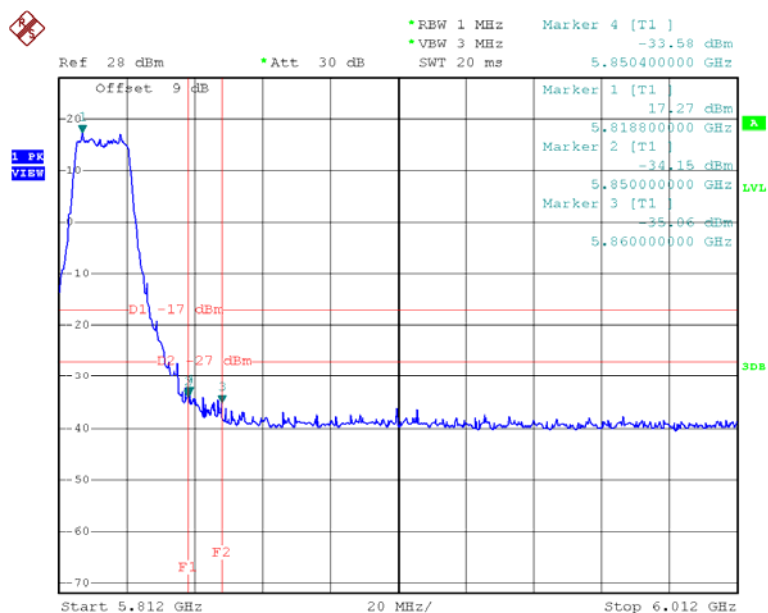
Test Mode: UNII-3/TX A Mode_ANT 2

TX A Mode CH149



Date: 3.APR.2015 17:04:32

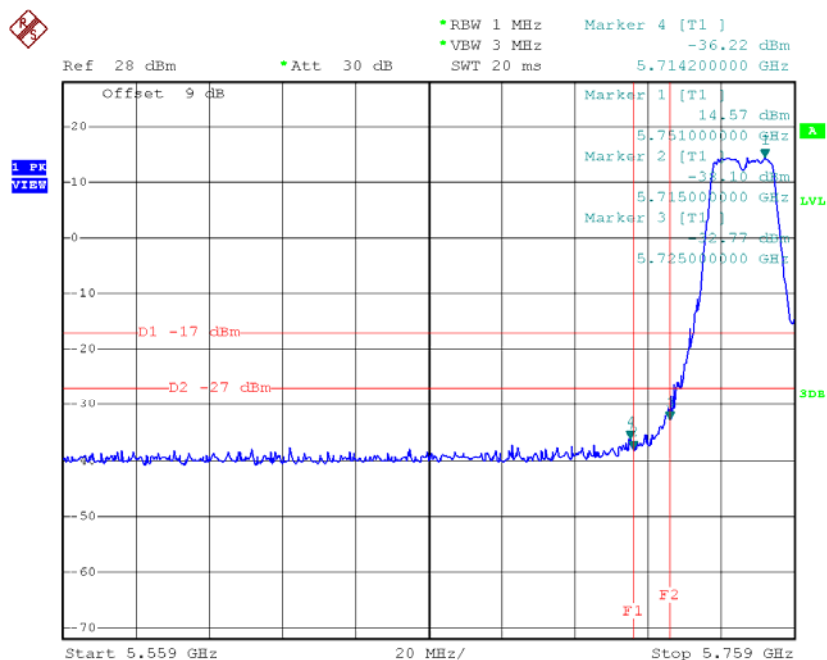
TX A Mode CH165



Date: 3.APR.2015 17:05:56

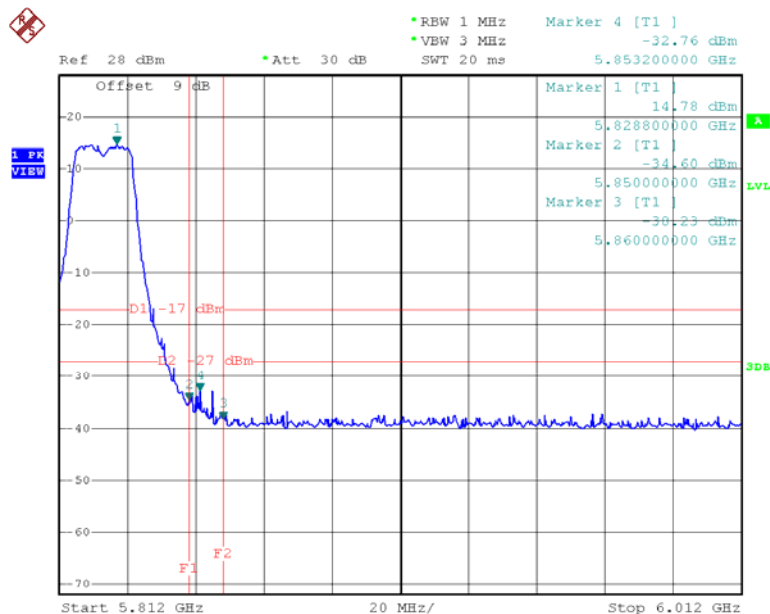
Test Mode: UNII-3/TX N20 Mode_ANT 1

TX HT20 mode CH149



Date: 3.APR.2015 17:52:18

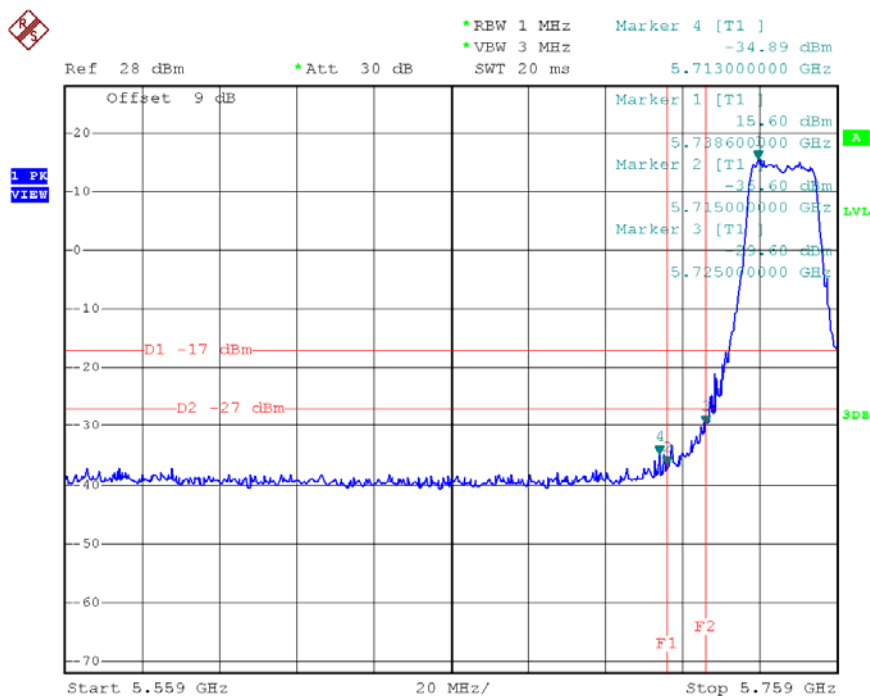
TX HT20 mode CH165



Date: 3.APR.2015 17:53:02

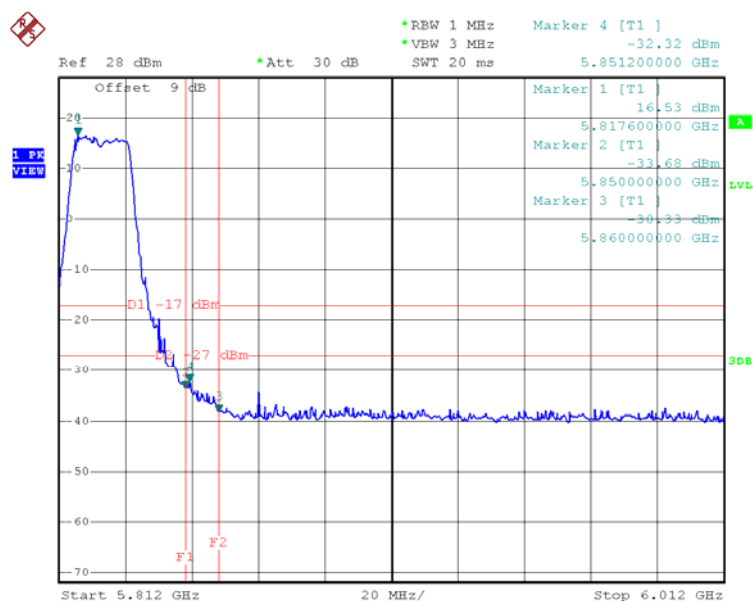
Test Mode: UNII-3/TX N20 Mode_ANT 2

TX HT20 mode CH149



Date: 3.APR.2015 17:11:15

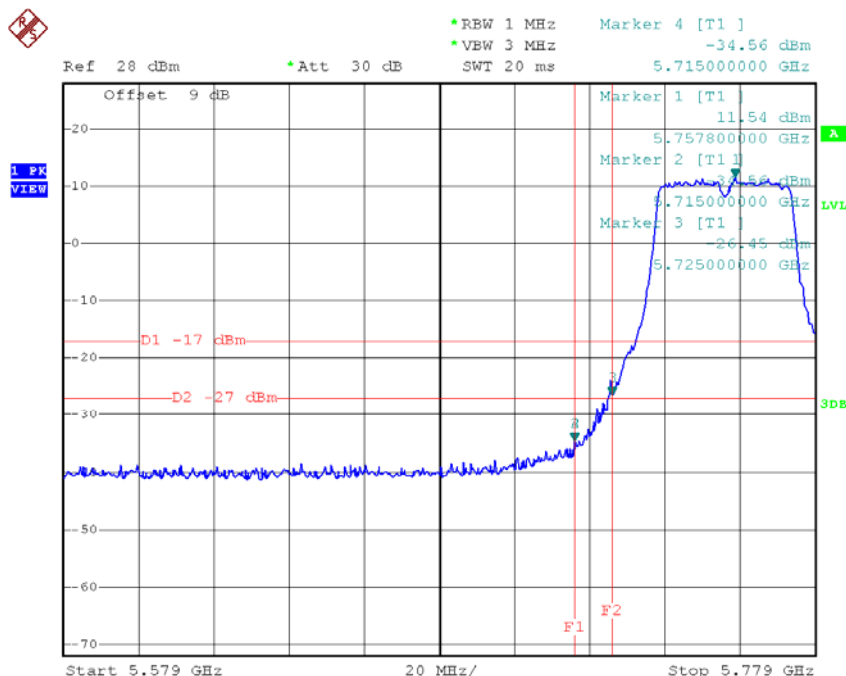
X HT20 mode CH165



Date: 3.APR.2015 17:12:22

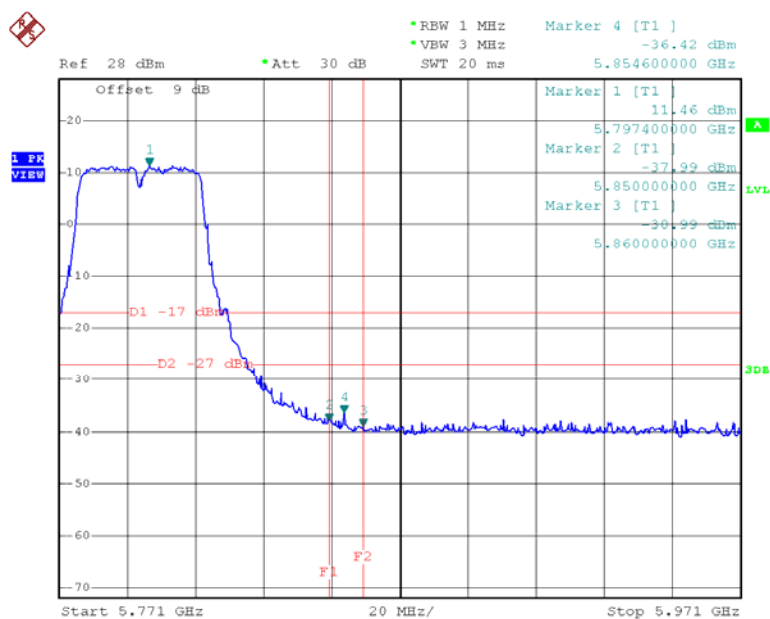
Test Mode: UNII-3/TX N40 Mode_ANT 1

UNII-3/TX HT40 mode CH151



Date: 3.APR.2015 18:02:04

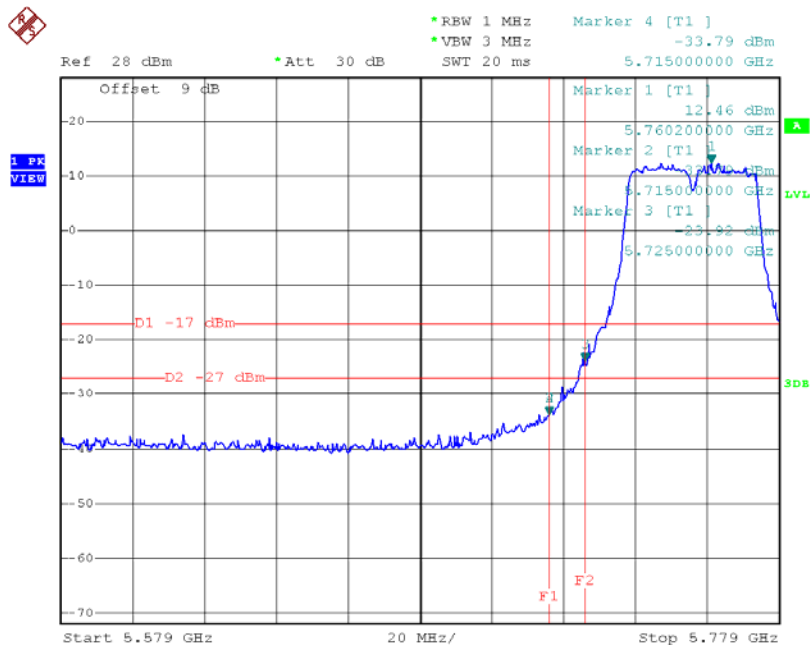
UNII-3/TX HT40 mode CH159



Date: 3.APR.2015 18:02:29

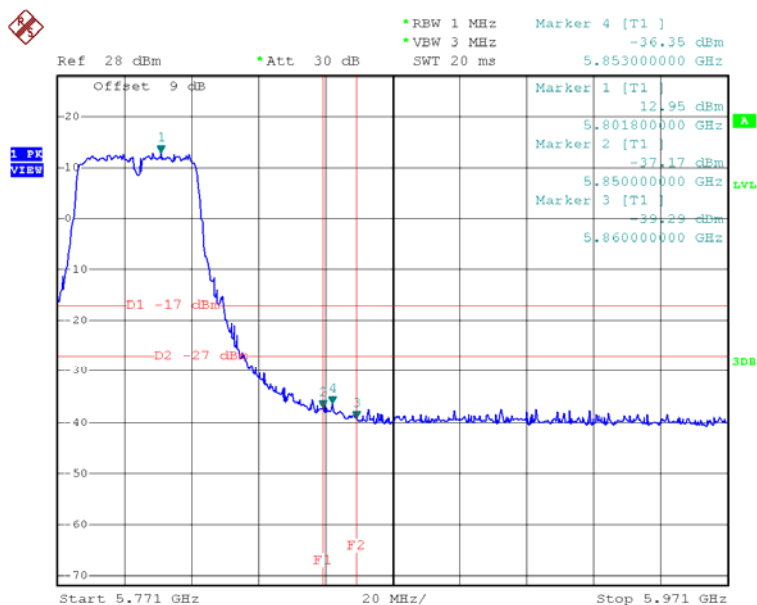
Test Mode:	UNII-3/TX N40 Mode_ANT 2
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TX HT40 mode CH151



Date: 3.APR.2015 17:21:33

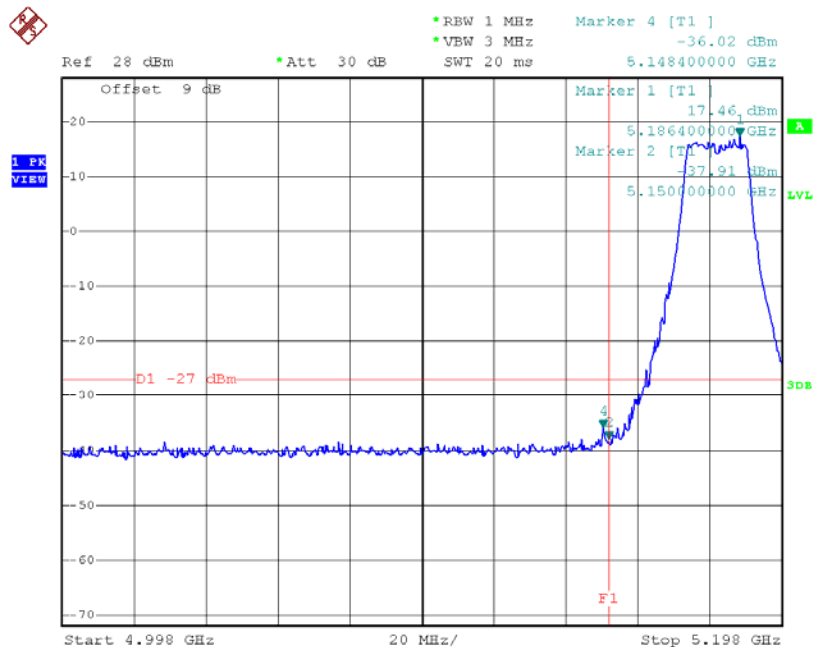
HT40 mode CH159



Date: 3.APR.2015 17:22:01

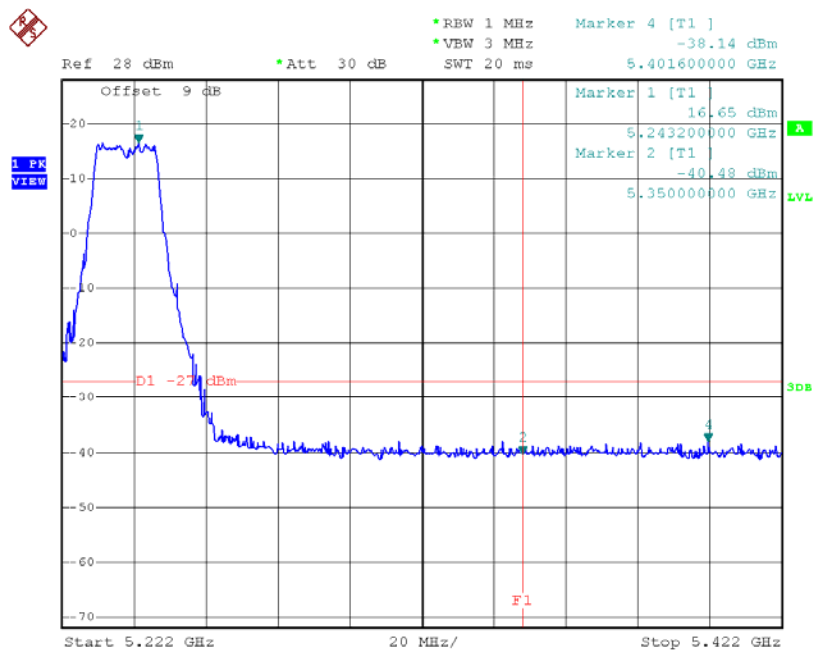
Test Mode: UNII-1/TX AC20 Mode_ANT 1

TX mode CH36



Date: 3.APR.2015 17:54:17

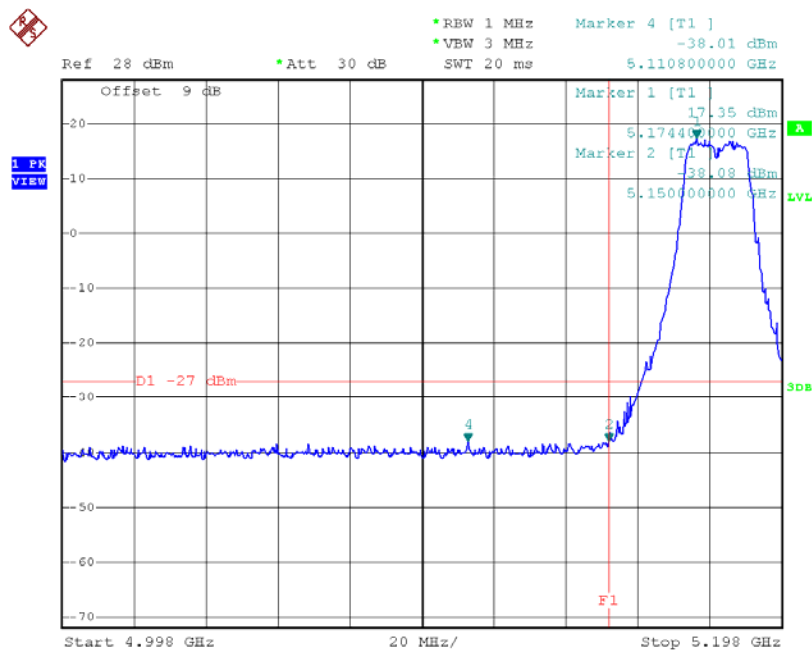
TX mode CH48



Date: 3.APR.2015 17:55:00

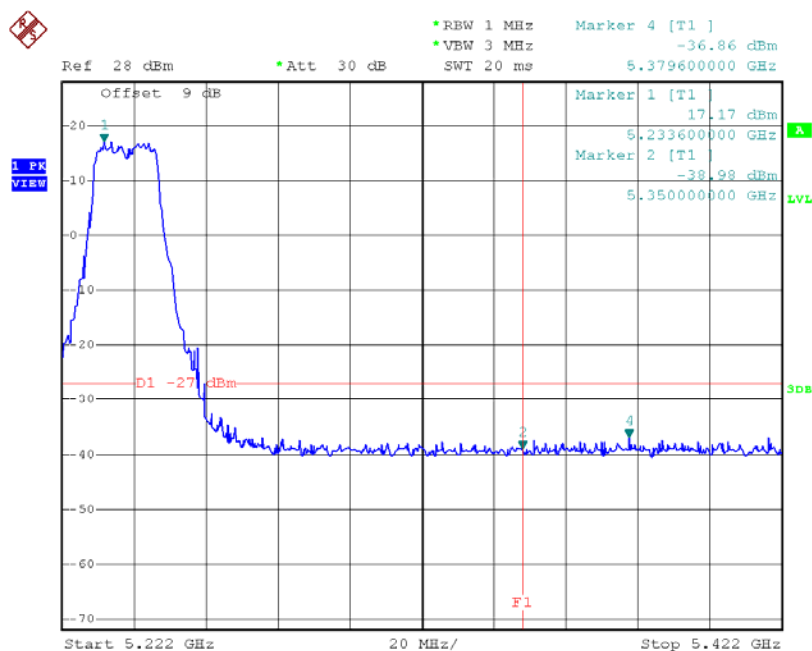
Test Mode:	UNII-1/TX AC20 Mode_ANT 2
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TX mode CH36



Date: 3.APR.2015 17:12:56

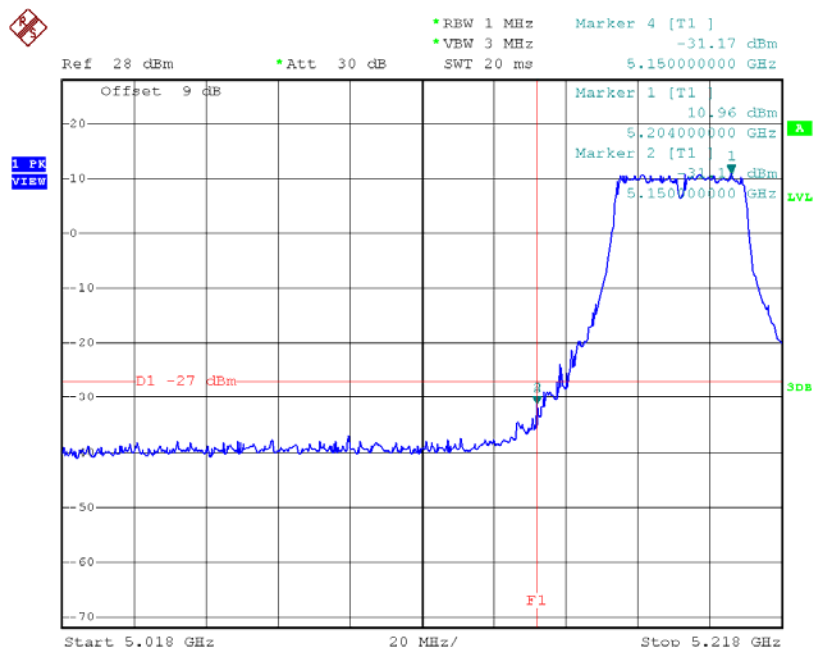
TX mode CH48



Date: 3.APR.2015 17:13:43

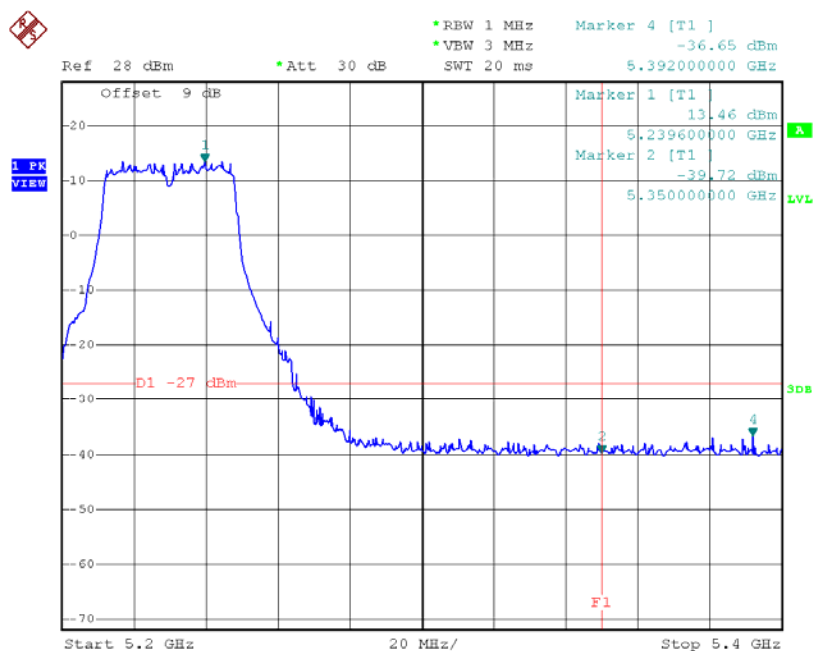
Test Mode:	UNII-1/TX AC40 Mode_ANT 1
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TX mode CH38



Date: 3.APR.2015 18:03:24

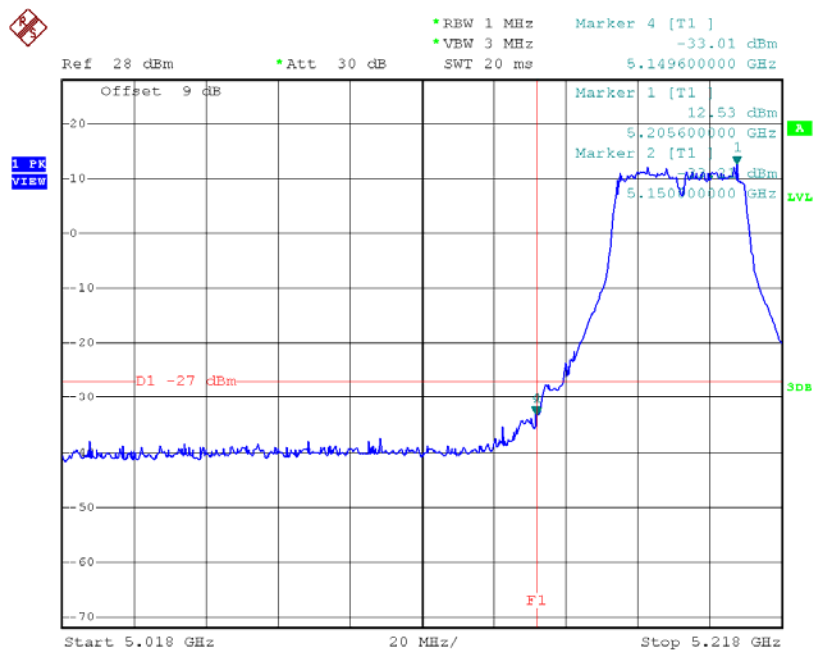
TX mode CH46



Date: 3.APR.2015 18:03:53

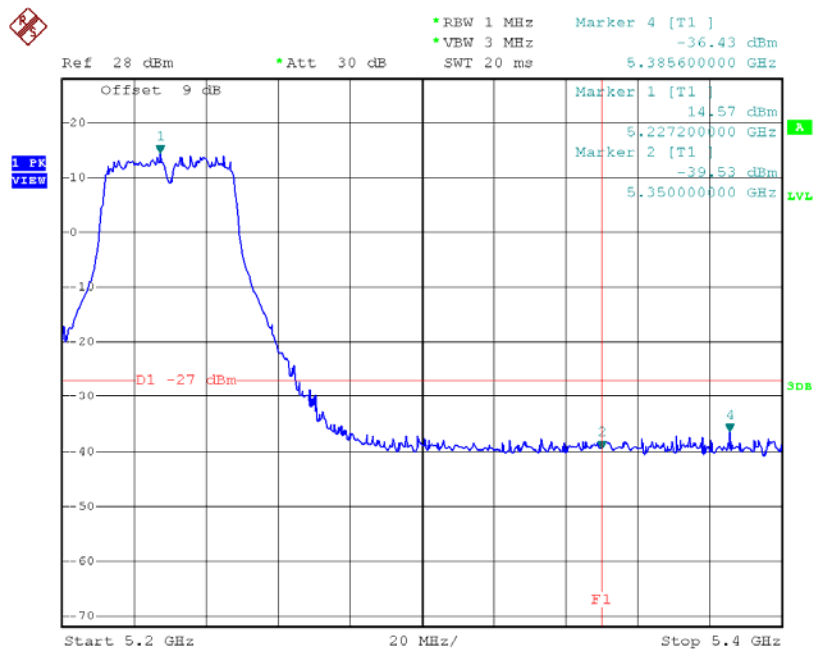
Test Mode: UNII-1/TX AC40 Mode_ANT 2

TX mode CH38



Date: 3.APR.2015 17:22:32

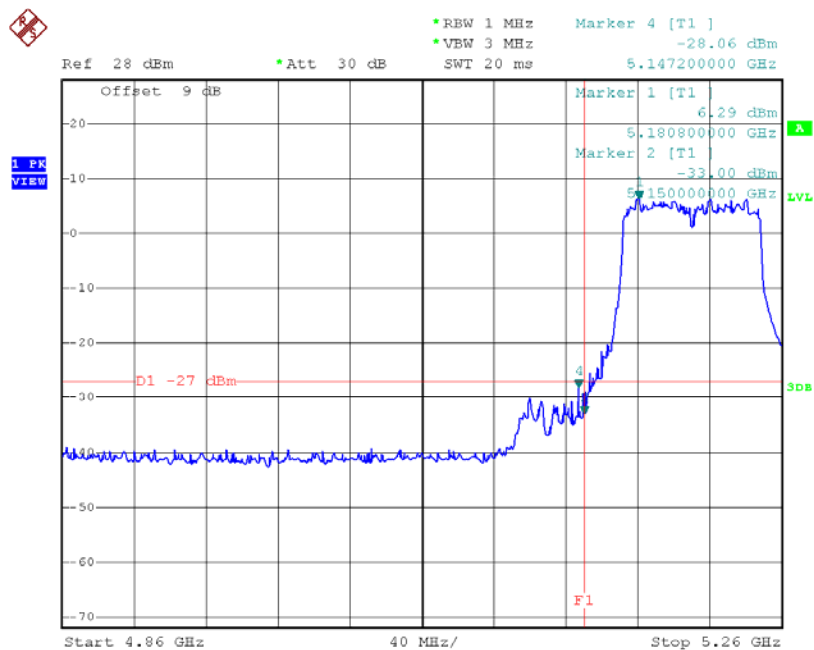
TX mode CH46



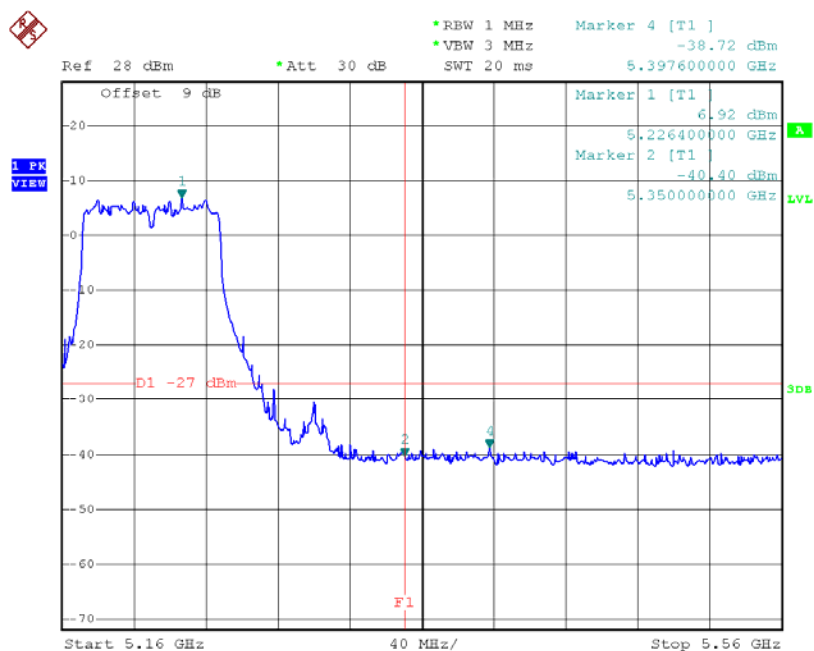
Date: 3.APR.2015 17:23:03

Test Mode: UNII-1/TX AC80 Mode_ANT 1

TX mode CH42



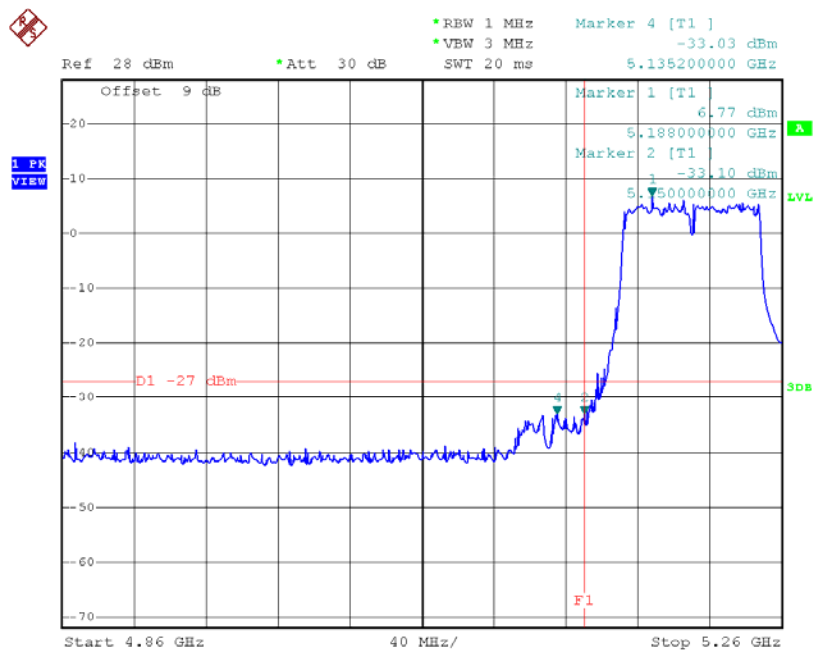
Date: 3.APR.2015 18:07:31



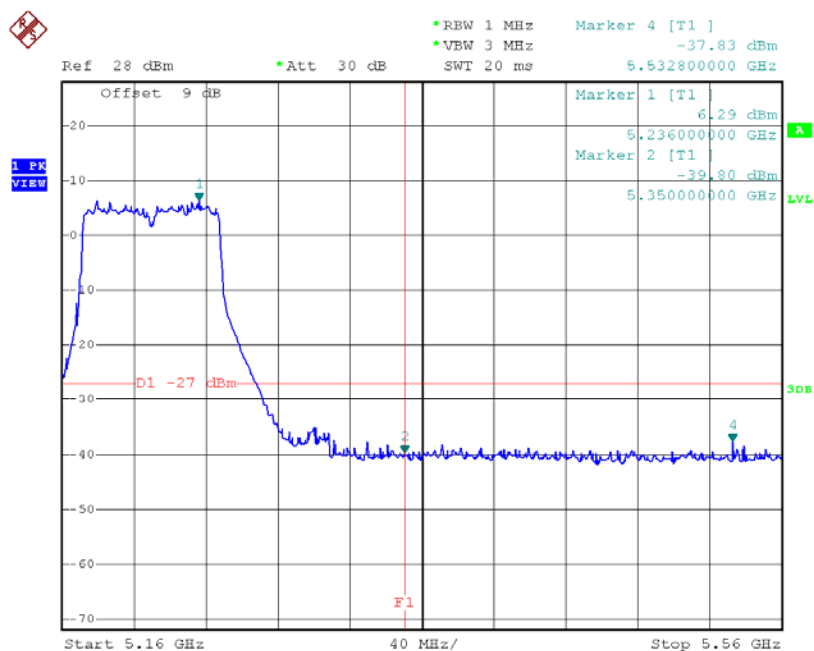
Date: 3.APR.2015 18:07:38

Test Mode: UNII-1/TX AC80 Mode_ANT 2

TX mode CH42



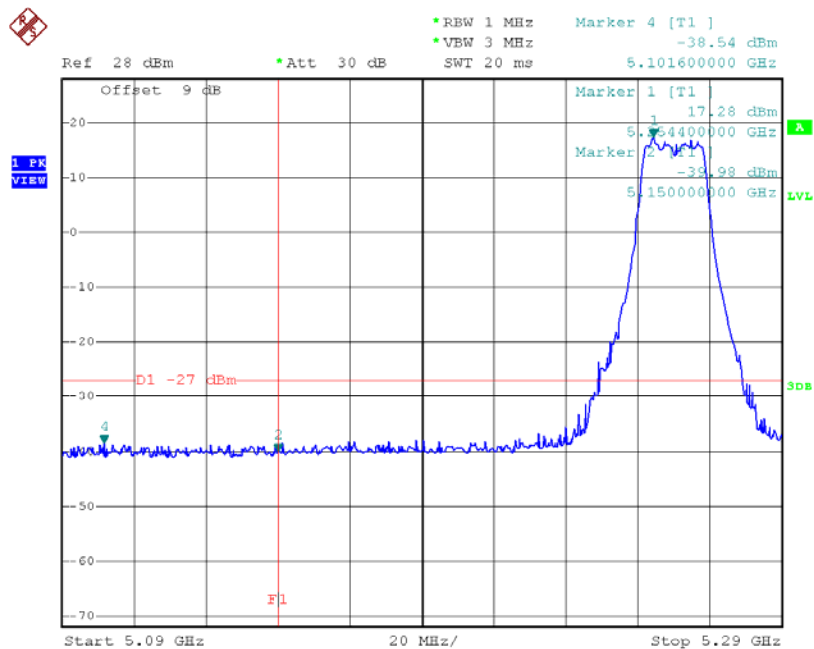
Date: 3.APR.2015 17:27:42



Date: 3.APR.2015 17:27:49

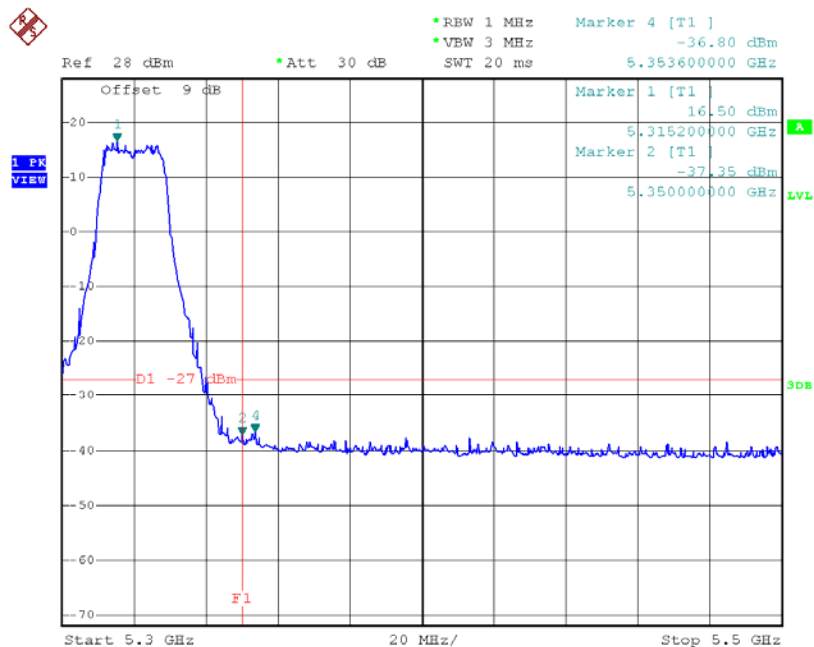
Test Mode: UNII-2A/TX AC20 Mode_ANT 1

TX mode CH52



Date: 3.APR.2015 17:55:23

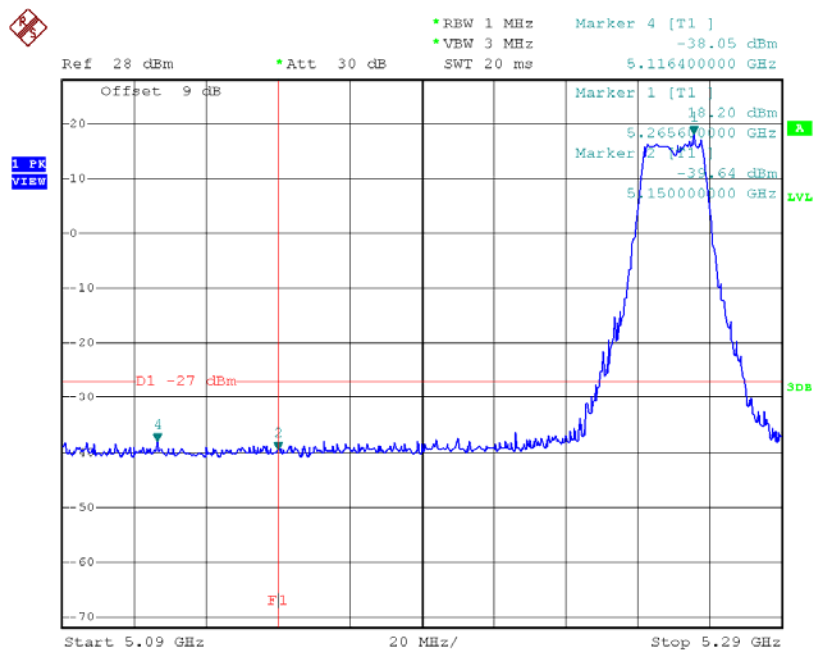
TX modeCH64



Date: 3.APR.2015 17:56:06

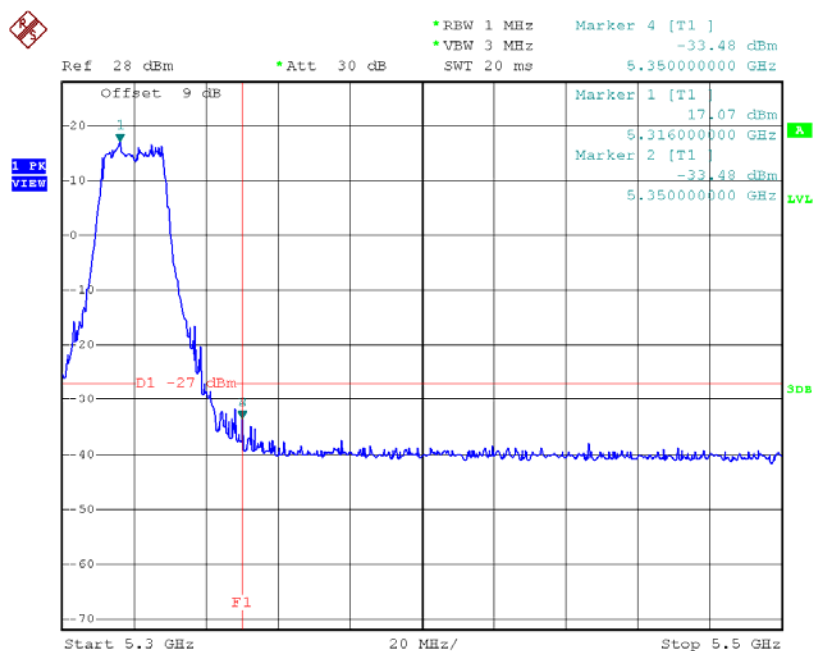
Test Mode: UNII-2A/TX AC20 Mode_ANT 2

TX mode CH52



Date: 3.APR.2015 17:14:07

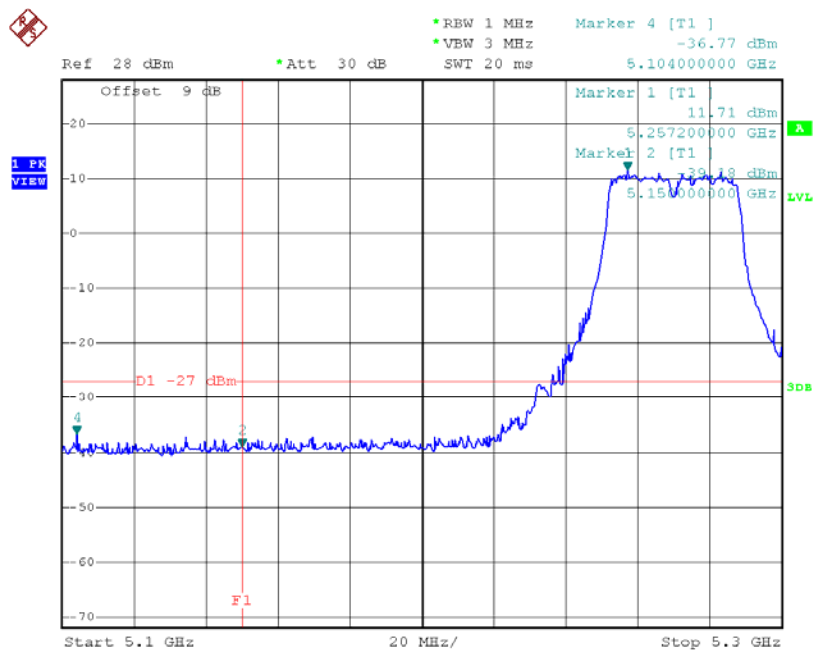
TX modeCH64



Date: 3.APR.2015 17:14:53

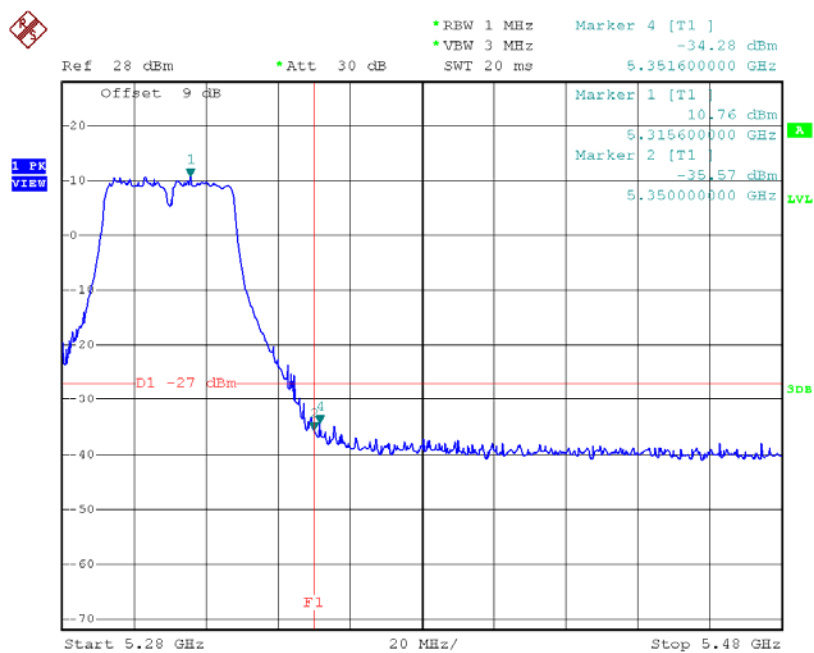
Test Mode: UNII-2A/TX AC40 Mode_ANT 1

TX mode CH54



Date: 3.APR.2015 18:04:18

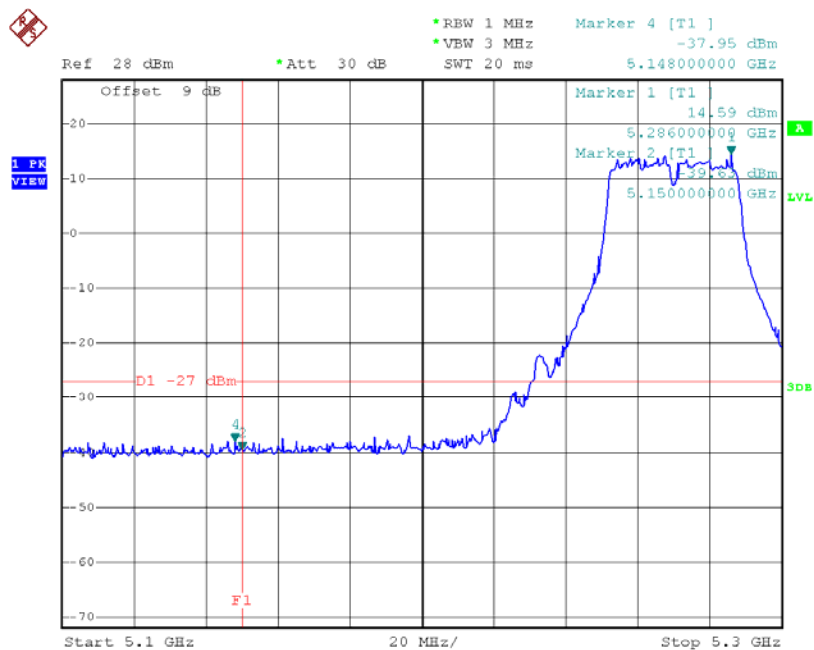
TX mode CH62



Date: 3.APR.2015 18:04:45

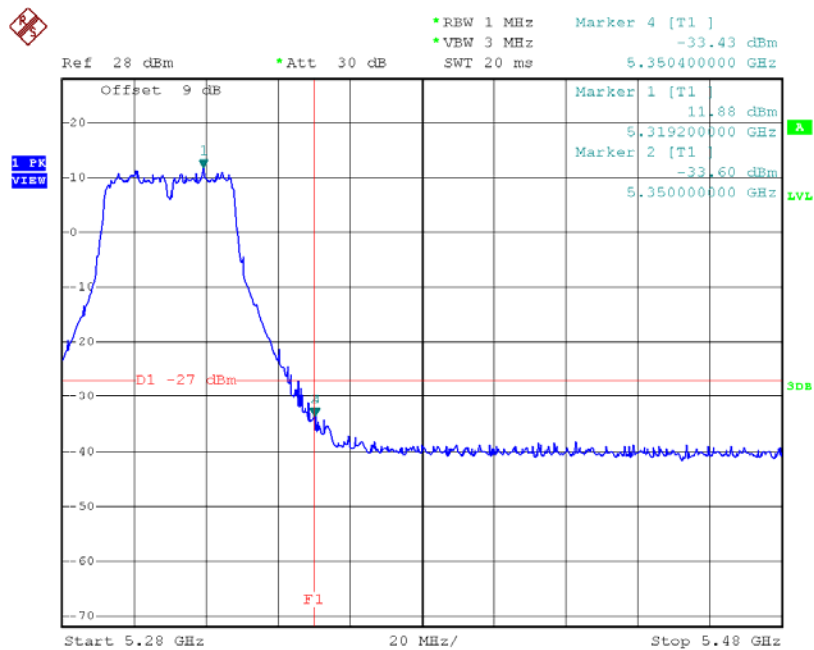
Test Mode: UNII-2A/TX AC40 Mode_ANT 2

TX mode CH54



Date: 3.APR.2015 17:23:40

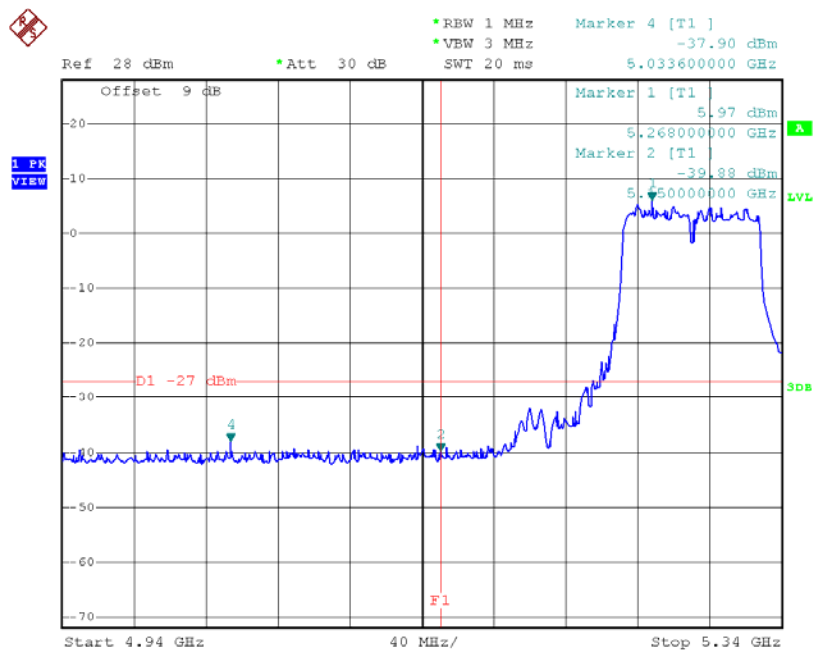
TX mode CH62



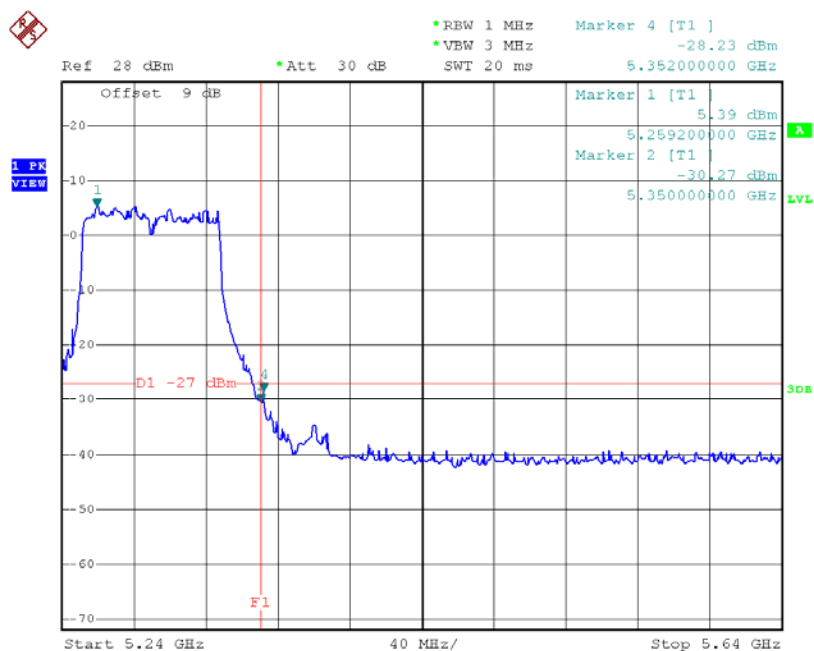
Date: 3.APR.2015 17:24:19

Test Mode:	UNII-2A/TX AC80 Mode_ANT 1
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TX mode CH58



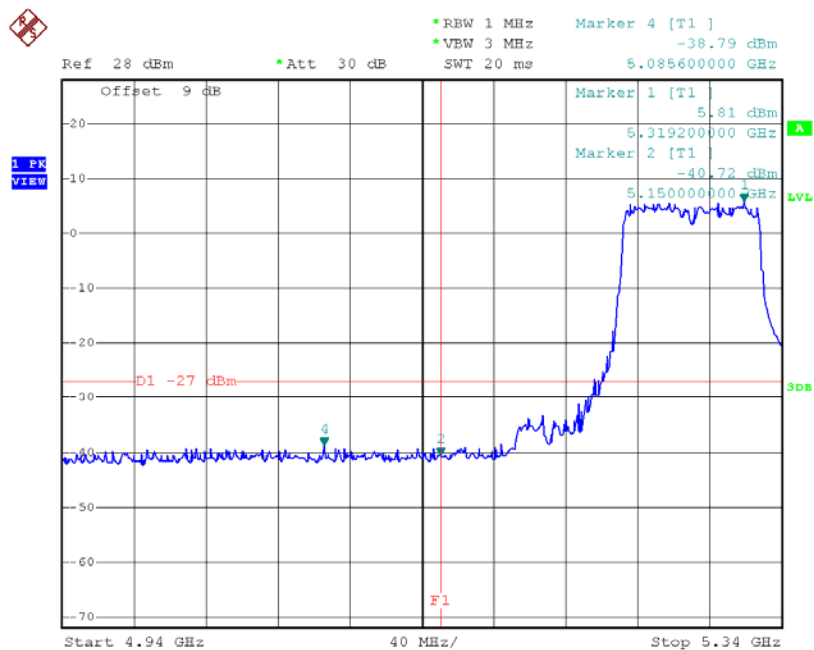
Date: 3.APR.2015 18:09:24



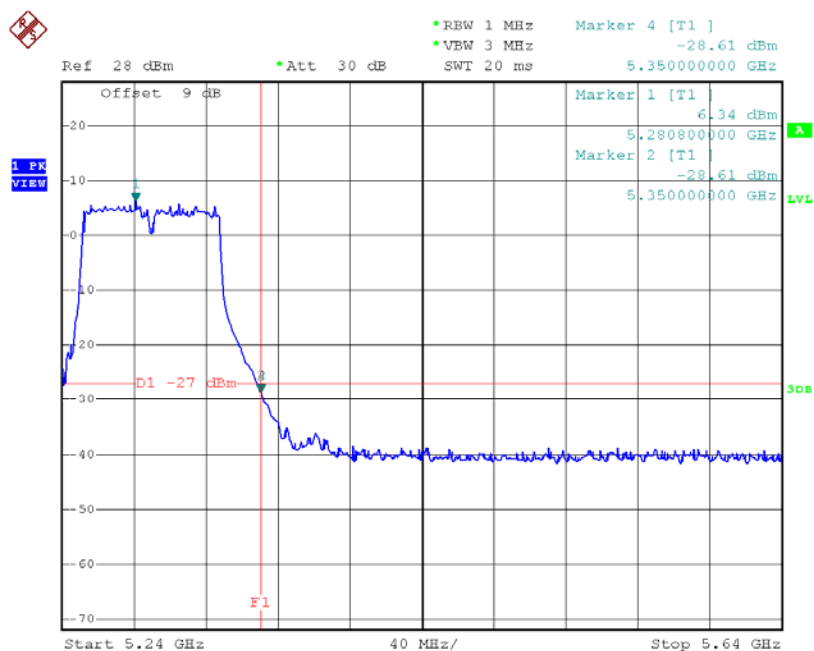
Date: 3.APR.2015 18:09:31

Test Mode:	UNII-2A/TX AC80 Mode_ANT 2
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TX mode CH58



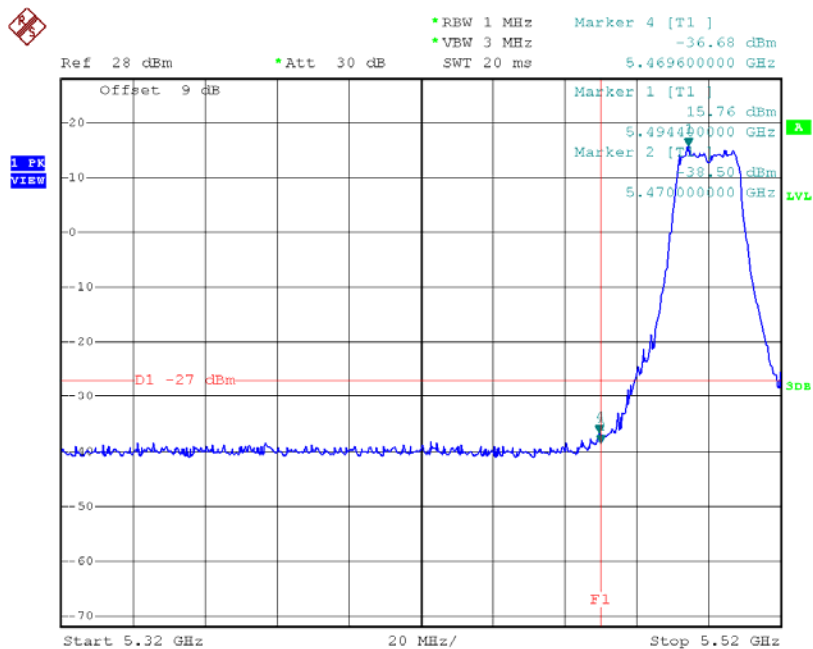
Date: 3.APR.2015 17:28:26



Date: 3.APR.2015 17:28:33

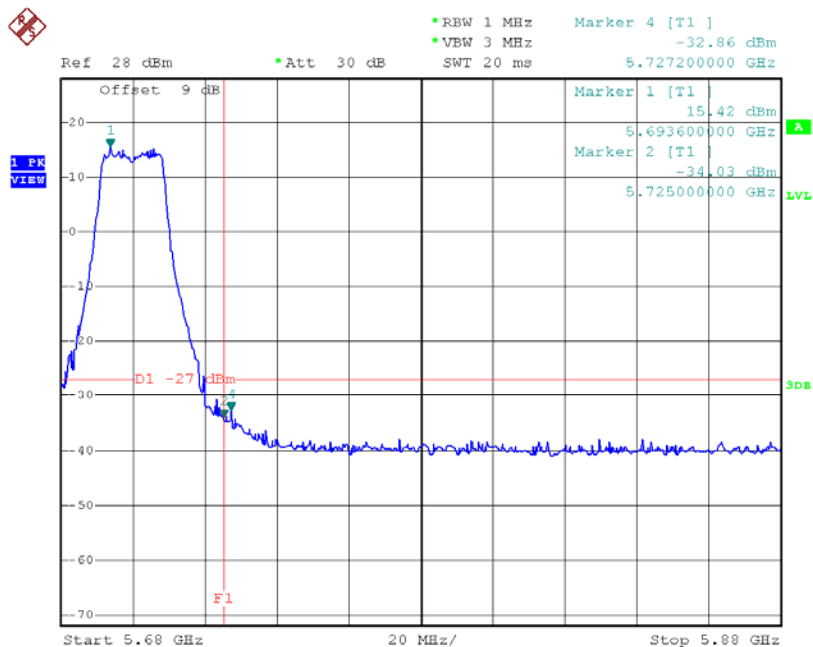
Test Mode: UNII-2C/TX AC20 Mode_ANT 1

TX mode CH100



Date: 3.APR.2015 17:56:29

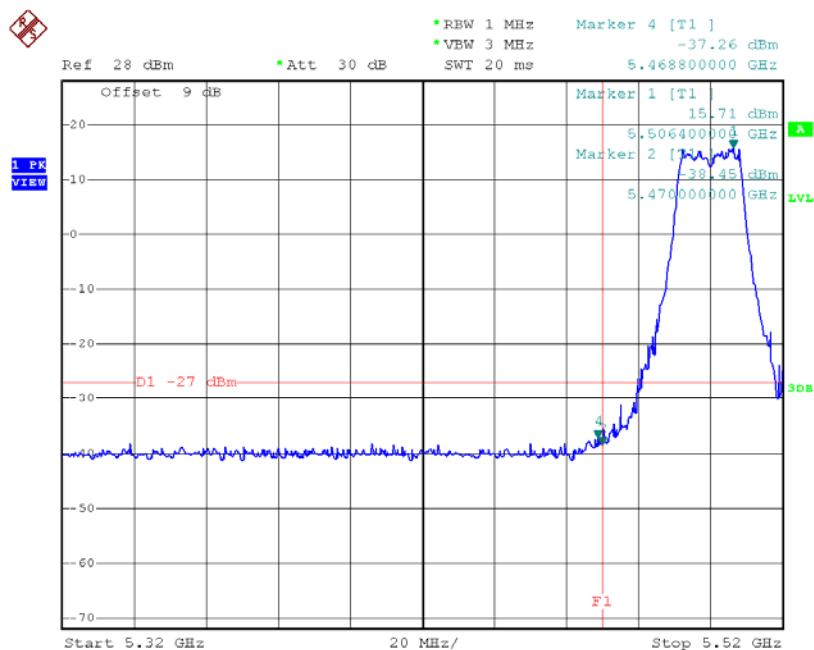
TX mode CH140



Date: 3.APR.2015 17:57:09

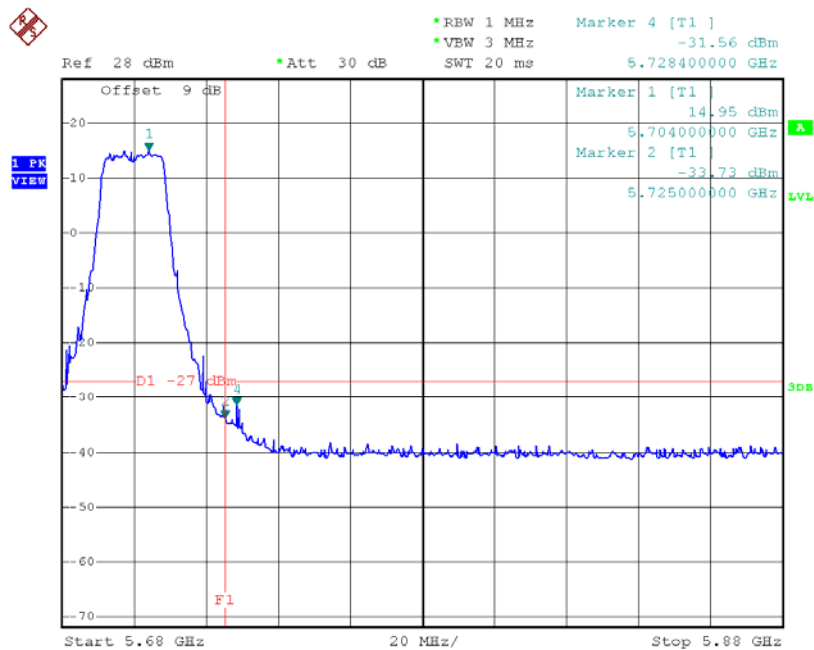
Test Mode:	UNII-2C/TX AC20 Mode_ANT 2
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TX mode CH100



Date: 3.APR.2015 17:15:17

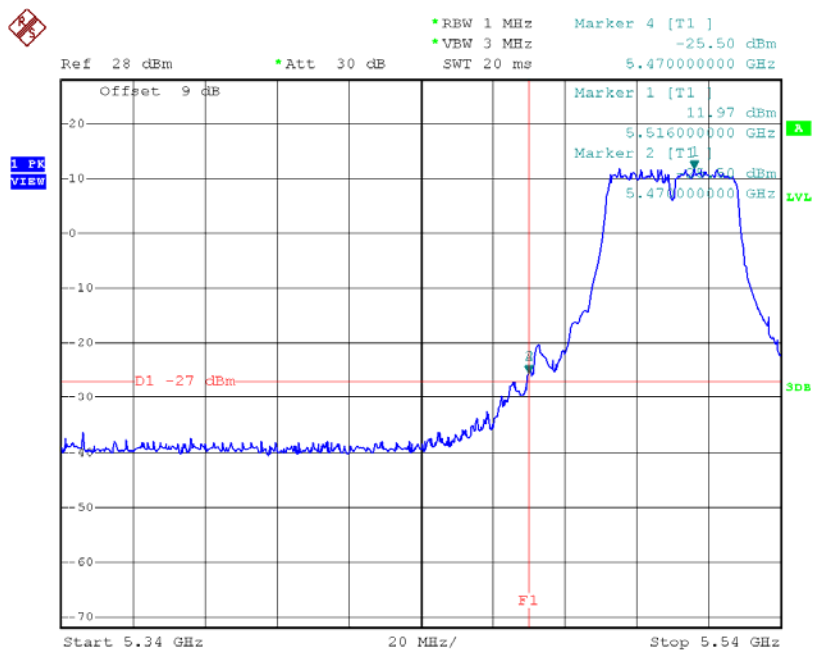
TX mode CH140



Date: 3.APR.2015 17:16:00

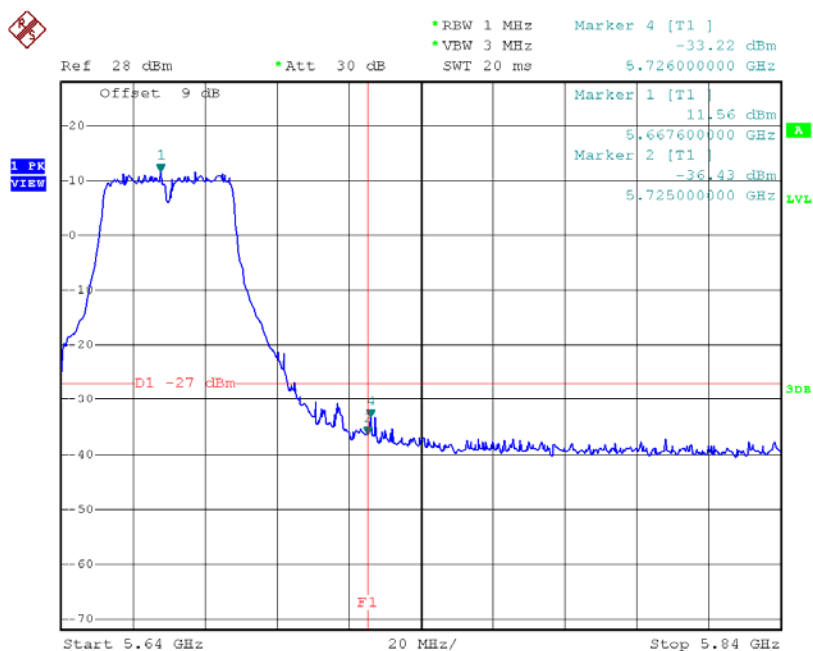
Test Mode:	UNII-2C/TX AC40 Mode_ANT 1
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TX mode CH102



Date: 3.APR.2015 18:05:18

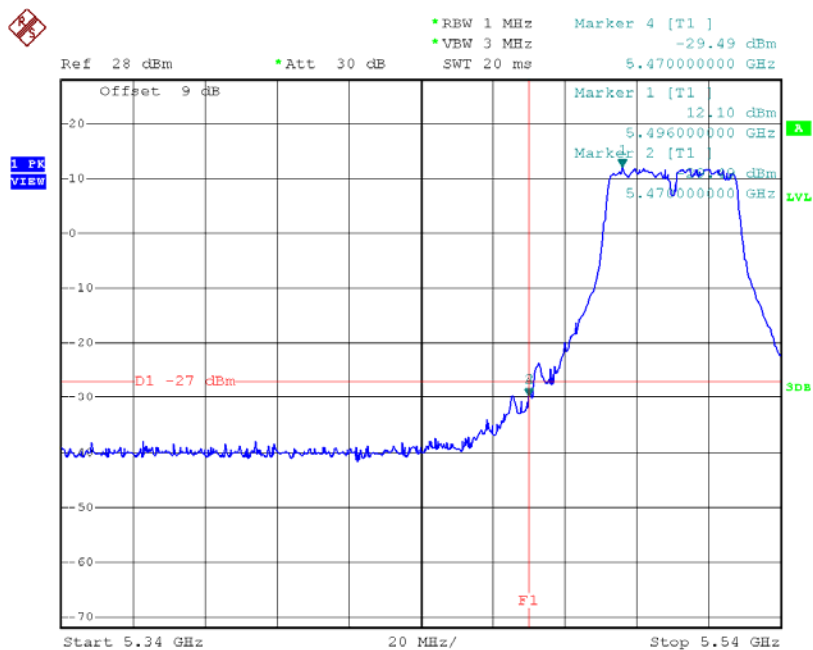
TX mode CH134



Date: 3.APR.2015 18:06:05

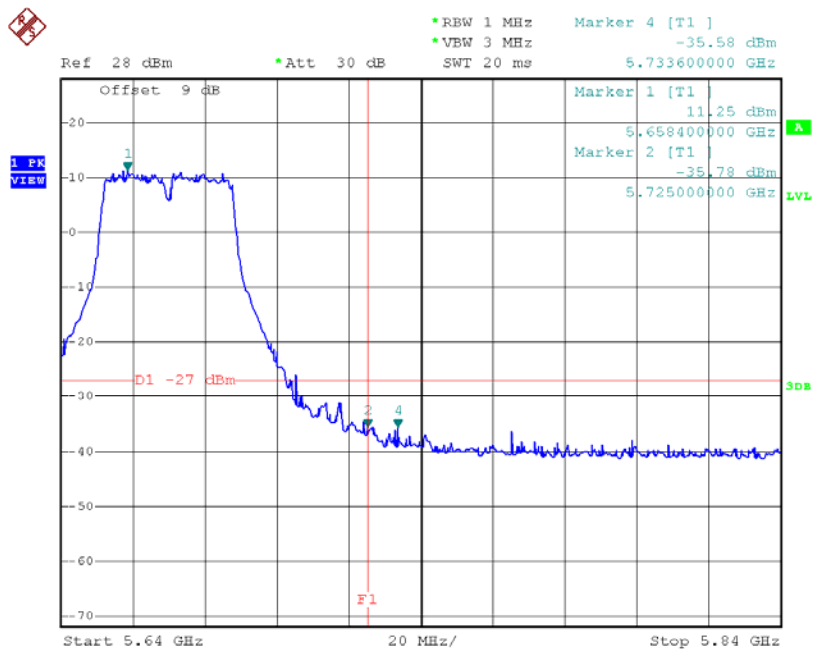
Test Mode: UNII-2C/TX AC40 Mode_ANT 2

TX mode CH102



Date: 3.APR.2015 17:24:44

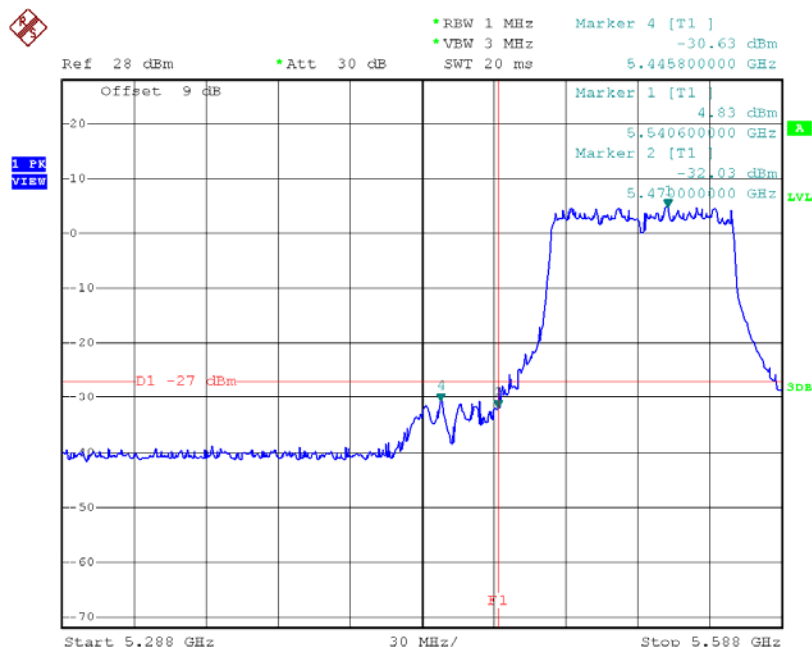
TX mode CH134



Date: 3.APR.2015 17:25:34

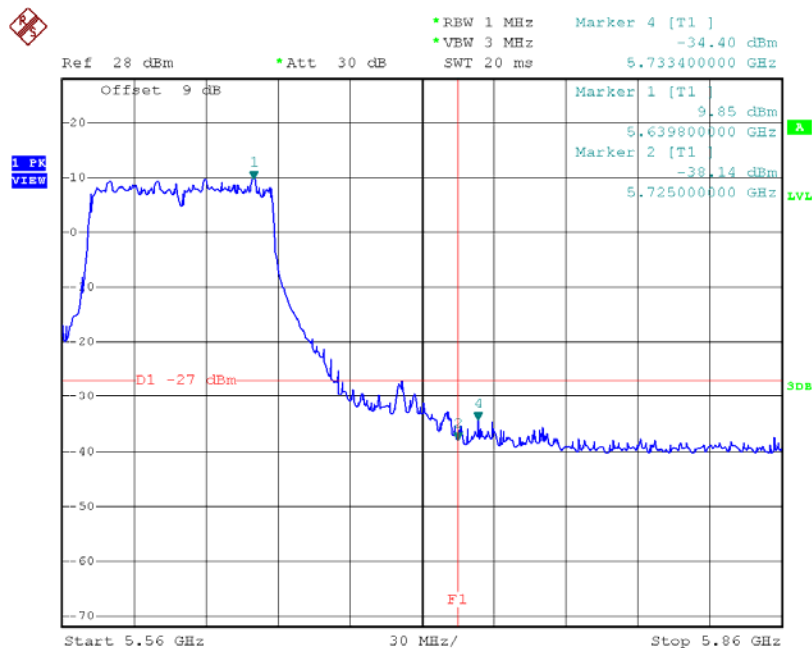
Test Mode:	UNII-2C/TX AC80 Mode_ANT 1
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TX mode CH106



Date: 3.APR.2015 18:10:05

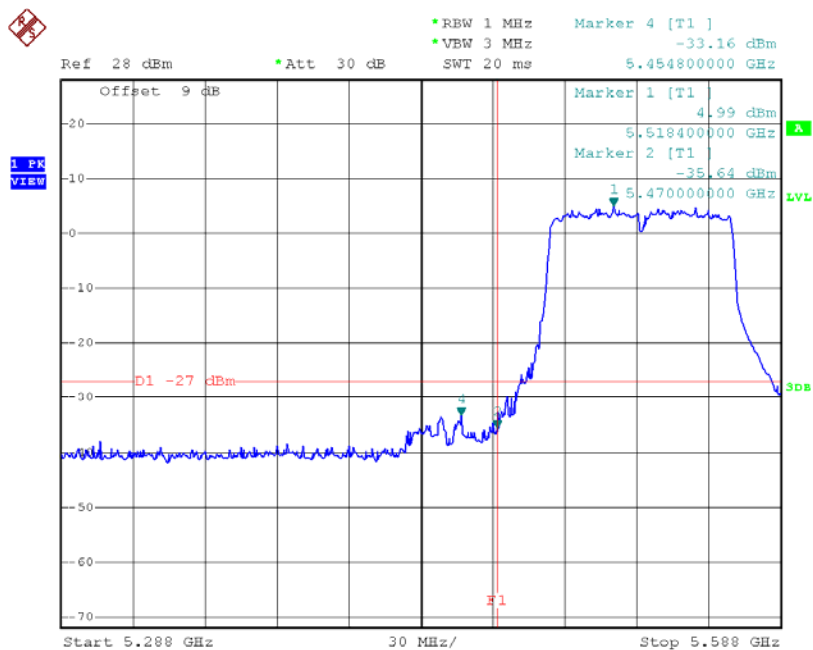
TX mode CH122



Date: 3.APR.2015 18:10:35

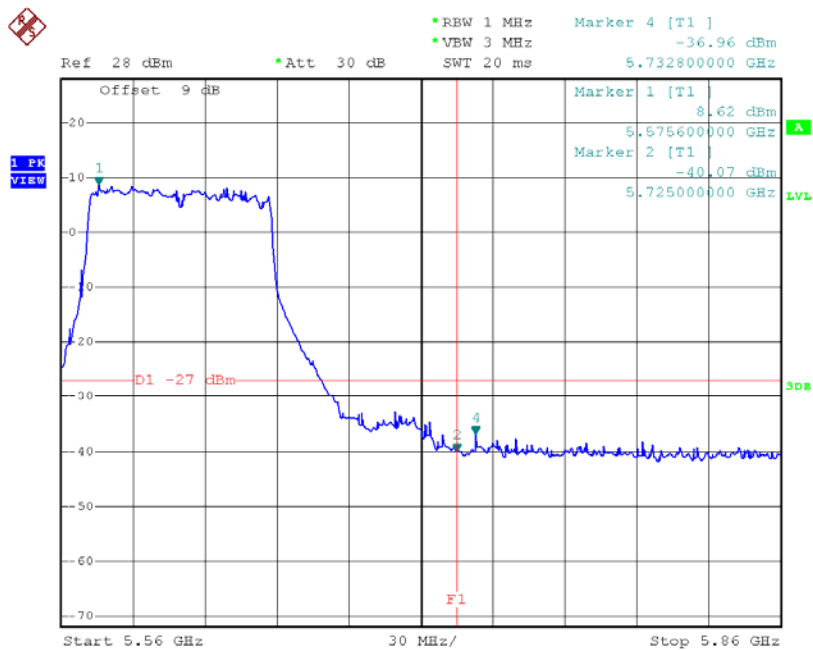
Test Mode: UNII-2C/TX AC80 Mode_ANT 2

TX mode CH106



Date: 3.APR.2015 17:29:03

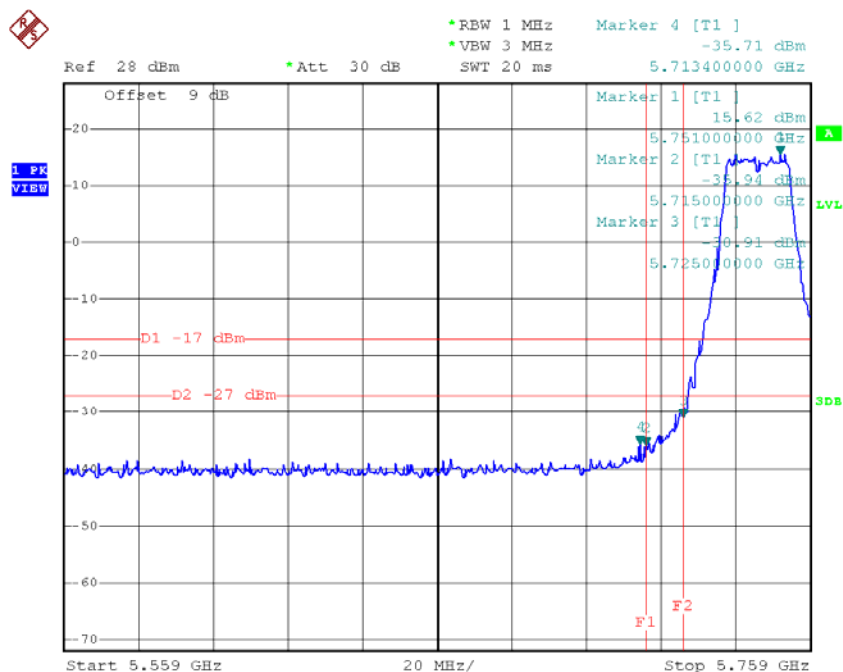
TX mode CH122



Date: 3.APR.2015 17:30:04

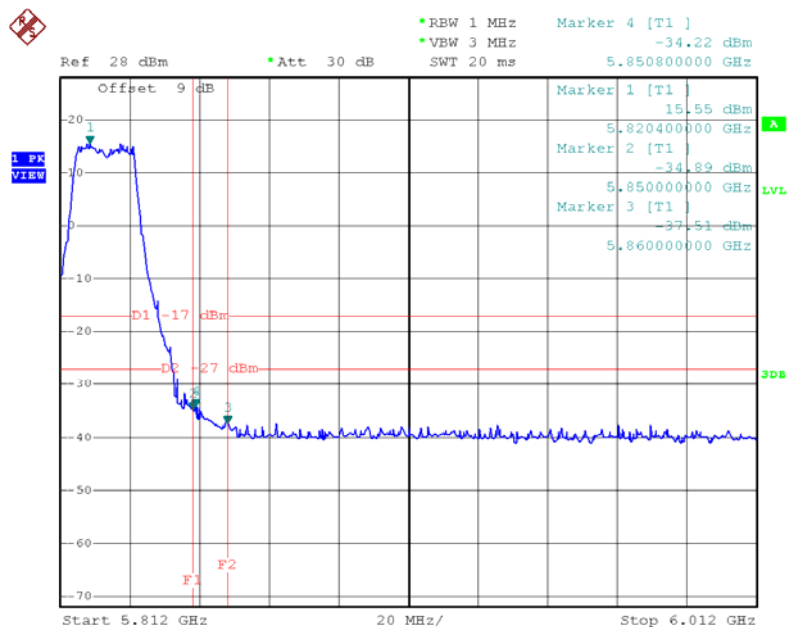
Test Mode: UNII-3/TX AC20 Mode_ANT 1

TXAC HT20 mode CH149



Date: 3.APR.2015 17:57:32

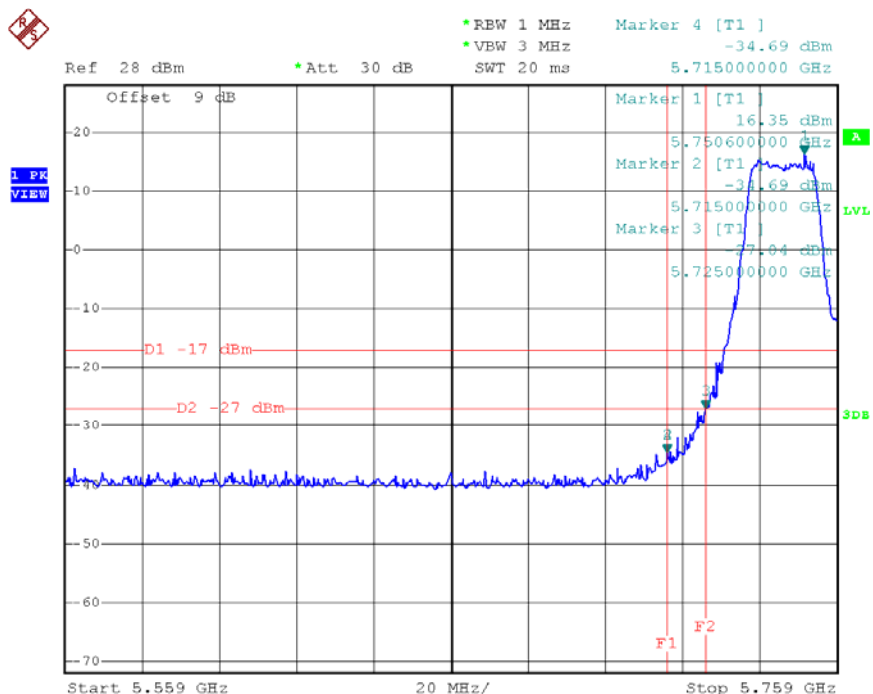
TXAC HT20 mode CH165



Date: 3.APR.2015 17:58:14

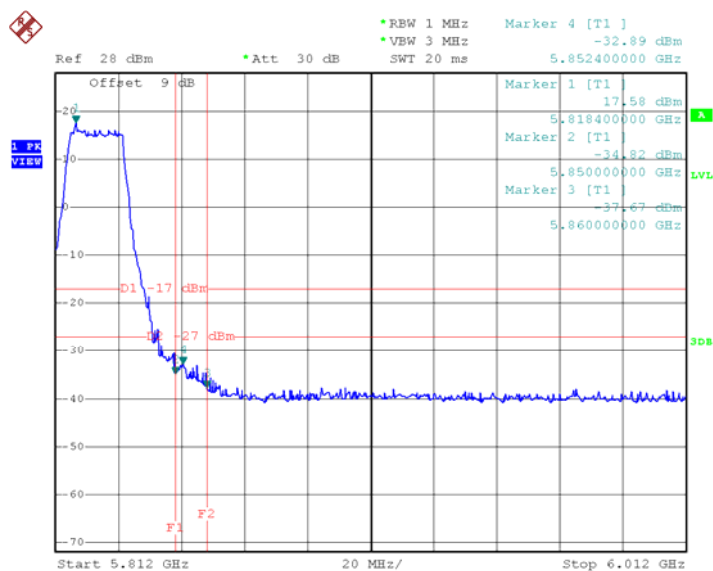
Test Mode: UNII-3/TX AC20 Mode_ANT 2

TXAC HT20 mode CH149



Date: 3.APR.2015 17:16:25

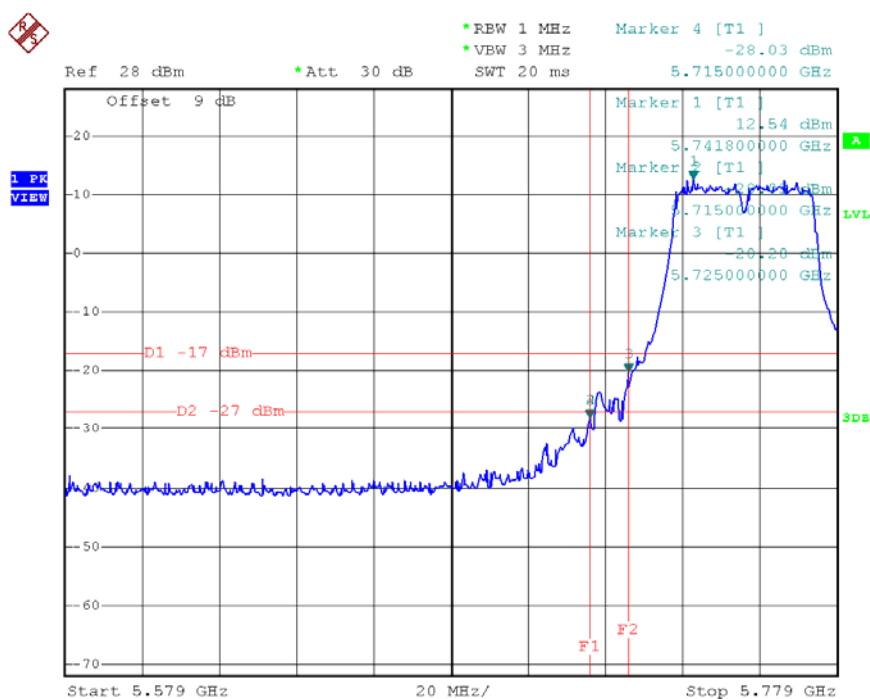
TXAC HT20 mode CH165



Date: 3.APR.2015 17:17:08

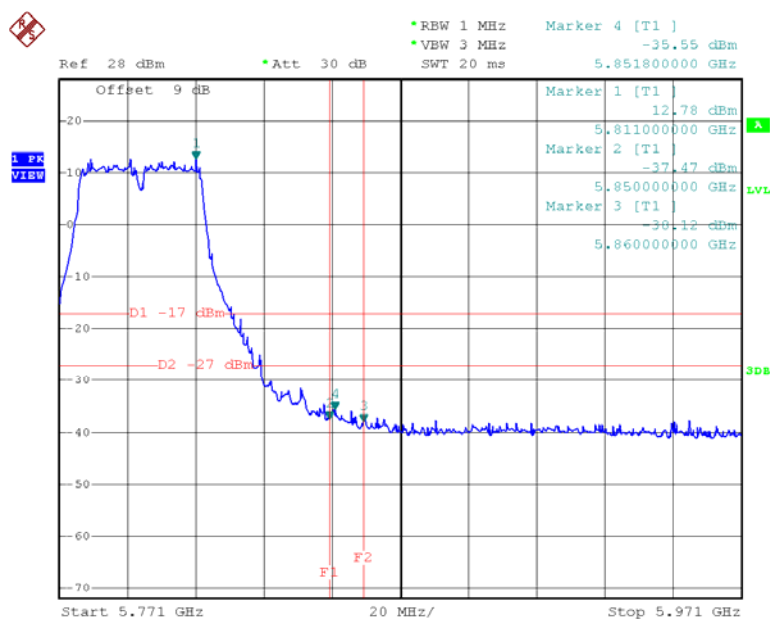
Test Mode: UNII-3/TX AC40 Mode_ANT 1

TXAC HT40 mode CH151



Date: 3.APR.2015 18:06:31

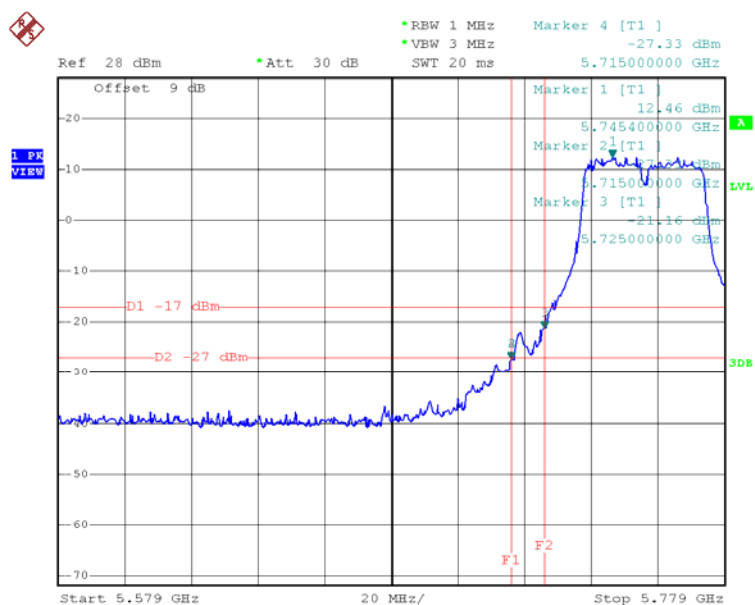
TXAC HT40 mode CH159



Date: 3.APR.2015 18:06:57

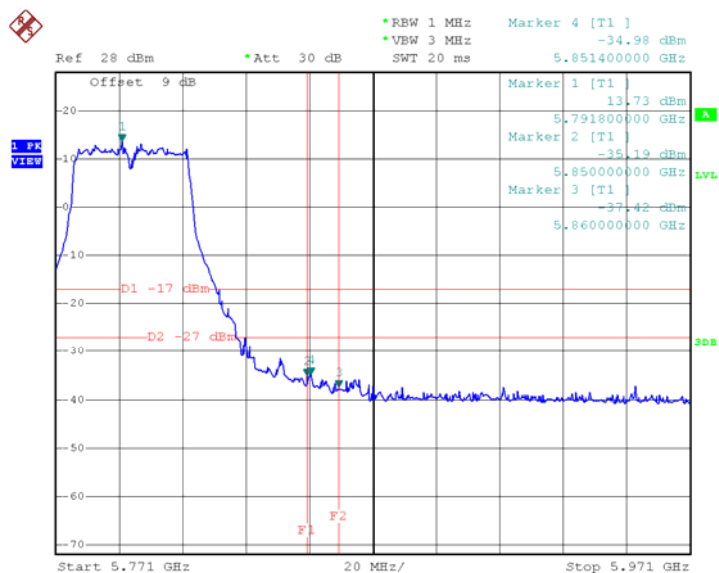
Test Mode: UNII-3/TX AC40 Mode_ANT 2

TX AC HT40 mode CH151



Date: 3.APR.2015 17:26:04

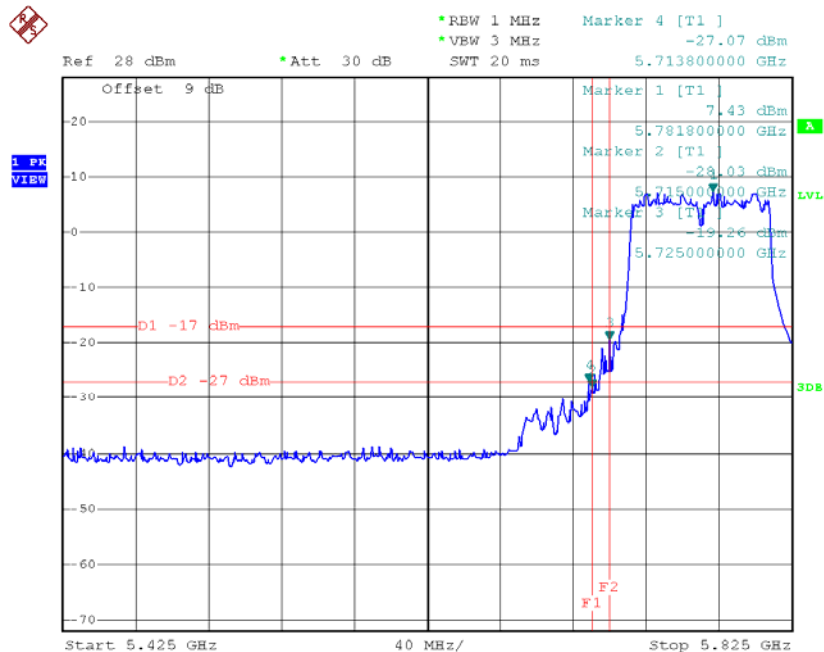
TX AC HT40 mode CH159



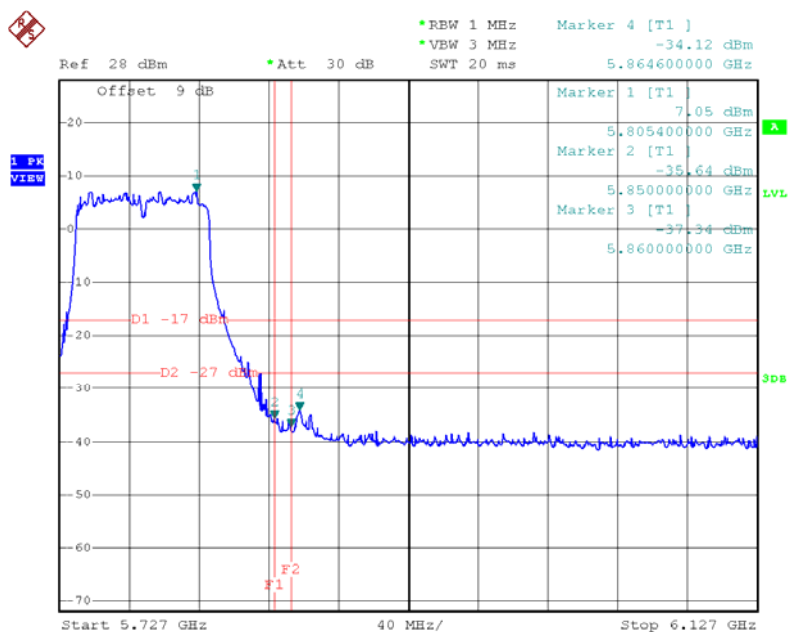
Date: 3.APR.2015 17:26:39

Test Mode: UNII-3/TX AC80 Mode_ANT 1

TXAC HT80 mode CH155



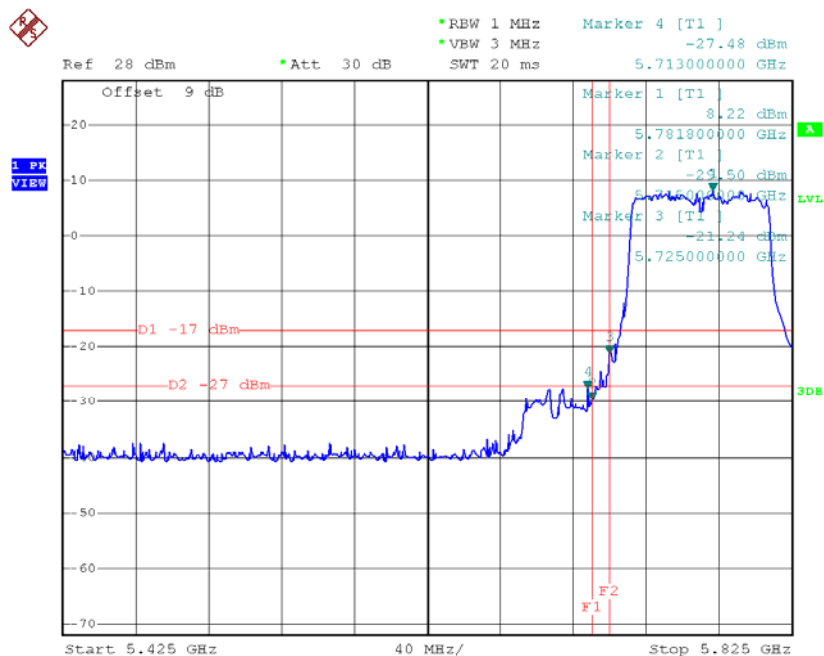
Date: 3.APR.2015 18:12:39



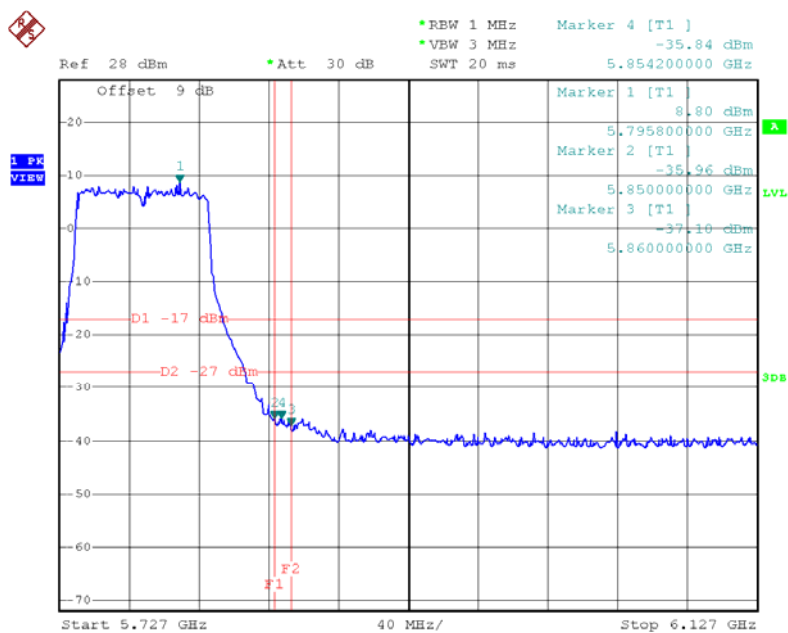
Date: 3.APR.2015 18:12:47

Test Mode: UNII-3/TX AC80 Mode_ANT 2

TX AC HT80 mode CH155



Date: 3.APR.2015 17:32:15

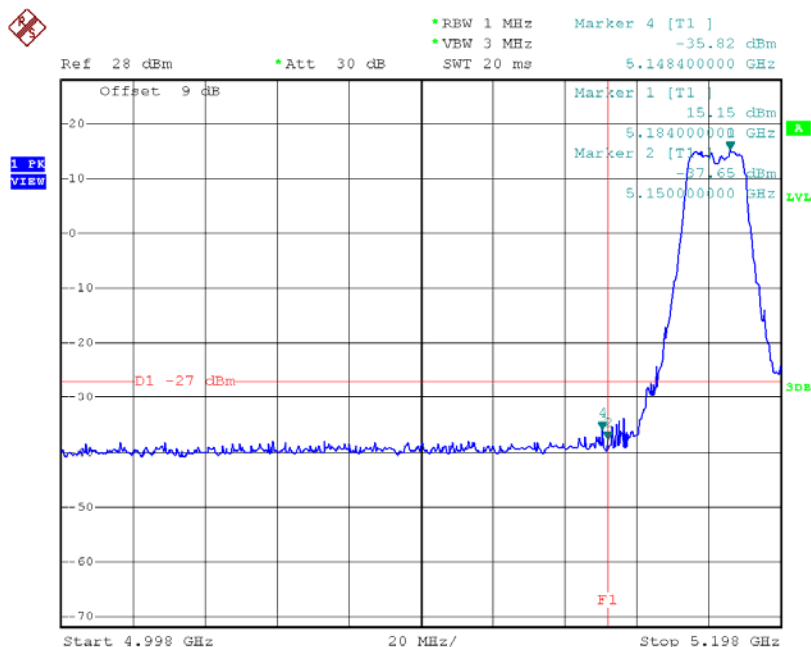


Date: 3.APR.2015 17:32:23

For 3TX

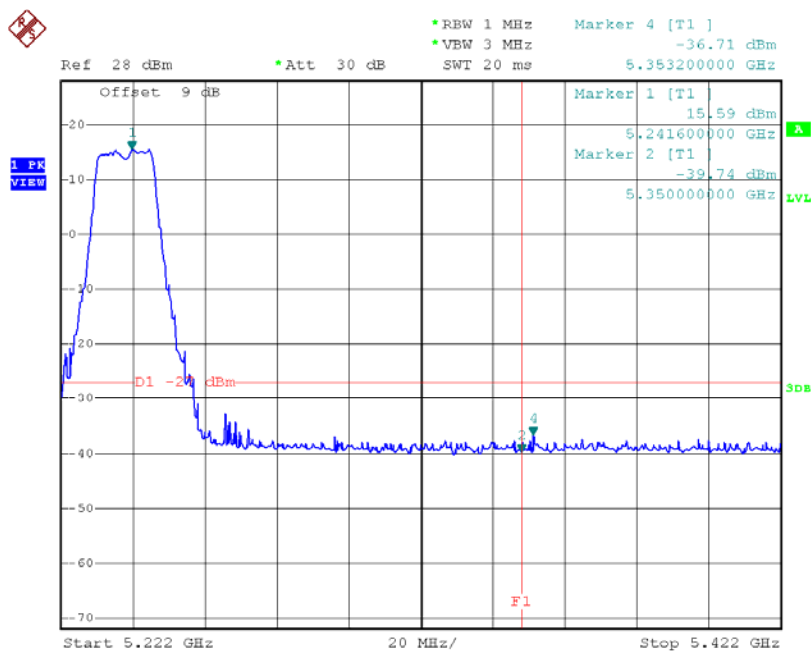
Test Mode:	UNII-1/TX A Mode_ANT 1
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TX mode CH36



Date: 7.APR.2015 11:47:01

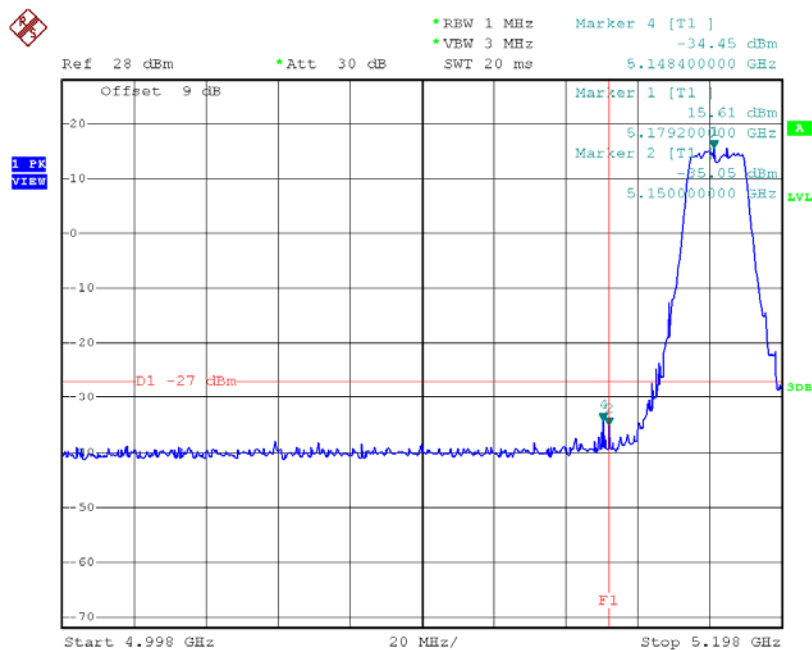
TX modeCH48



Date: 7.APR.2015 11:50:41

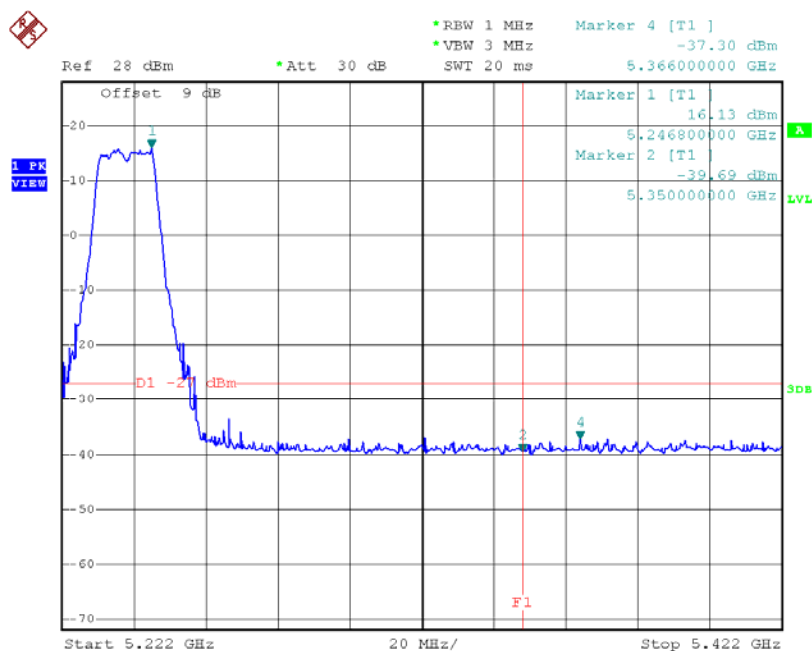
Test Mode:	UNII-1/TX A Mode_ANT 2
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TX mode CH36



Date: 7.APR.2015 14:05:22

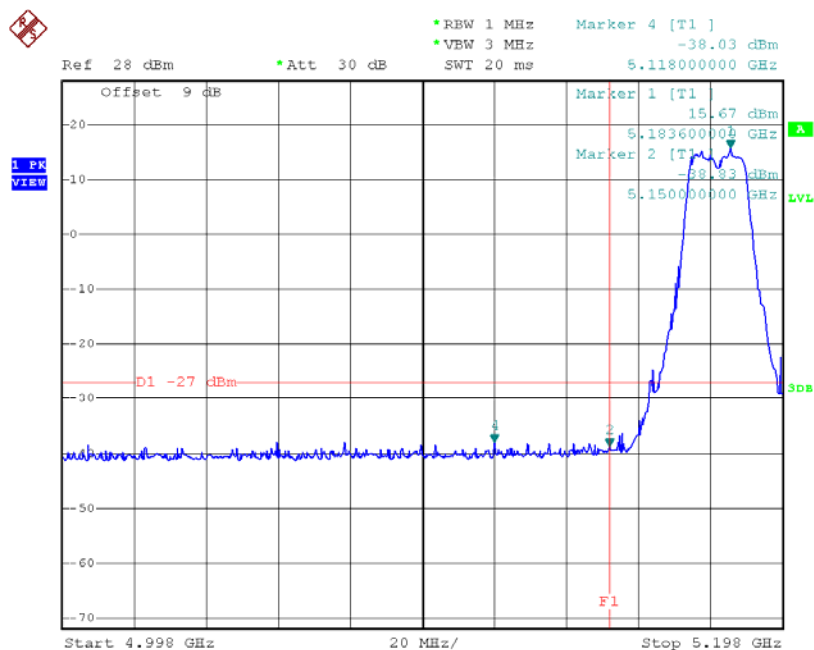
TX mode CH48



Date: 7.APR.2015 14:07:09

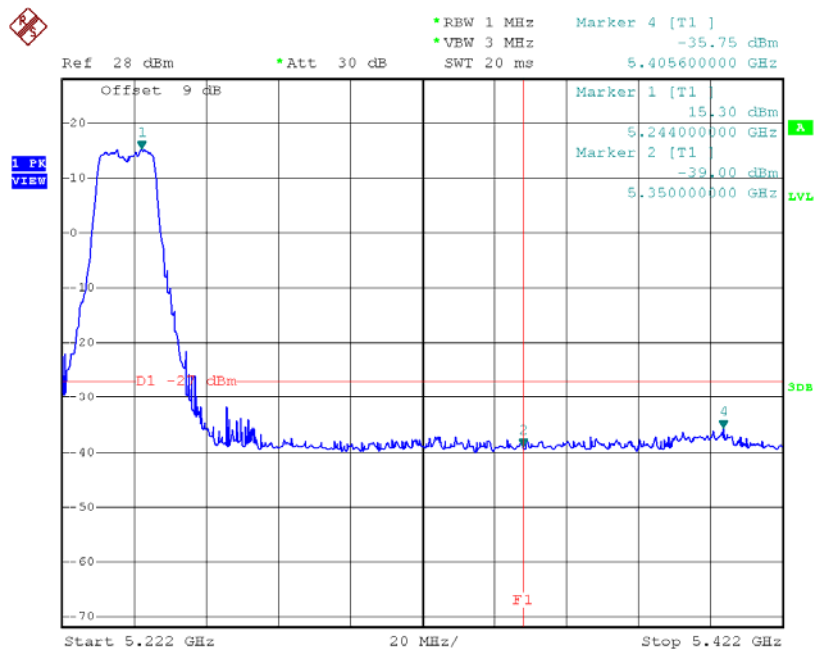
Test Mode:	UNII-1/TX A Mode_ANT 3
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TX mode CH36



Date: 7.APR.2015 14:45:01

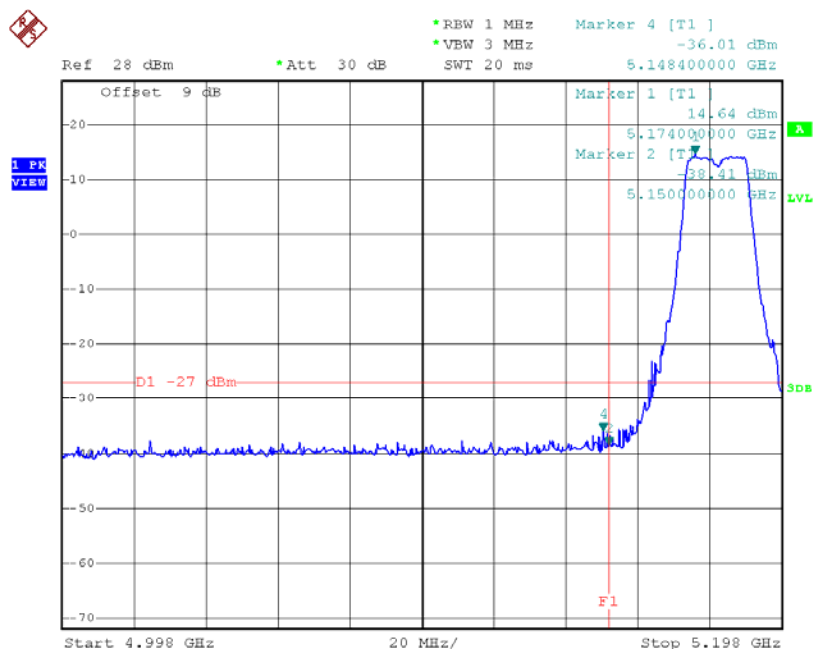
TX mode CH48



Date: 7.APR.2015 14:46:44

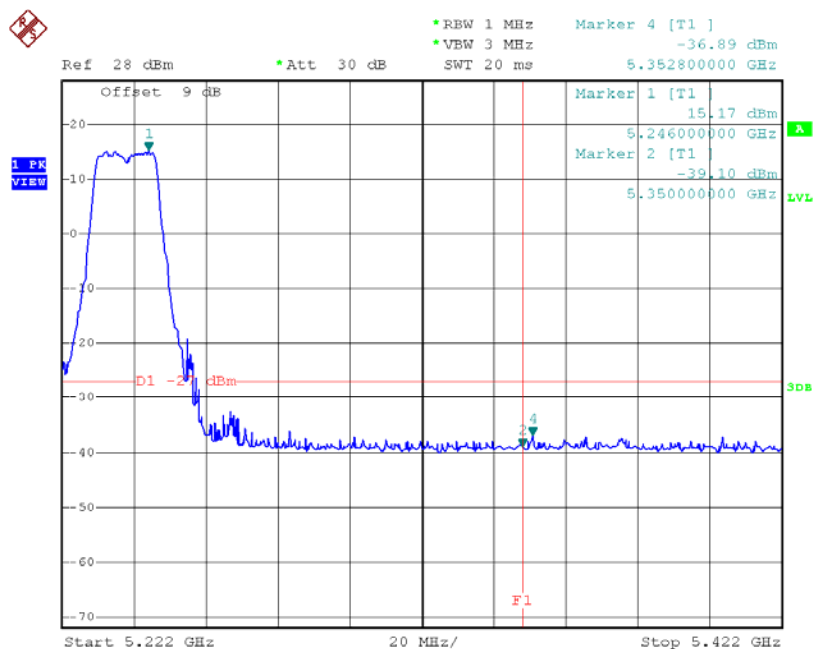
Test Mode: UNII-1/TX N20 Mode_ANT 1

TX mode CH36



Date: 7.APR.2015 11:58:34

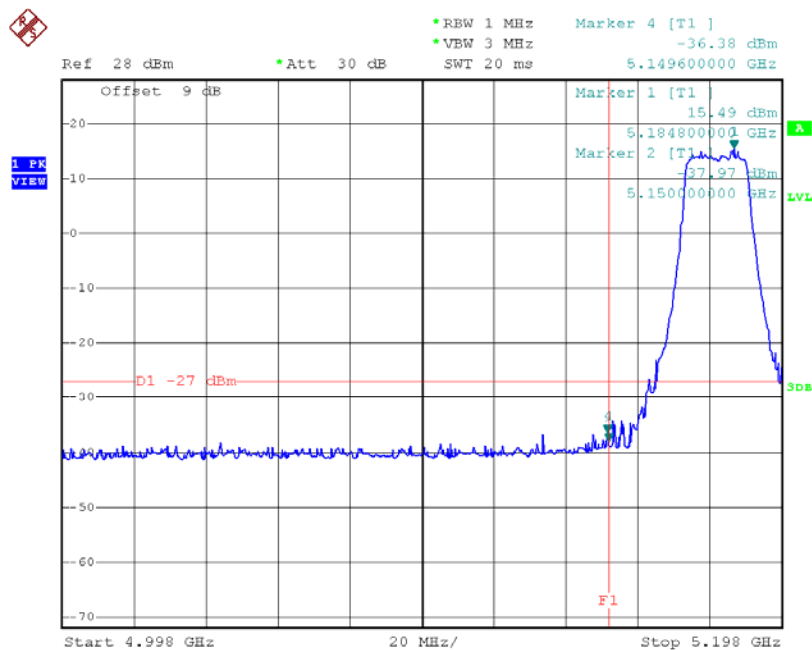
TX modeCH48



Date: 7.APR.2015 11:59:27

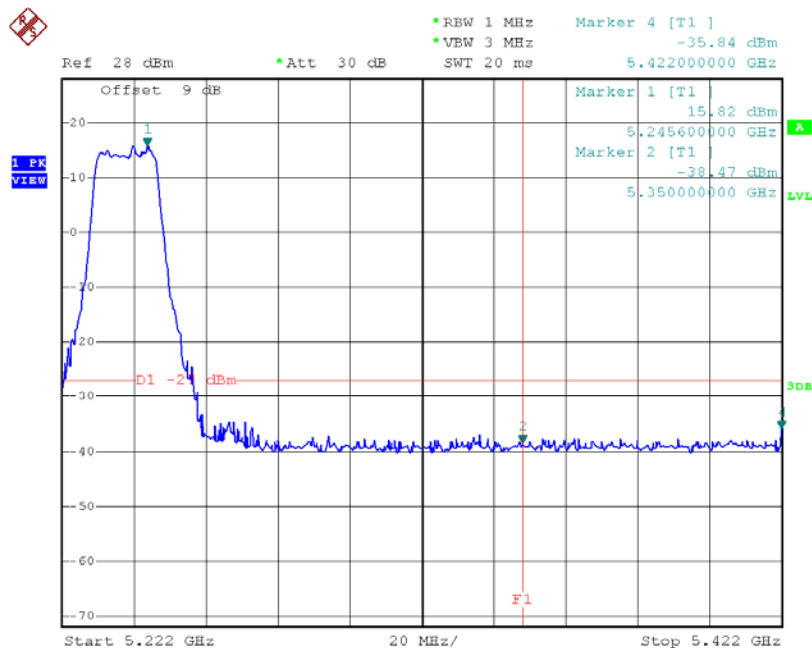
Test Mode:	UNII-1/TX N20 Mode_ANT 2
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TX mode CH36



Date: 7.APR.2015 14:15:19

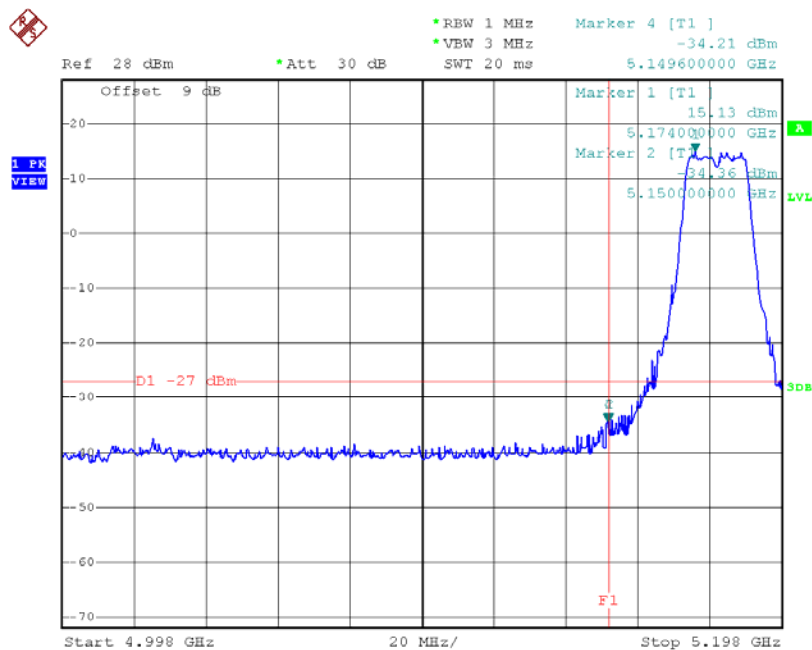
TX mode CH48



Date: 7.APR.2015 14:16:10

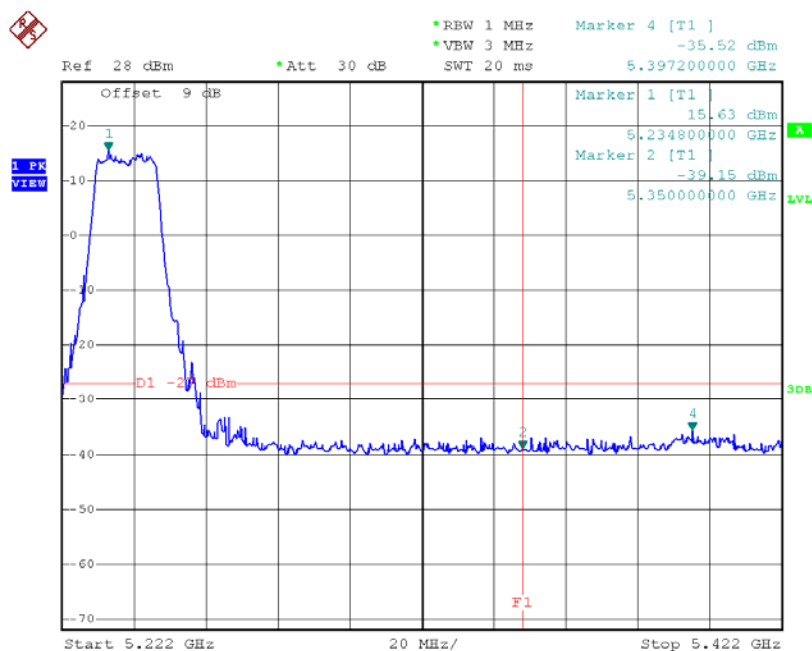
Test Mode: UNII-1/TX N20 Mode_ANT 3

TX mode CH36



Date: 7.APR.2015 14:54:25

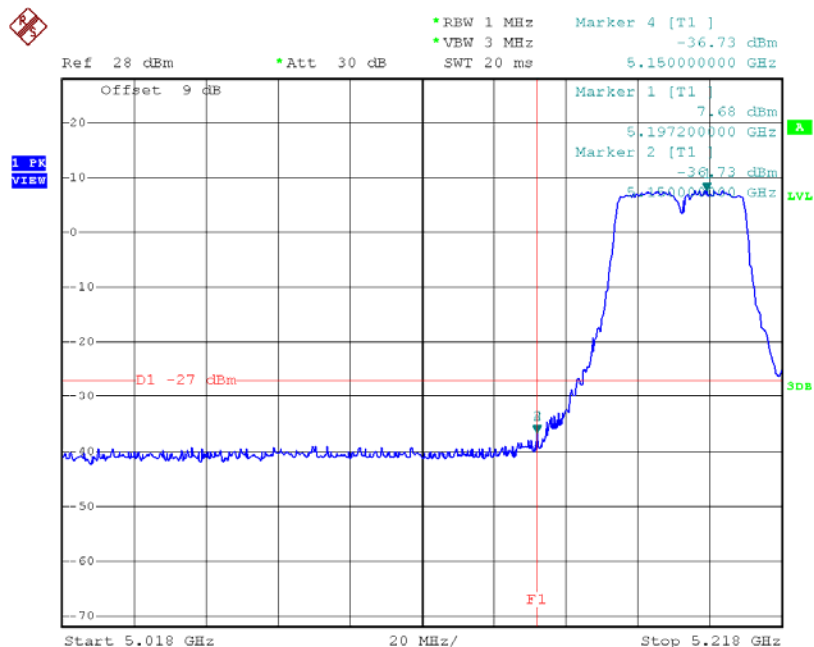
TX mode CH48



Date: 7.APR.2015 14:55:19

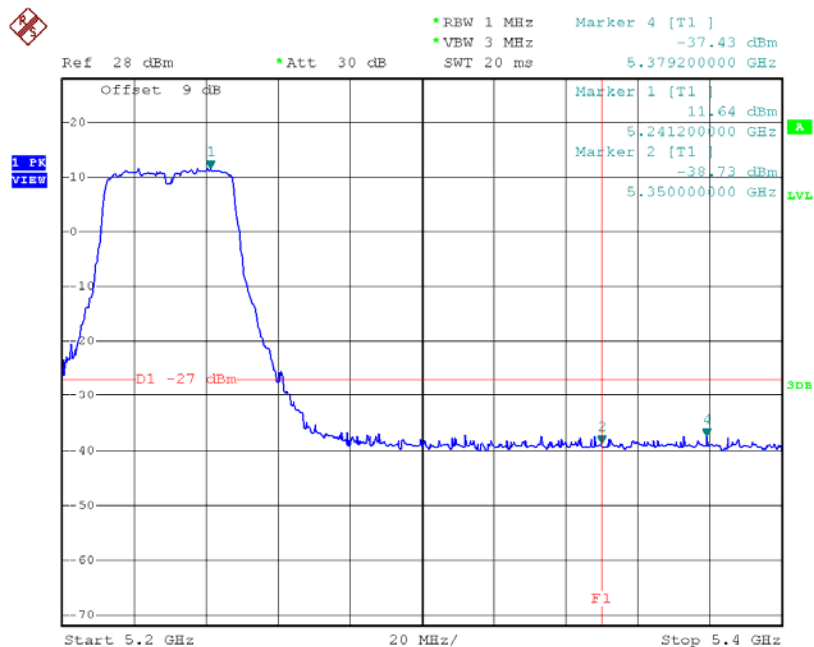
Test Mode: UNII-1/TX N40 Mode_ANT 1

TX mode CH38



Date: 7.APR.2015 12:12:38

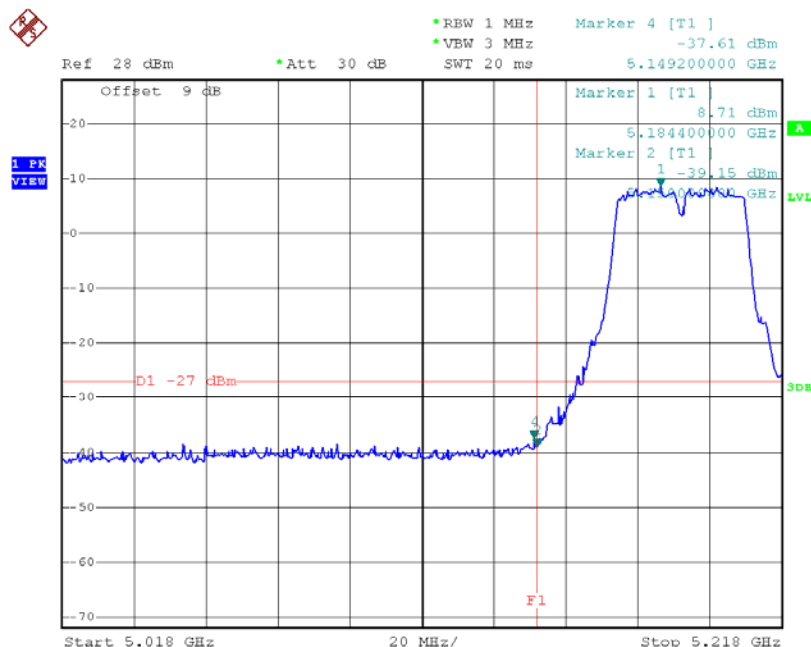
TX modeCH46



Date: 7.APR.2015 12:13:14

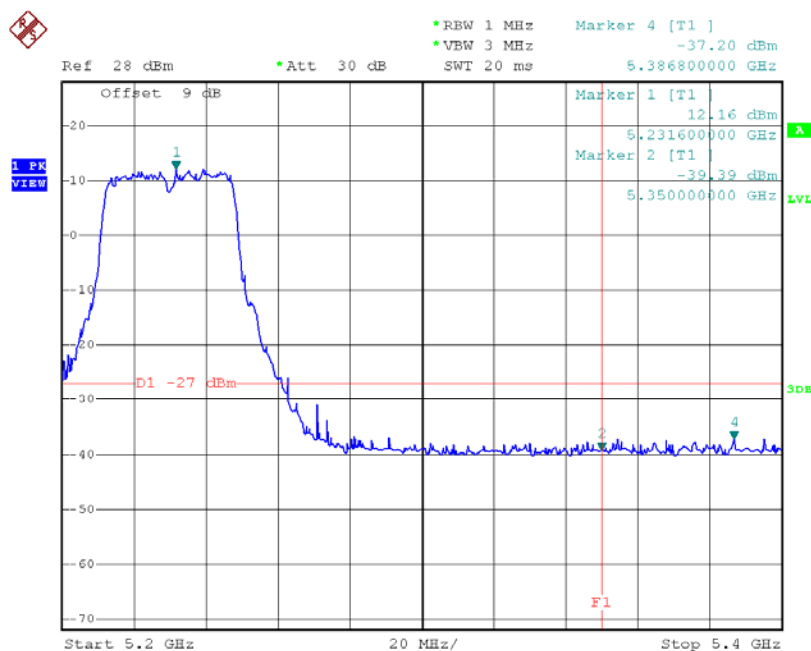
Test Mode:	UNII-1/TX N40 Mode_ANT 2
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TX mode CH38



Date: 7.APR.2015 14:25:59

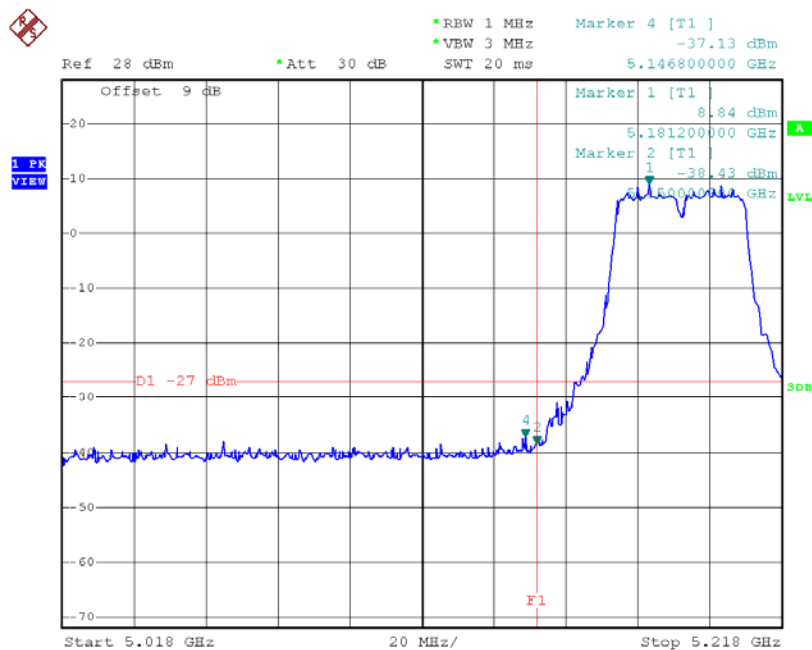
TX mode CH46



Date: 7.APR.2015 14:27:00

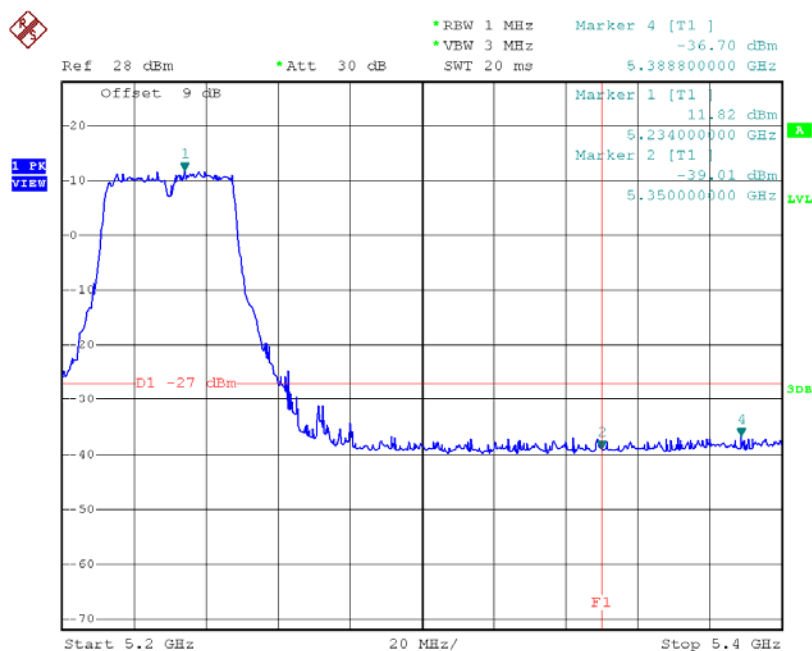
Test Mode:	UNII-1/TX N40 Mode_ANT 3
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TX mode CH38



Date: 7.APR.2015 15:04:44

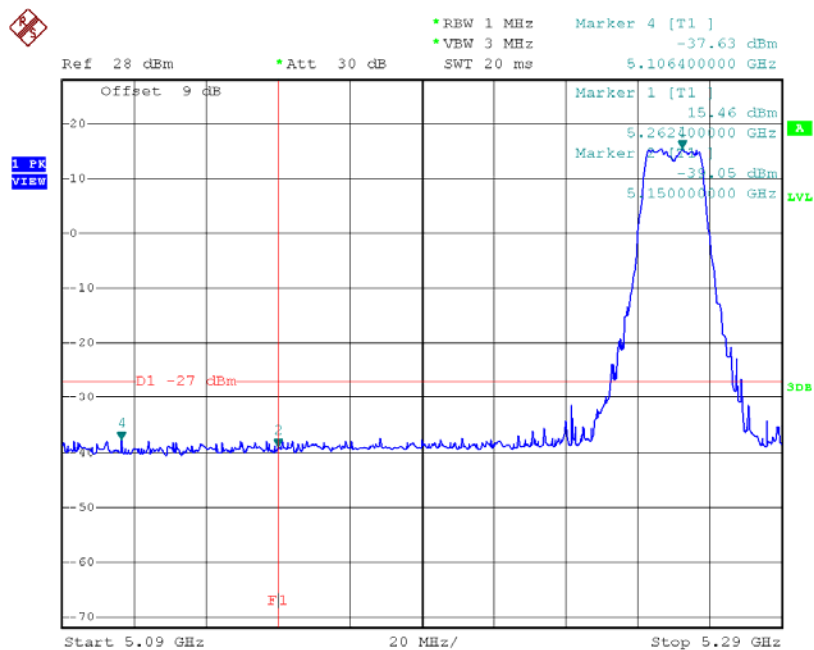
TX mode CH46



Date: 7.APR.2015 15:05:14

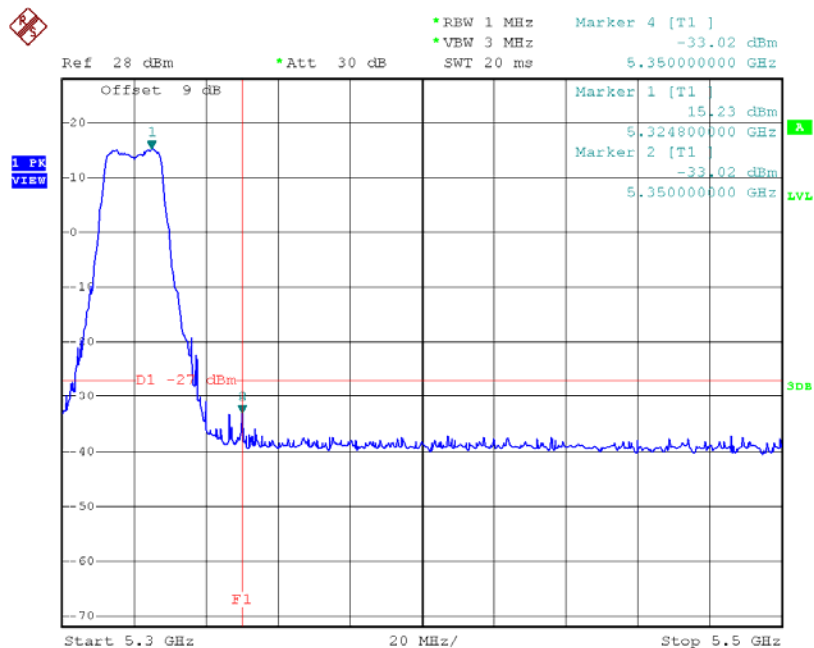
Test Mode:	UNII-2A/TX A Mode_ANT 1
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TX mode CH52



Date: 7.APR.2015 11:51:39

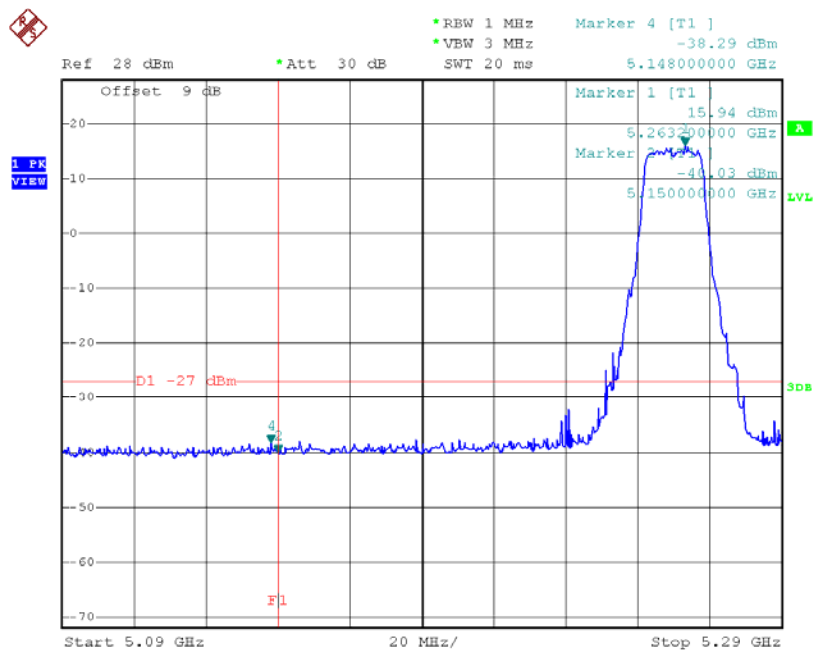
TX mode CH64



Date: 7.APR.2015 11:53:04

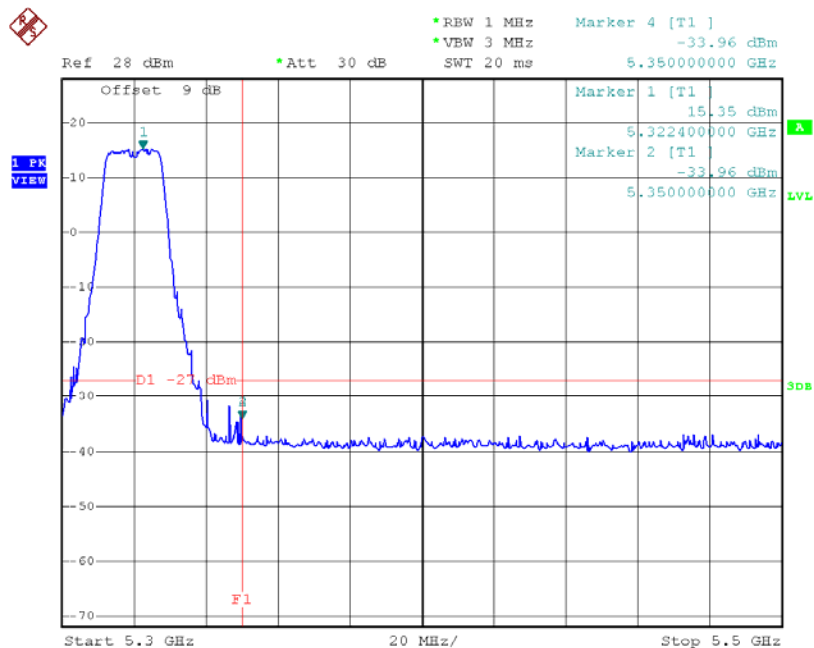
Test Mode:	UNII-2A/TX A Mode_ANT 2
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TX mode CH52



Date: 7.APR.2015 14:07:55

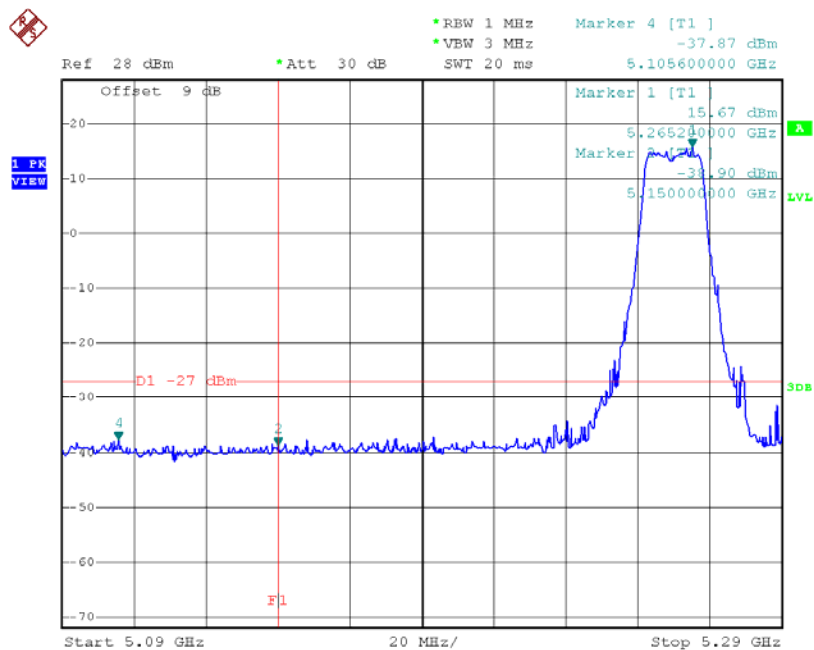
TX mode CH64



Date: 7.APR.2015 14:10:03

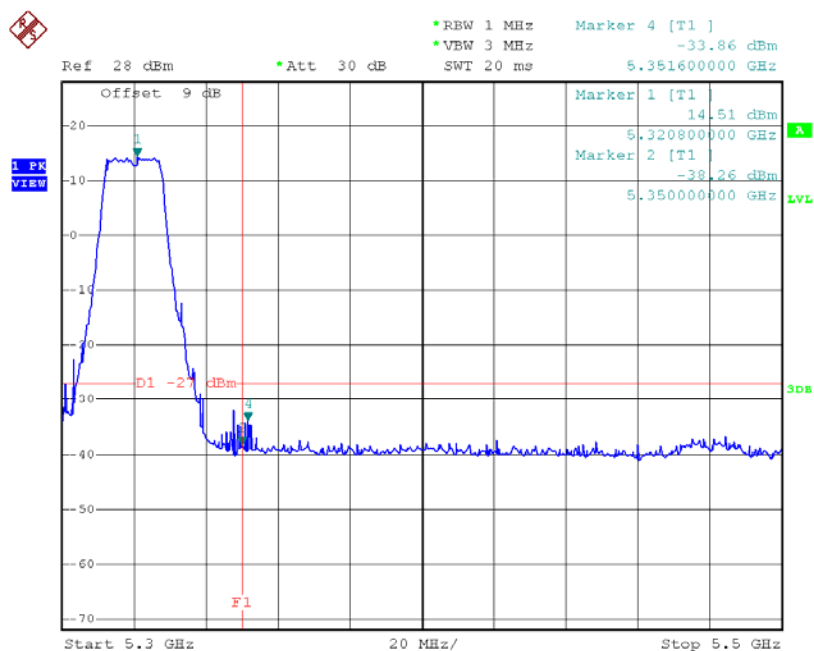
Test Mode:	UNII-2A/TX A Mode_ANT 3
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TX mode CH52



Date: 7.APR.2015 14:47:30

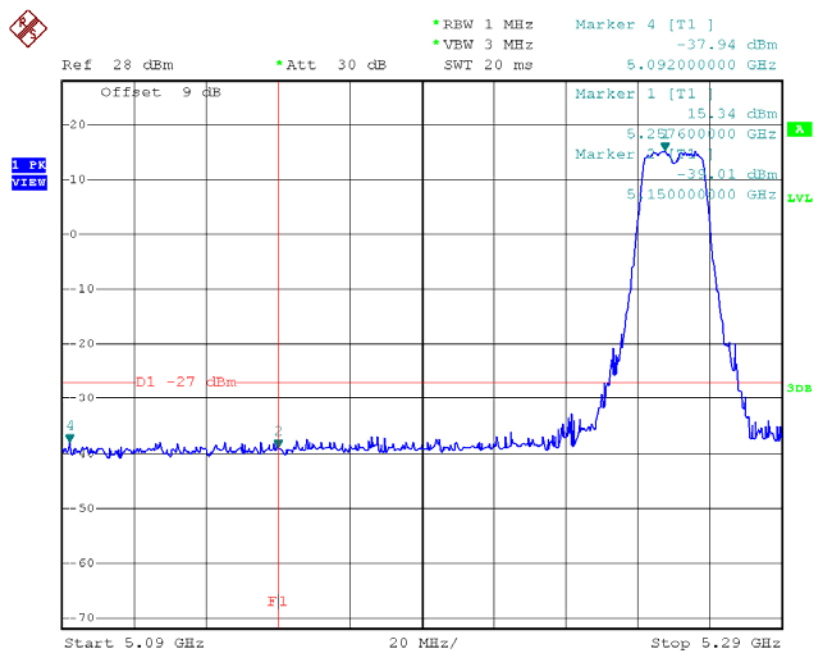
TX mode CH64



Date: 7.APR.2015 14:48:52

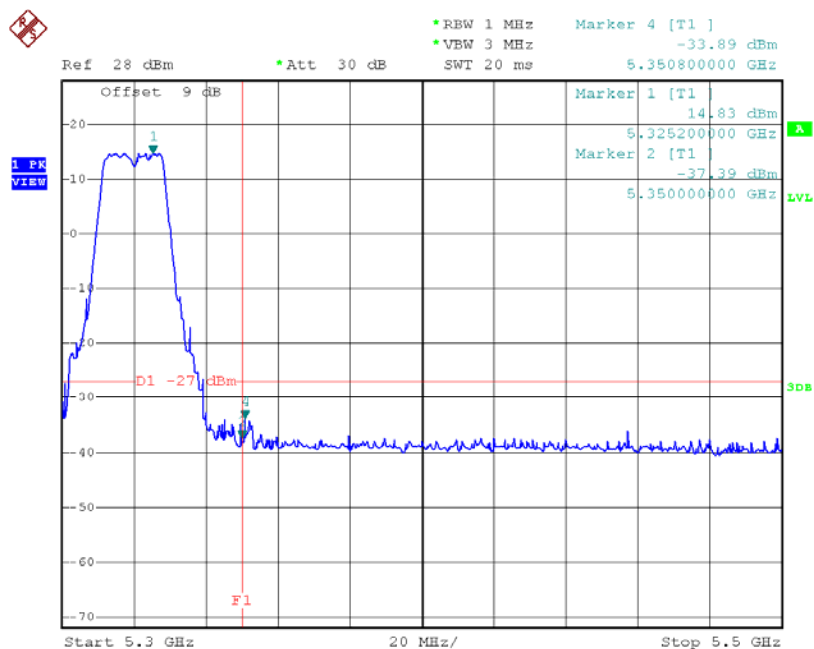
Test Mode: UNII-2A/TX N20 Mode_ANT 1

TX mode CH52



Date: 7.APR.2015 11:59:54

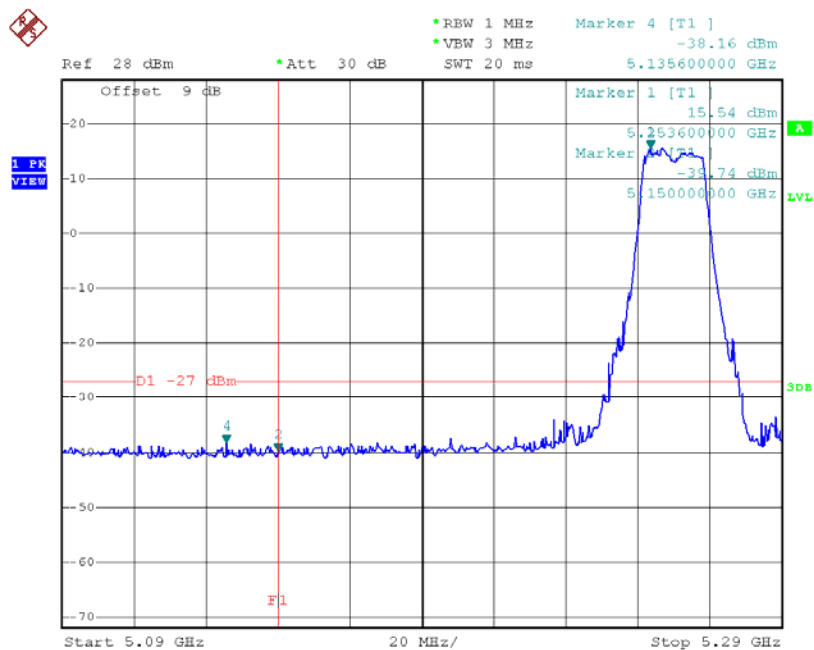
TX modeCH64



Date: 7.APR.2015 12:01:26

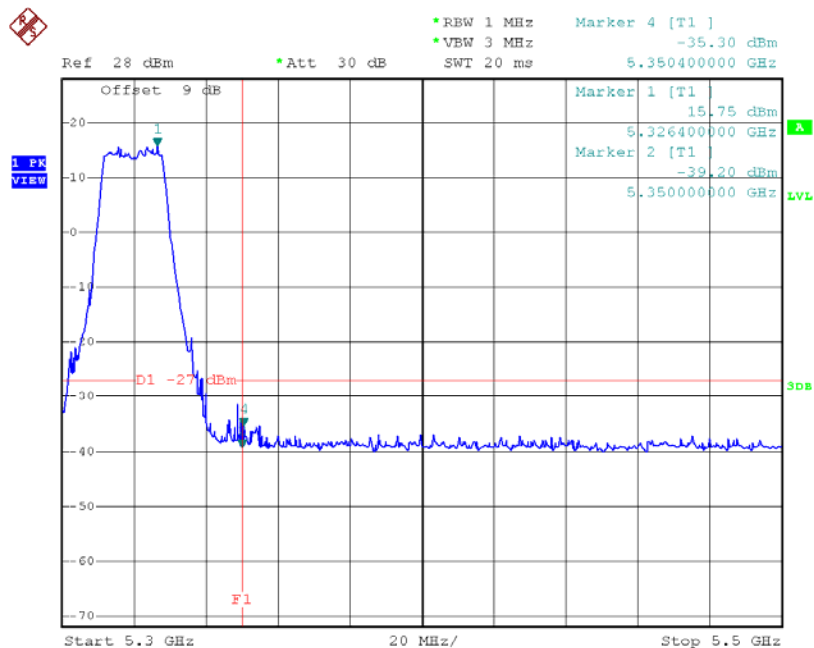
Test Mode:	UNII-2A/TX N20 Mode_ANT 2
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TX mode CH52



Date: 7.APR.2015 14:16:36

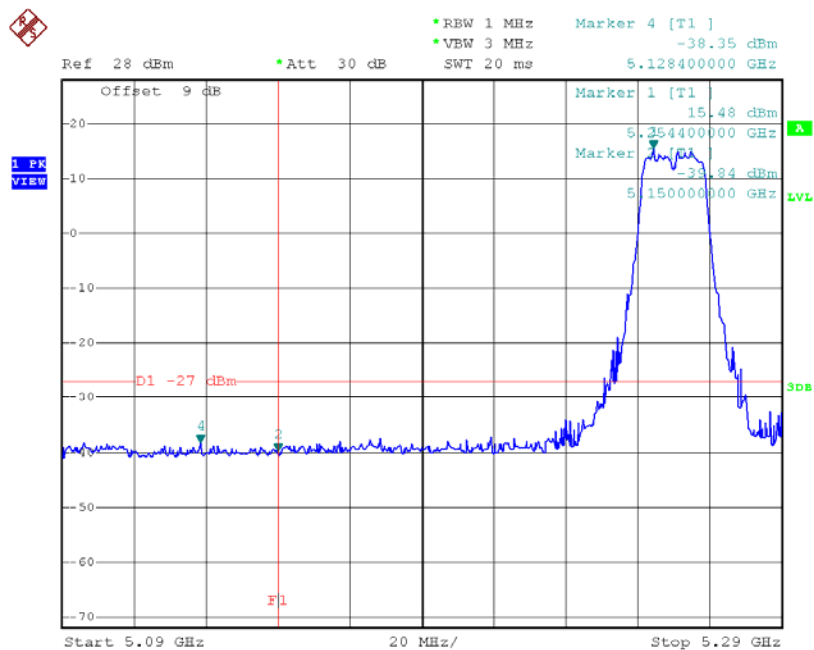
TX mode CH64



Date: 7.APR.2015 14:17:27

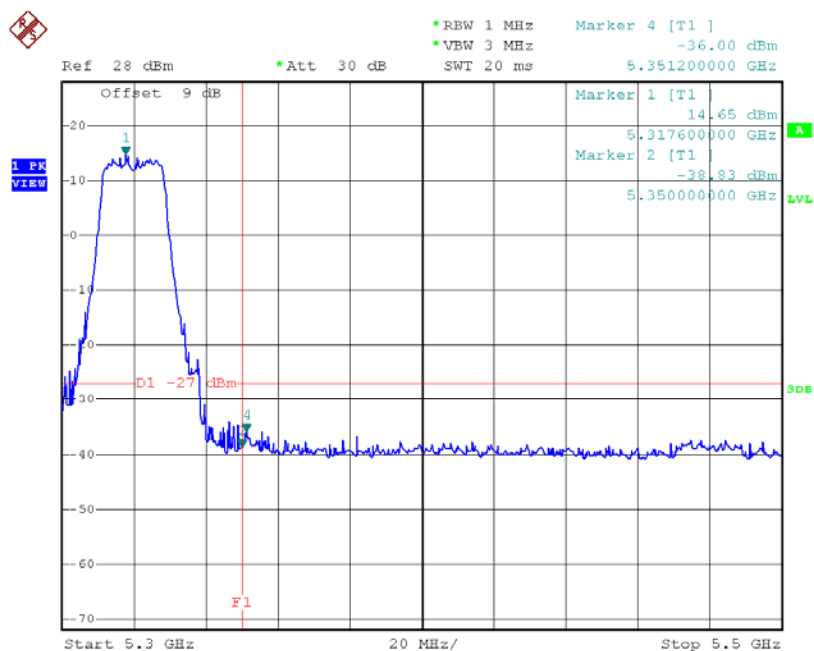
Test Mode: UNII-2A/TX N20 Mode_ANT 3

TX mode CH52



Date: 7.APR.2015 14:55:45

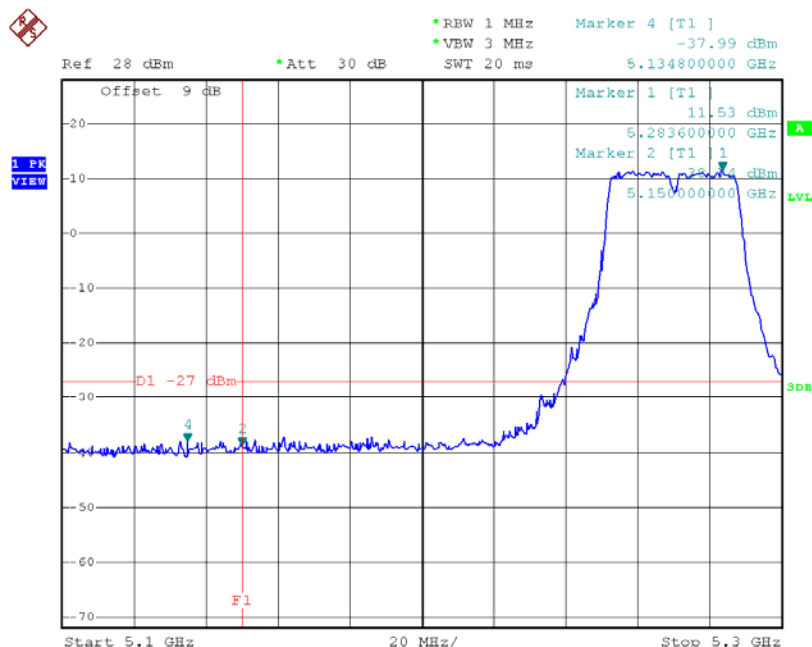
TX modeCH64



Date: 7.APR.2015 14:56:34

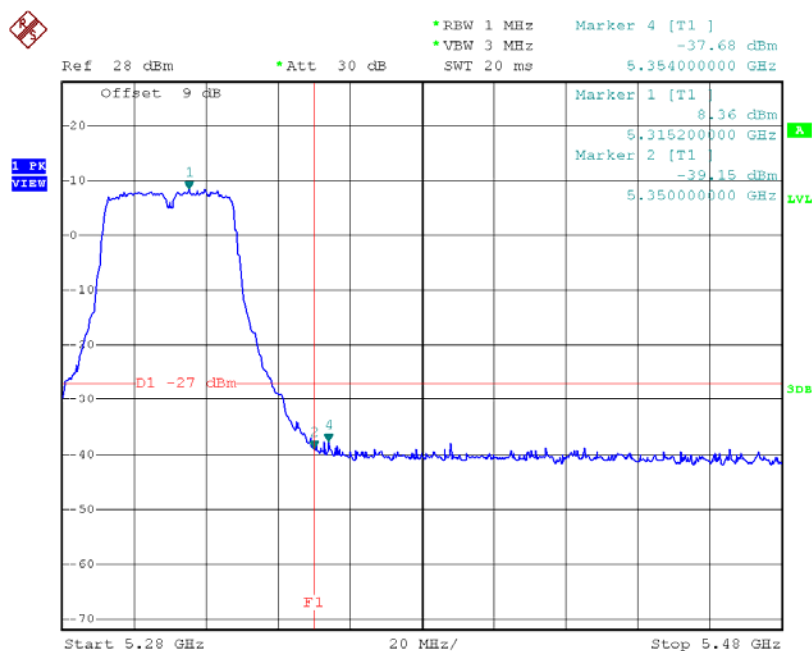
Test Mode:	UNII-2A/TX N40 Mode_ANT 1
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TX mode CH54



Date: 7.APR.2015 12:13:51

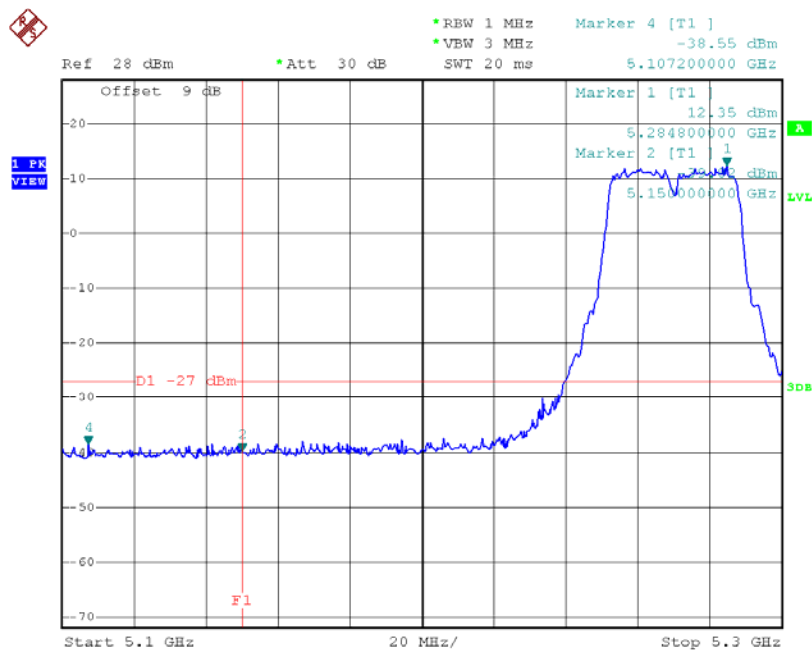
TX mode CH62



Date: 7.APR.2015 12:14:21

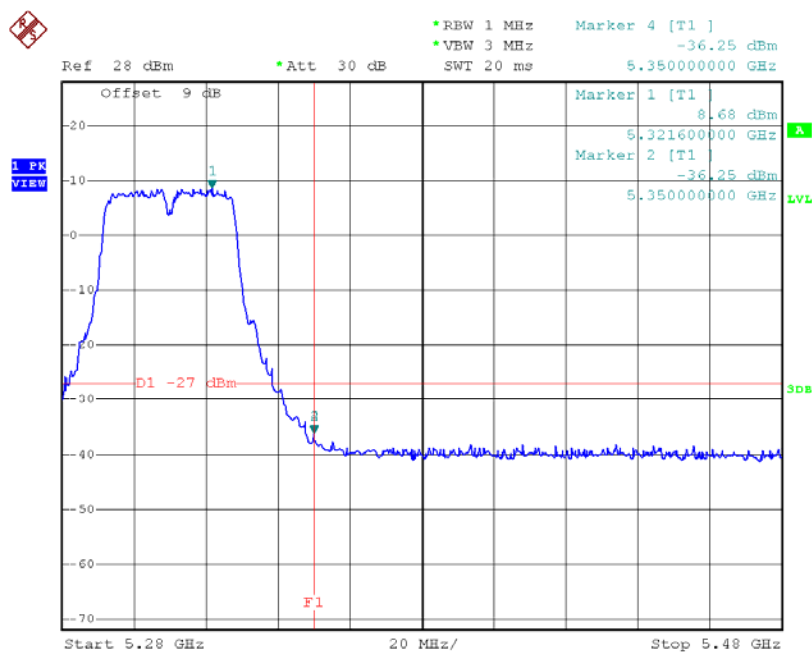
Test Mode: UNII-2A/TX N40 Mode_ANT 2

TX mode CH54



Date: 7.APR.2015 14:27:36

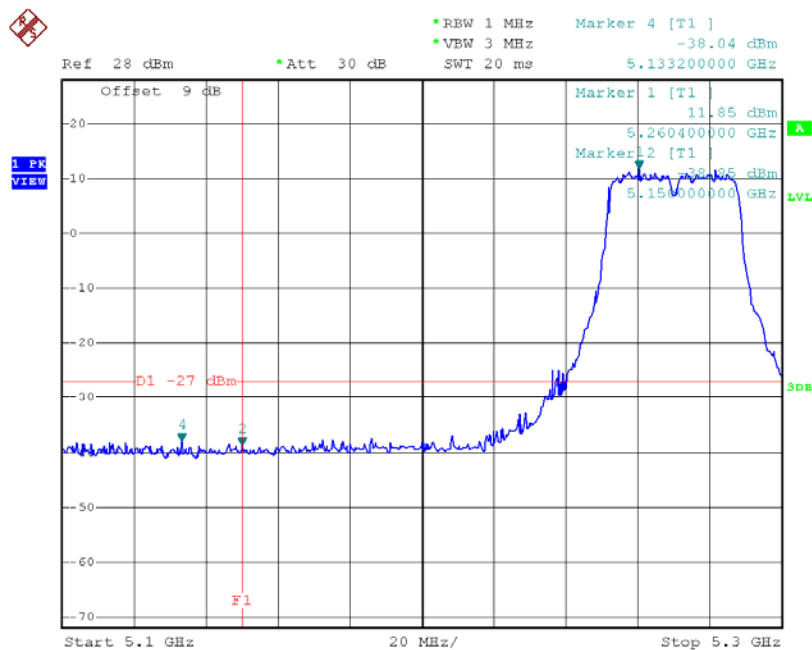
TX mode CH62



Date: 7.APR.2015 14:28:14

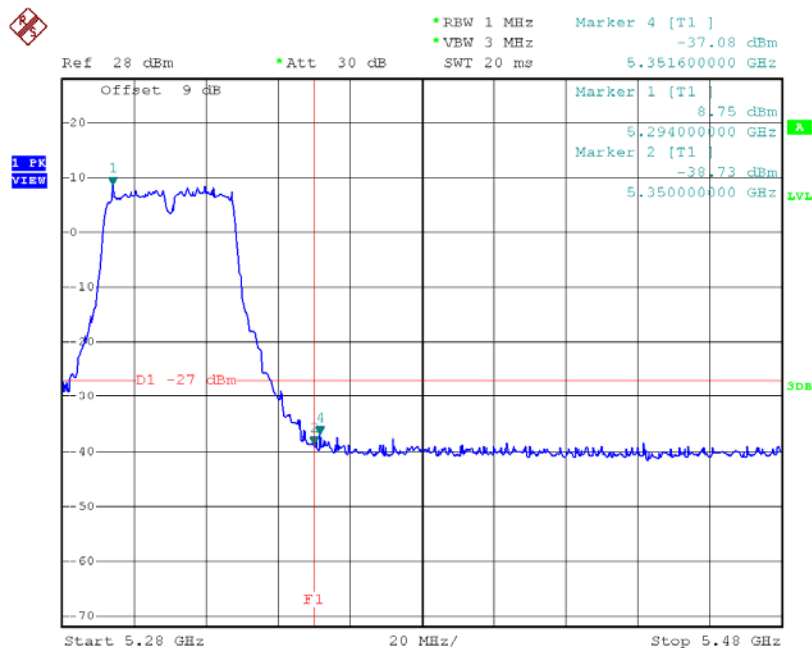
Test Mode: UNII-2A/TX N40 Mode_ANT 3

TX mode CH54



Date: 7.APR.2015 15:05:47

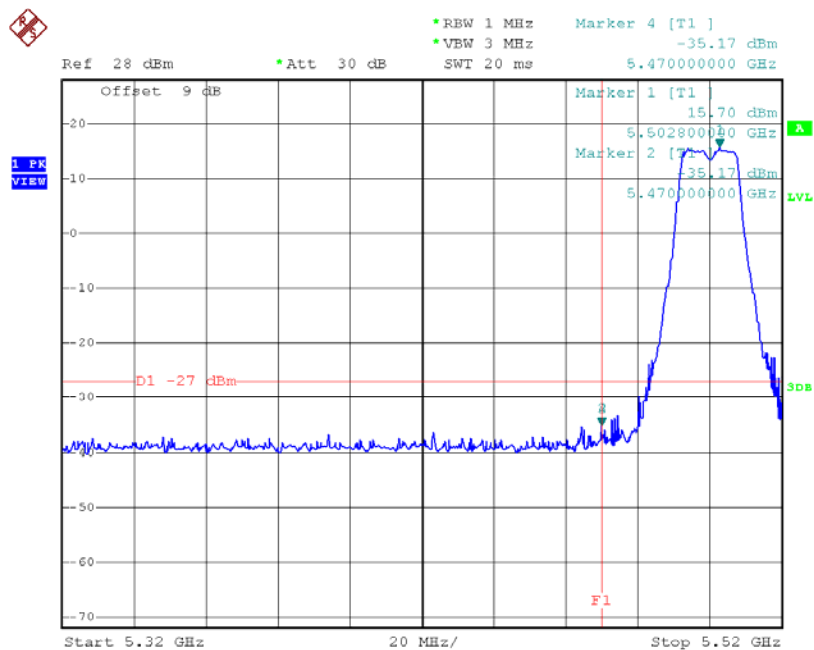
TX mode CH62



Date: 7.APR.2015 15:06:25

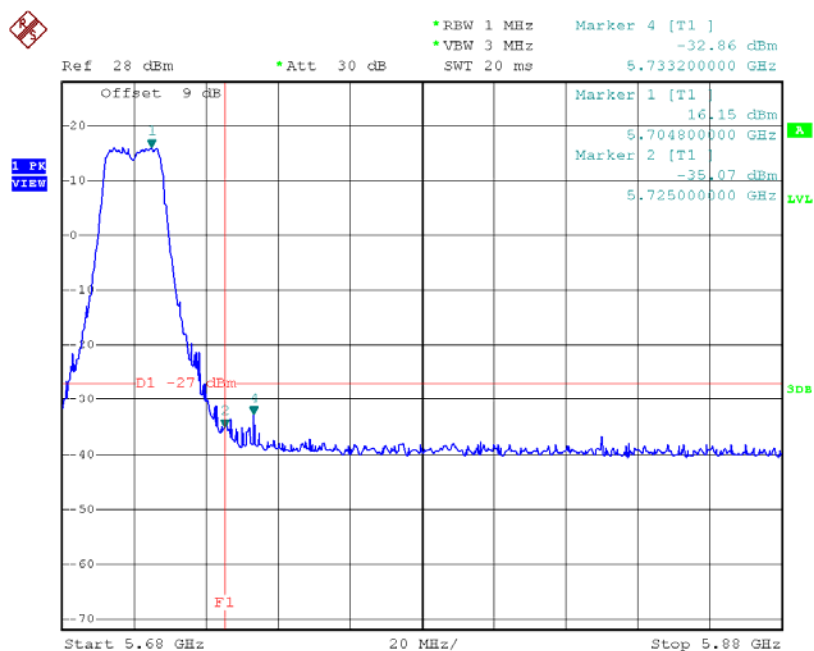
Test Mode:	UNII-2C/TX A Mode_ANT 1
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TX mode CH100



Date: 7.APR.2015 11:53:51

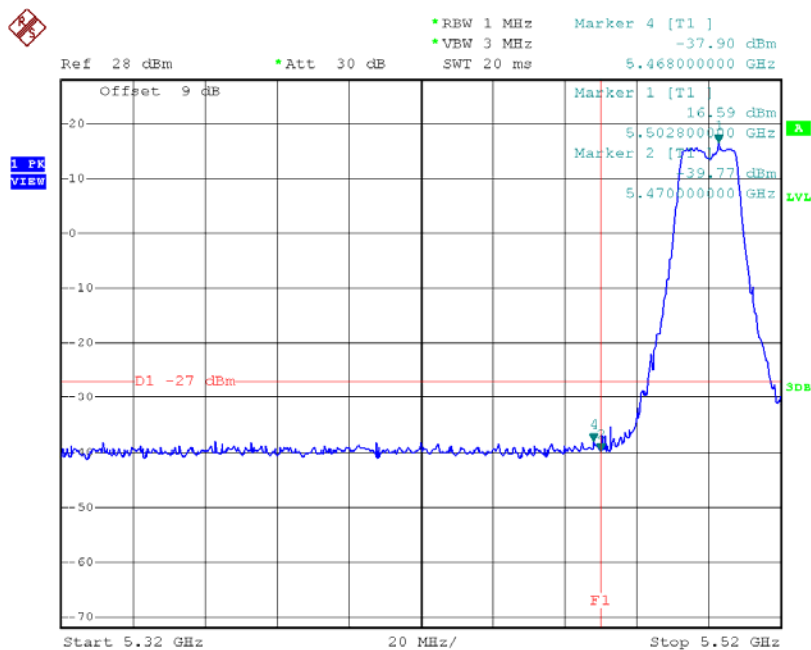
TX mode CH140



Date: 7.APR.2015 11:55:18

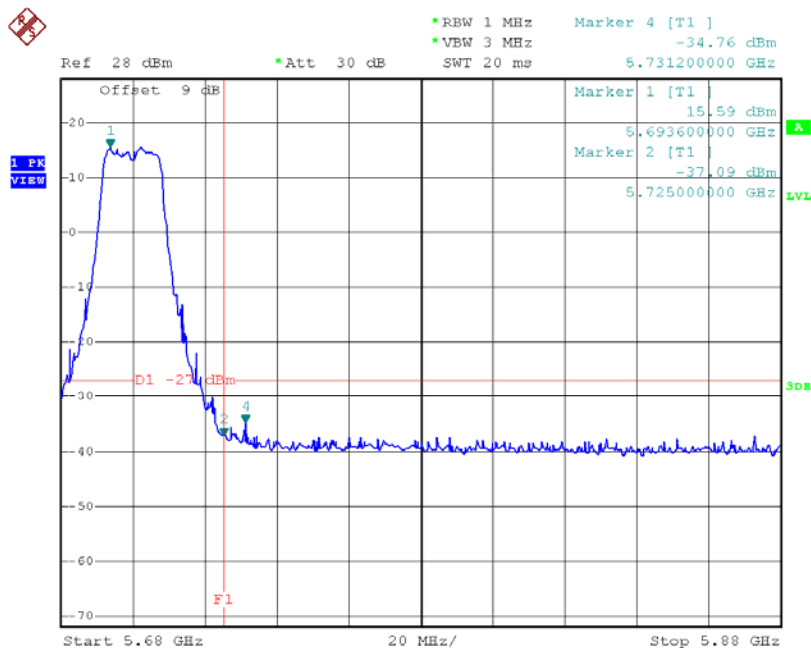
Test Mode:	UNII-2C/TX A Mode_ANT 2
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TX mode CH100



Date: 7.APR.2015 14:10:52

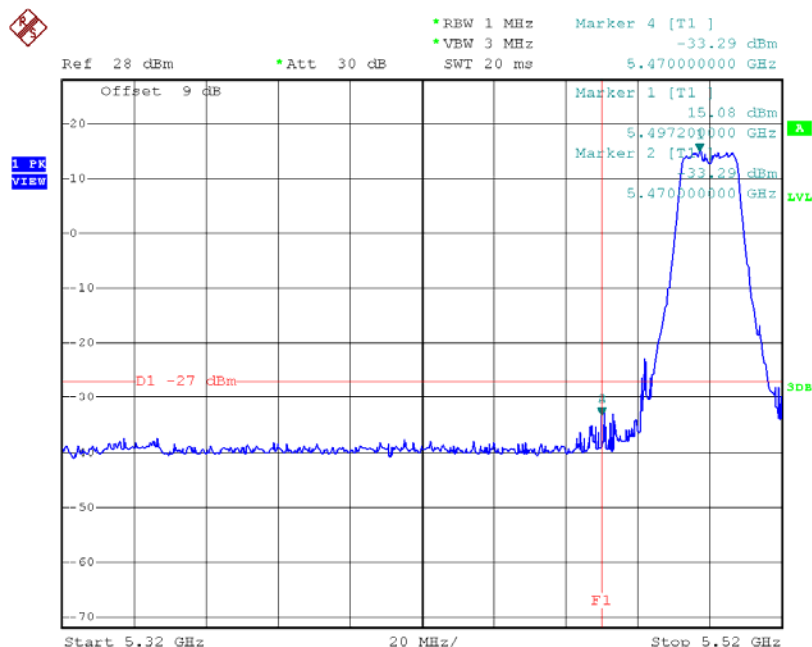
TX mode CH140



Date: 7.APR.2015 14:12:14

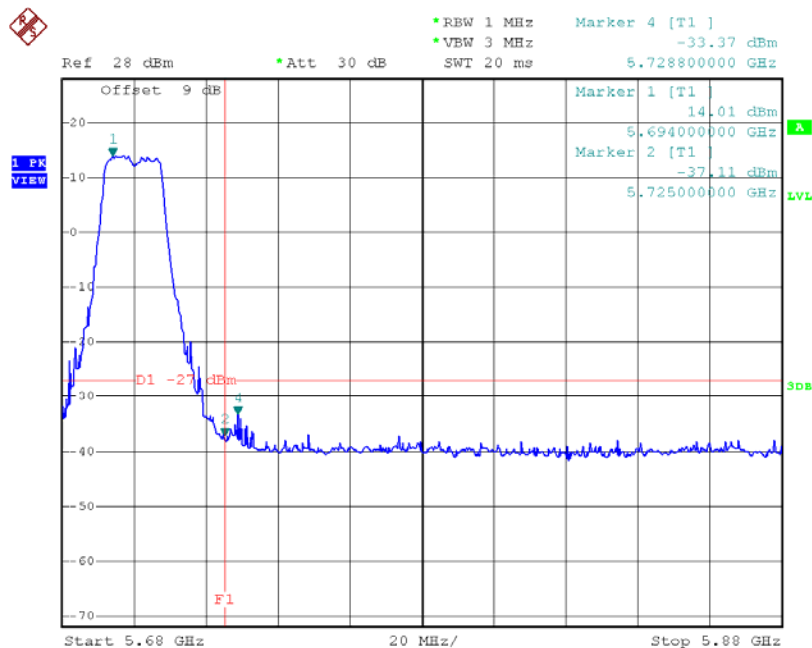
Test Mode:	UNII-2C/TX A Mode_ANT 3
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TX mode CH100



Date: 7.APR.2015 14:49:39

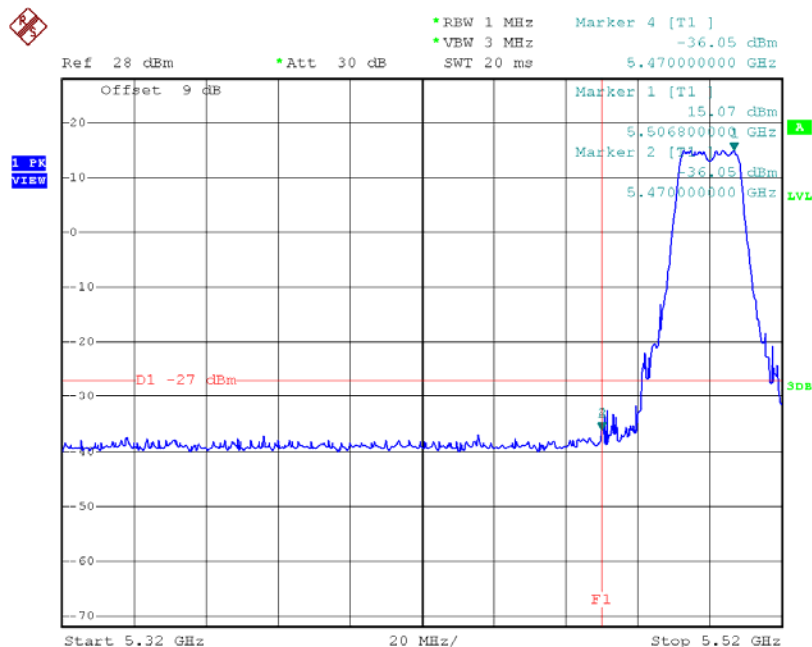
TX mode CH140



Date: 7.APR.2015 14:51:01

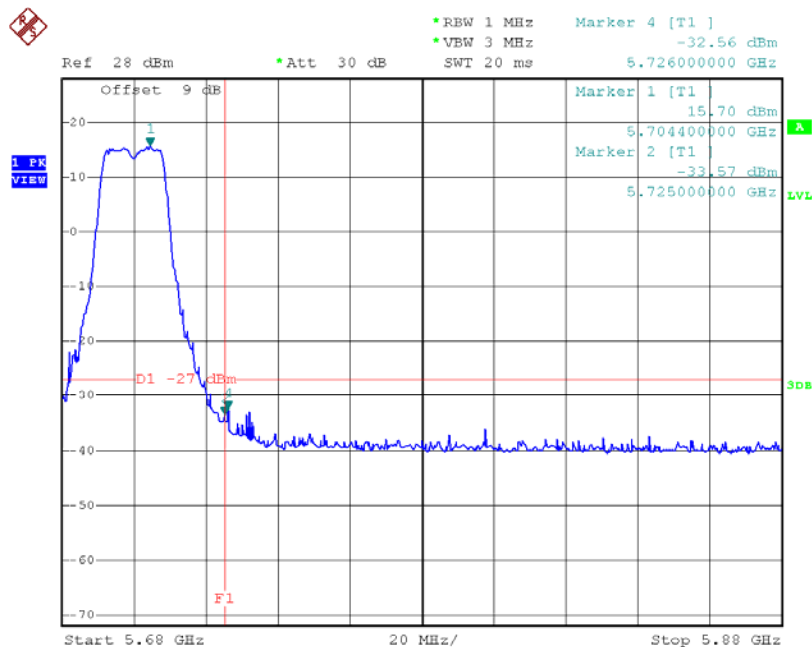
Test Mode: UNII-2C/TX N20 Mode_ANT 1

TX mode CH100



Date: 7.APR.2015 12:02:21

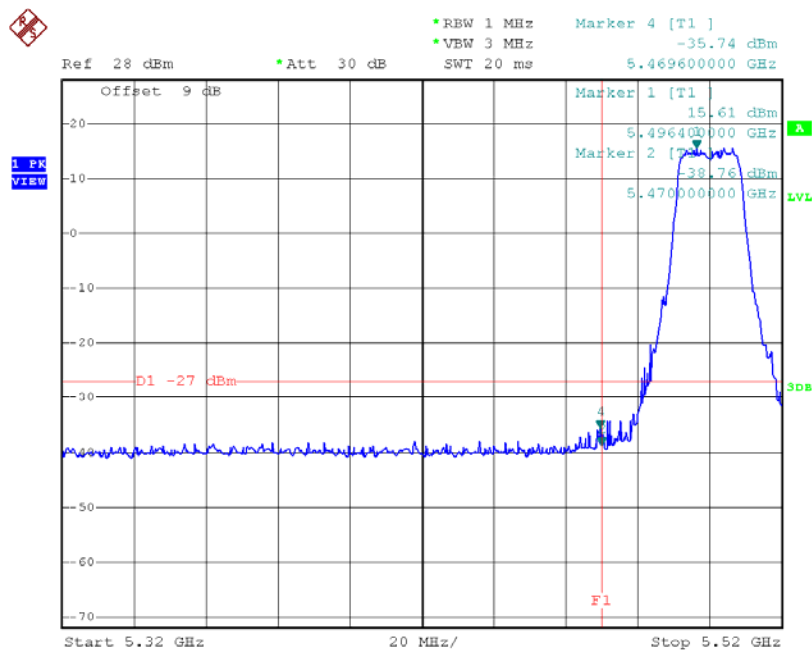
TX mode CH140



Date: 7.APR.2015 12:03:20

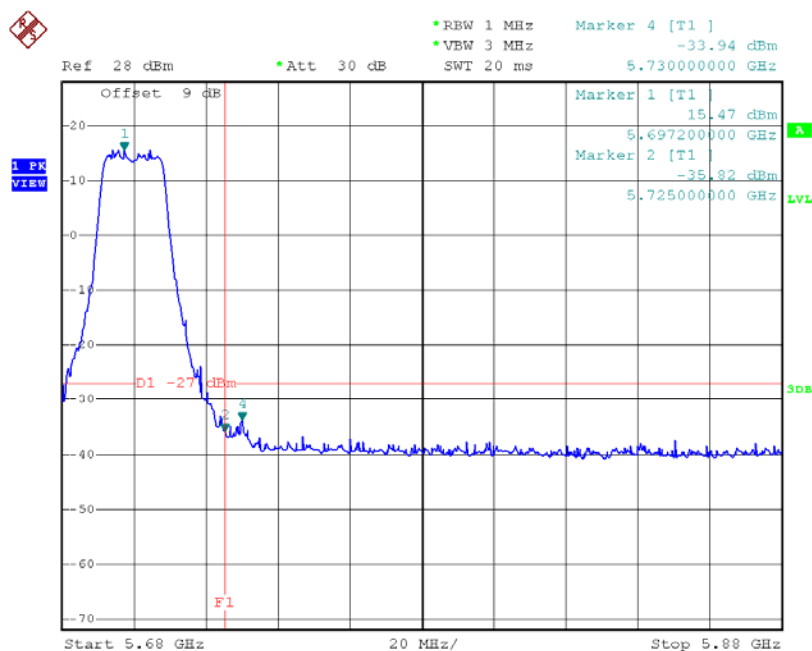
Test Mode:	UNII-2C/TX N20 Mode_ANT 2
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TX mode CH100



Date: 7.APR.2015 14:17:52

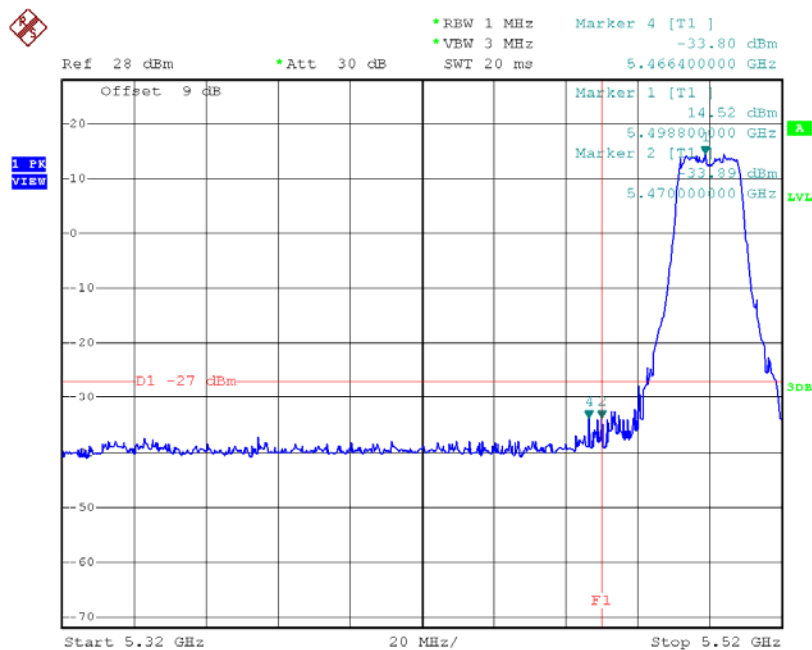
TX mode CH140



Date: 7.APR.2015 14:18:38

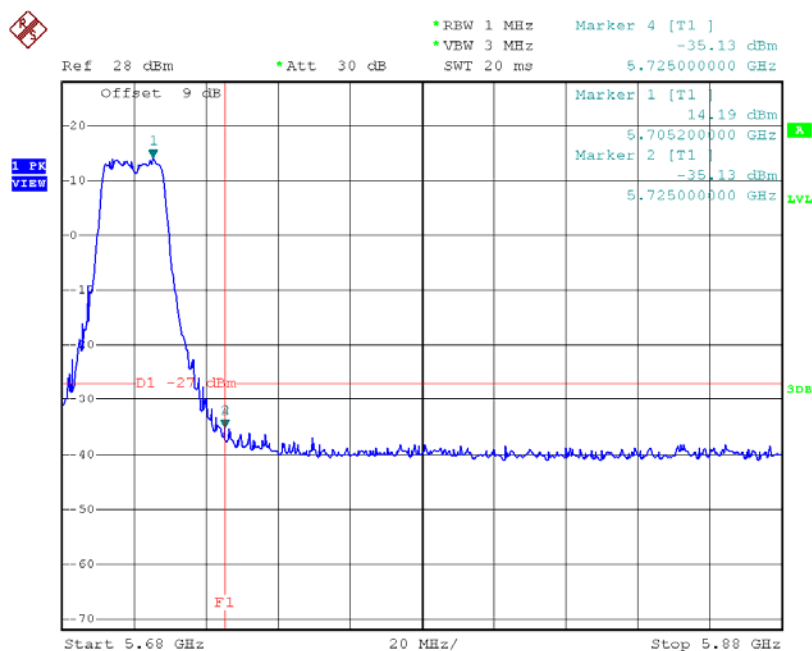
Test Mode: UNII-2C/TX N20 Mode_ANT 3

TX mode CH100



Date: 7.APR.2015 14:57:00

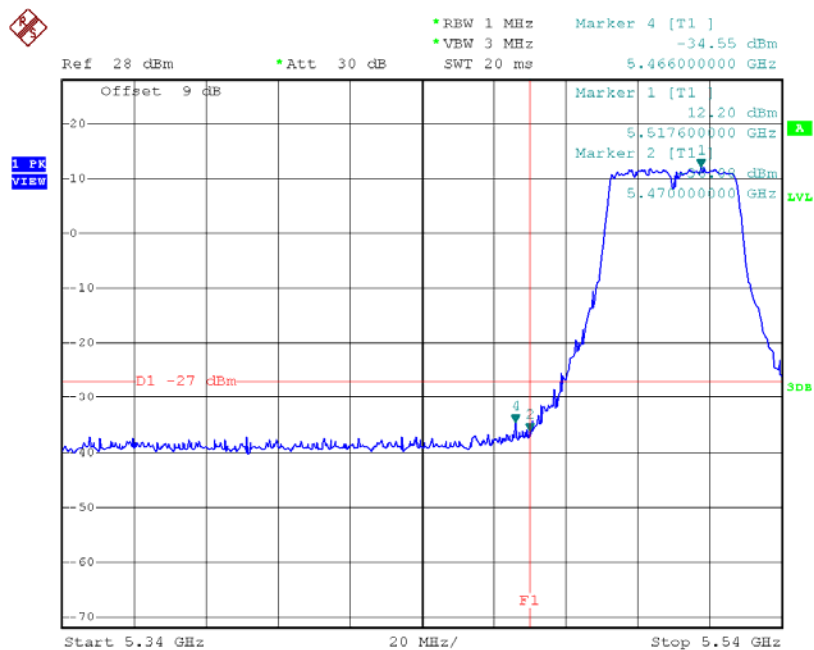
TX mode CH140



Date: 7.APR.2015 14:57:47

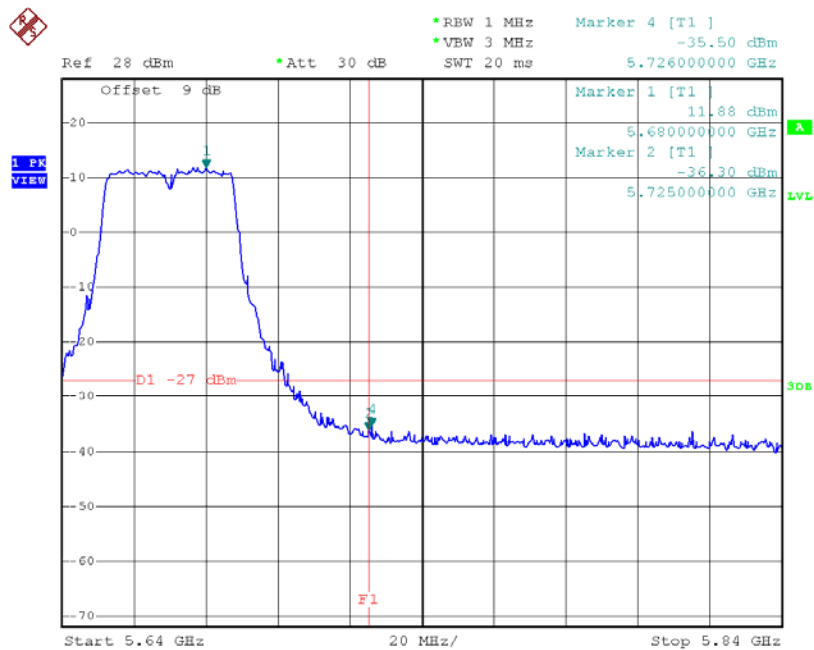
Test Mode: UNII-2C/TX N40 Mode_ANT 1

TX mode CH102



Date: 7.APR.2015 12:14:54

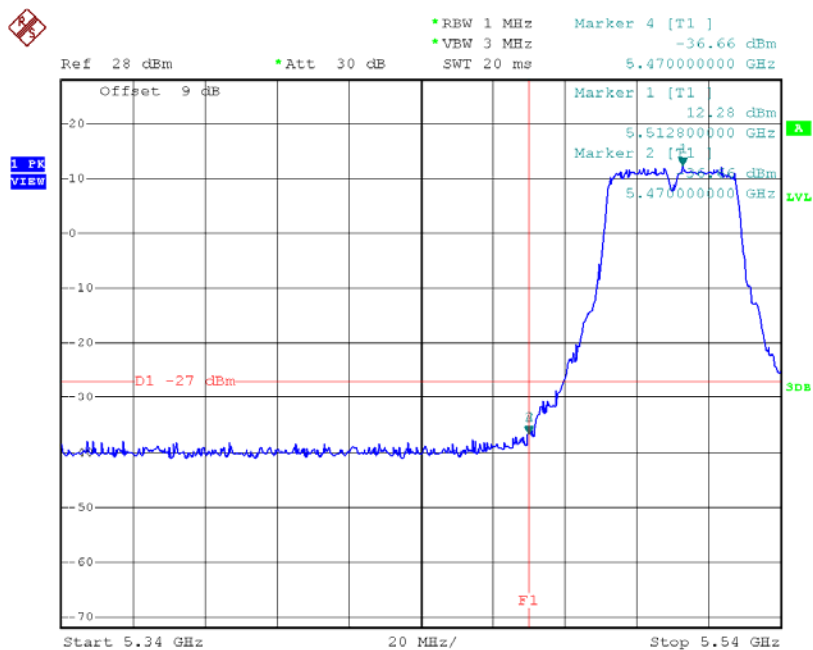
TX mode CH134



Date: 7.APR.2015 12:15:45

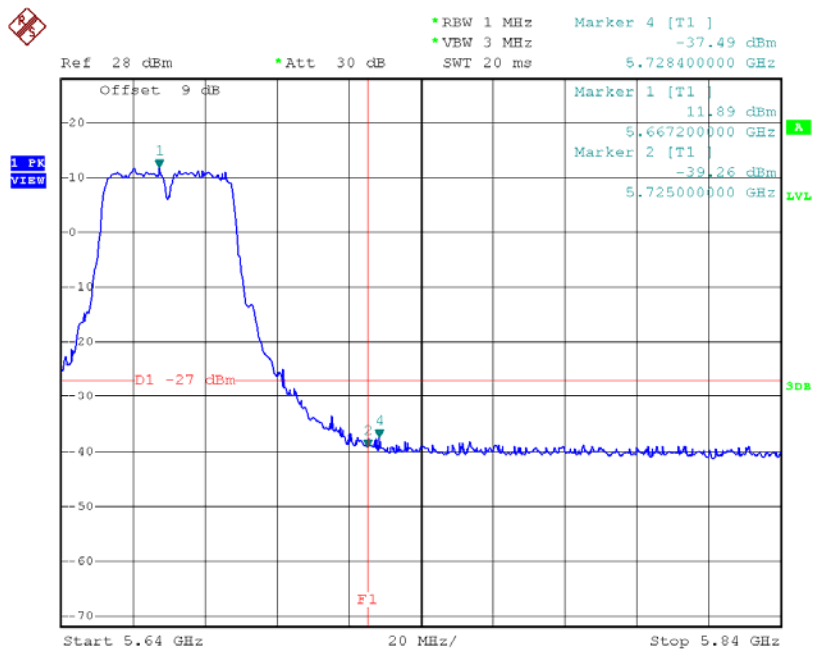
Test Mode: UNII-2C/TX N40 Mode_ANT 2

TX mode CH102



Date: 7.APR.2015 14:30:11

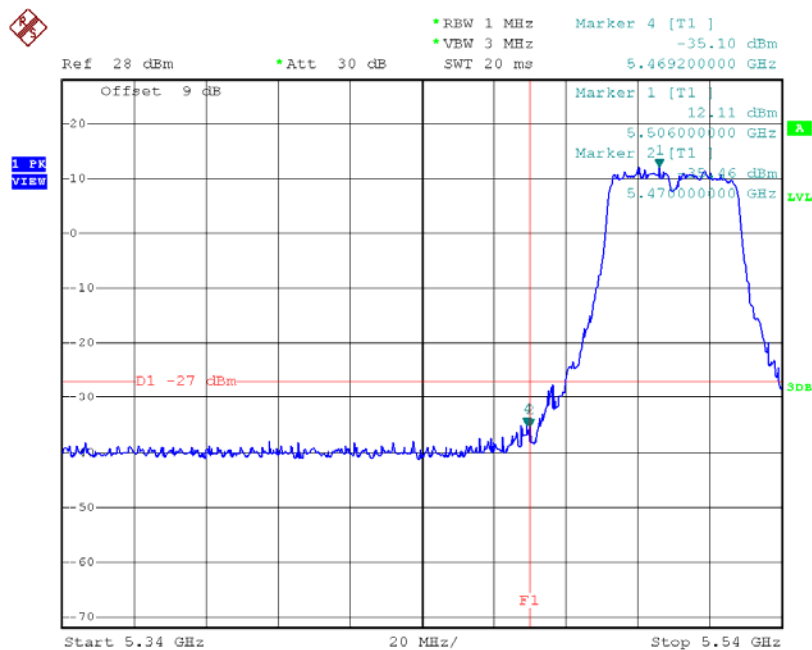
TX mode CH134



Date: 7.APR.2015 14:31:14

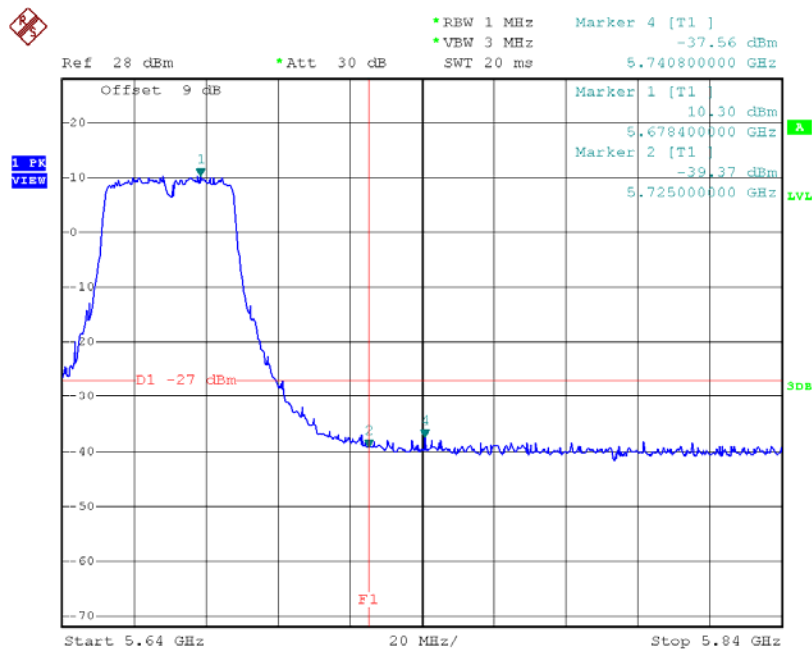
Test Mode: UNII-2C/TX N40 Mode_ANT 3

TX mode CH102



Date: 7.APR.2015 15:06:51

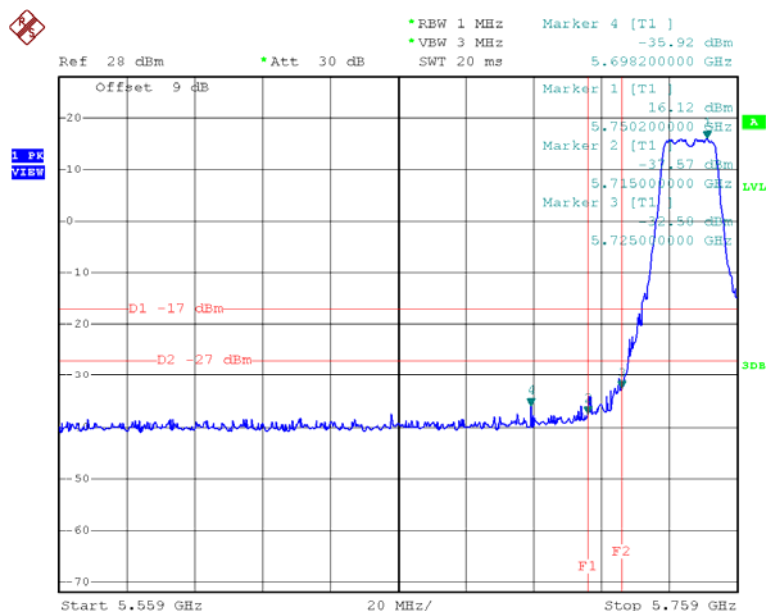
TX mode CH134



Date: 7.APR.2015 15:07:39

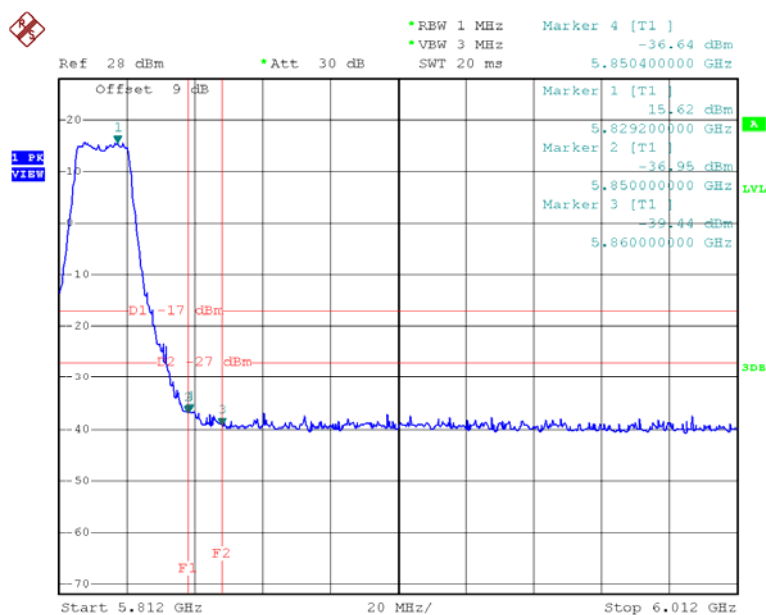
Test Mode: UNII-3/TX A Mode_ANT 1

TX A Mode CH149



Date: 7.APR.2015 11:56:08

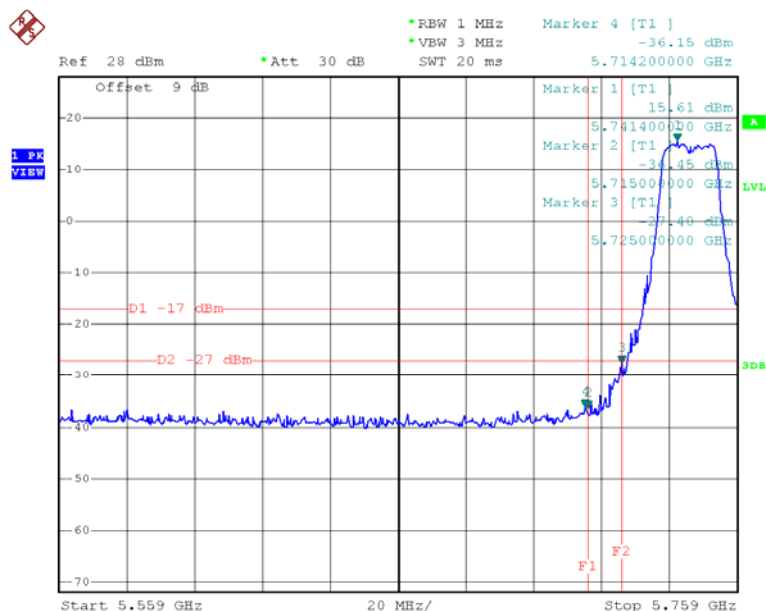
TX A Mode CH165



Date: 7.APR.2015 11:57:35

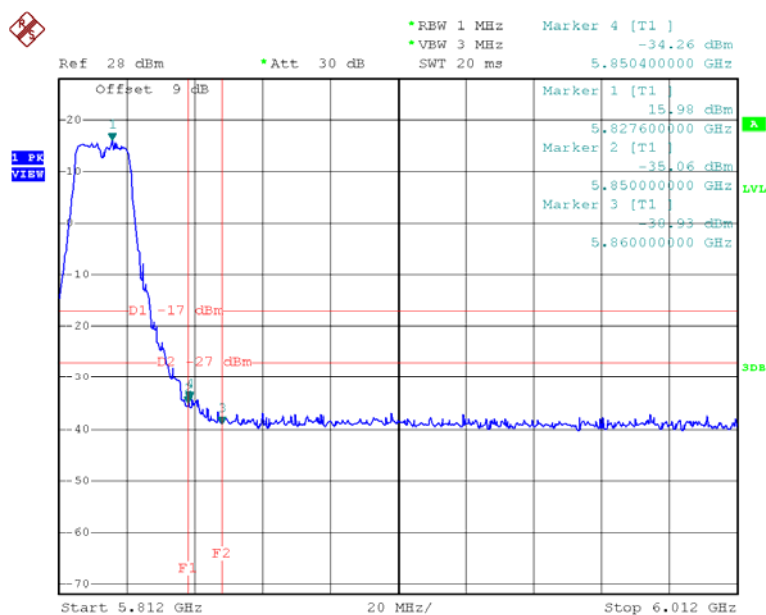
Test Mode: UNII-3/TX A Mode_ANT 2

TX A Mode CH149



Date: 7.APR.2015 14:12:59

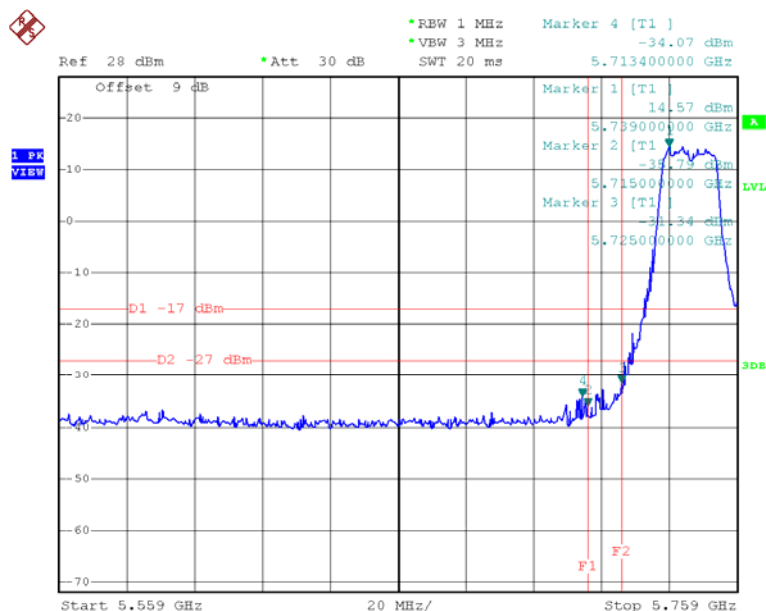
TX A Mode CH165



Date: 7.APR.2015 14:14:25

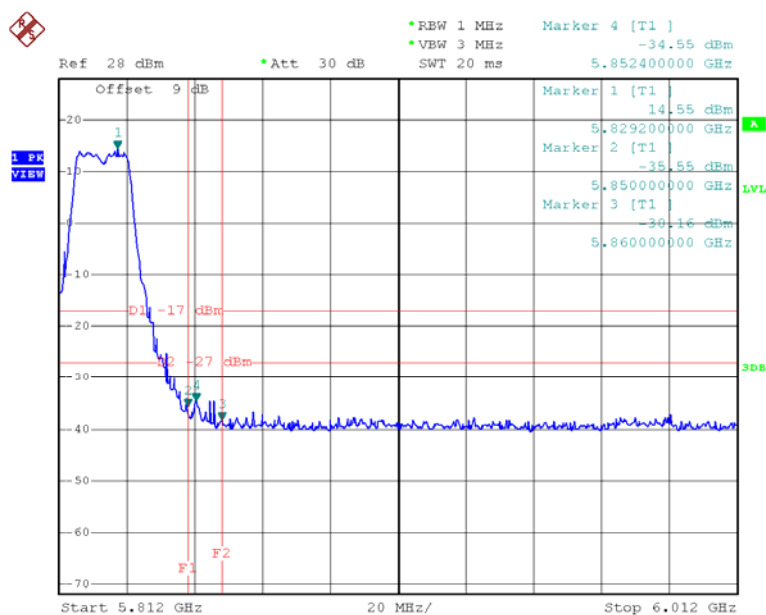
Test Mode: UNII-3/TX A Mode_ANT 3

TX A Mode CH149



Date: 7.APR.2015 14:51:47

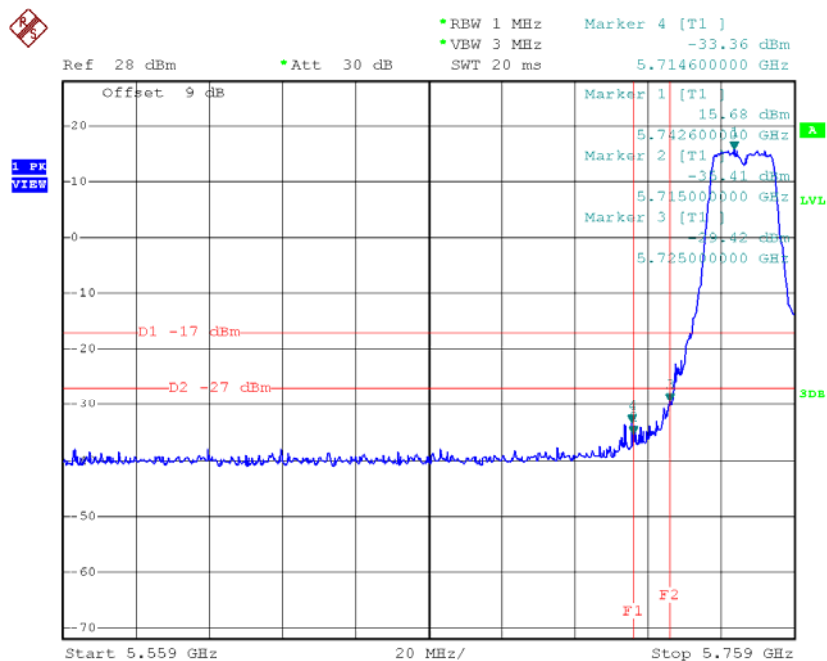
TX A Mode CH165



Date: 7.APR.2015 14:53:29

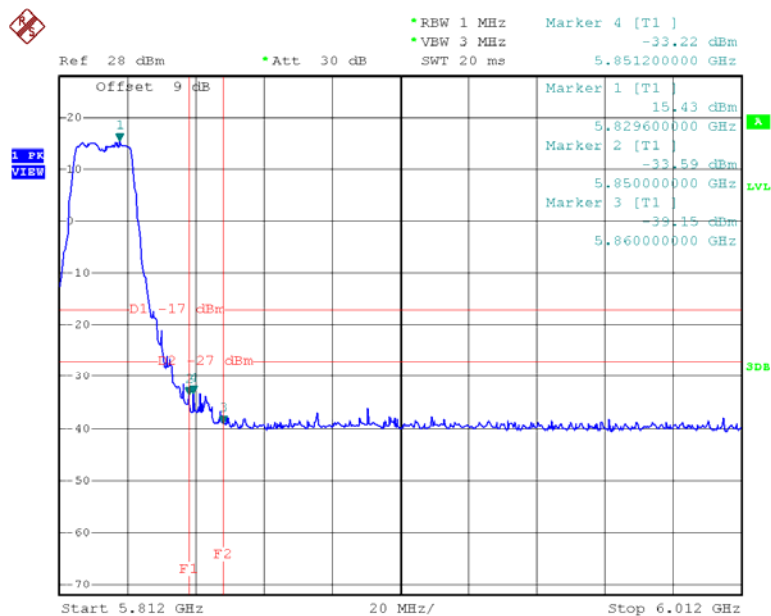
Test Mode: UNII-3/TX N20 Mode_ANT 1

TX HT20 mode CH149



Date: 7.APR.2015 12:03:45

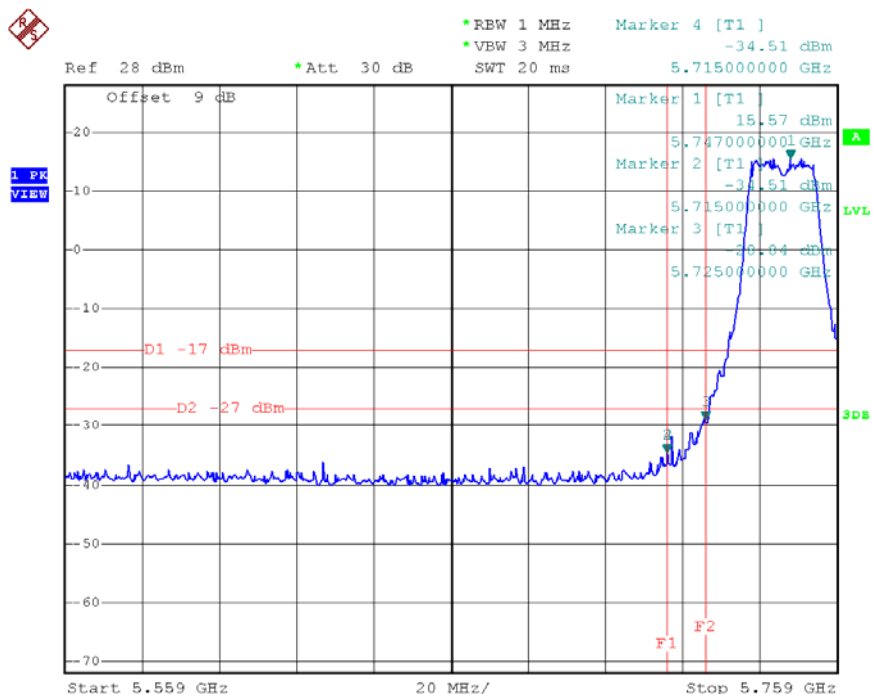
TX HT20 mode CH165



Date: 7.APR.2015 12:04:38

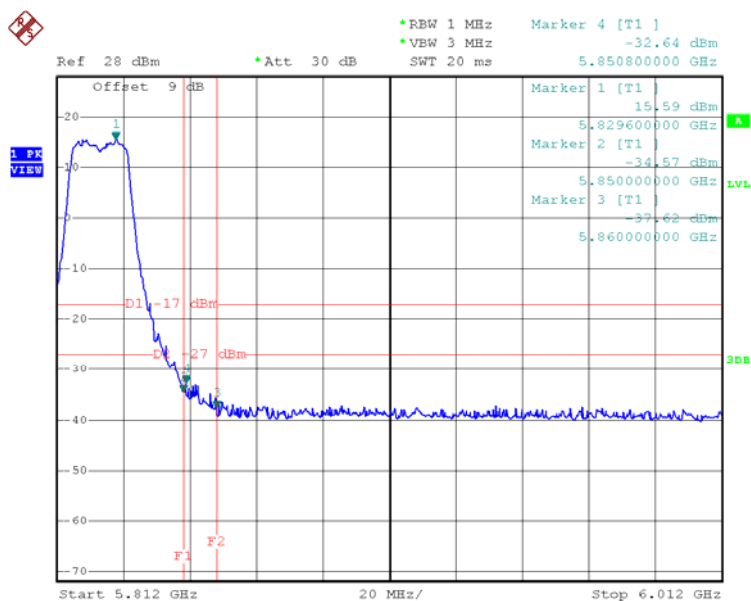
Test Mode: UNII-3/TX N20 Mode_ANT 2

TX HT20 mode CH149



Date: 7.APR.2015 14:19:05

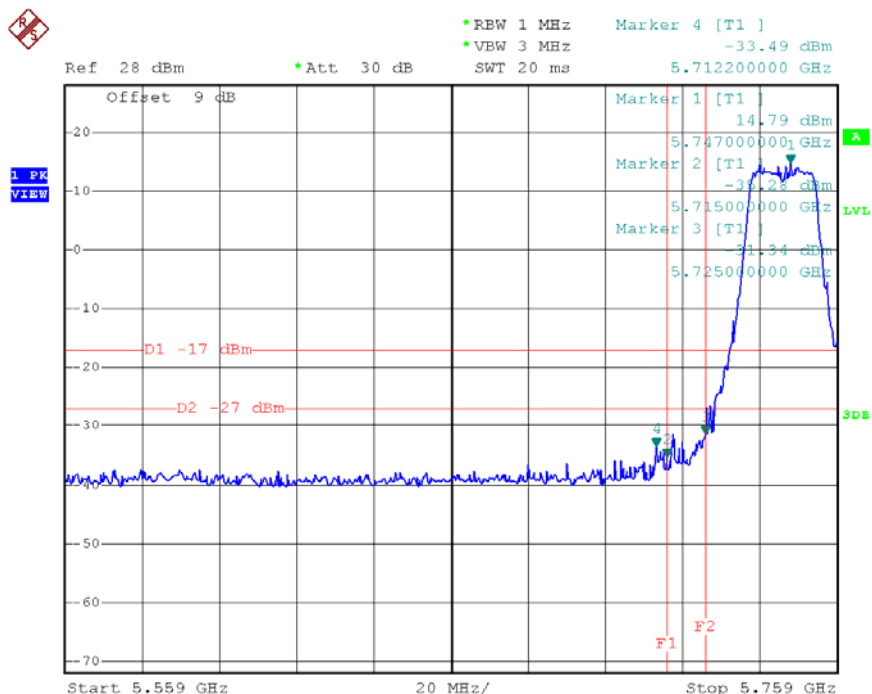
X HT20 mode CH165



Date: 7.APR.2015 14:19:53

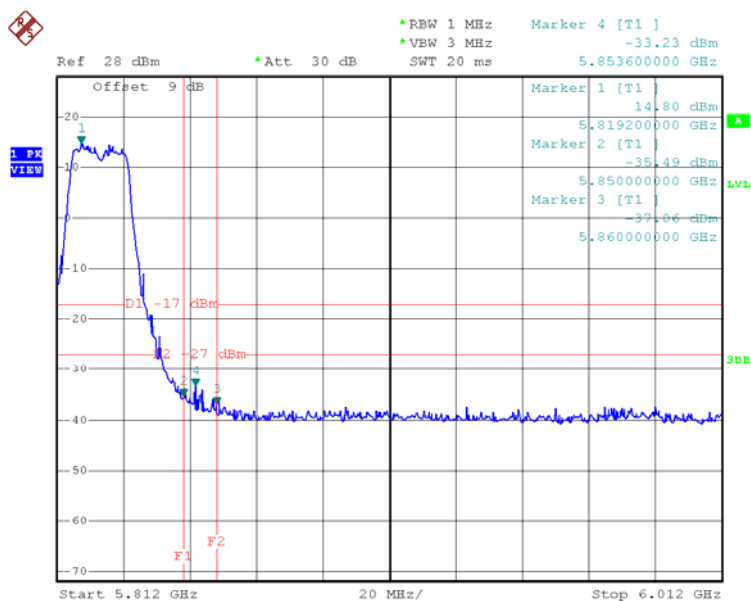
Test Mode: UNII-3/TX N20 Mode_ANT 3

TX HT20 mode CH149



Date: 7.APR.2015 14:58:12

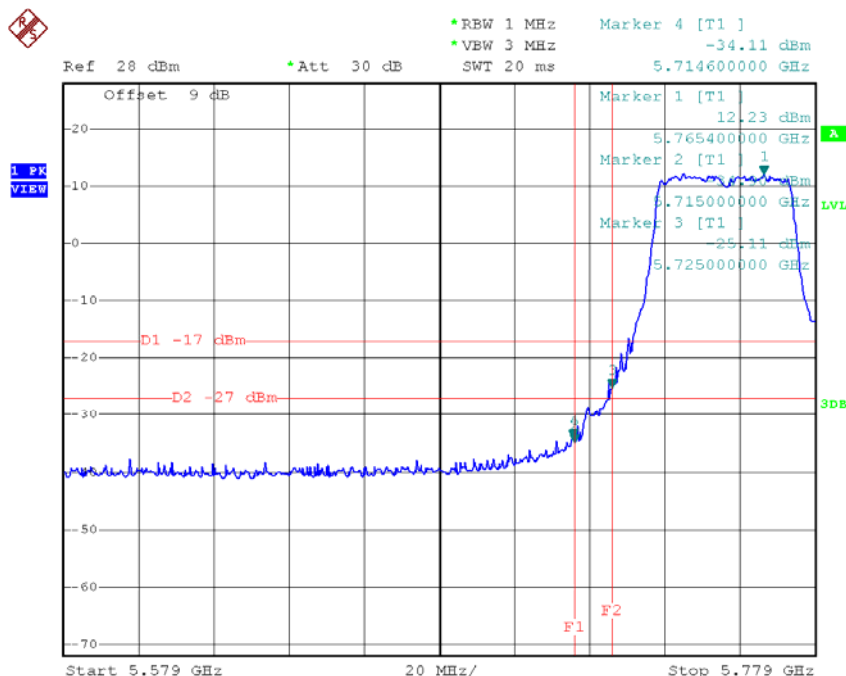
X HT20 mode CH165



Date: 7.APR.2015 14:58:57

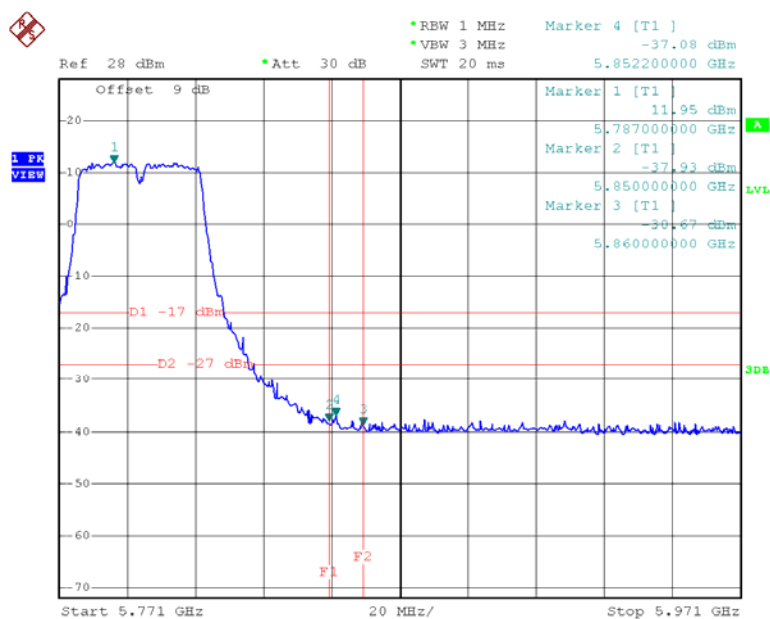
Test Mode: UNII-3/TX N40 Mode_ANT 1

UNII-3/TX HT40 mode CH151



Date: 7.APR.2015 12:16:11

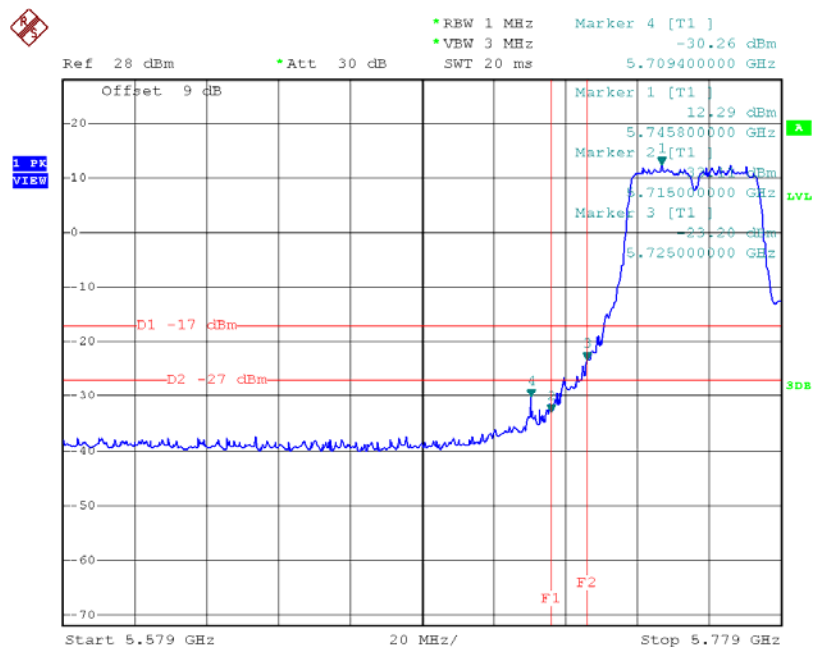
UNII-3/TX HT40 mode CH159



Date: 7.APR.2015 12:16:37

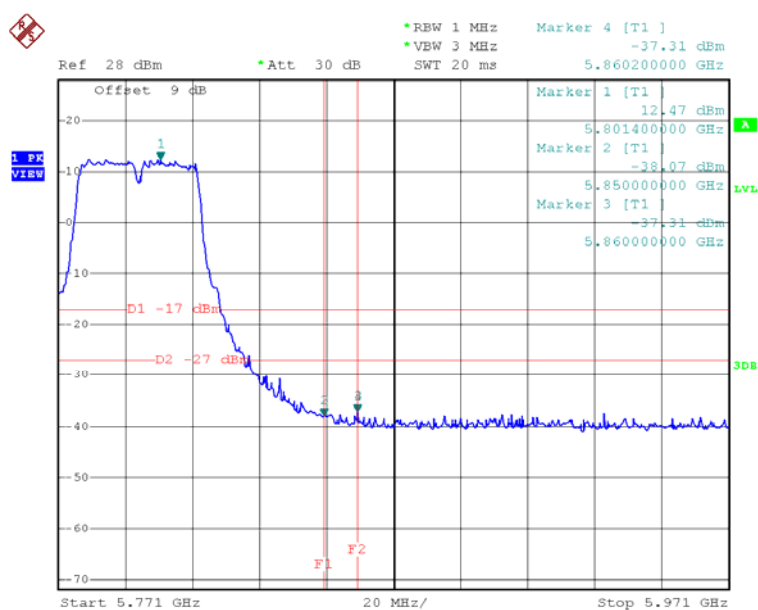
Test Mode: UNII-3/TX N40 Mode_ANT 2

TX HT40 mode CH151



Date: 7.APR.2015 14:31:47

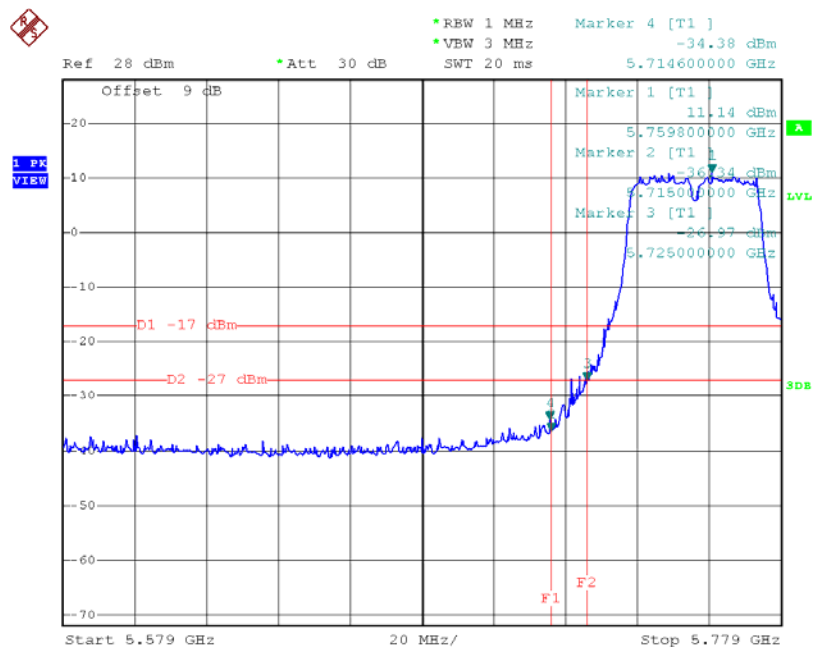
HT40 mode CH159



Date: 7.APR.2015 14:32:16

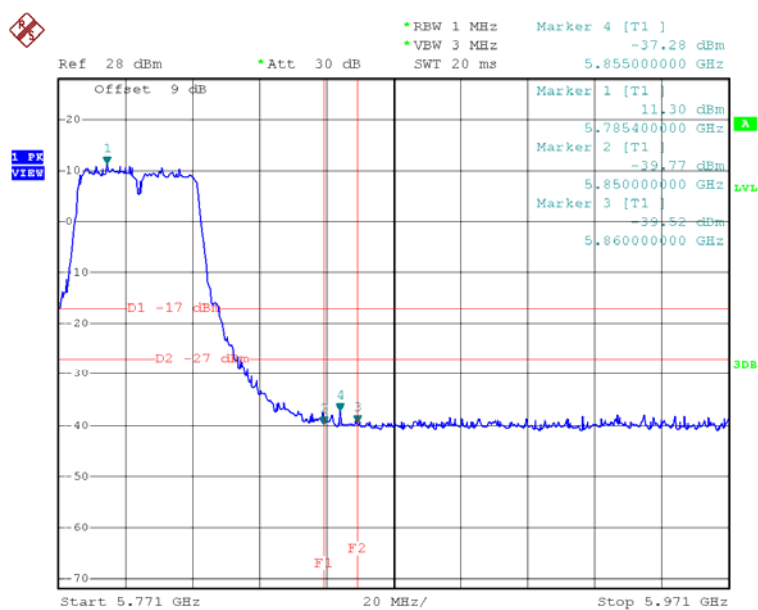
Test Mode: UNII-3/TX N40 Mode_ANT 3

TX HT40 mode CH151



Date: 7.APR.2015 15:08:05

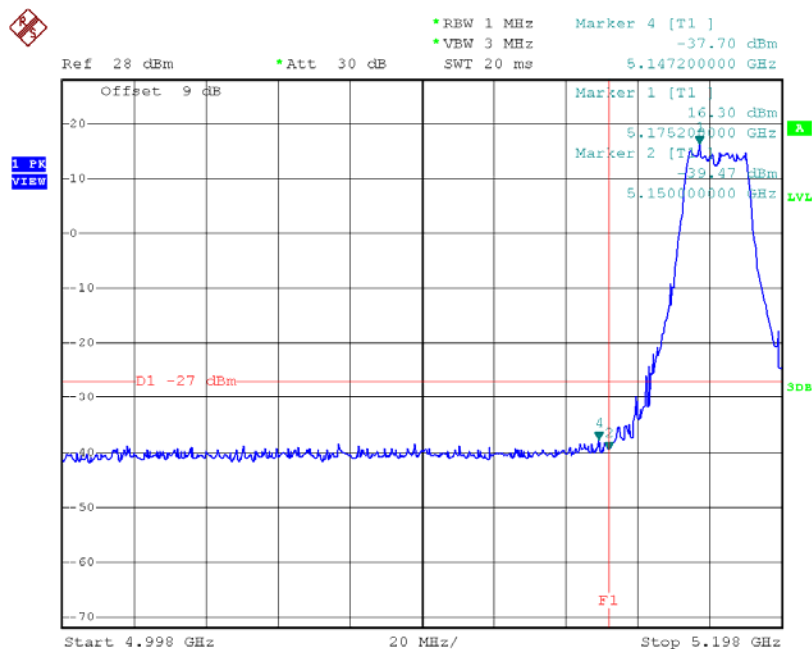
HT40 mode CH159



Date: 7.APR.2015 15:08:31

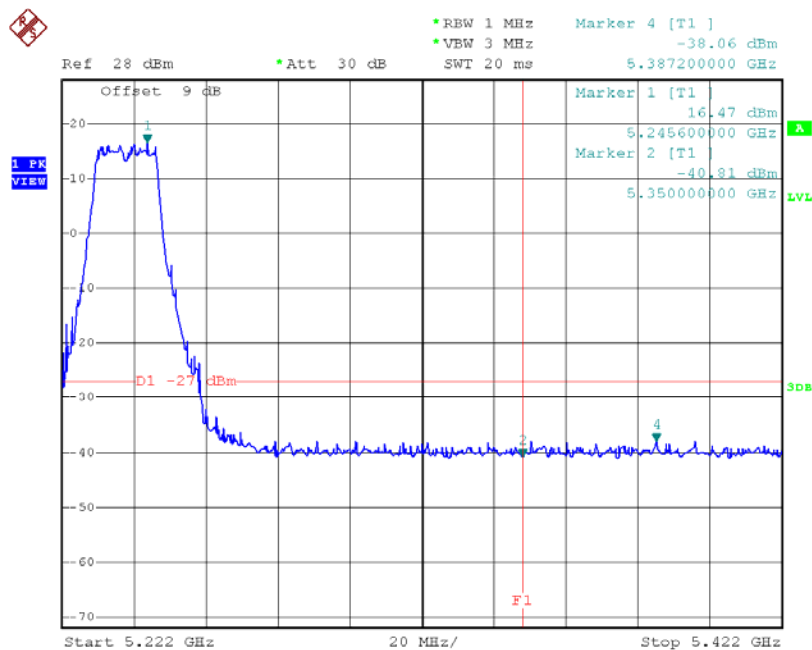
Test Mode: UNII-1/TX AC20 Mode_ANT 1

TX mode CH36



Date: 7.APR.2015 12:05:26

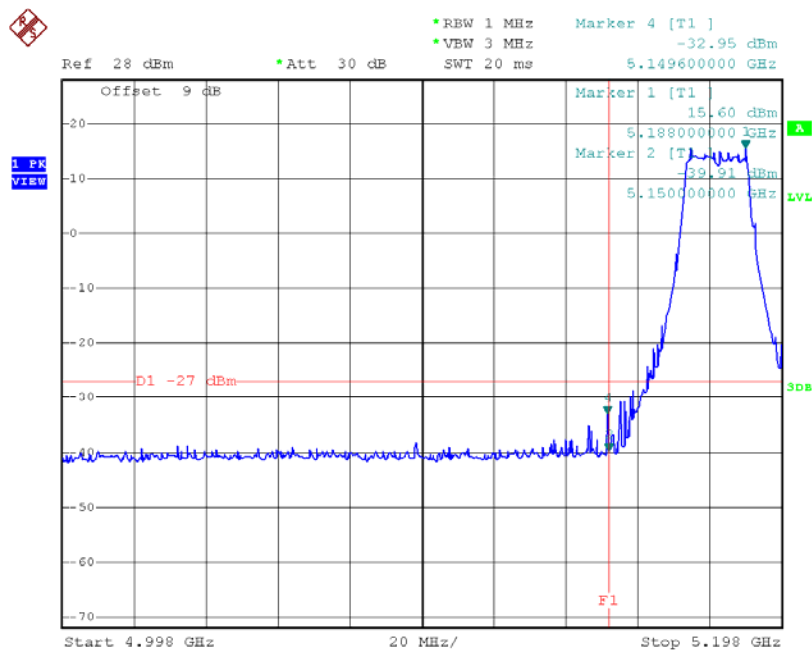
TX mode CH48



Date: 7.APR.2015 12:07:54

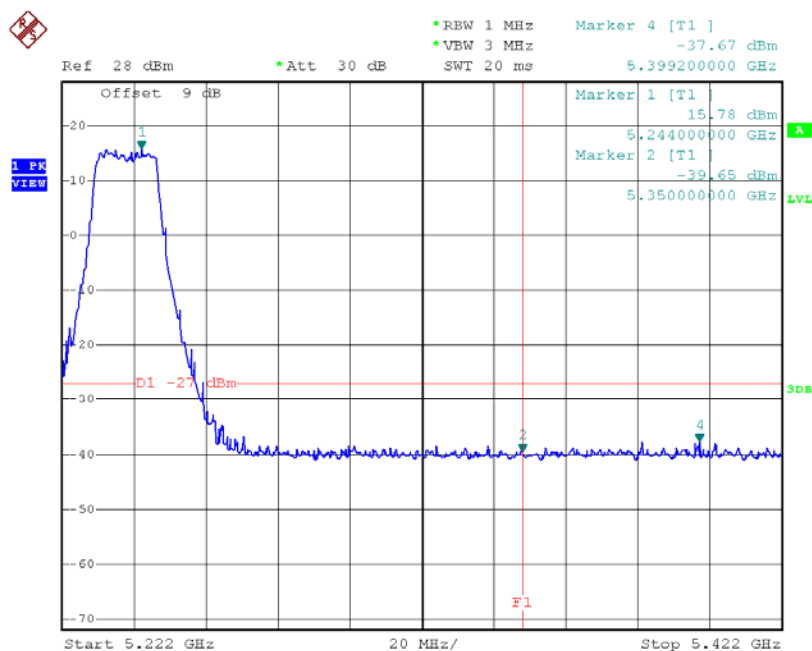
Test Mode:	UNII-1/TX AC20 Mode_ANT 2
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TX mode CH36



Date: 7.APR.2015 14:20:29

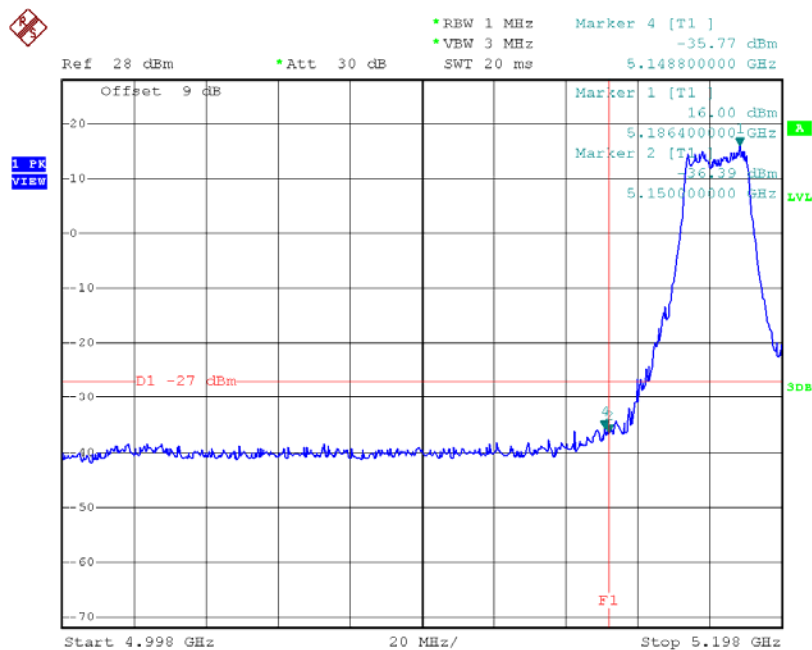
TX mode CH48



Date: 7.APR.2015 14:21:17

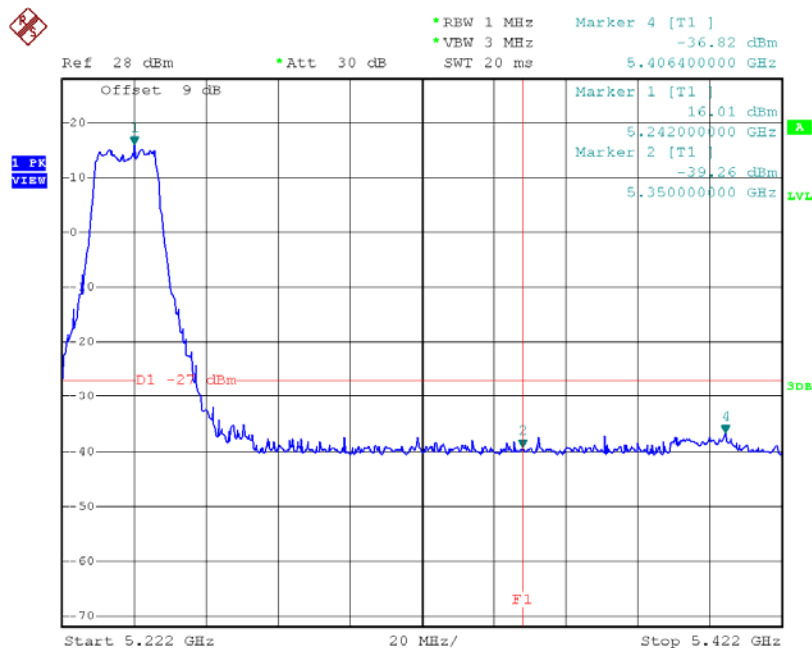
Test Mode: UNII-1/TX AC20 Mode_ANT 3

TX mode CH36



Date: 7.APR.2015 14:59:27

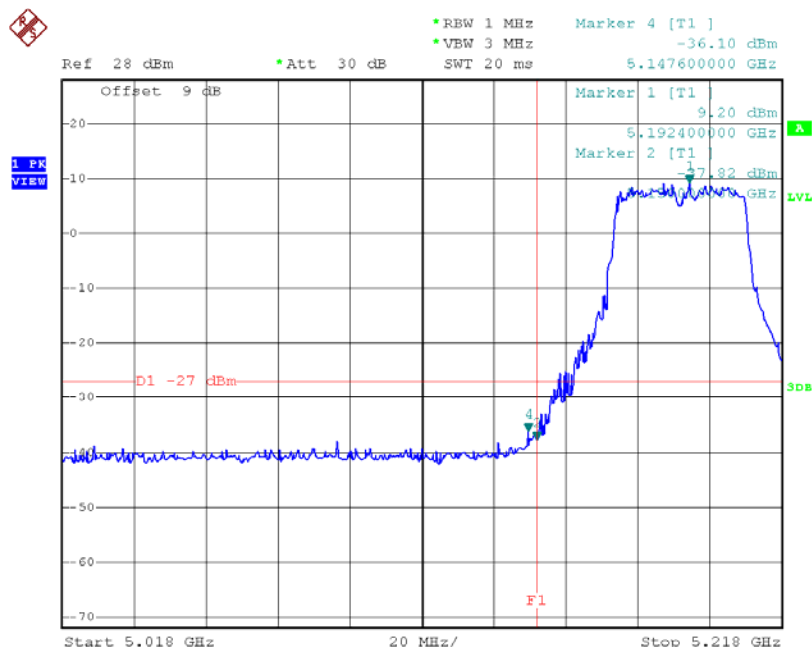
TX modeCH48



Date: 7.APR.2015 15:00:17

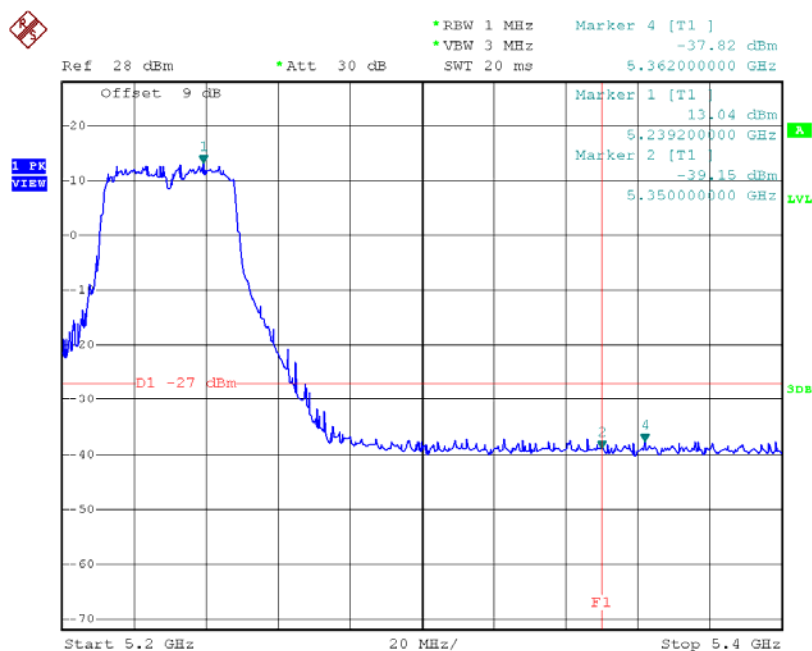
Test Mode: UNII-1/TX AC40 Mode_ANT 1

TX mode CH38



Date: 7.APR.2015 12:17:10

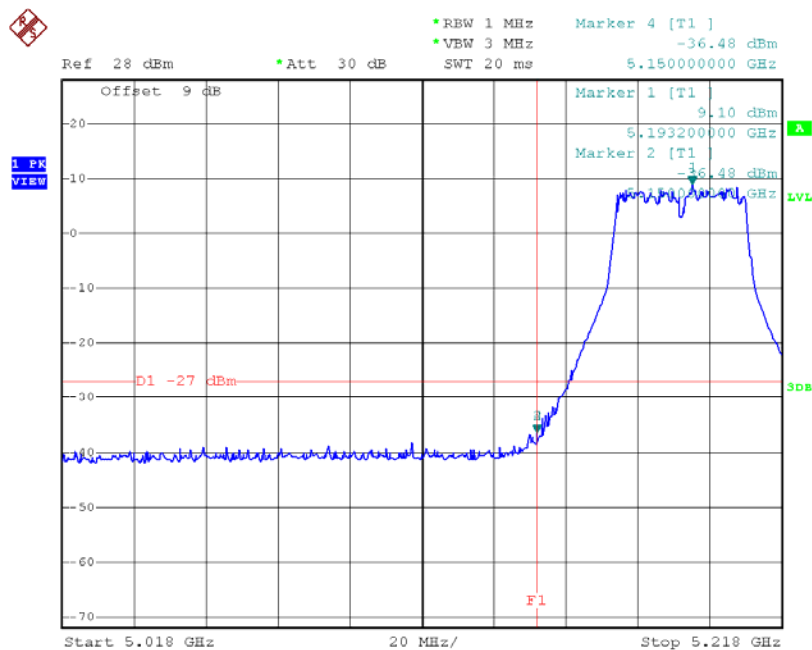
TX mode CH46



Date: 7.APR.2015 12:17:47

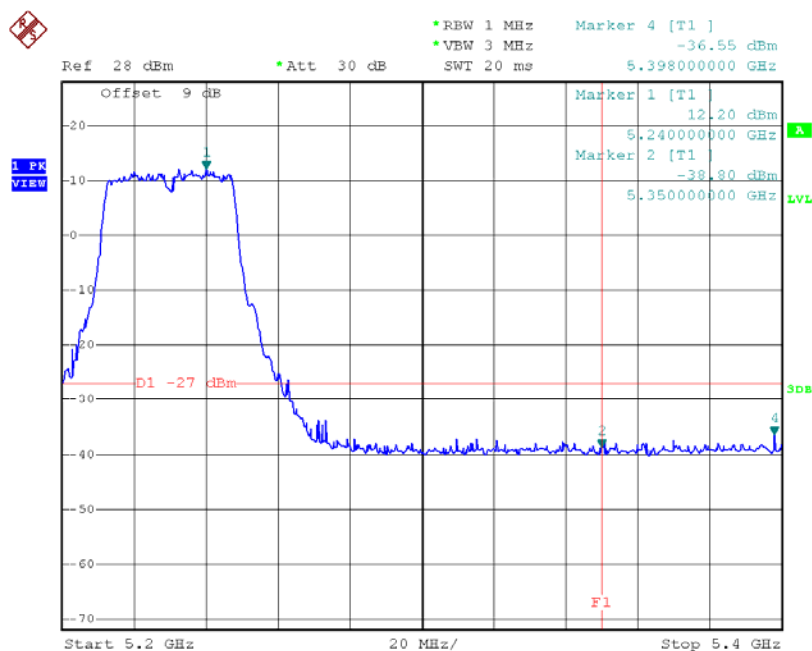
Test Mode:	UNII-1/TX AC40 Mode_ANT 2
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TX mode CH38



Date: 7.APR.2015 14:33:45

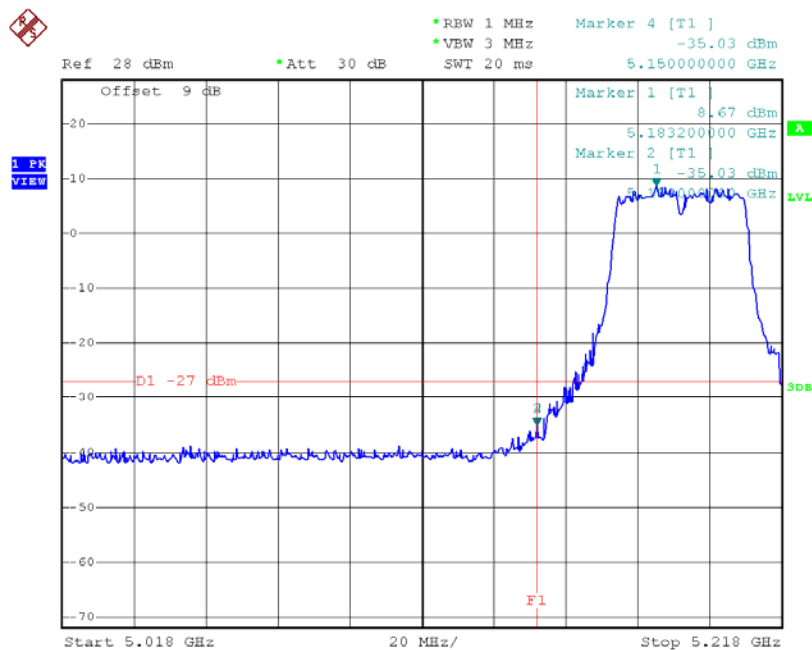
TX mode CH46



Date: 7.APR.2015 14:34:13

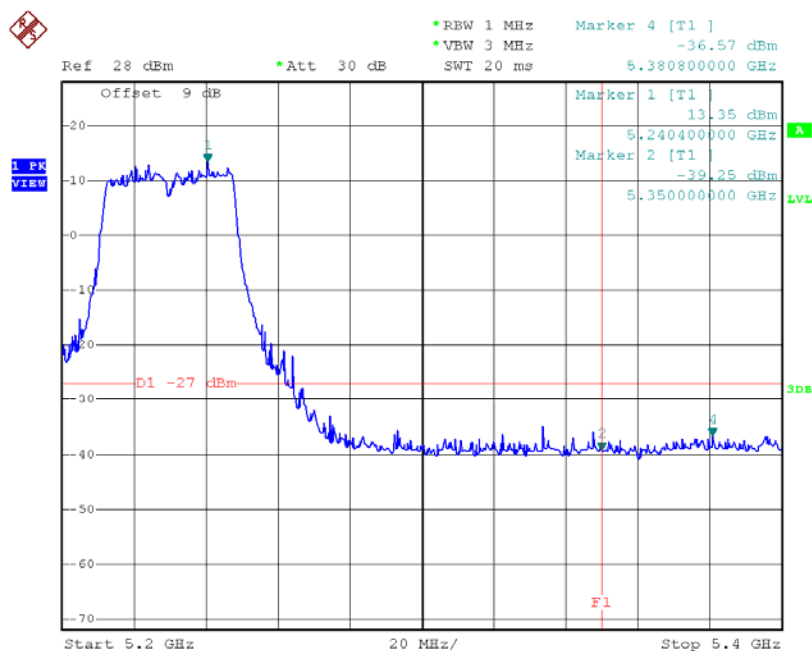
Test Mode: UNII-1/TX AC40 Mode_ANT 3

TX mode CH38



Date: 7.APR.2015 15:09:04

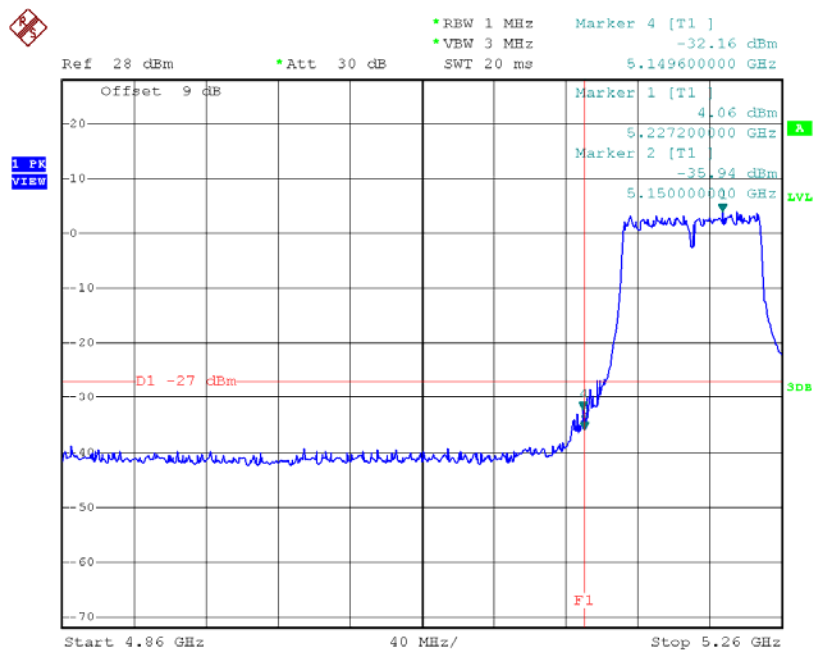
TX modeCH46



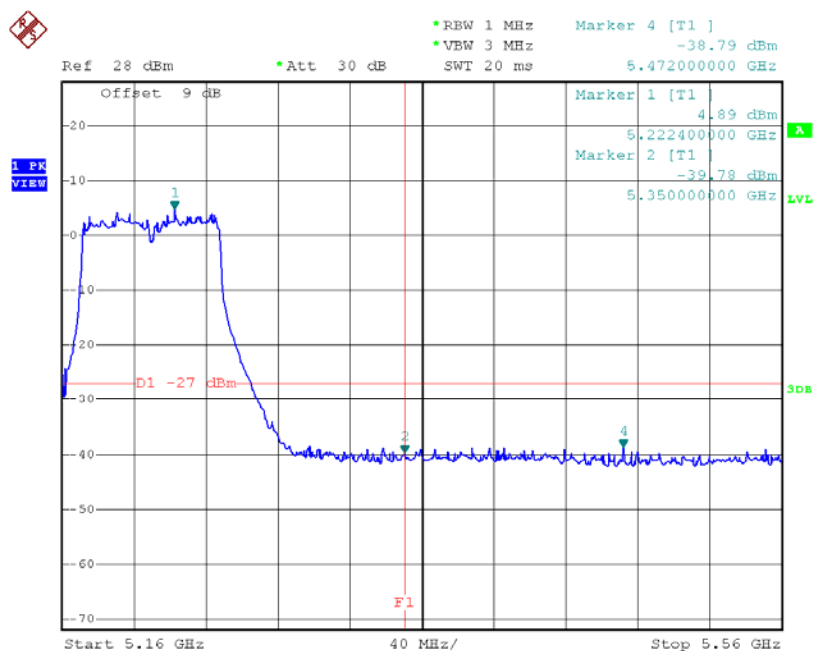
Date: 7.APR.2015 15:09:41

Test Mode: UNII-1/TX AC80 Mode_ANT 1

TX mode CH42



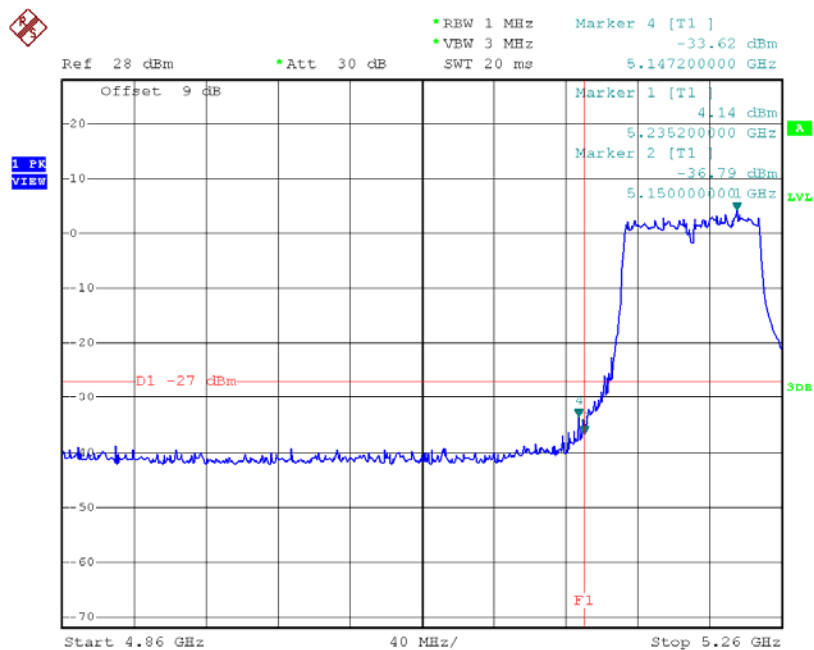
Date: 7.APR.2015 12:22:02



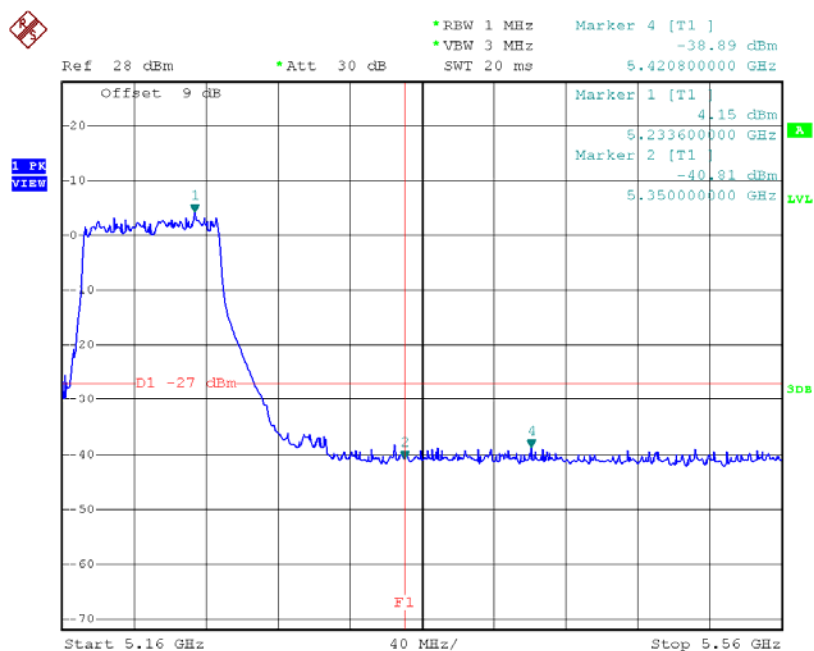
Date: 7.APR.2015 12:22:09

Test Mode: UNII-1/TX AC80 Mode_ANT 2

TX mode CH42



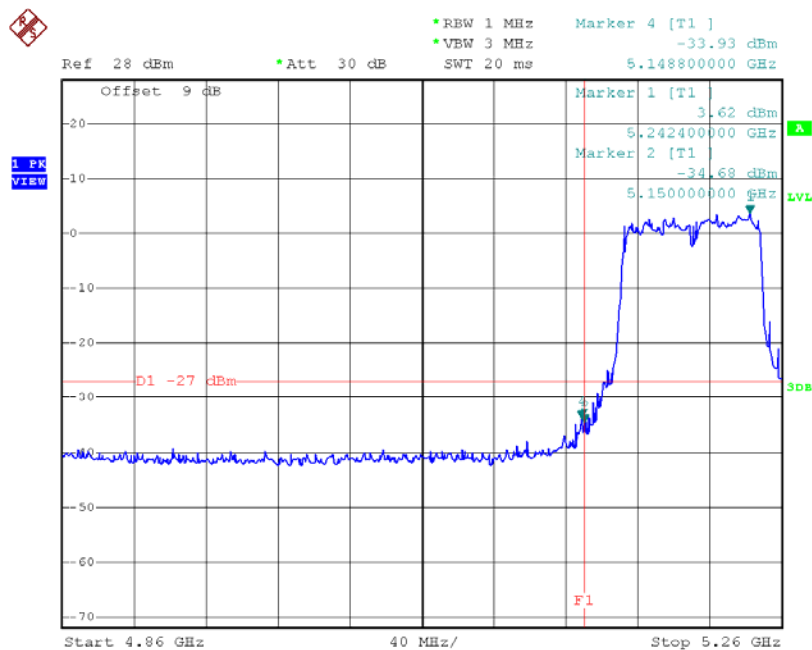
Date: 7.APR.2015 14:38:58



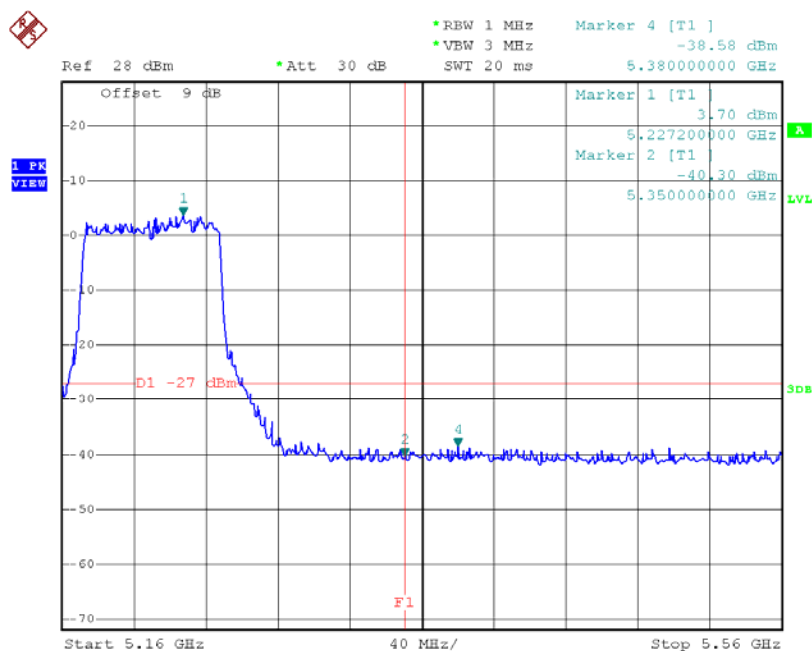
Date: 7.APR.2015 14:39:06

Test Mode: UNII-1/TX AC80 Mode_ANT 3

TX mode CH42



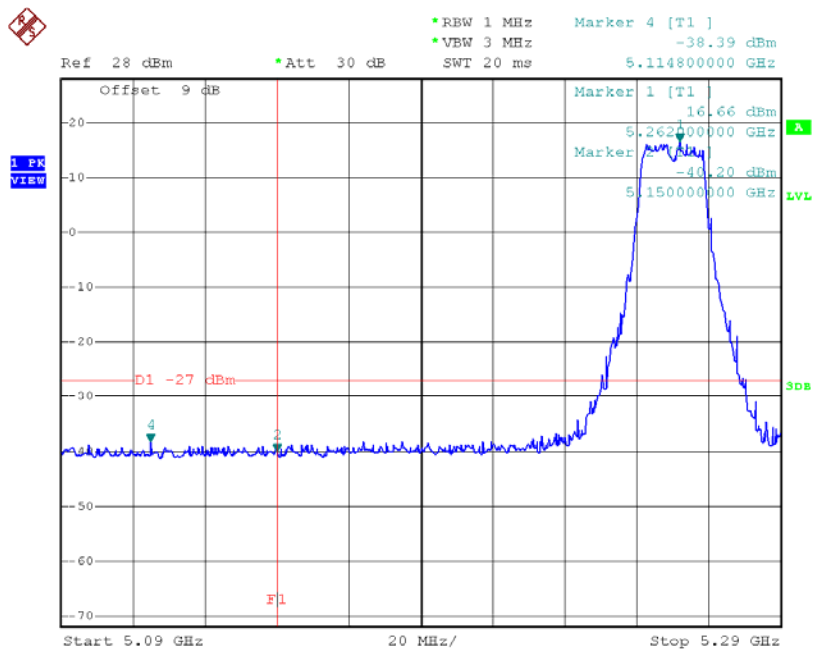
Date: 7.APR.2015 15:13:19



Date: 7.APR.2015 15:13:27

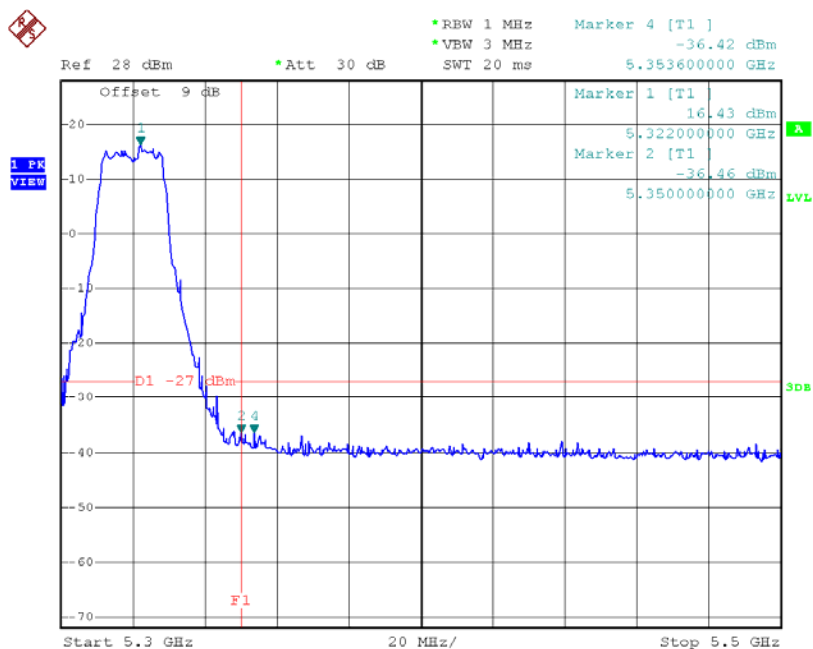
Test Mode: UNII-2A/TX AC20 Mode_ANT 1

TX mode CH52



Date: 7.APR.2015 12:08:24

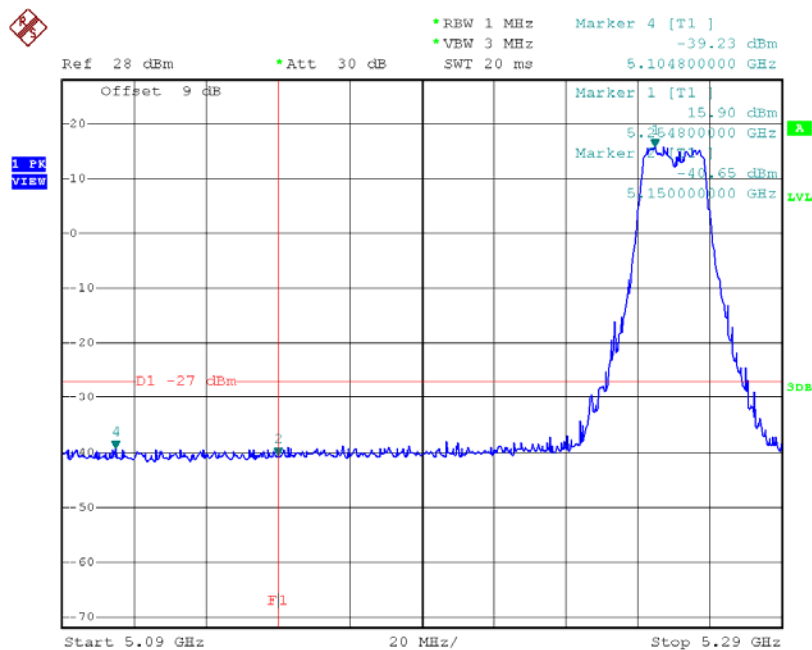
TX modeCH64



Date: 7.APR.2015 12:09:14

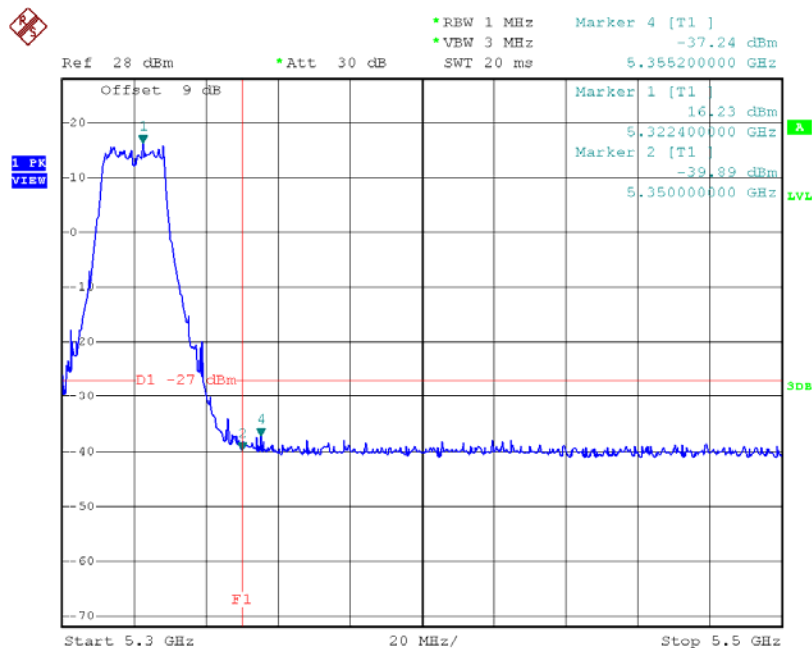
Test Mode:	UNII-2A/TX AC20 Mode_ANT 2
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TX mode CH52



Date: 7.APR.2015 14:21:44

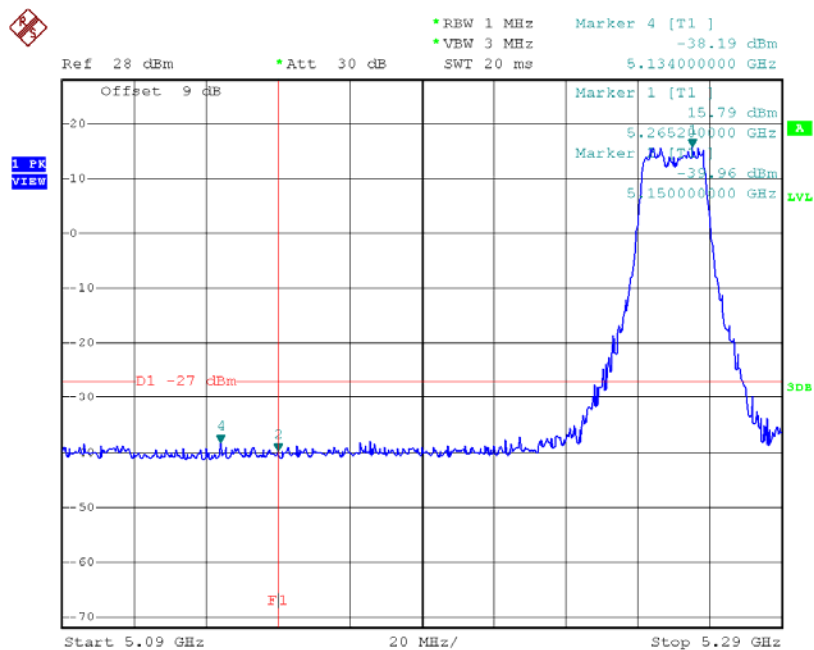
TX mode CH64



Date: 7.APR.2015 14:22:48

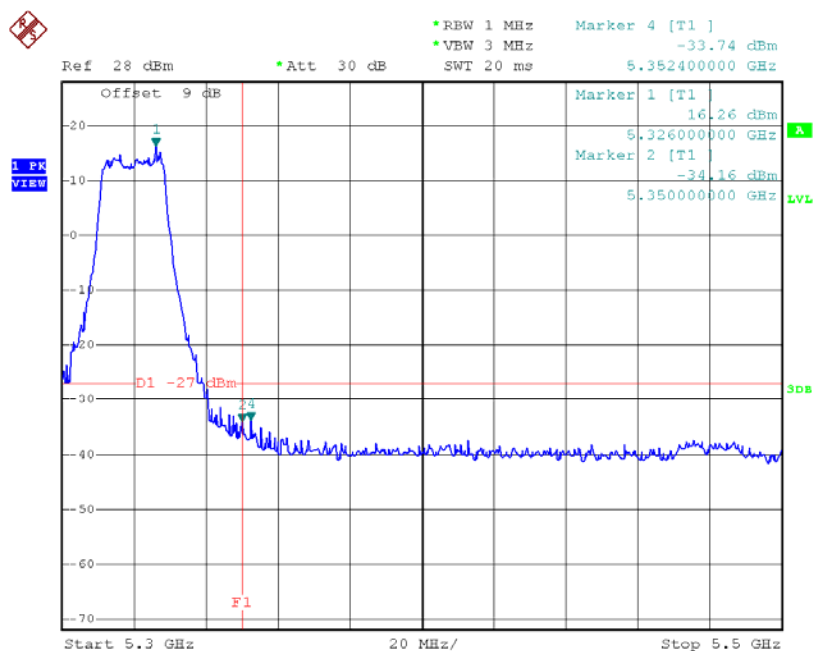
Test Mode:	UNII-2A/TX AC20 Mode_ANT 3
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TX mode CH52



Date: 7.APR.2015 15:00:43

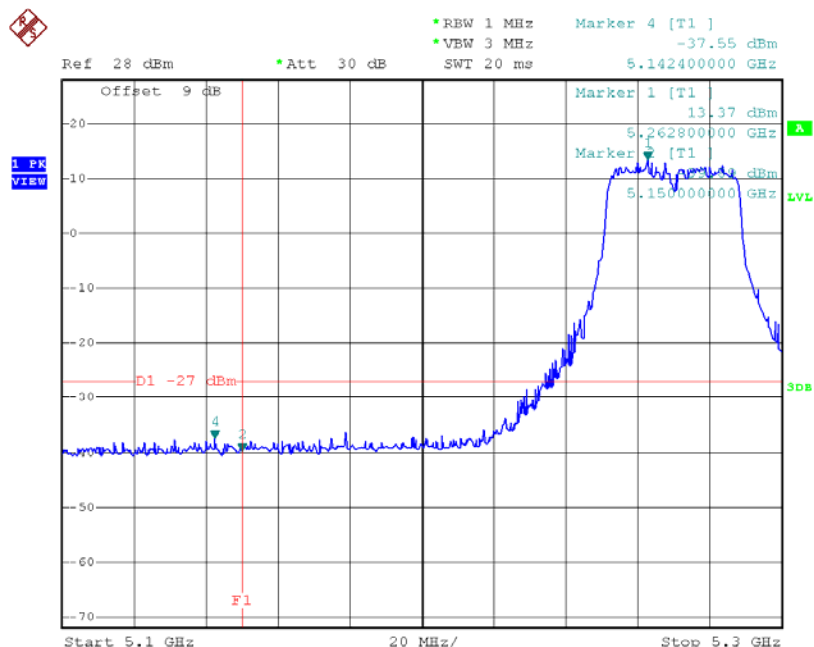
TX mode CH64



Date: 7.APR.2015 15:01:28

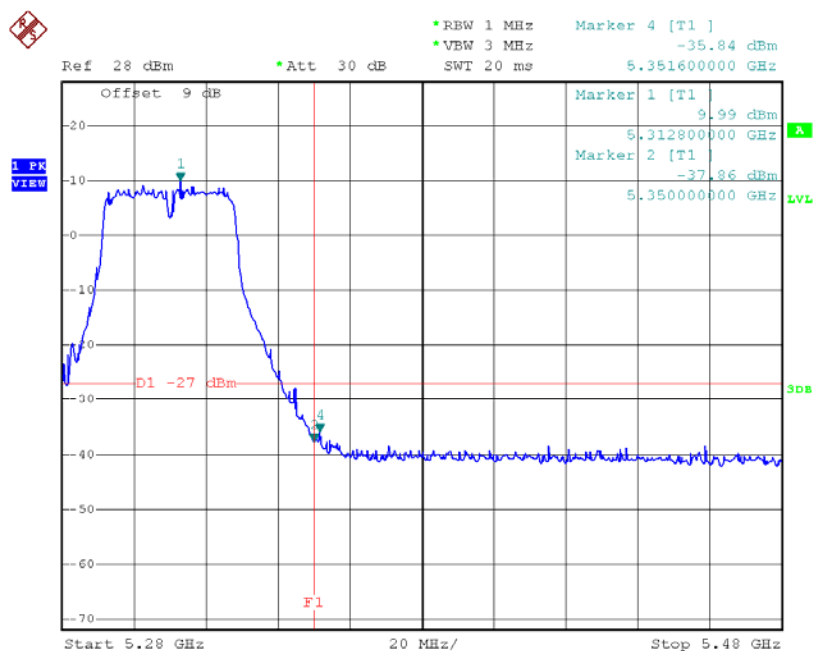
Test Mode:	UNII-2A/TX AC40 Mode_ANT 1
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TX mode CH54



Date: 7.APR.2015 12:18:20

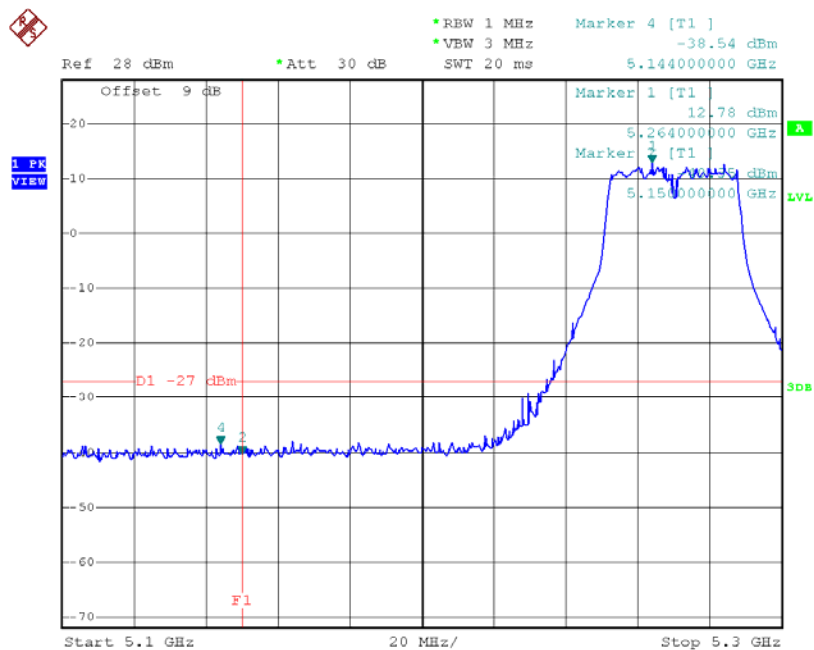
TX mode CH62



Date: 7.APR.2015 12:18:58

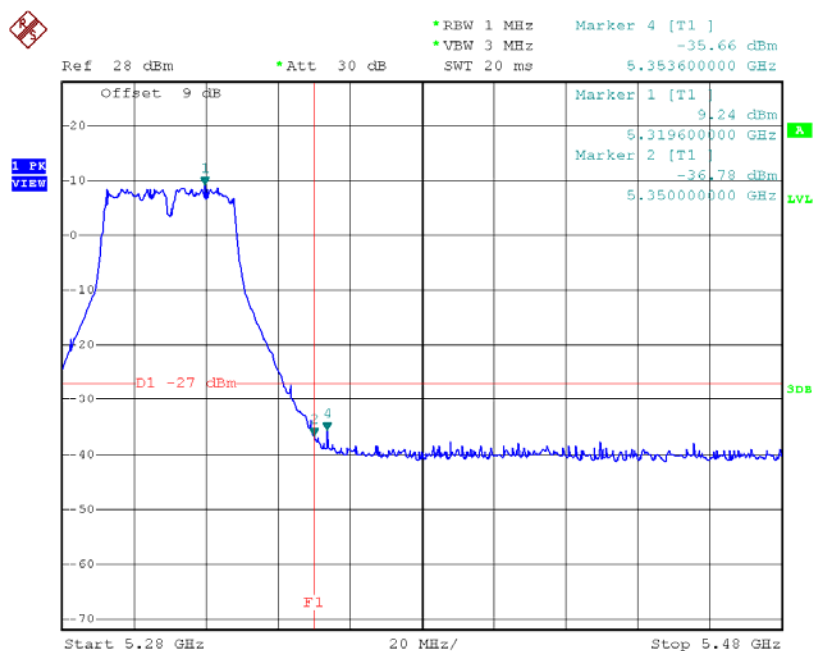
Test Mode:	UNII-2A/TX AC40 Mode_ANT 2
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TX mode CH54



Date: 7.APR.2015 14:34:44

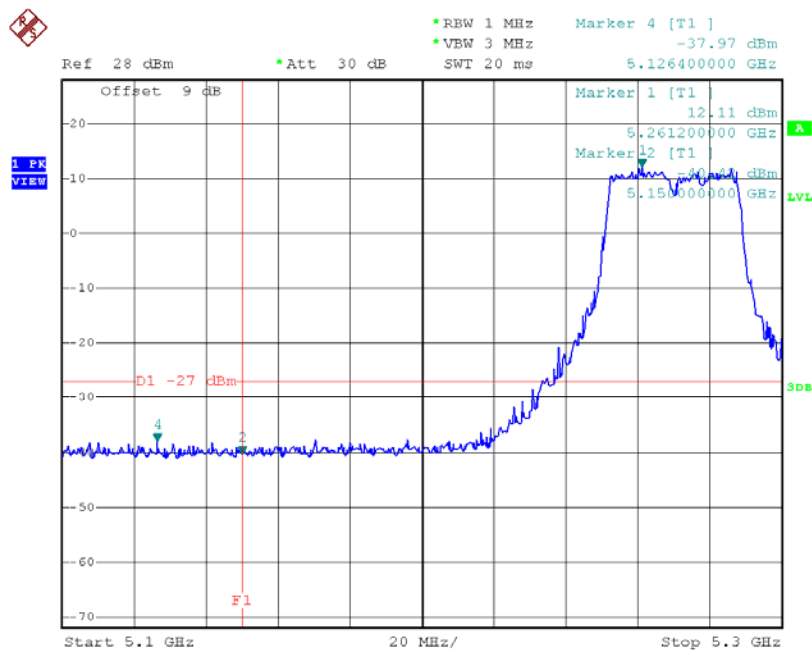
TX mode CH62



Date: 7.APR.2015 14:35:32

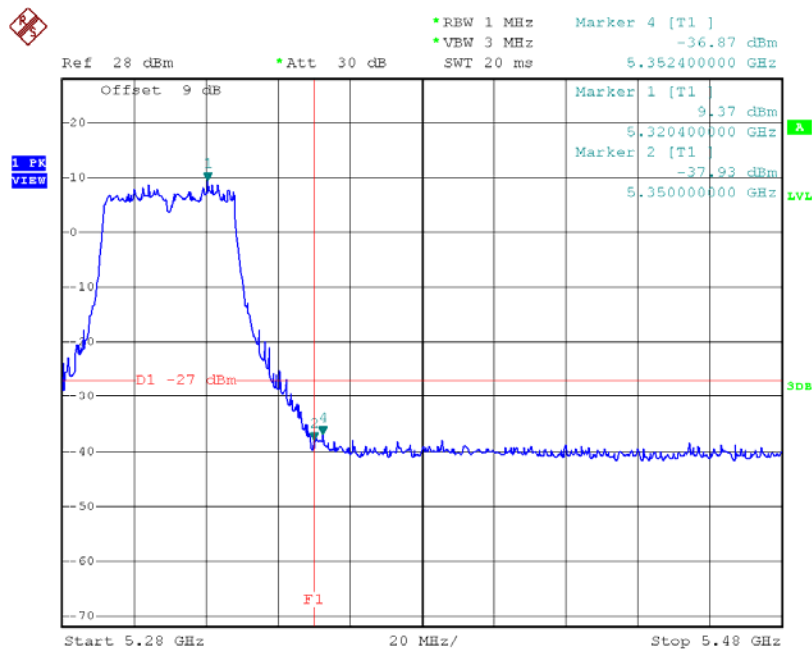
Test Mode: UNII-2A/TX AC40 Mode_ANT 3

TX mode CH54



Date: 7.APR.2015 15:10:10

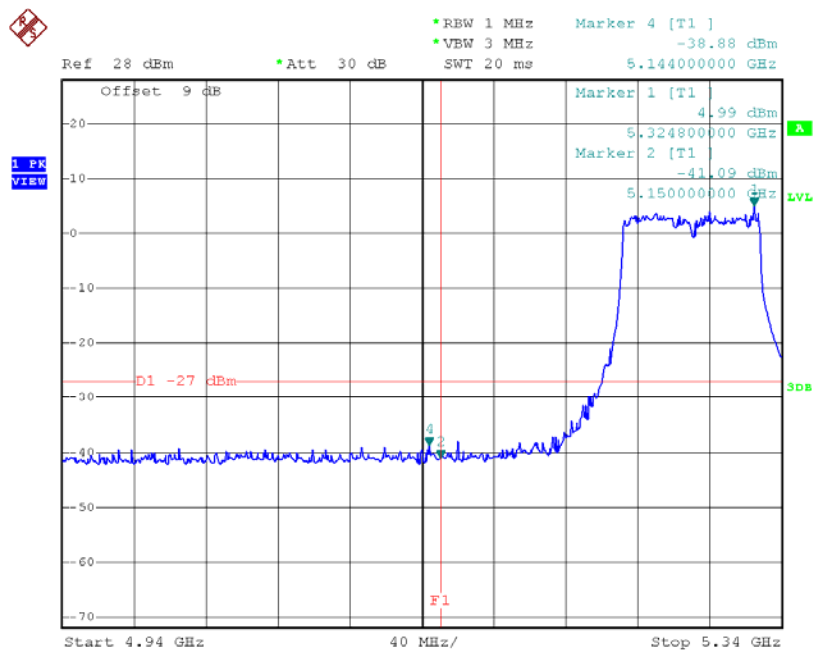
TX mode CH62



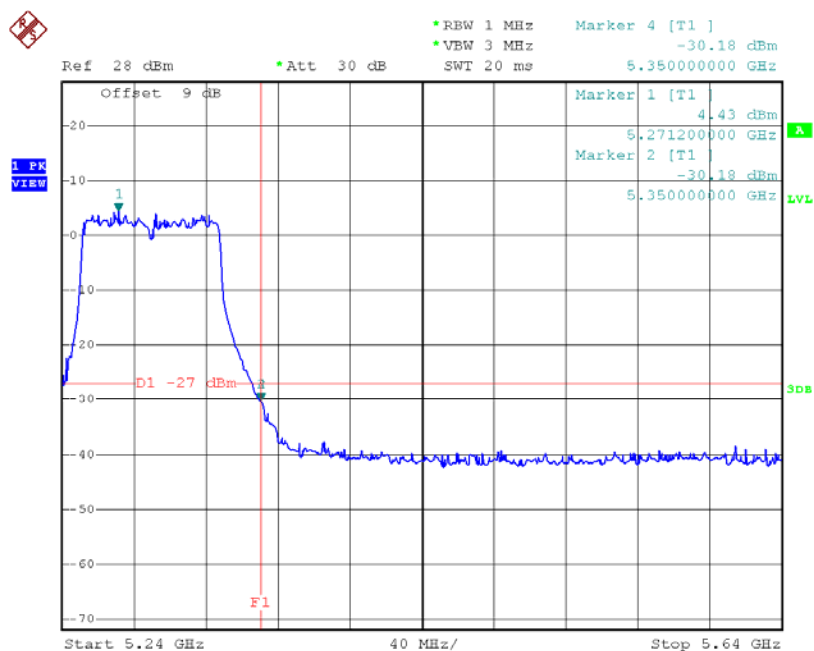
Date: 7.APR.2015 15:10:43

Test Mode:	UNII-2A/TX AC80 Mode_ANT 1
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TX mode CH58



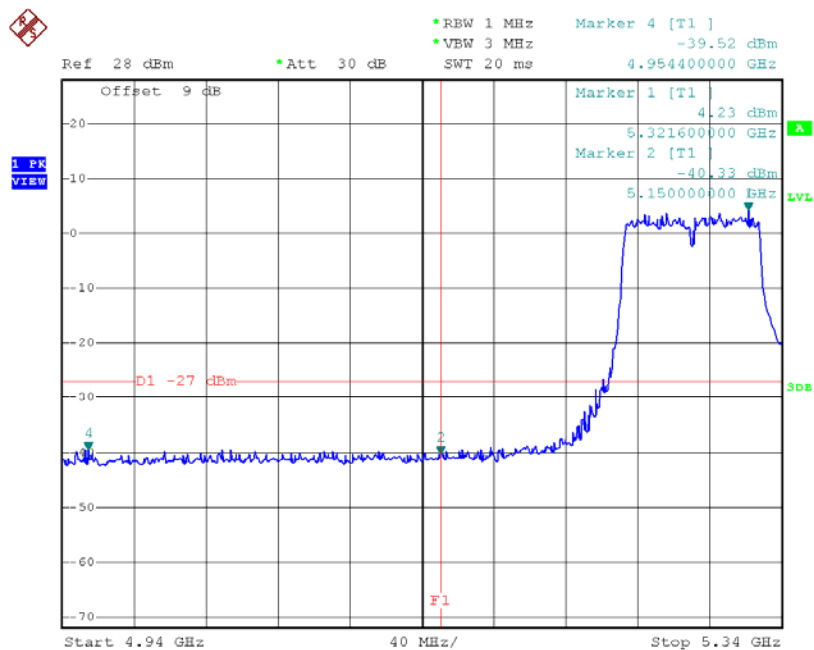
Date: 7.APR.2015 12:22:47



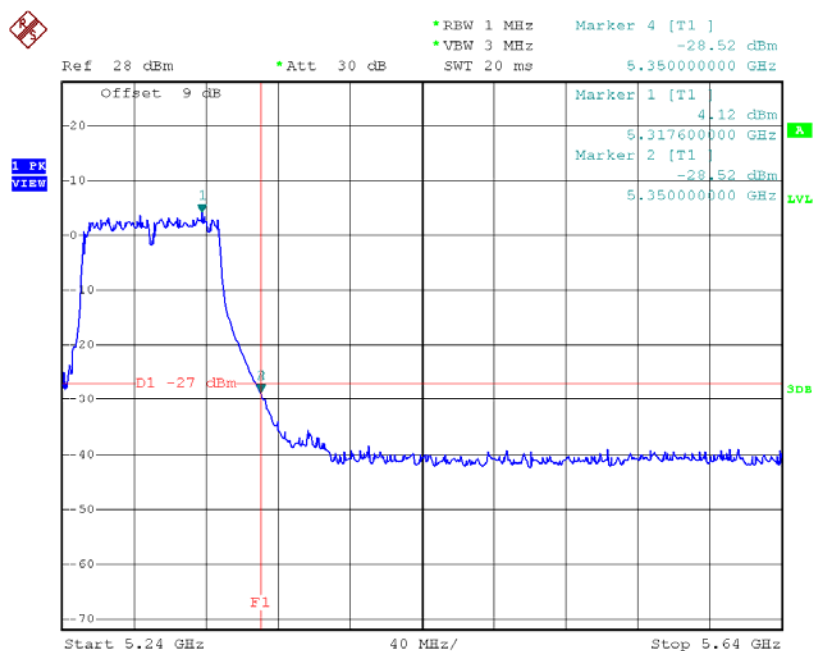
Date: 7.APR.2015 12:22:54

Test Mode:	UNII-2A/TX AC80 Mode_ANT 2
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TX mode CH58



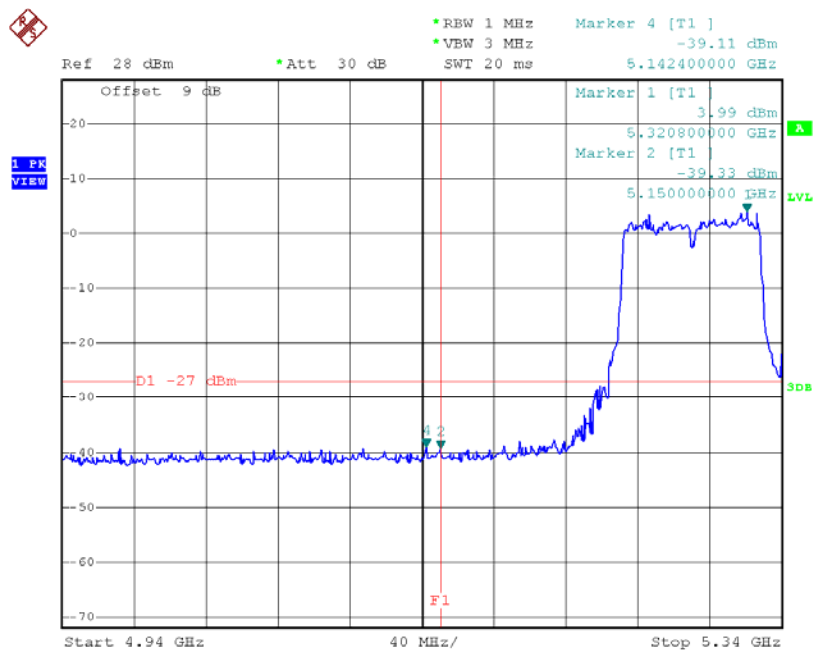
Date: 7.APR.2015 14:39:45



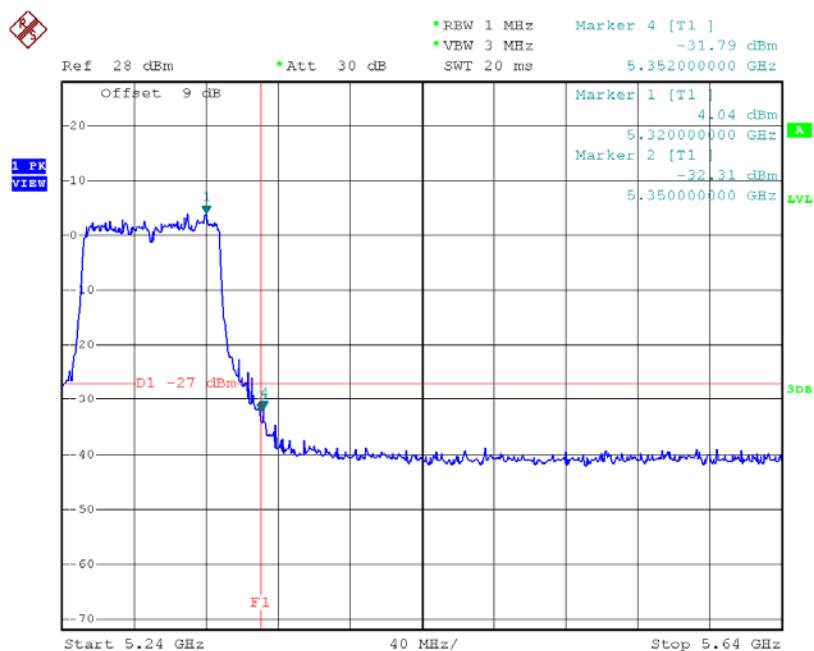
Date: 7.APR.2015 14:39:53

Test Mode: UNII-2A/TX AC80 Mode_ANT 3

TX mode CH58



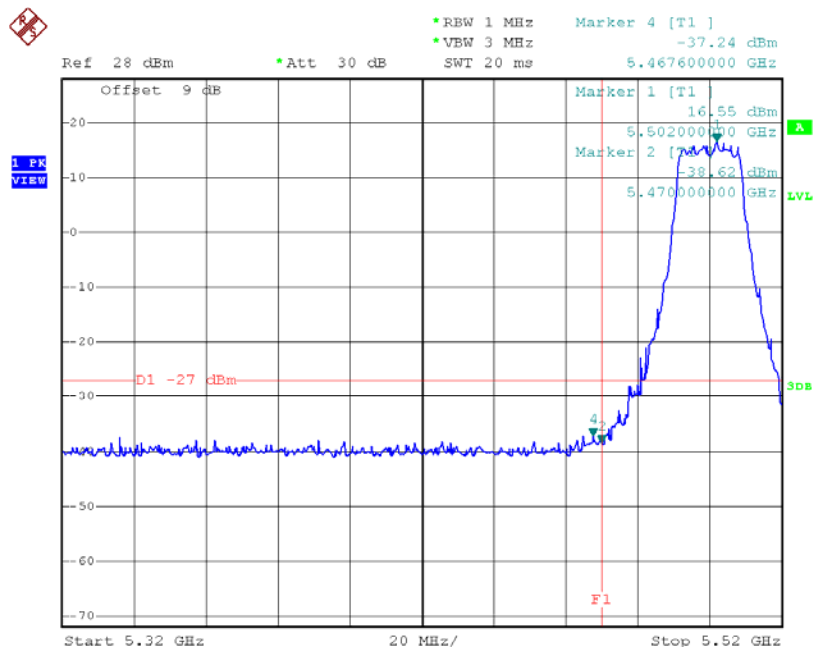
Date: 7.APR.2015 15:14:03



Date: 7.APR.2015 15:14:10

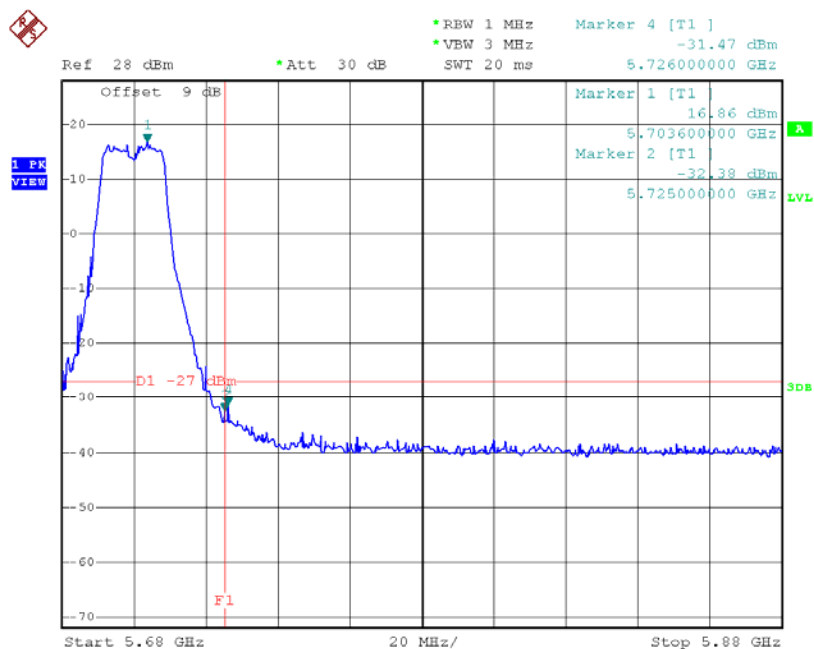
Test Mode: UNII-2C/TX AC20 Mode_ANT 1

TX mode CH100



Date: 7.APR.2015 12:09:51

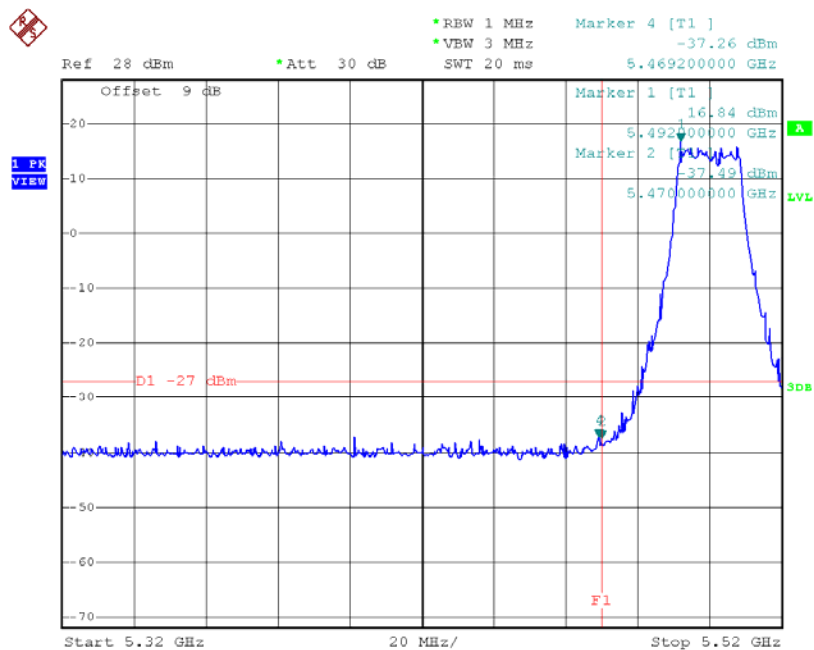
TX mode CH140



Date: 7.APR.2015 12:10:34

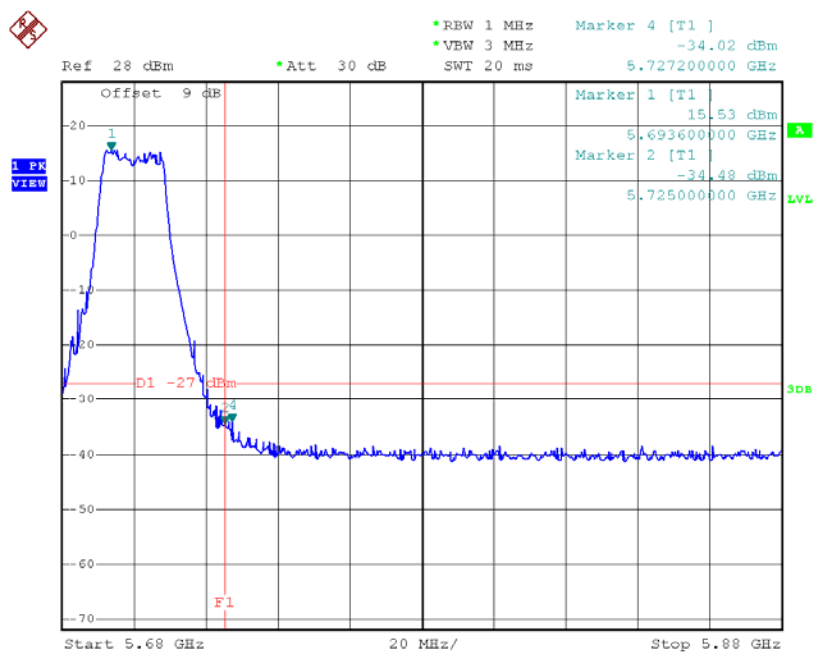
Test Mode: UNII-2C/TX AC20 Mode_ANT 2

TX mode CH100



Date: 7.APR.2015 14:23:12

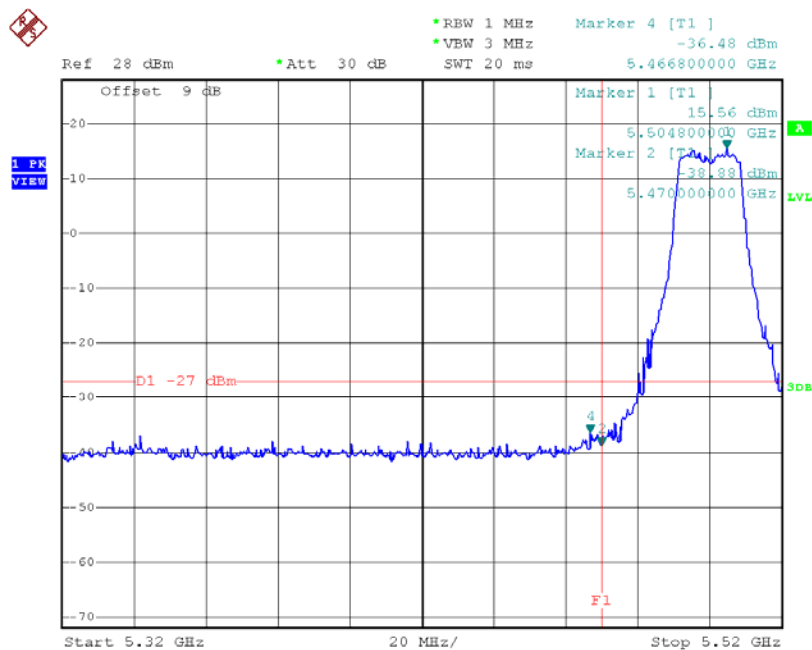
TX mode CH140



Date: 7.APR.2015 14:24:05

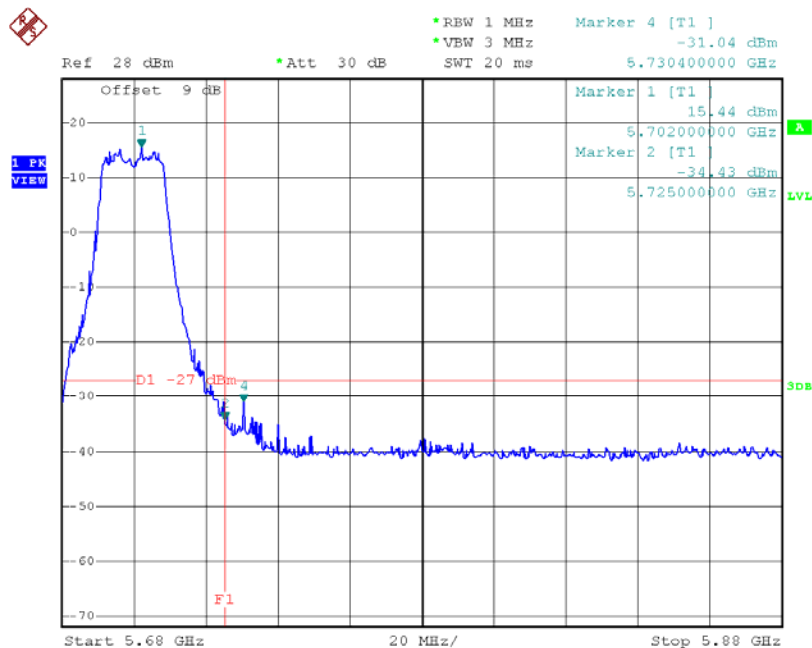
Test Mode: UNII-2C/TX AC20 Mode_ANT 3

TX mode CH100



Date: 7.APR.2015 15:01:52

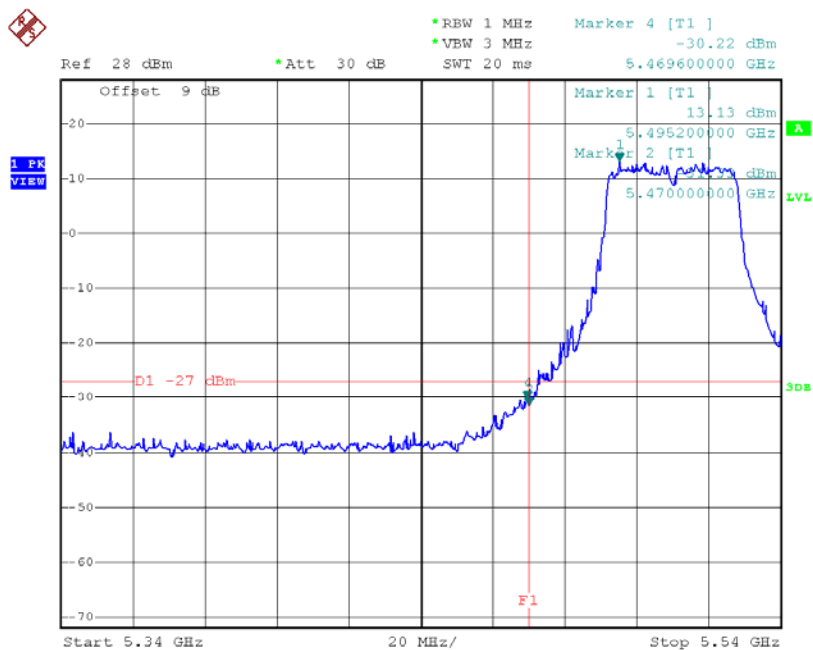
TX mode CH140



Date: 7.APR.2015 15:02:35

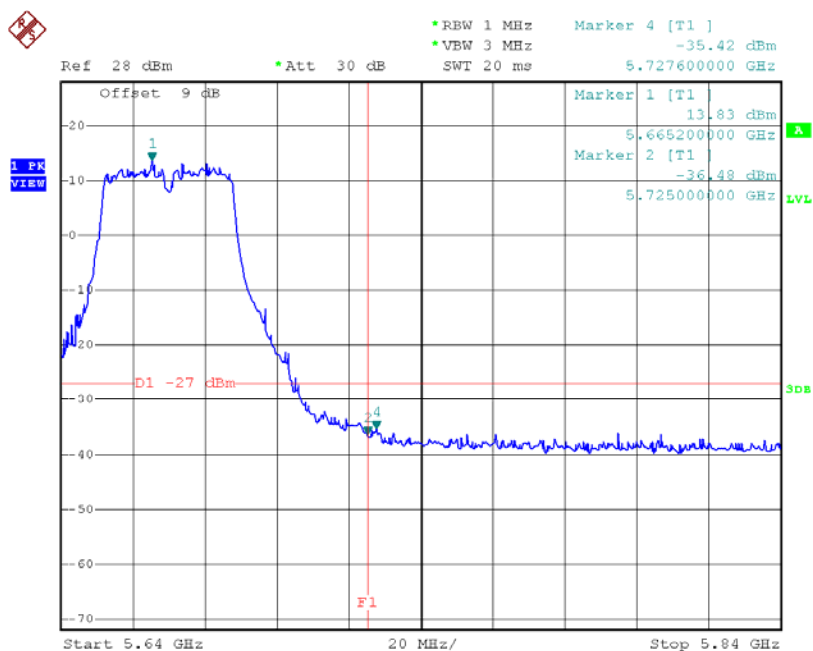
Test Mode:	UNII-2C/TX AC40 Mode_ANT 1
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TX mode CH102



Date: 7.APR.2015 12:19:24

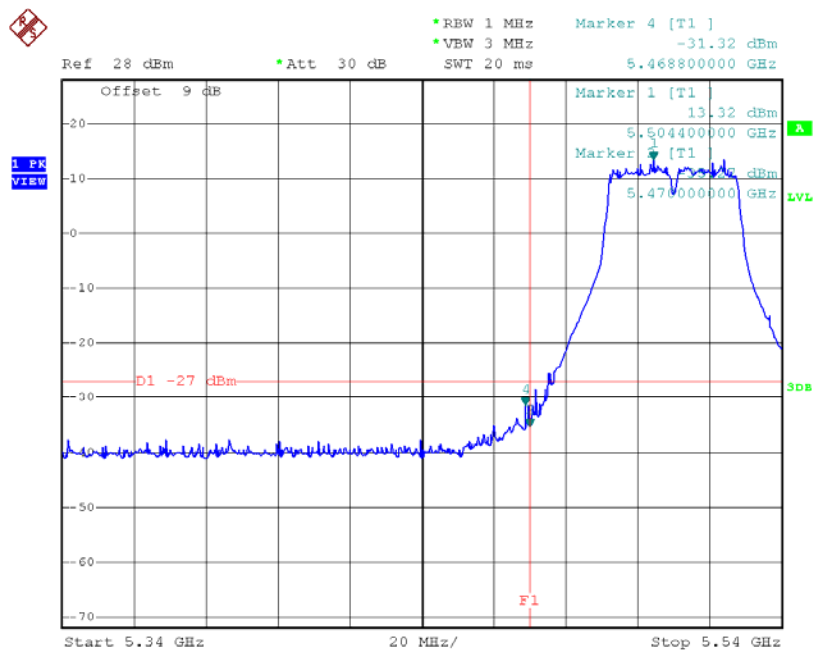
TX mode CH134



Date: 7.APR.2015 12:20:23

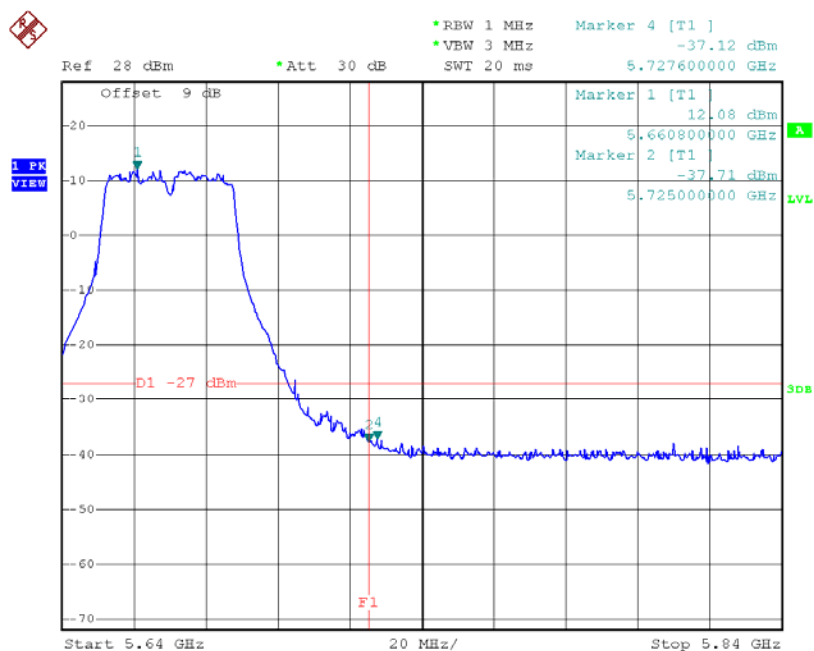
Test Mode: UNII-2C/TX AC40 Mode_ANT 2

TX mode CH102



Date: 7.APR.2015 14:35:59

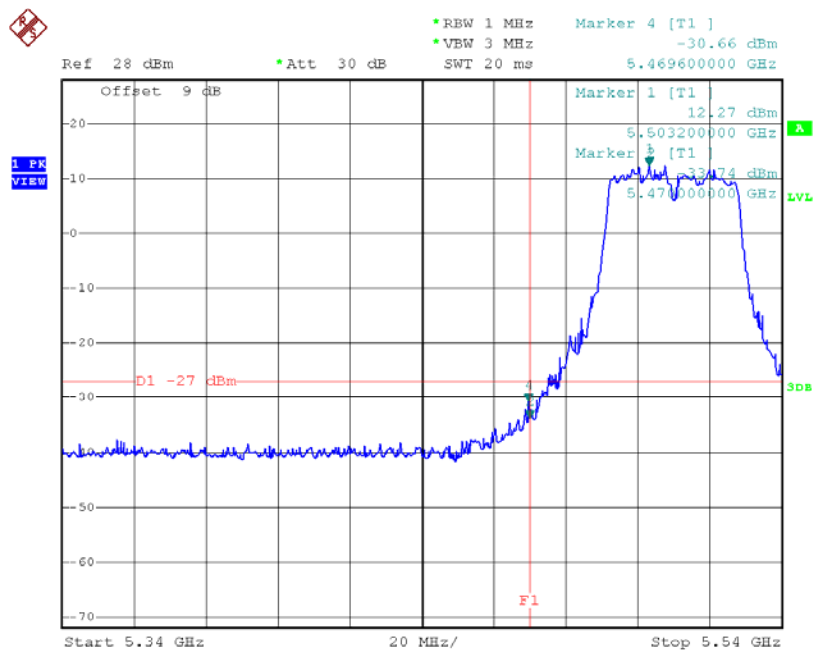
TX mode CH134



Date: 7.APR.2015 14:37:19

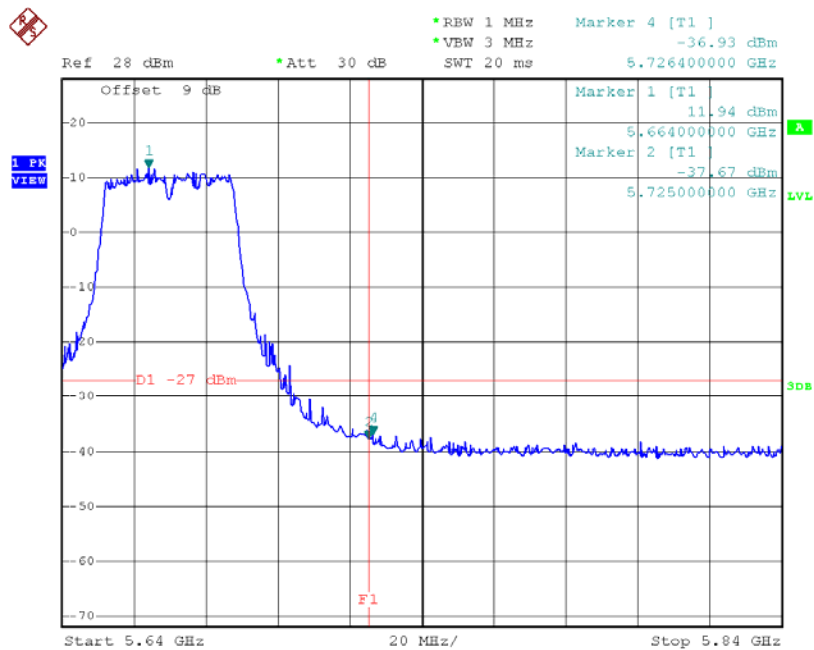
Test Mode: UNII-2C/TX AC40 Mode_ANT 3

TX mode CH102



Date: 7.APR.2015 15:11:08

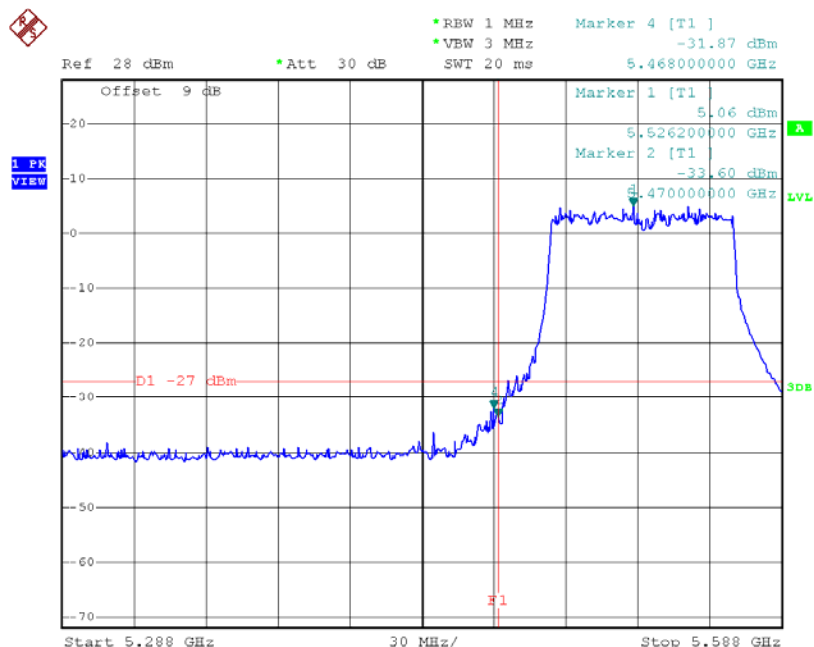
TX mode CH134



Date: 7.APR.2015 15:11:51

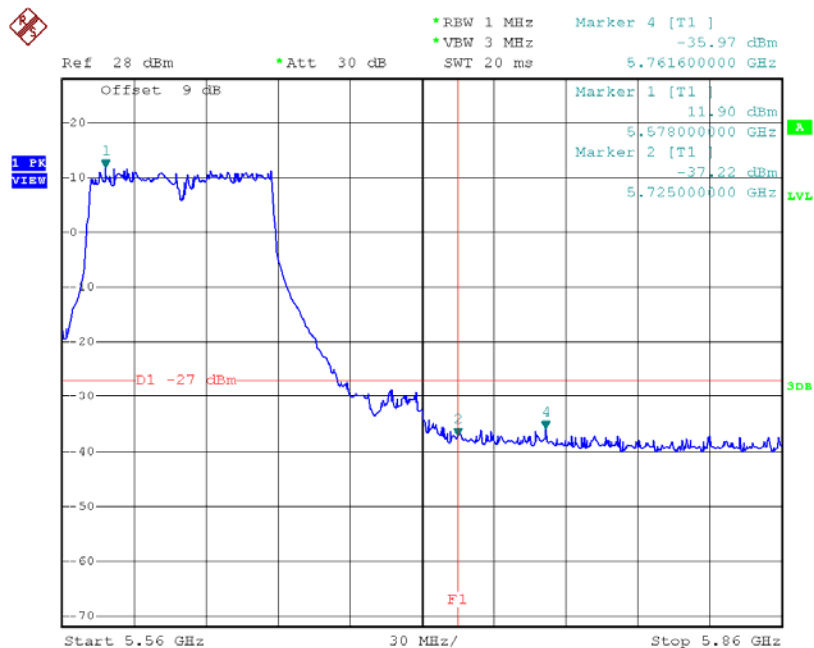
Test Mode:	UNII-2C/TX AC80 Mode_ANT 1
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TX mode CH106



Date: 7.APR.2015 12:23:23

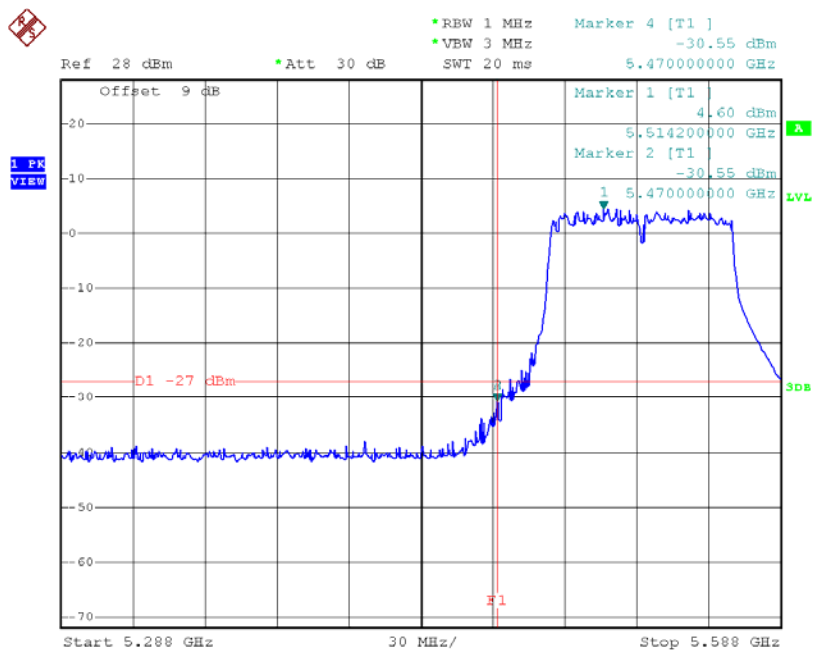
TX mode CH122



Date: 7.APR.2015 12:24:00

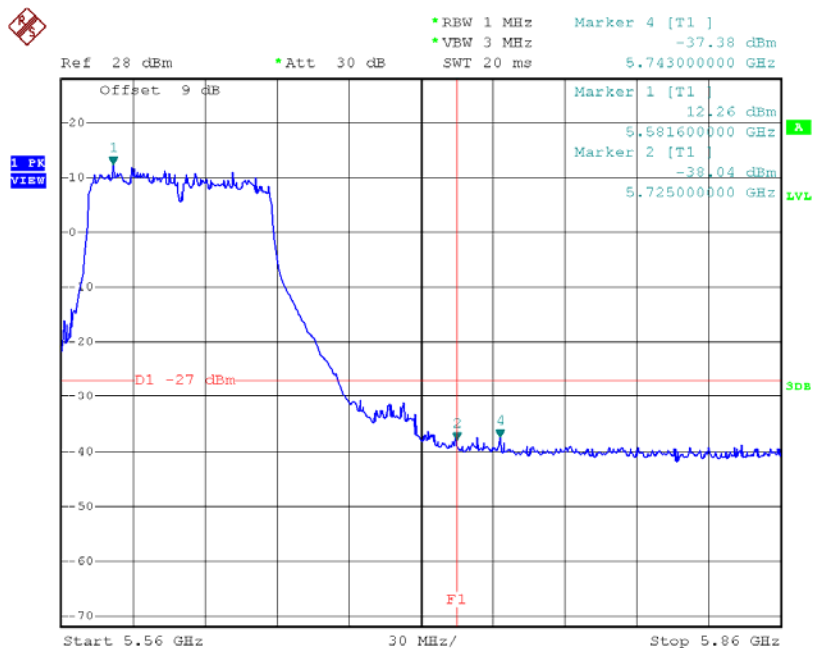
Test Mode:	UNII-2C/TX AC80 Mode_ANT 2
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TX mode CH106



Date: 7.APR.2015 14:40:24

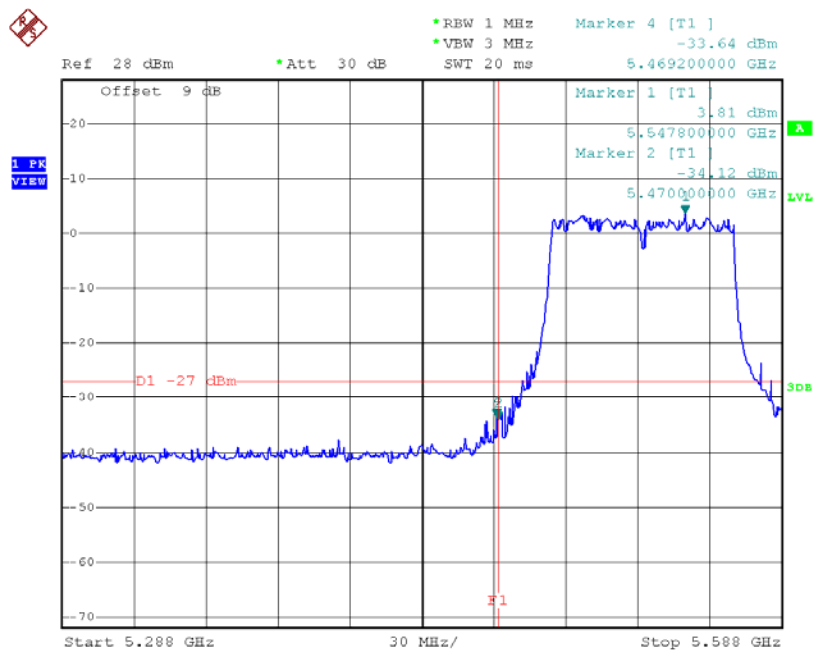
TX mode CH122



Date: 7.APR.2015 14:40:57

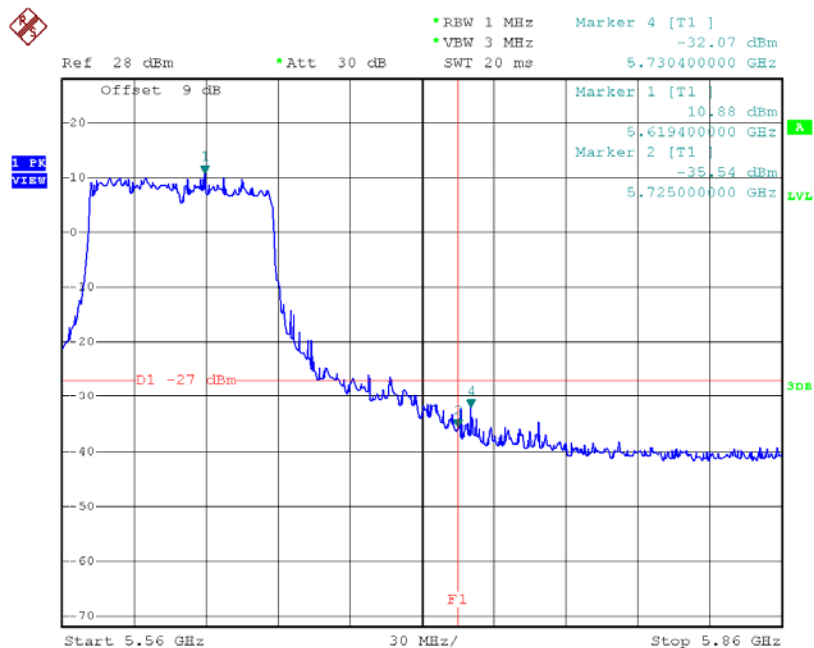
Test Mode: UNII-2C/TX AC80 Mode_ANT 3

TX mode CH106



Date: 7.APR.2015 15:14:42

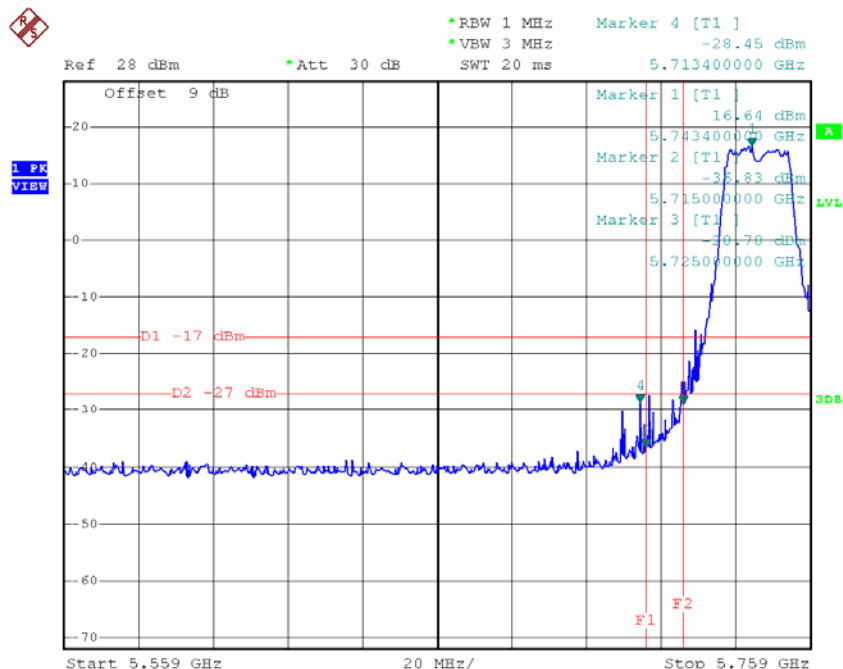
TX mode CH122



Date: 7.APR.2015 15:15:12

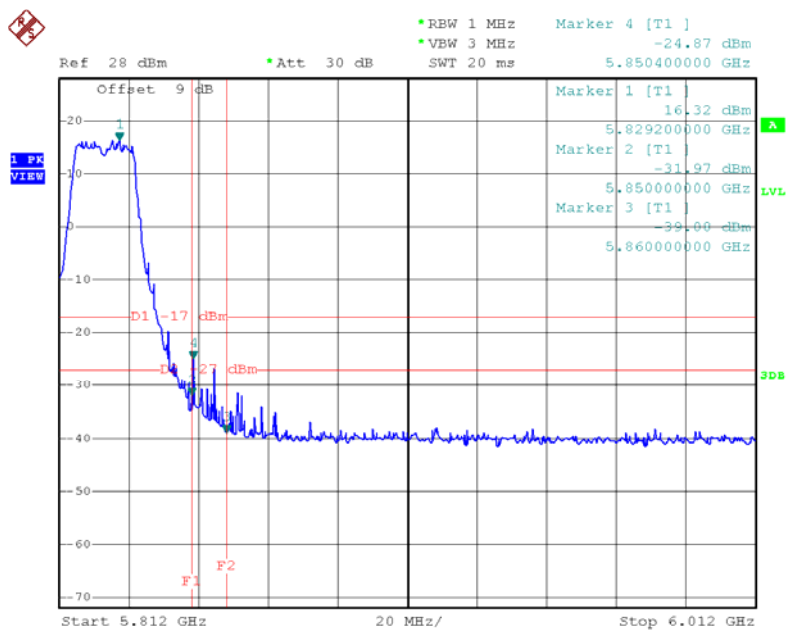
Test Mode: UNII-3/TX AC20 Mode_ANT 1

TX AC HT20 mode CH149



Date: 7.APR.2015 12:11:02

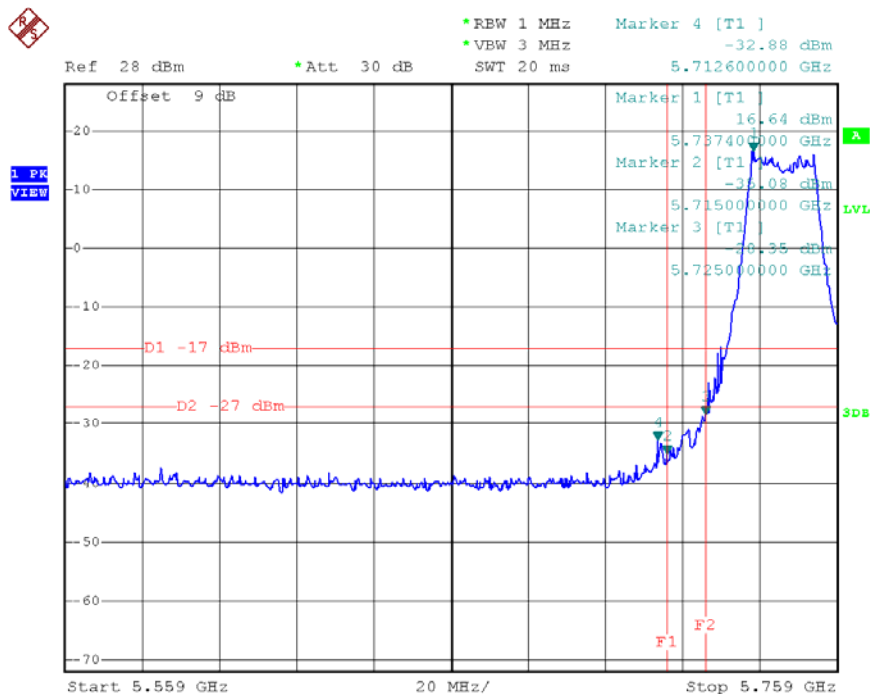
TX AC HT20 mode CH165



Date: 7.APR.2015 12:11:47

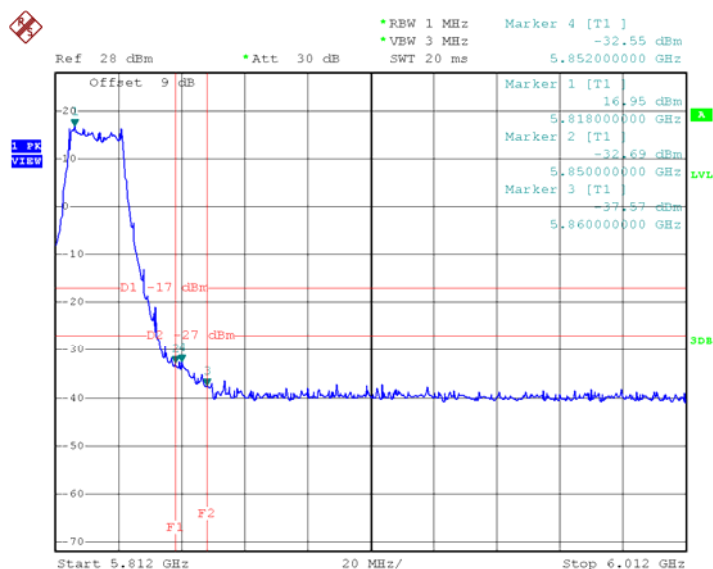
Test Mode: UNII-3/TX AC20 Mode_ANT 2

TX AC HT20 mode CH149



Date: 7.APR.2015 14:24:34

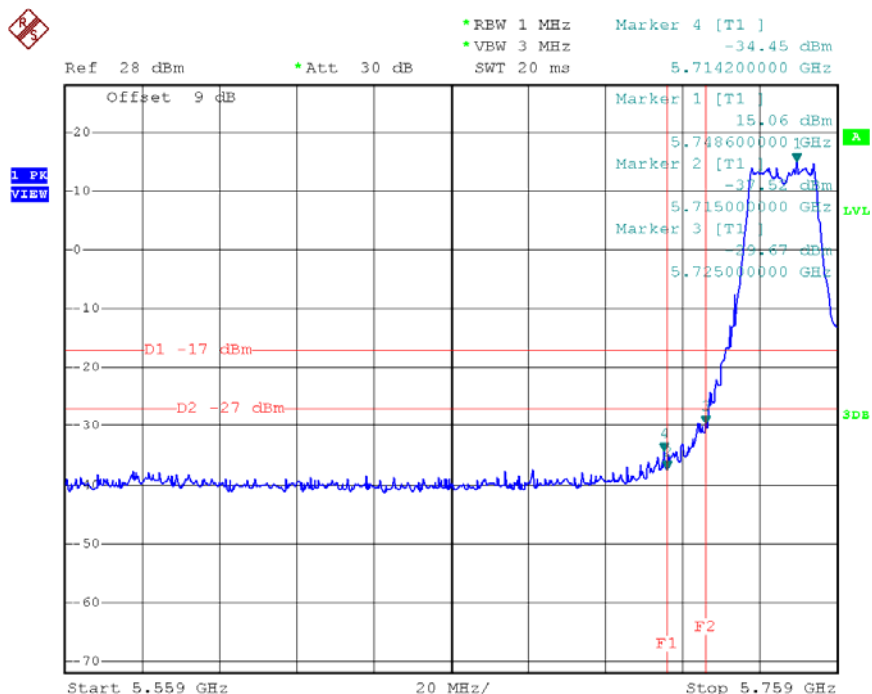
TX AC HT20 mode CH165



Date: 7.APR.2015 14:25:26

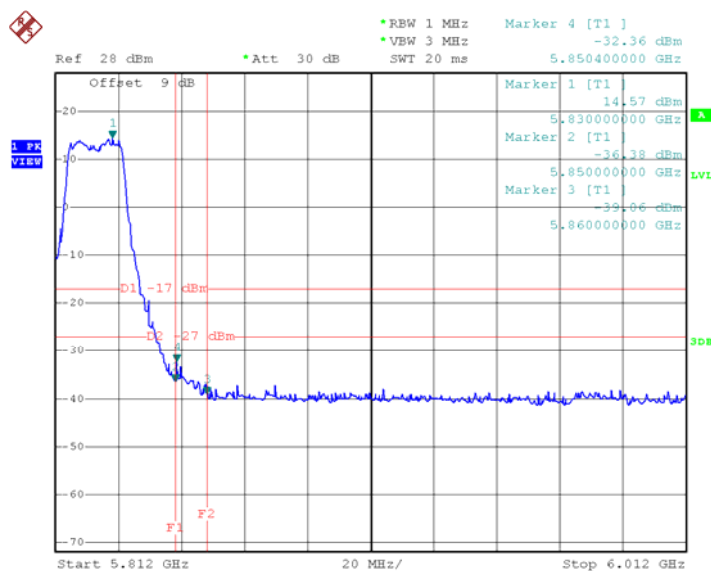
Test Mode: UNII-3/TX AC20 Mode_ANT 3

TX AC HT20 mode CH149



Date: 7.APR.2015 15:03:03

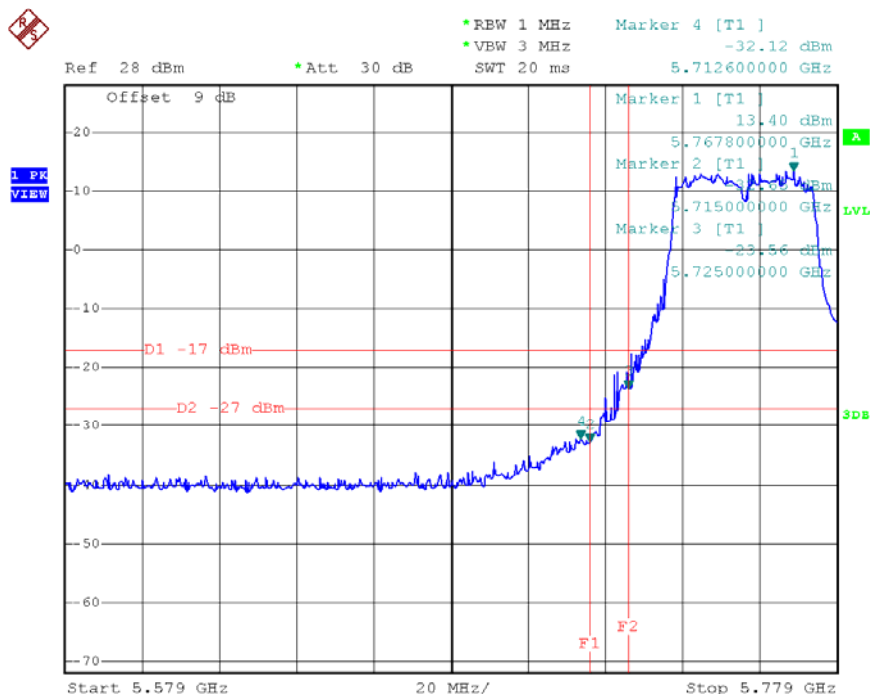
TX AC HT20 mode CH165



Date: 7.APR.2015 15:03:56

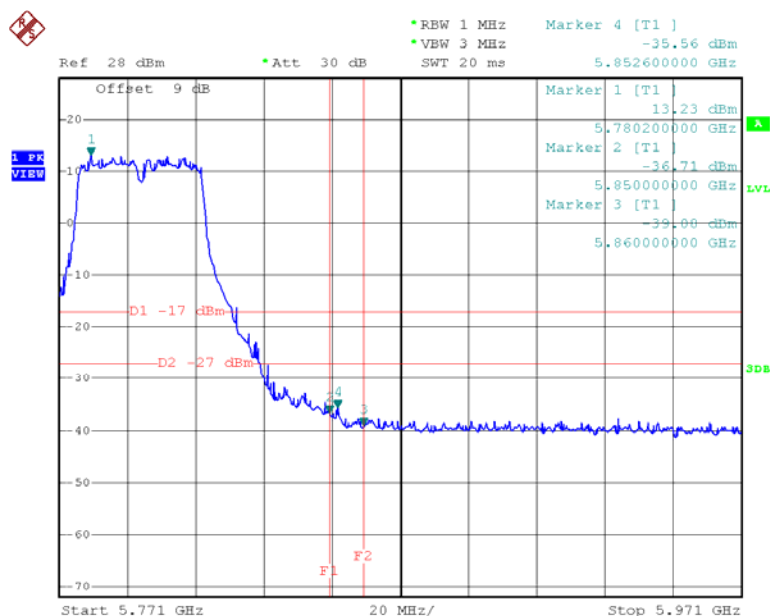
Test Mode: UNII-3/TX AC40 Mode_ANT 1

TX AC HT40 mode CH151



Date: 7.APR.2015 12:20:48

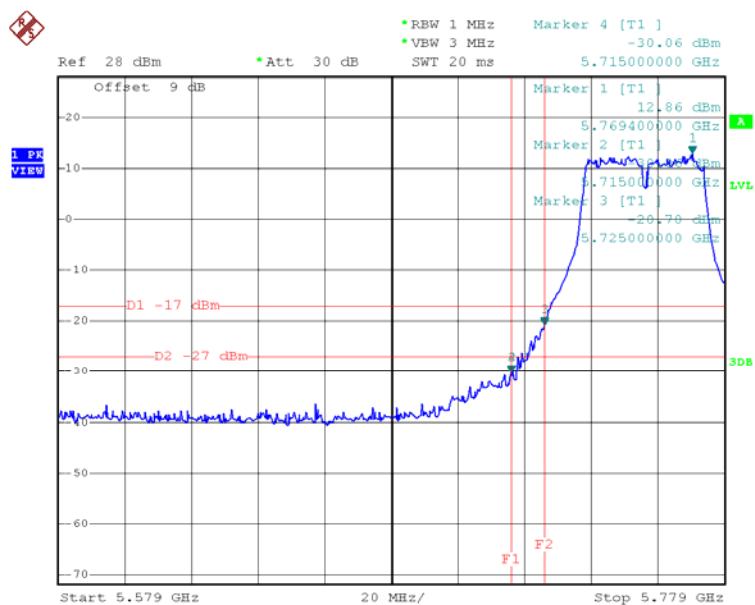
TX AC HT40 mode CH159



Date: 7.APR.2015 12:21:17

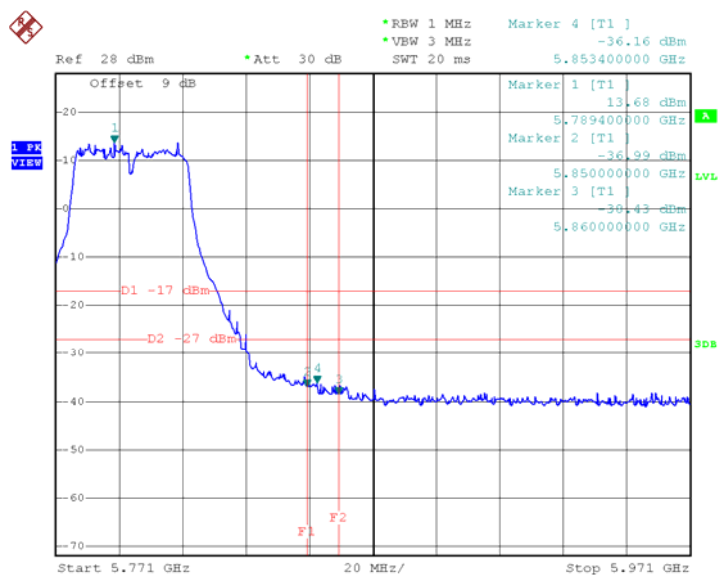
Test Mode: UNII-3/TX AC40 Mode_ANT 2

TX AC HT40 mode CH151



Date: 7.APR.2015 14:37:53

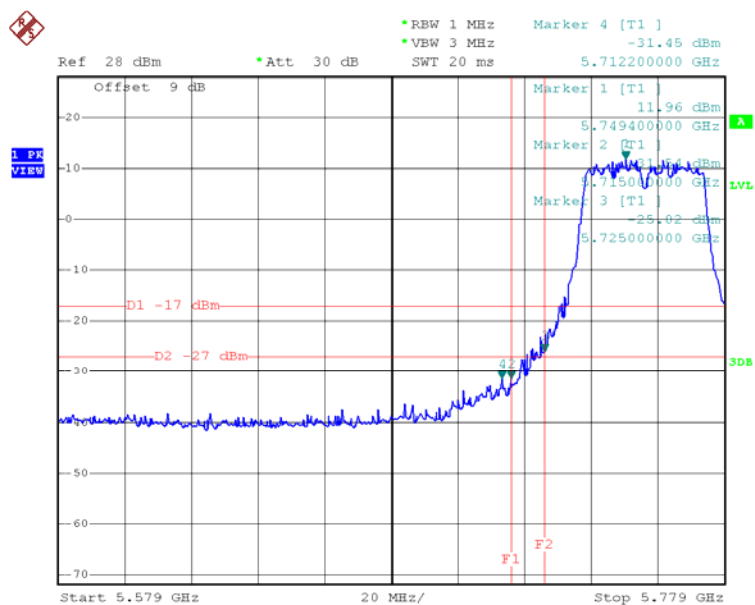
TX AC HT40 mode CH159



Date: 7.APR.2015 14:38:20

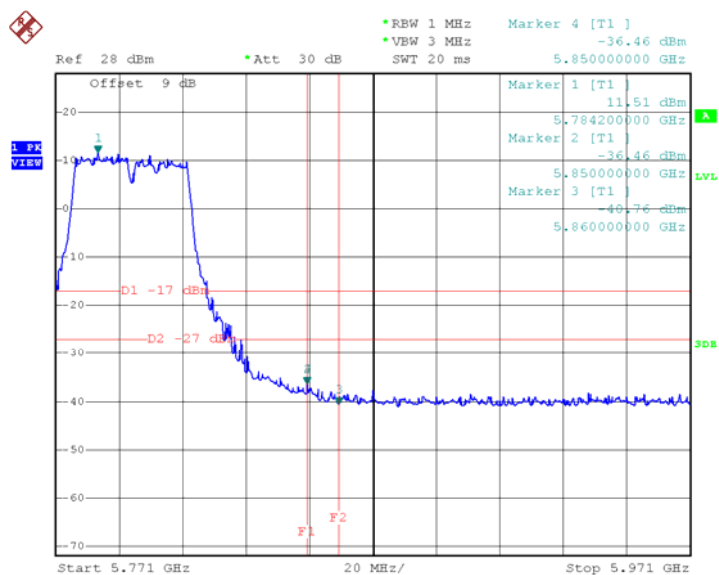
Test Mode: UNII-3/TX AC40 Mode_ANT 3

TX AC HT40 mode CH151



Date: 7.APR.2015 15:12:16

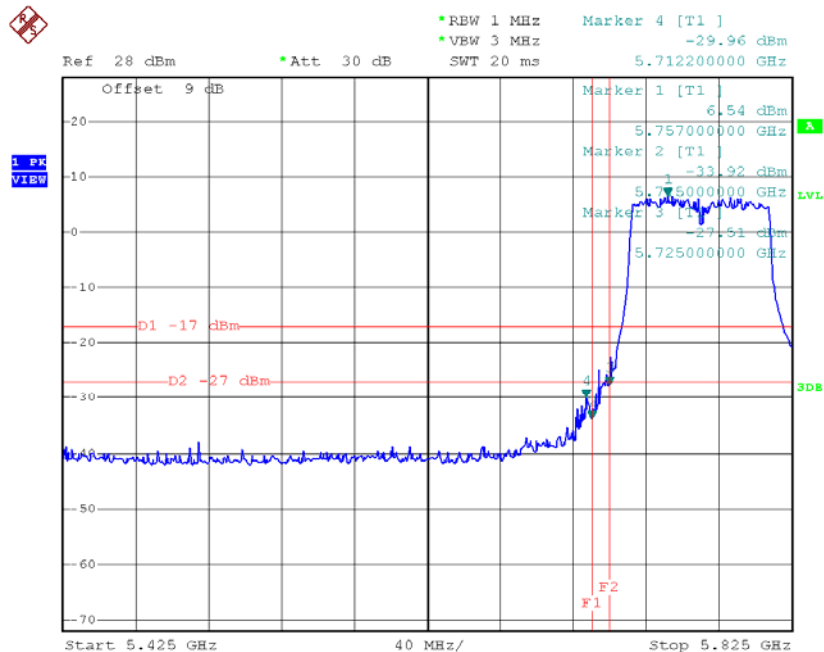
TX AC HT40 mode CH159



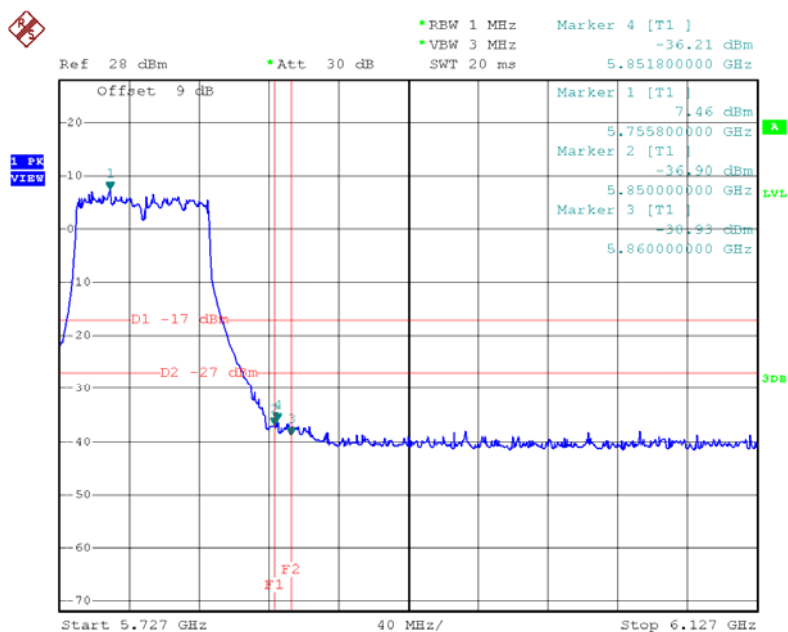
Date: 7.APR.2015 15:12:42

Test Mode: UNII-3/TX AC80 Mode_ANT 1

TX AC HT80 mode CH155



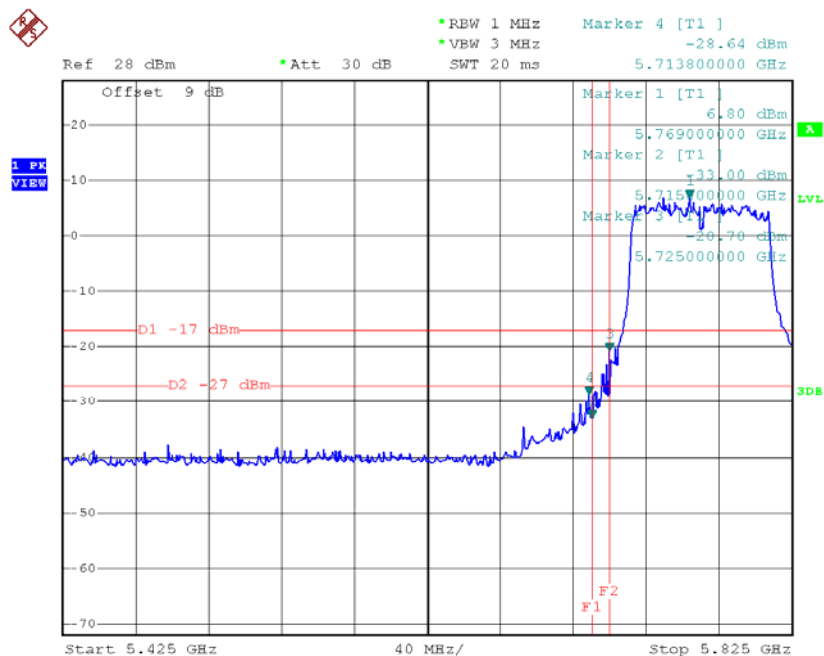
Date: 7.APR.2015 12:26:36



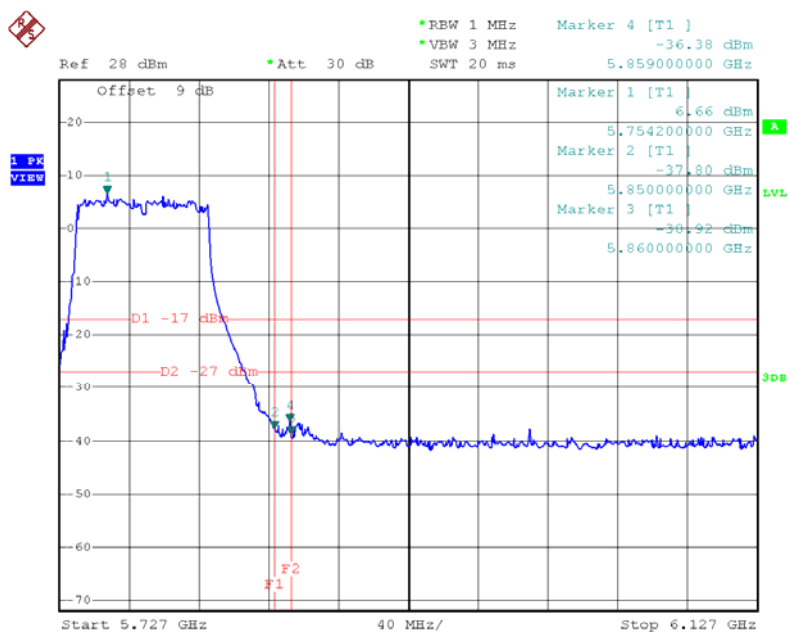
Date: 7.APR.2015 12:26:44

Test Mode: UNII-3/TX AC80 Mode_ANT 2

TX AC HT80 mode CH155



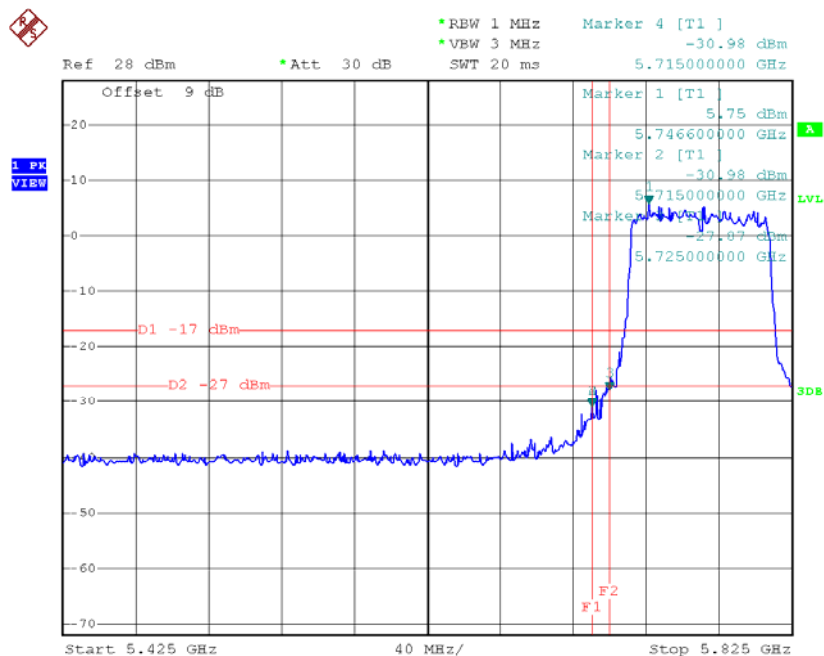
Date: 7.APR.2015 14:41:31



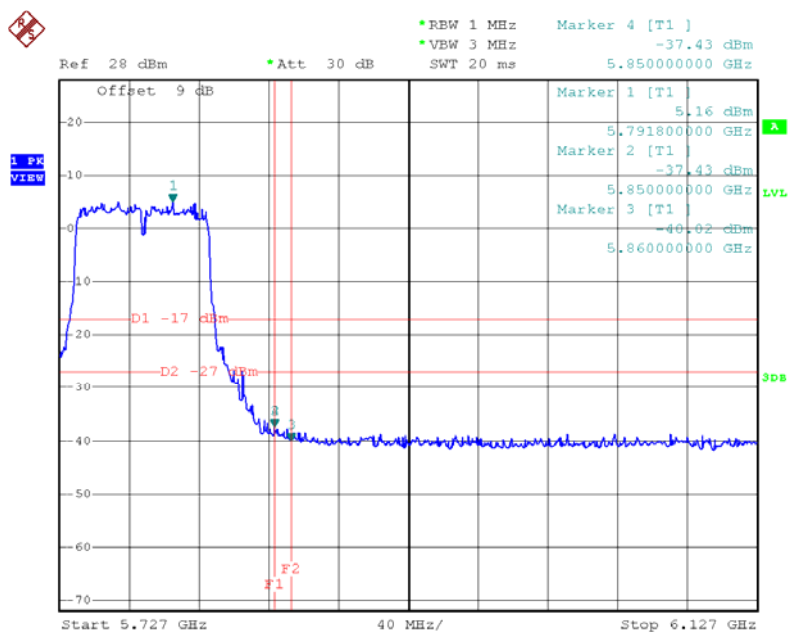
Date: 7.APR.2015 14:41:39

Test Mode: UNII-3/TX AC80 Mode_ANT 3

TX AC HT80 mode CH155



Date: 7.APR.2015 15:15:45



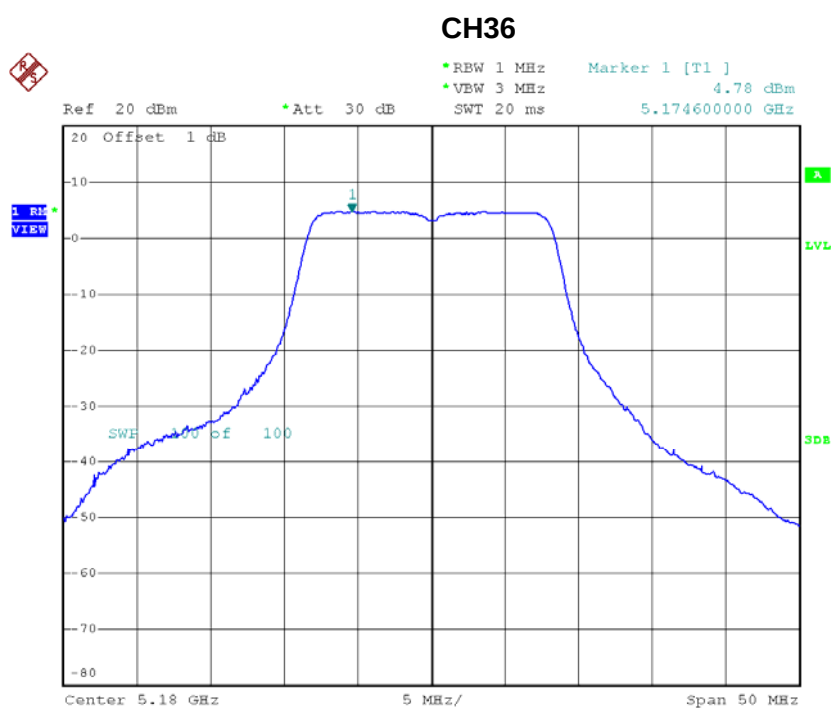
Date: 7.APR.2015 15:15:52

ATTACHMENTH - POWER SPECTRAL DENSITY

For 1TX

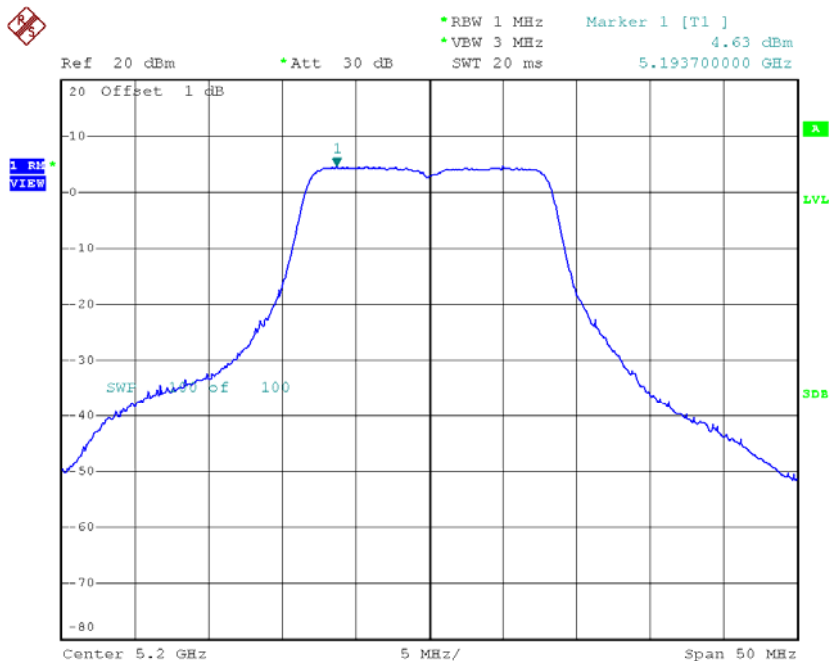
Test Mode: UNII-1/ TX A Mode_CH36/CH40/CH48

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	4.78	0.09	4.87	15.00
CH40	5200	4.63	0.09	4.72	15.00
CH48	5240	4.39	0.09	4.48	15.00



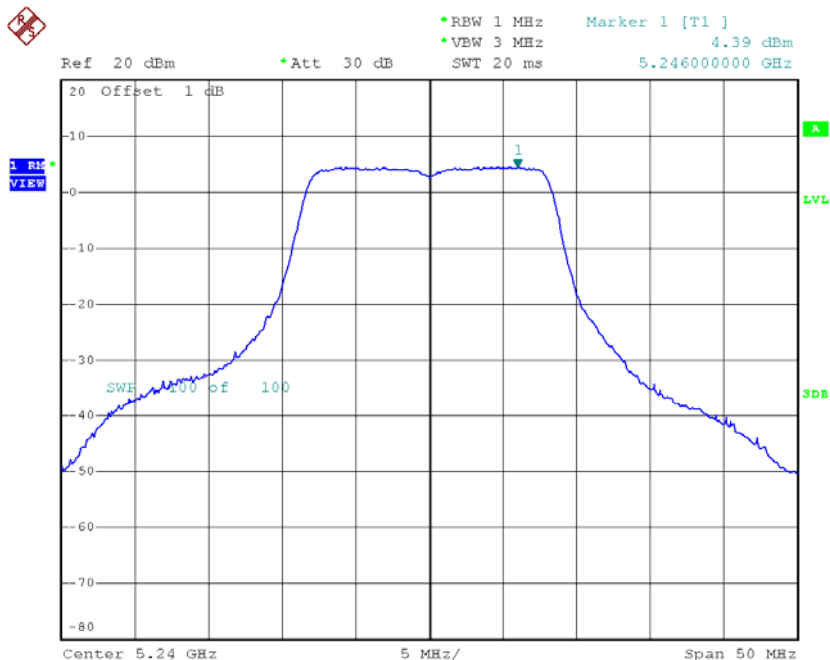
Date: 3.APR.2015 15:33:26

CH40



Date: 3.APR.2015 15:35:44

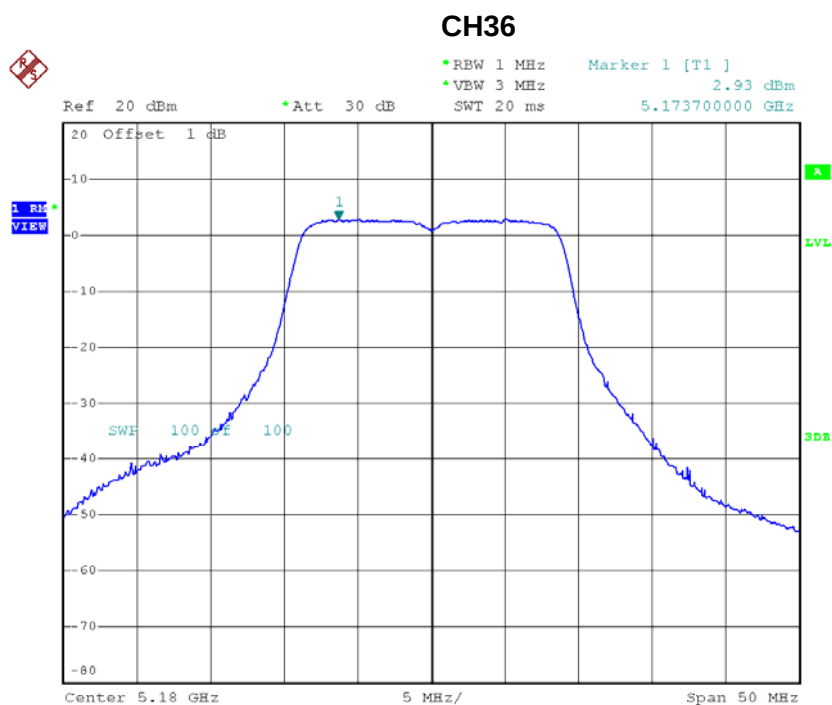
CH48



Date: 3.APR.2015 15:36:35

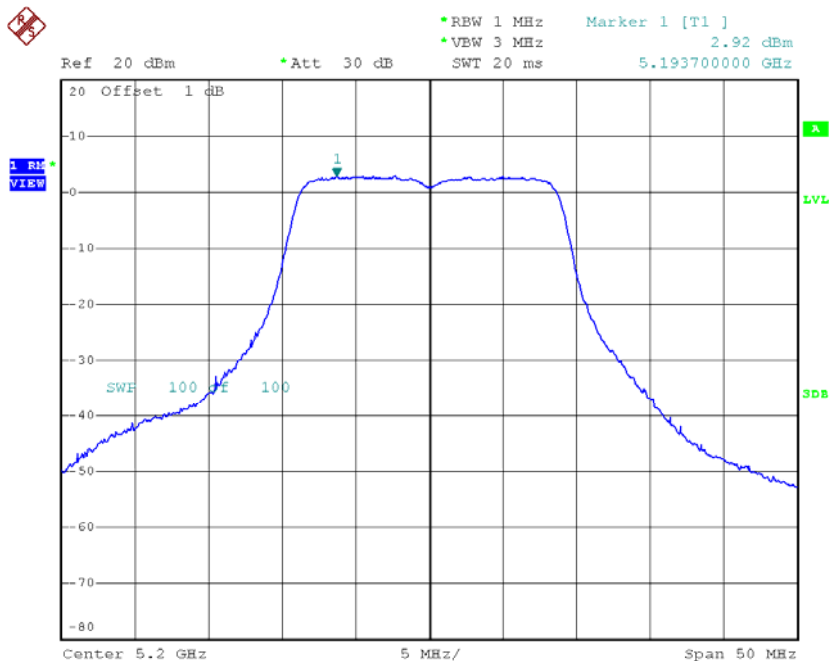
Test Mode: UNII-1/TX N20 Mode_CH36/CH40/CH48

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	2.93	0.07	3.00	15.00
CH40	5200	2.92	0.07	2.99	15.00
CH48	5240	2.83	0.07	2.90	15.00



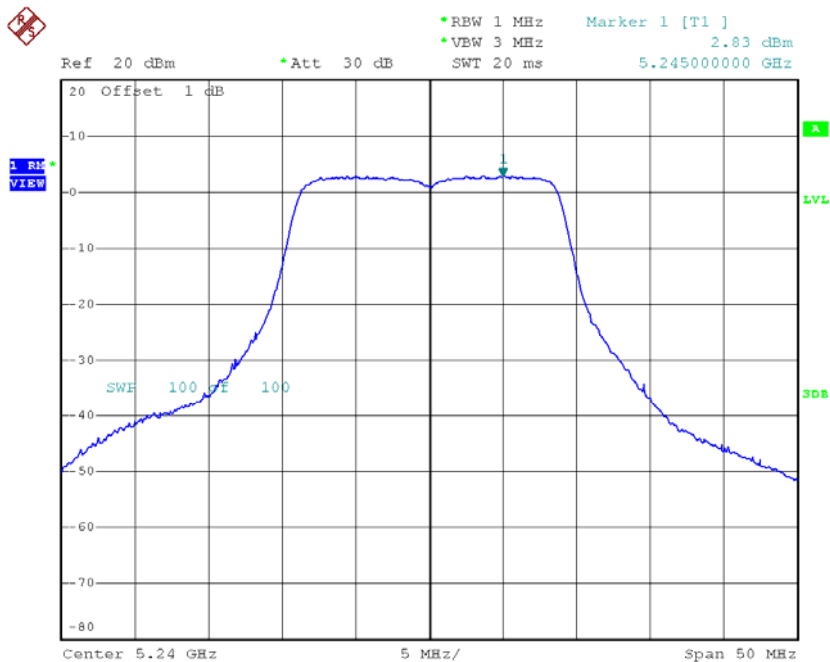
Date: 3.APR.2015 15:47:54

CH40



Date: 3.APR.2015 15:49:05

CH48

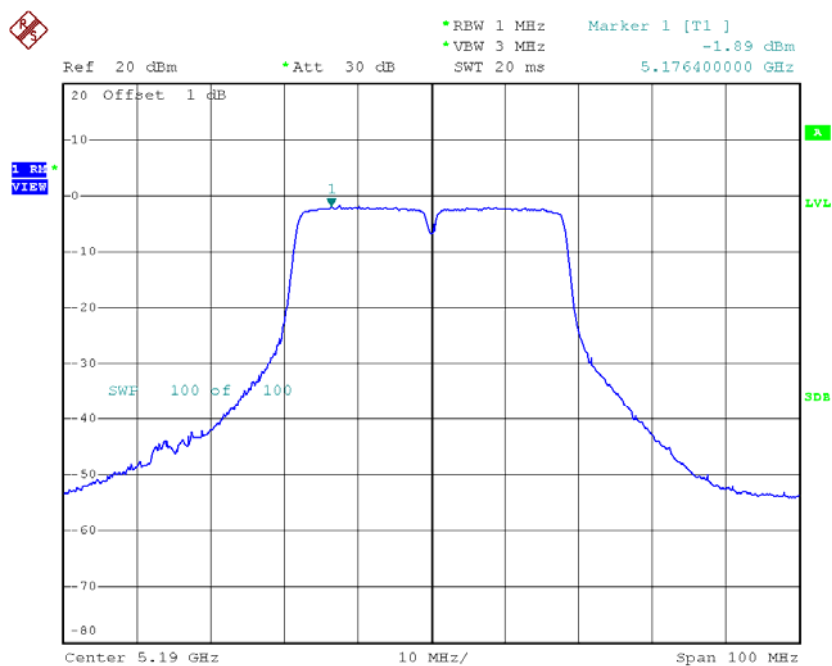


Date: 3.APR.2015 15:49:49

Test Mode: UNII-1/TX N40 Mode_CH38/CH46

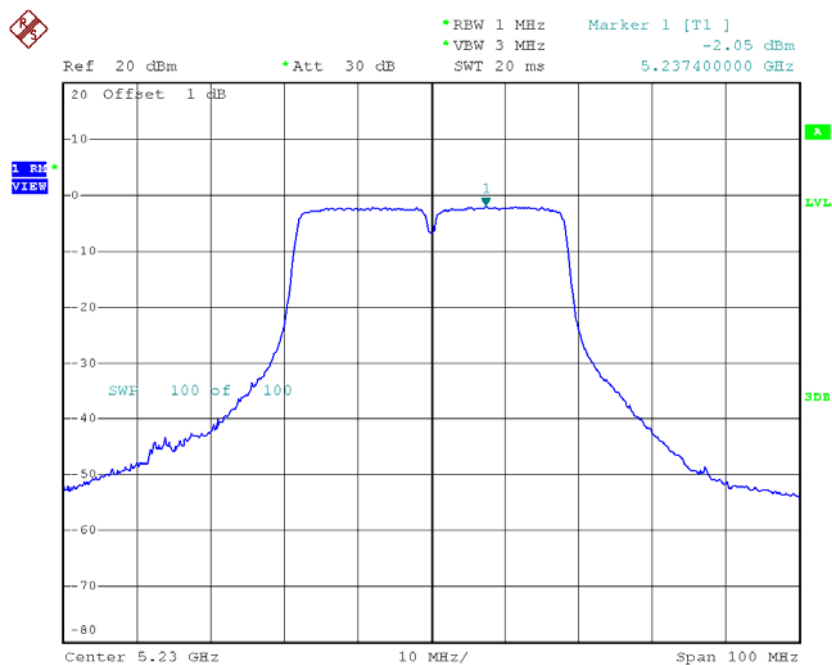
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH38	5190	-1.89	0.32	-1.57	15.00
CH46	5230	-2.05	0.32	-1.73	15.00

CH38



Date: 3.APR.2015 16:06:19

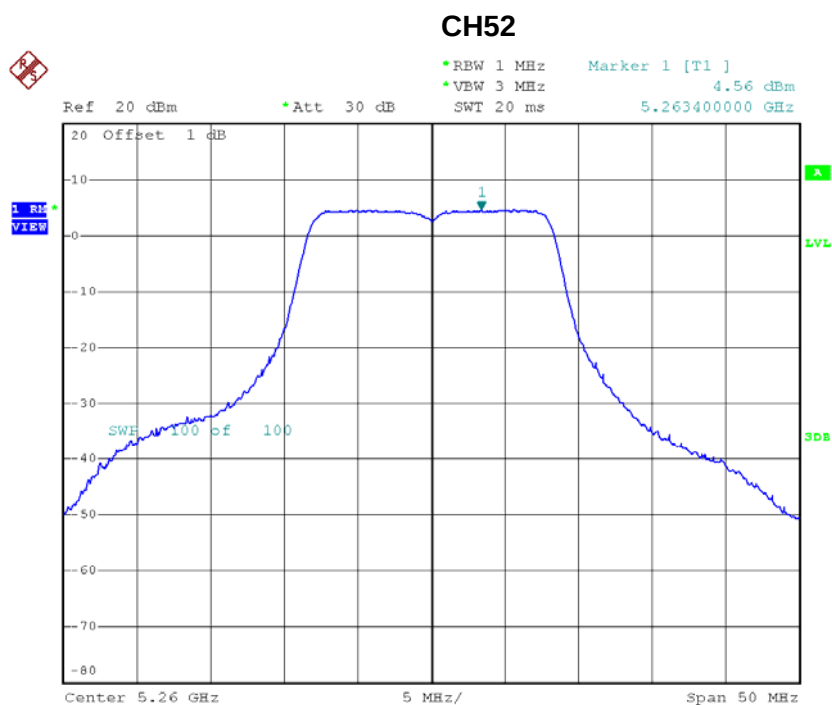
CH46



Date: 3.APR.2015 16:07:23

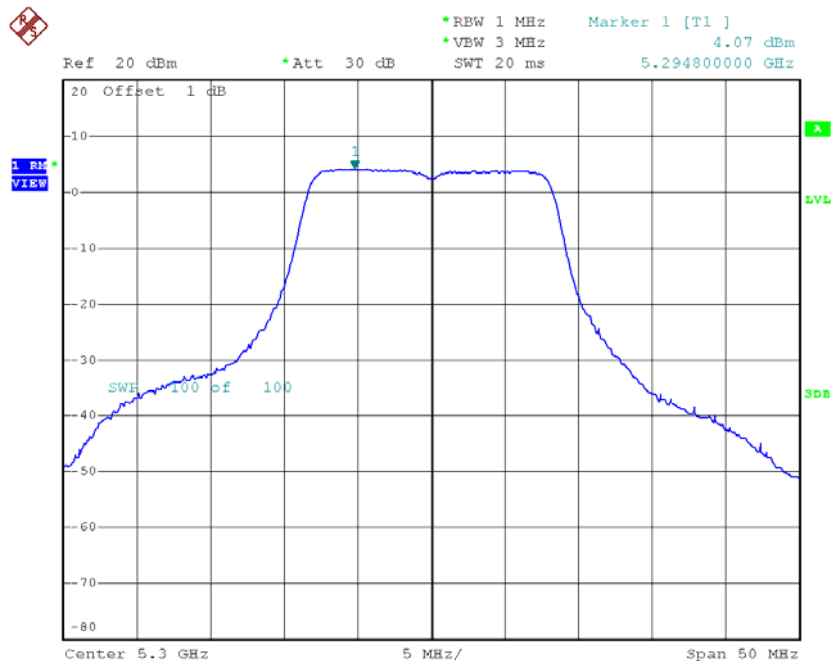
Test Mode: UNII-2A/ TX A Mode_CH52/CH60/CH64

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH52	5260	4.56	0.09	4.65	9.00
CH60	5300	4.07	0.09	4.16	9.00
CH64	5320	3.67	0.09	3.76	9.00



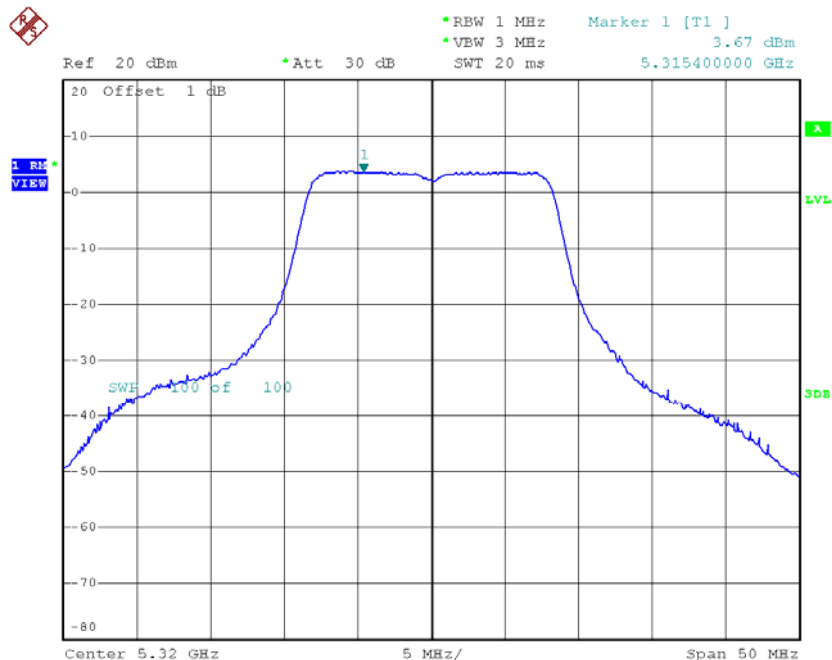
Date: 3.APR.2015 15:37:35

CH60



Date: 3.APR.2015 15:39:12

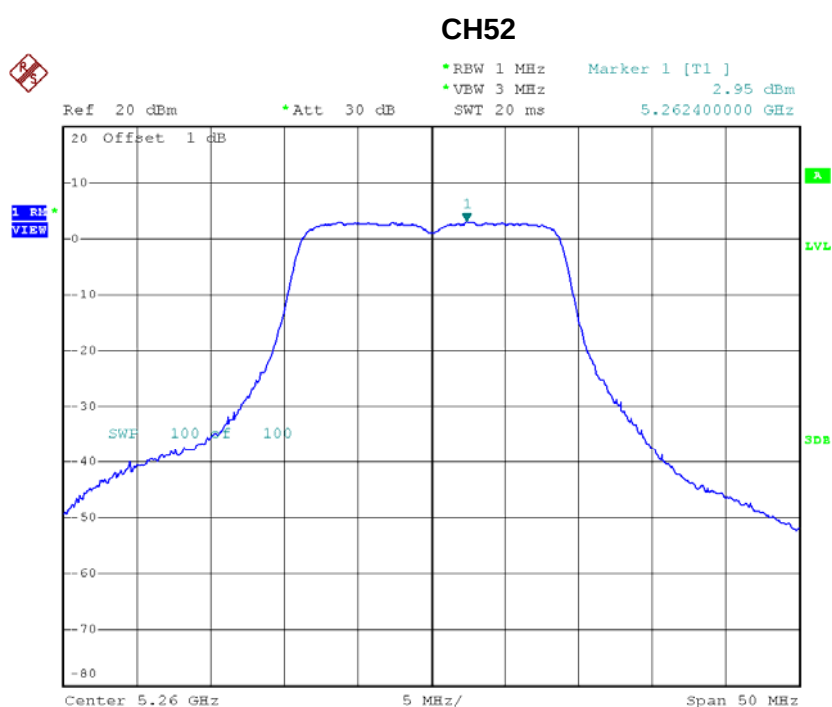
CH64



Date: 3.APR.2015 15:40:05

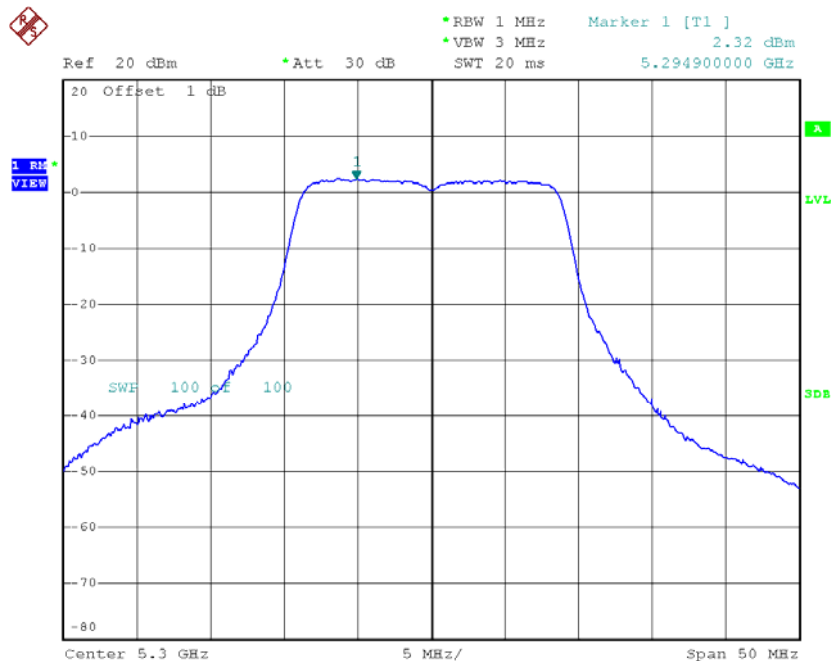
Test Mode: UNII-2A/TX N20 Mode_CH52/CH60/CH64

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH52	5260	2.95	0.07	3.02	9.00
CH60	5300	2.32	0.07	2.39	9.00
CH64	5320	2.14	0.07	2.21	9.00



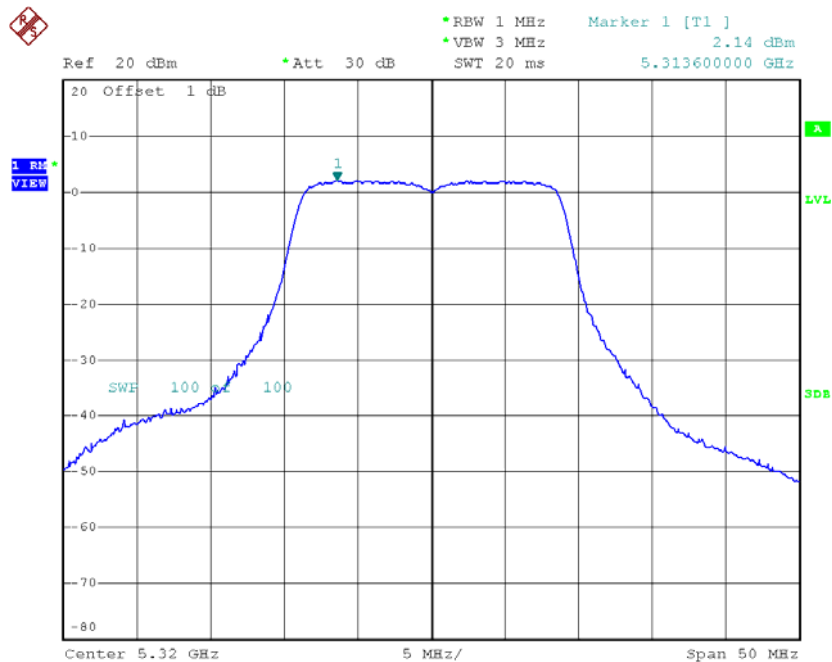
Date: 3.APR.2015 15:50:43

CH60



Date: 3.APR.2015 15:51:37

CH64

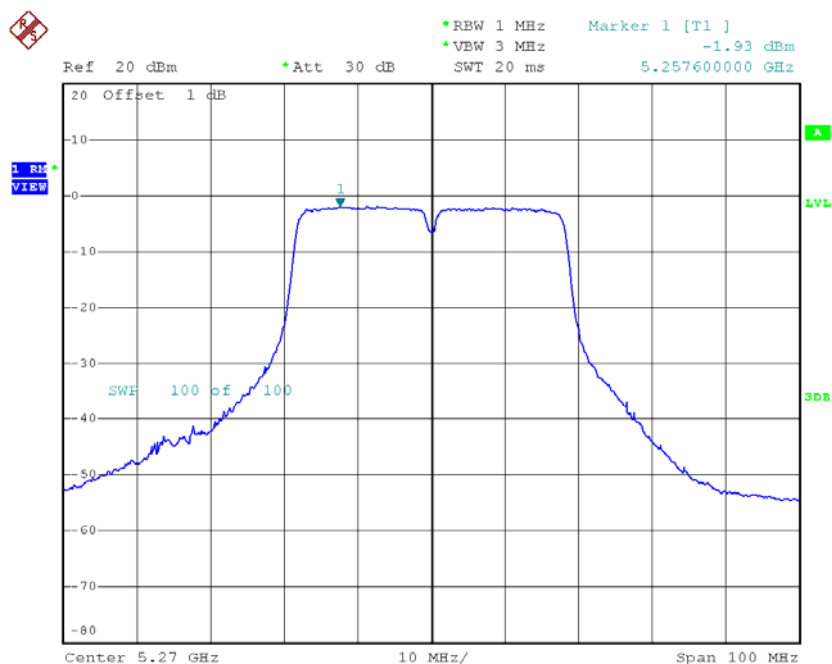


Date: 3.APR.2015 15:52:09

Test Mode: UNII-2A/TX N40 Mode_CH54/CH62

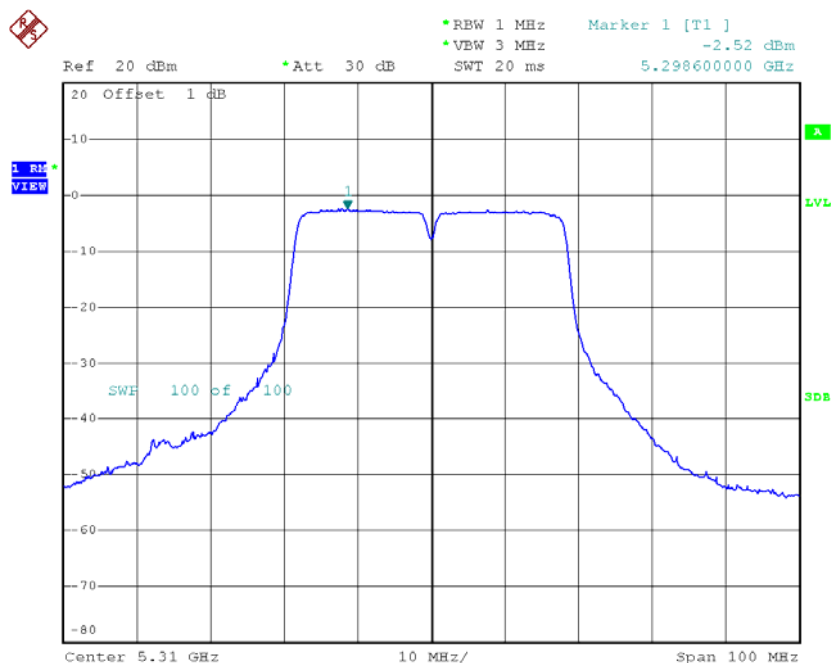
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH54	5270	-1.93	0.32	-1.61	9.00
CH62	5310	-2.52	0.32	-2.20	9.00

CH54



Date: 3.APR.2015 16:08:03

CH62

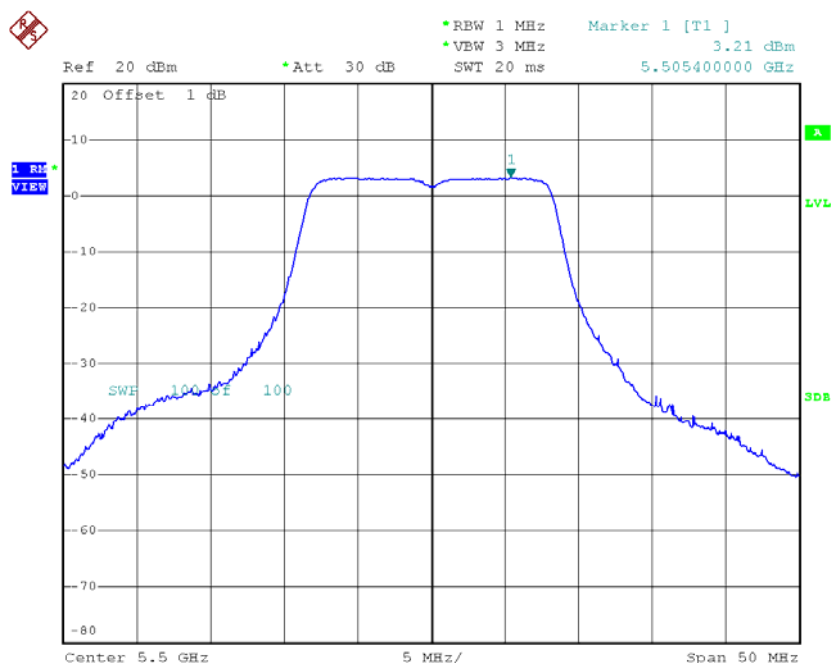


Date: 3.APR.2015 16:09:08

Test Mode: UNII-2C/ TX A Mode_CH100/CH116/CH140

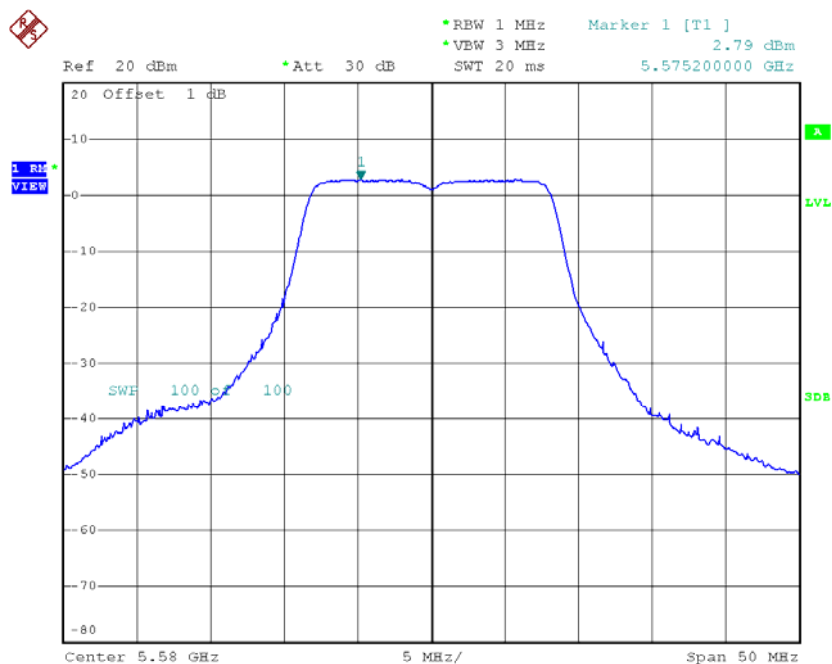
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH100	5500	3.21	0.09	3.30	9.00
CH116	5580	2.79	0.09	2.88	9.00
CH140	5700	3.12	0.09	3.21	9.00

CH100



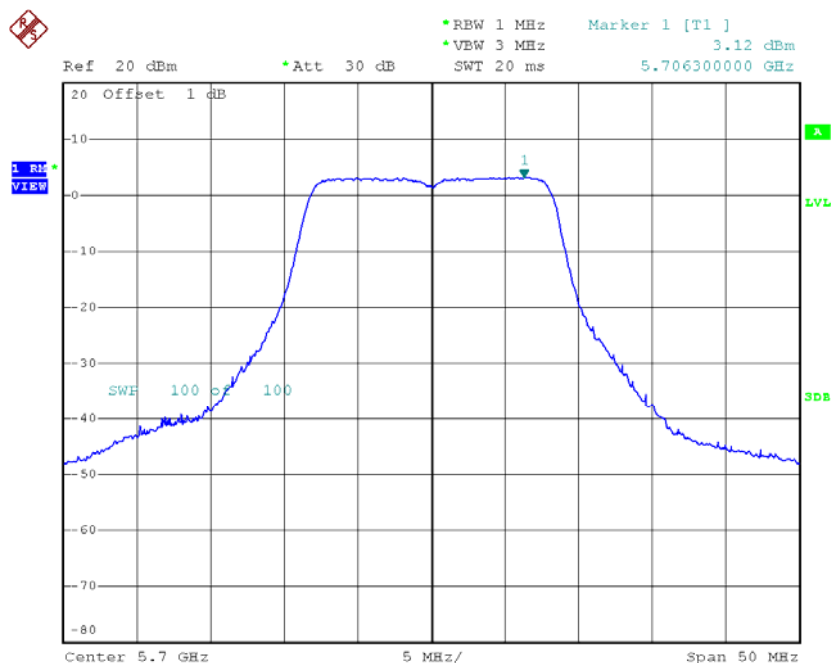
Date: 3.APR.2015 15:41:02

CH116



Date: 3.APR.2015 15:42:25

CH140

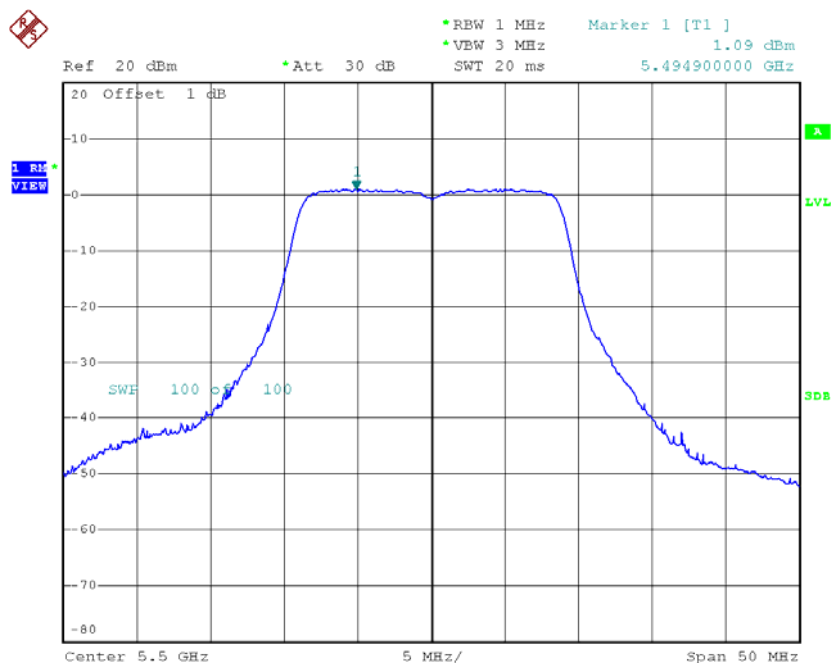


Date: 3.APR.2015 15:43:17

Test Mode: UNII-2C/TX N20 Mode_CH100/CH116/CH140

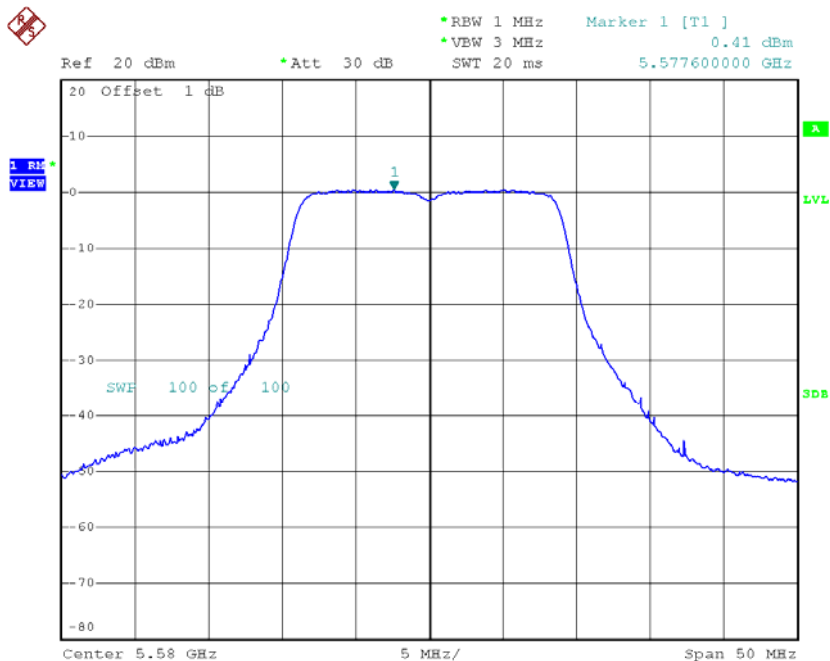
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH100	5500	1.09	0.07	1.16	9.00
CH116	5580	0.41	0.07	0.48	9.00
CH140	5700	0.92	0.07	0.99	9.00

CH100



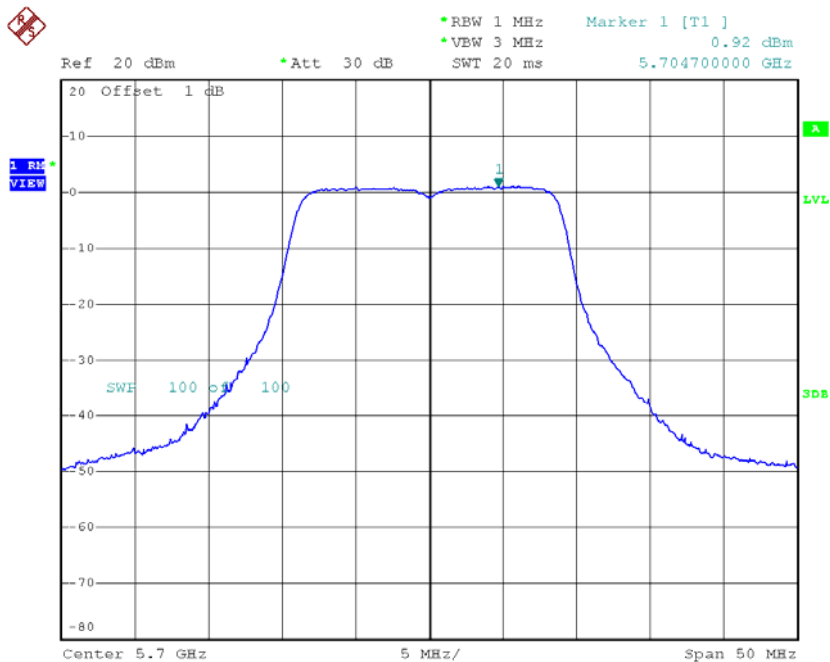
Date: 3.APR.2015 15:52:46

CH116



Date: 3.APR.2015 15:53:46

CH140

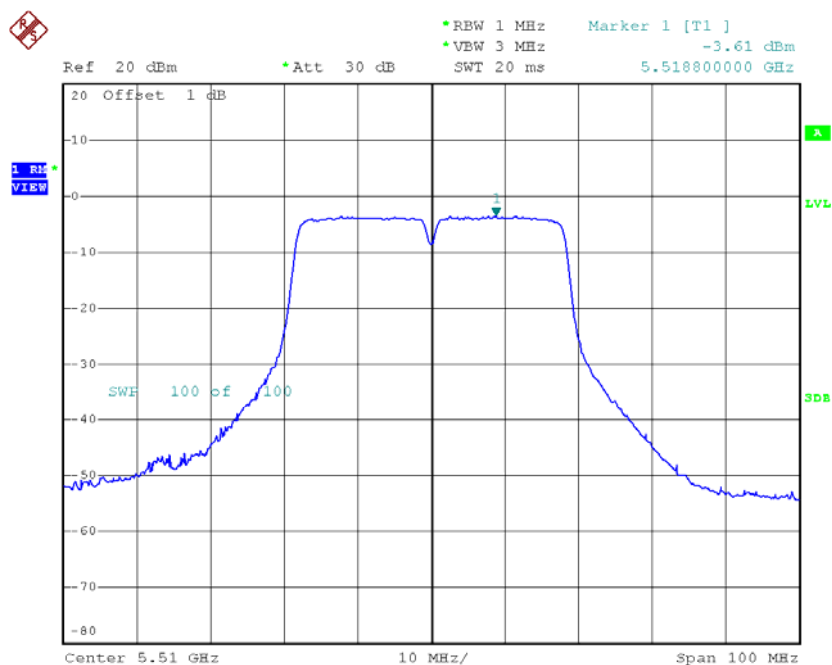


Date: 3.APR.2015 15:54:18

Test Mode: UNII-2C/TX N40 Mode_CH102/CH110/CH134

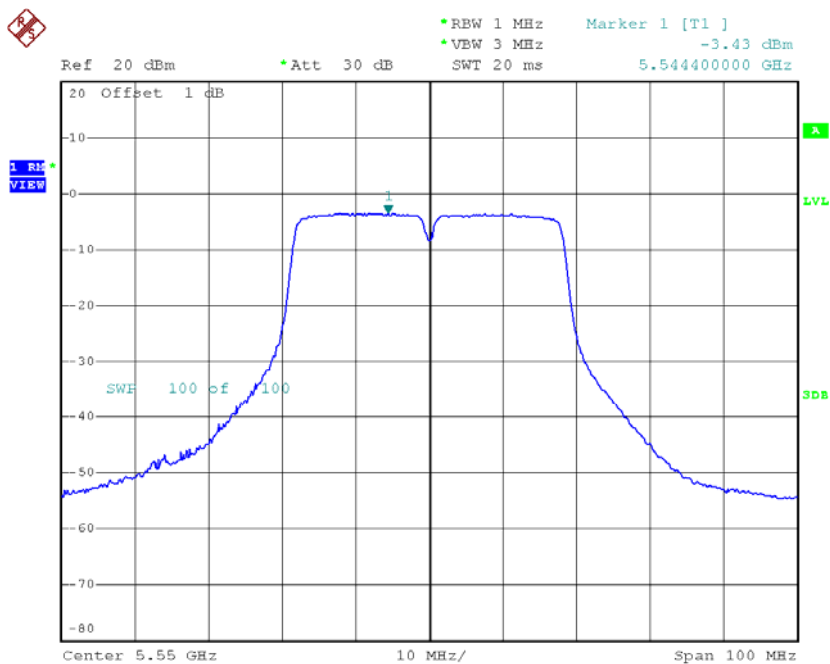
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH102	5510	-3.61	0.32	-3.29	9.00
CH110	5550	-3.43	0.32	-3.11	9.00
CH134	5670	-3.93	0.32	-3.61	9.00

CH102



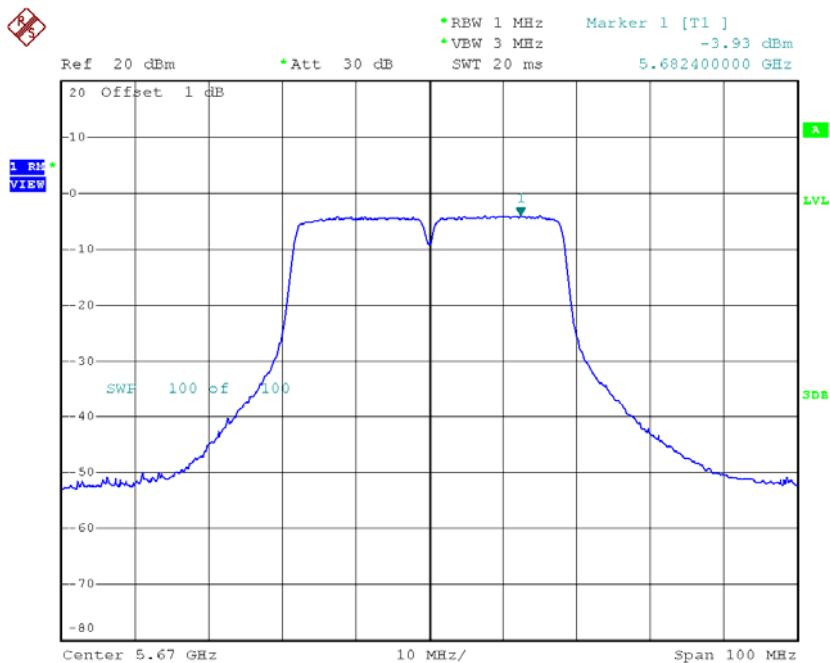
Date: 3.APR.2015 16:09:47

CH110



Date: 3.APR.2015 16:24:11

CH134

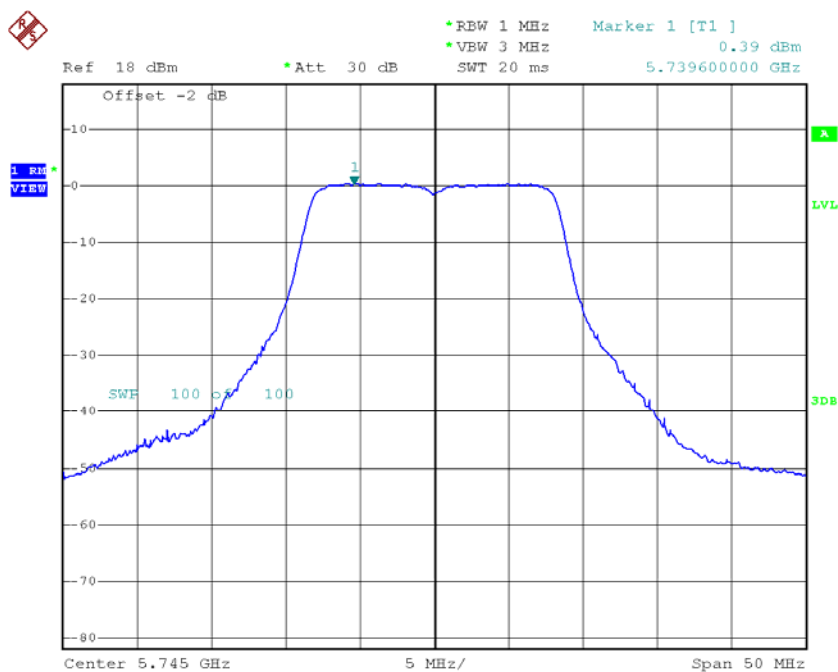


Date: 3.APR.2015 16:24:52

Test Mode: UNII-3/TX A Mode_CH149/CH157/CH165

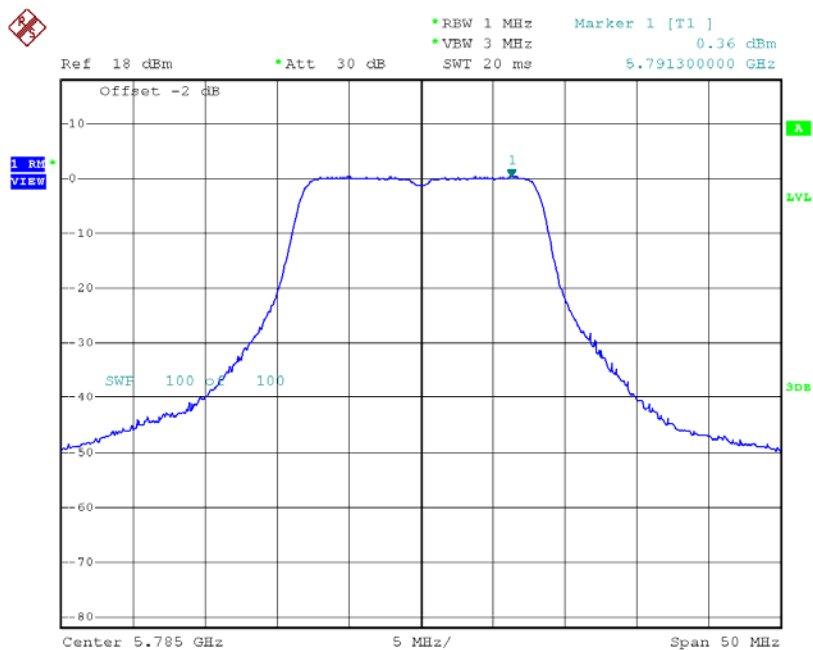
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH149	5745	0.39	0.09	0.48	28.00
CH157	5785	0.36	0.09	0.45	28.00
CH165	5825	0.46	0.09	0.55	28.00

TX CH149



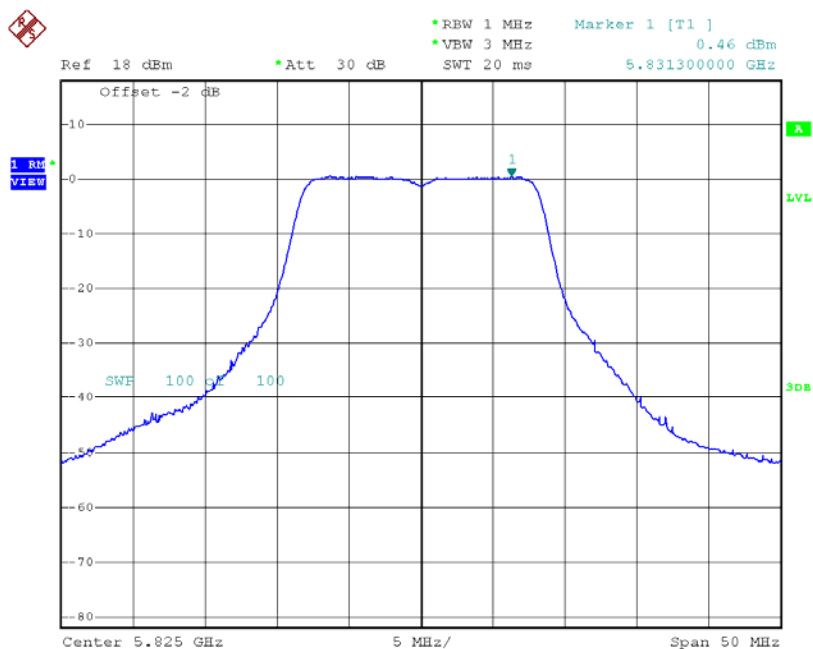
Date: 3.APR.2015 15:44:16

TX CH157



Date: 3.APR.2015 15:45:36

TX CH165

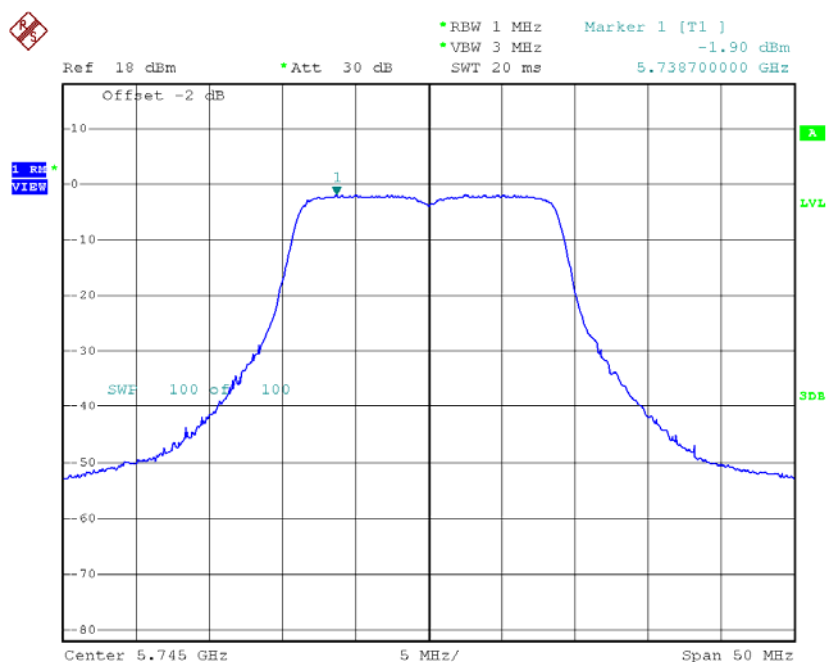


Date: 3.APR.2015 15:46:39

Test Mode: UNII-3/ TX N20 Mode_CH149/CH157/CH165

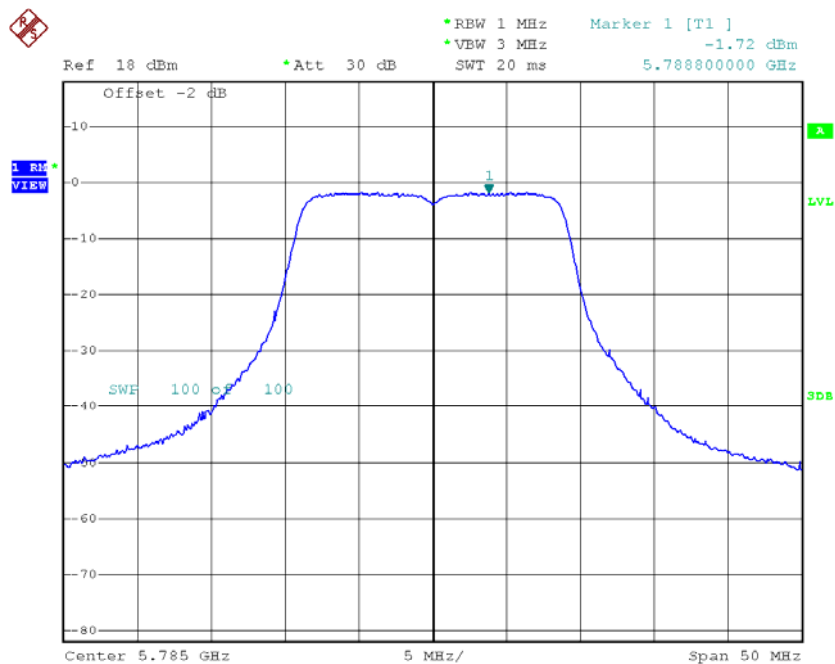
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH149	5745	-1.90	0.07	-1.83	28.00
CH157	5785	-1.72	0.07	-1.65	28.00
CH165	5825	-1.53	0.07	-1.46	28.00

TX CH149



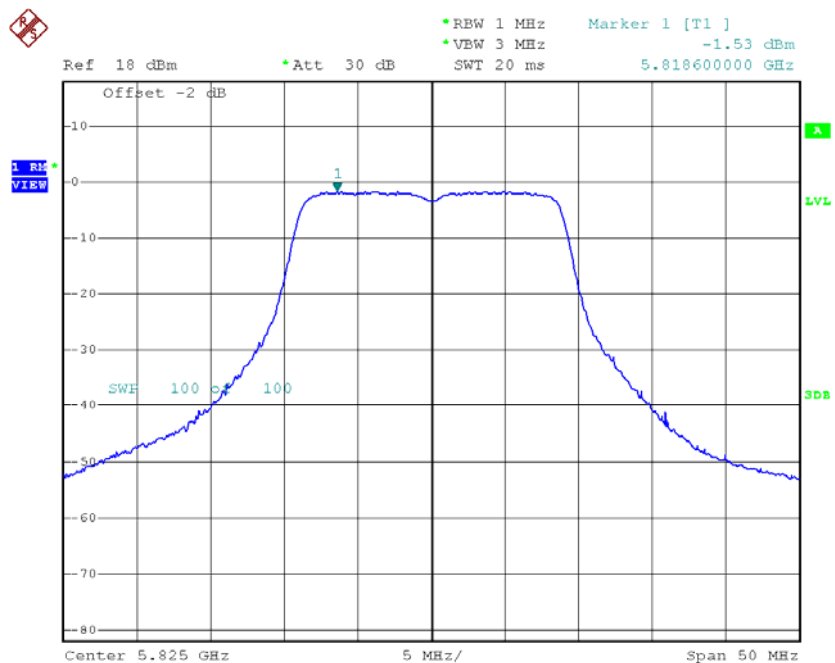
Date: 3.APR.2015 15:54:57

TX CH157



Date: 3.APR.2015 15:55:57

TX CH165

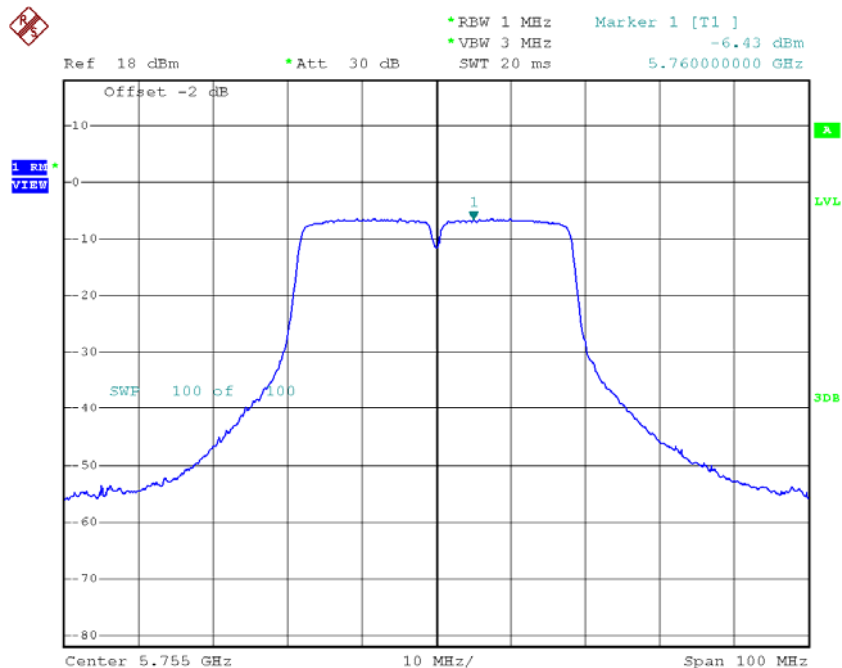


Date: 3.APR.2015 15:56:30

Test Mode: UNII-3/ TX N40 Mode_CH151/CH159

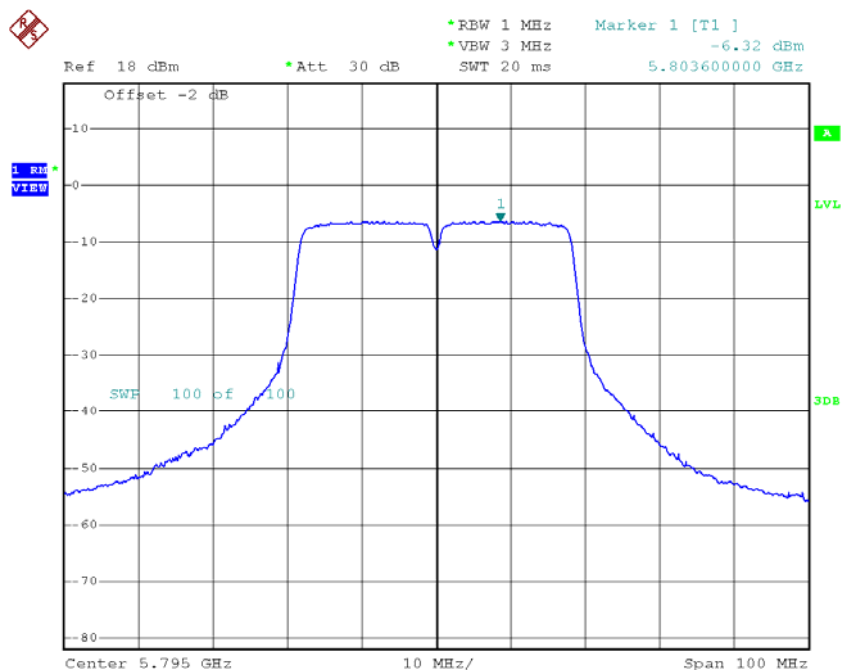
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH151	5755	-6.43	0.32	-6.11	28.00
CH159	5795	-6.32	0.32	-6.00	28.00

TX CH151



Date: 3.APR.2015 16:25:34

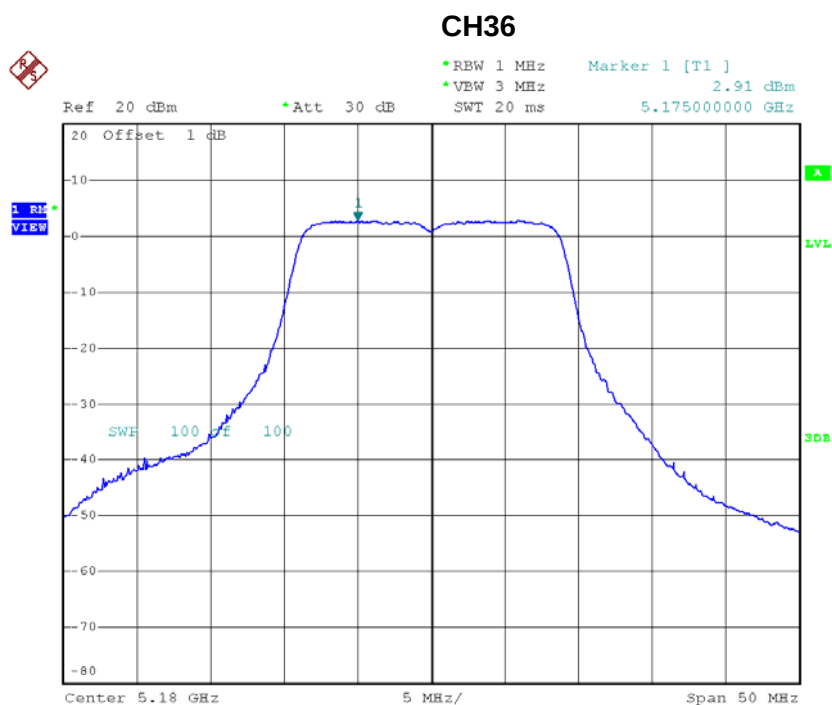
TX CH159



Date: 3.APR.2015 16:26:25

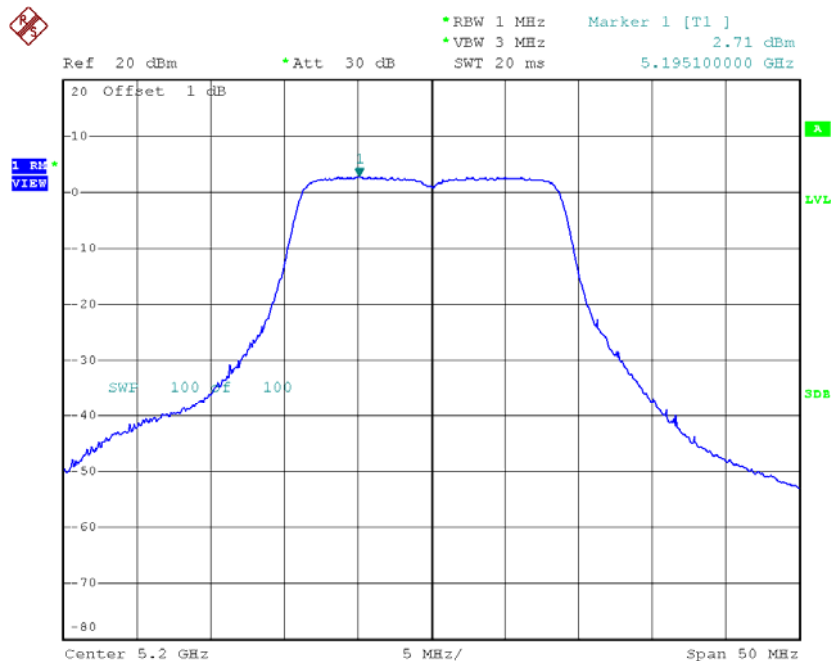
Test Mode: UNII-1/TX AC20 Mode_CH36/CH40/CH48

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	2.91	0.07	2.98	15.00
CH40	5200	2.71	0.07	2.78	15.00
CH48	5240	2.90	0.07	2.97	15.00



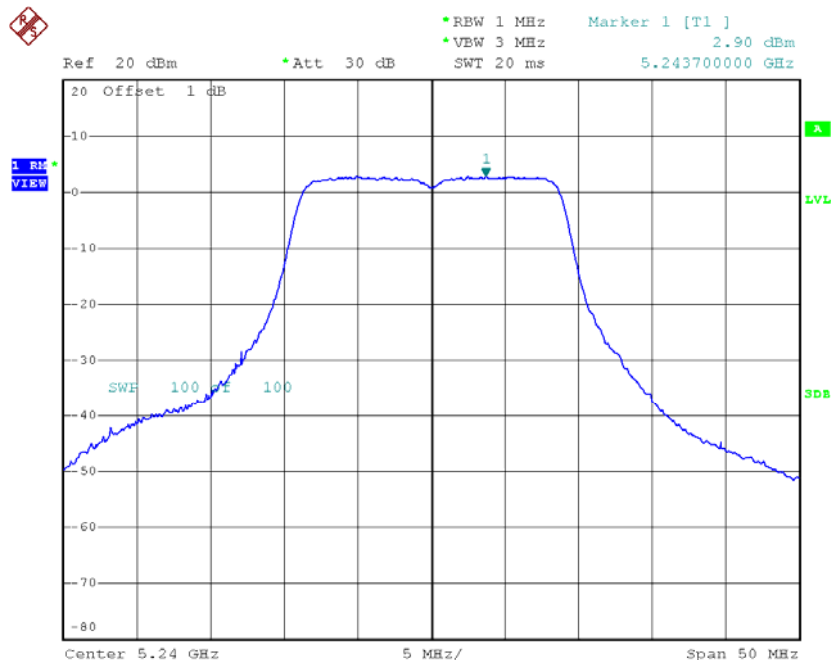
Date: 3.APR.2015 15:57:29

CH40



Date: 3.APR.2015 15:58:34

CH48

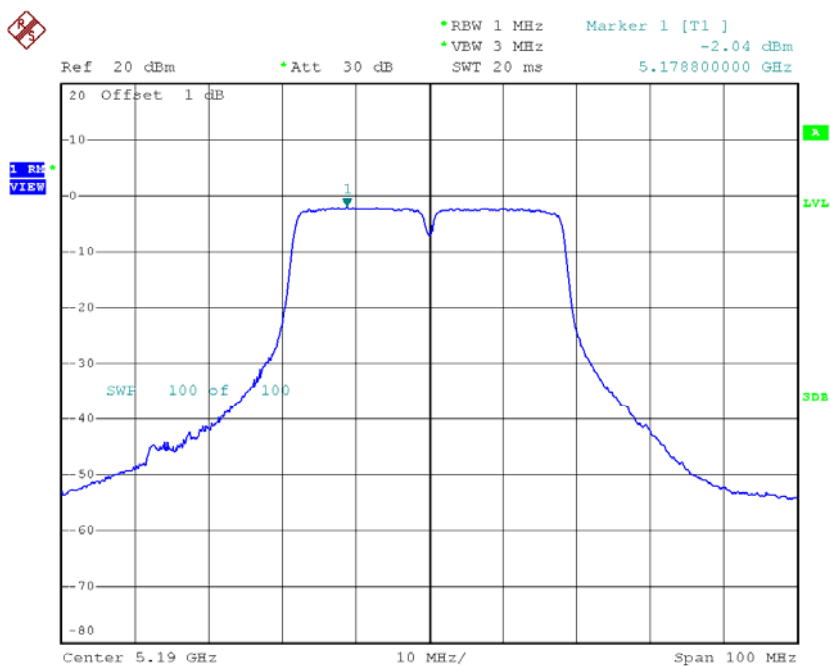


Date: 3.APR.2015 15:59:08

Test Mode: UNII-1/TX AC40 Mode_CH38/CH46

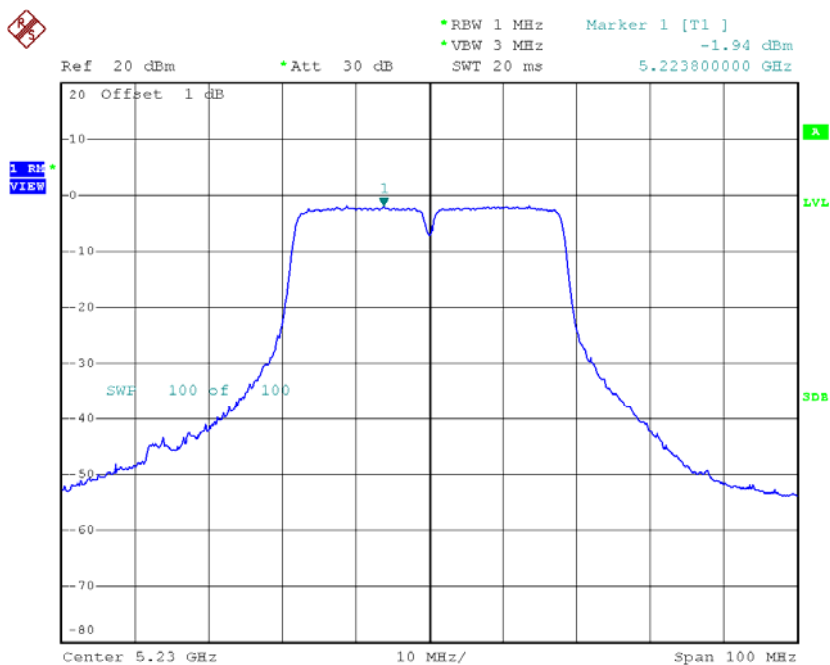
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH38	5190	-2.04	0.26	-1.78	15.00
CH46	5230	-1.94	0.26	-1.68	15.00

CH38



Date: 3.APR.2015 16:27:45

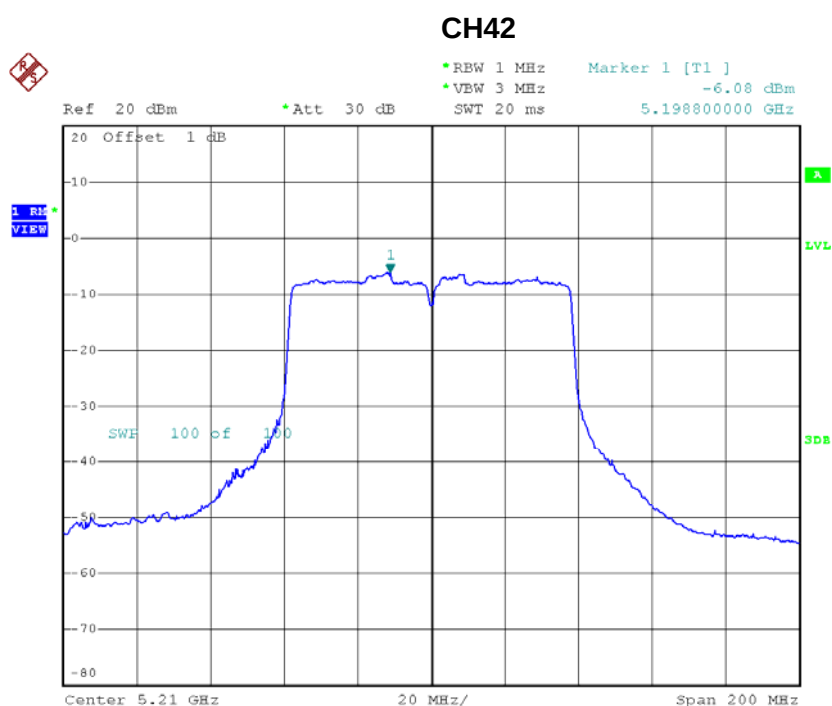
CH46



Date: 3.APR.2015 16:28:39

Test Mode: UNII-1/TX AC80 Mode_CH42

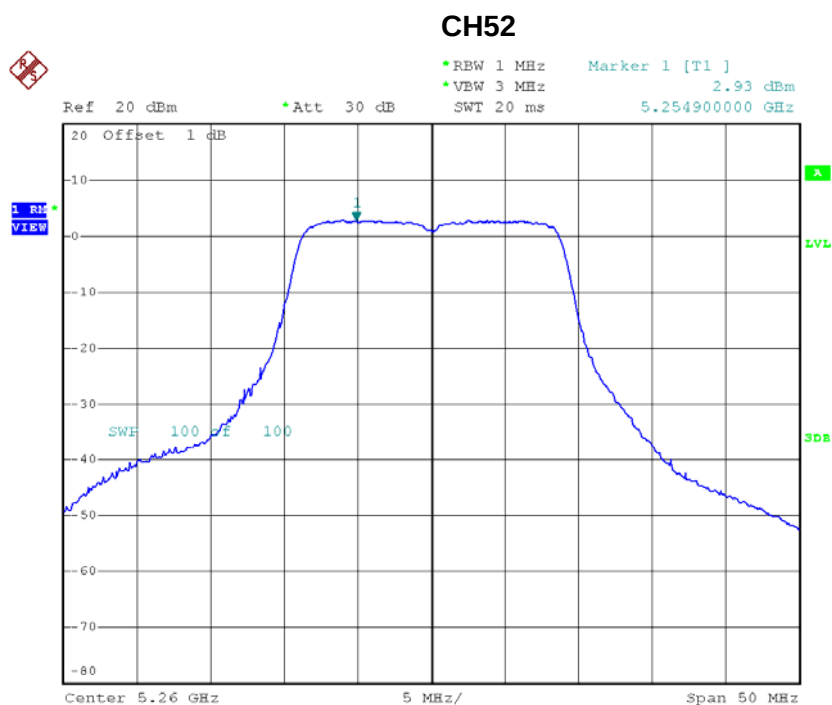
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH42	5210	-6.08	0.61	-5.47	15.00



Date: 3.APR.2015 16:34:57

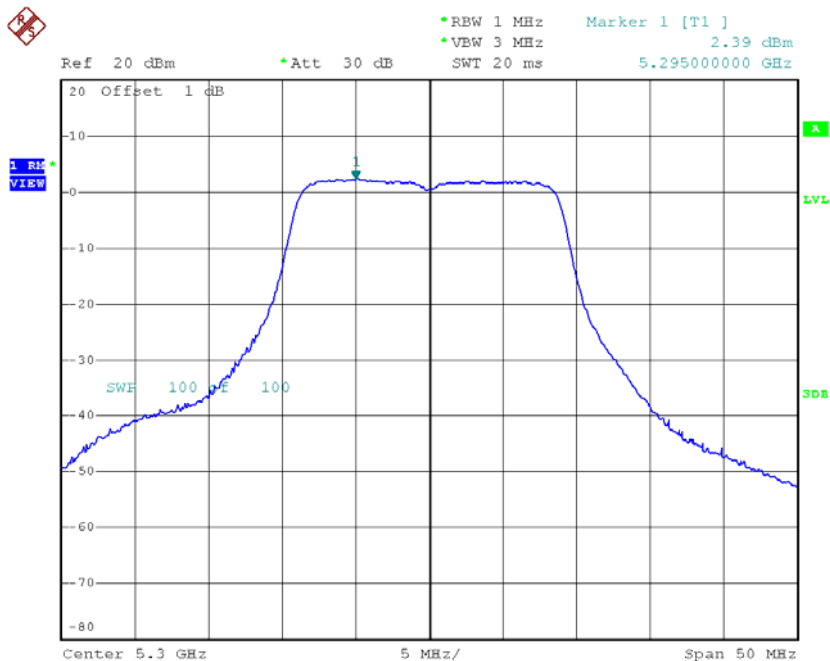
Test Mode: UNII-2A/TX AC20 Mode_CH52/CH60/CH64

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH52	5260	2.93	0.07	3.00	9.00
CH60	5300	2.39	0.07	2.46	9.00
CH64	5320	2.08	0.07	2.15	9.00



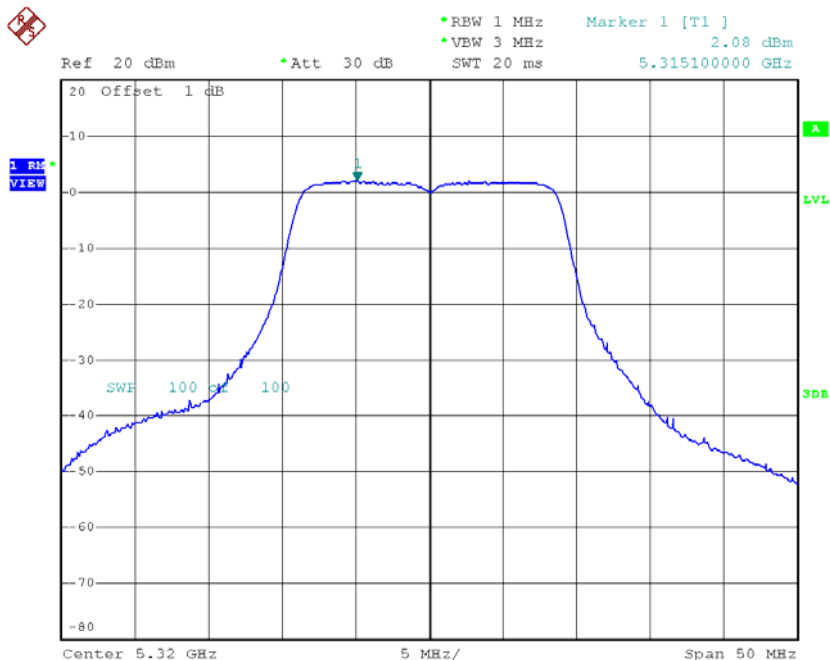
Date: 3.APR.2015 15:59:46

CH60



Date: 3.APR.2015 16:00:40

CH64

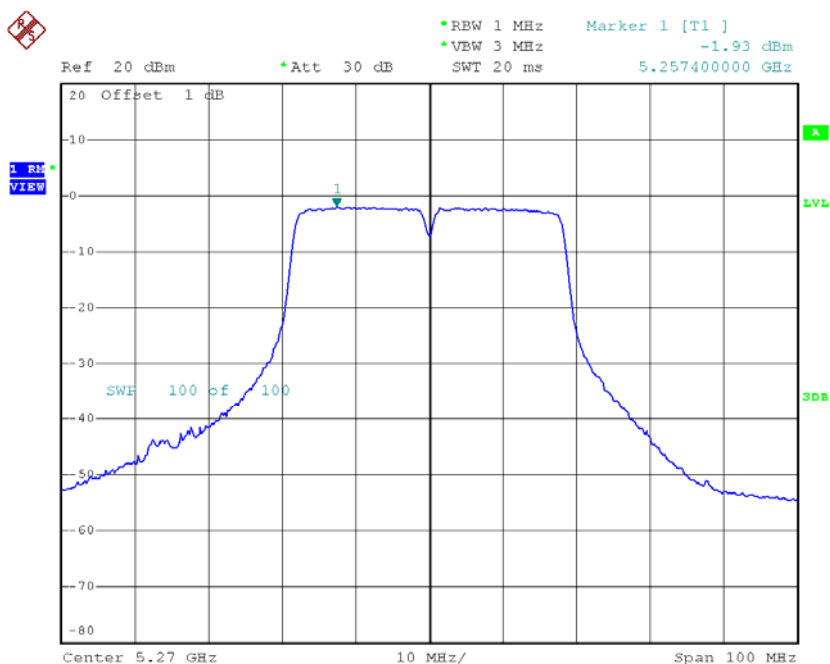


Date: 3.APR.2015 16:01:12

Test Mode: UNII-2A/TX AC40 Mode_CH54/CH62

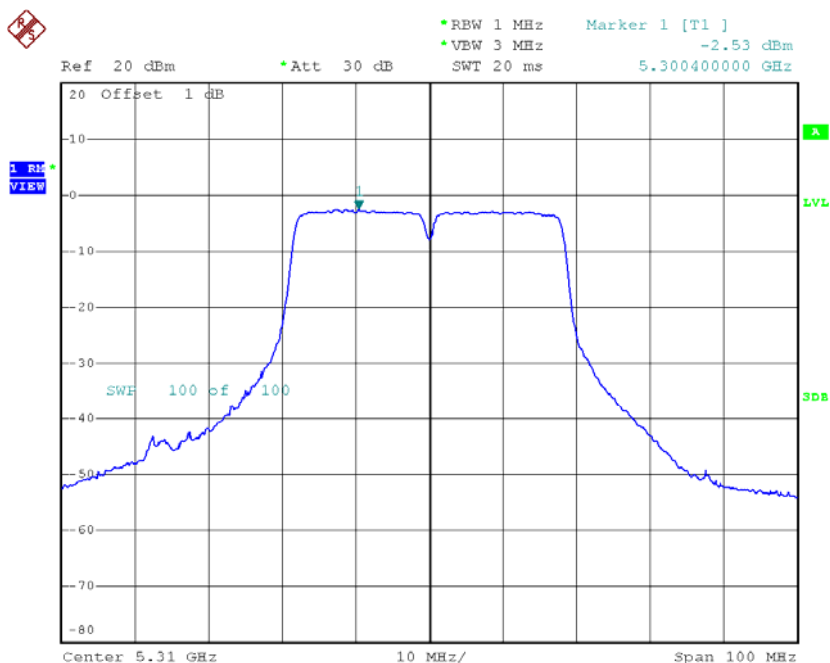
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH54	5270	-1.93	0.26	-1.67	9.00
CH62	5310	-2.53	0.26	-2.27	9.00

CH54



Date: 3.APR.2015 16:29:18

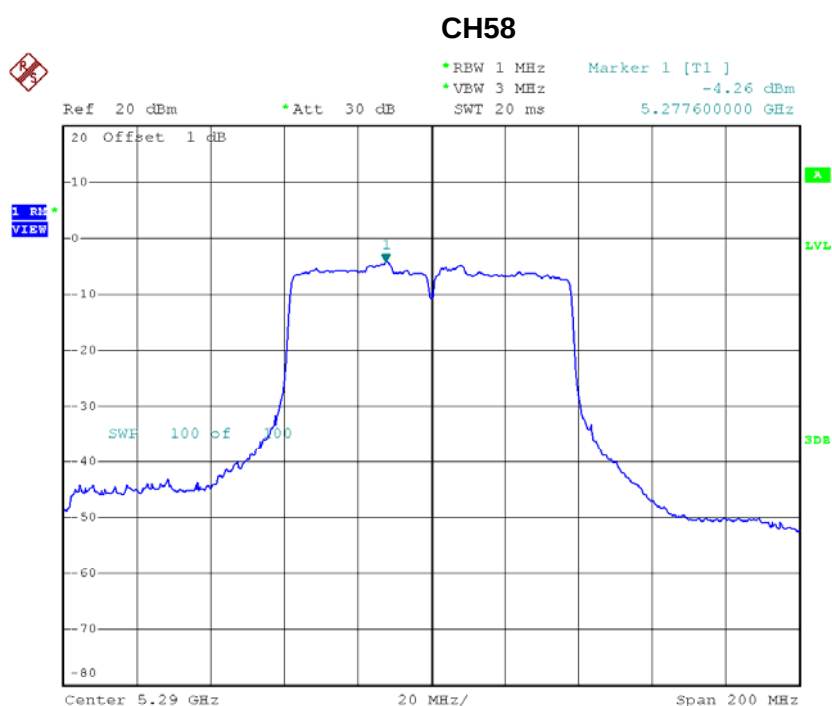
CH62



Date: 3.APR.2015 16:30:11

Test Mode: UNII-2A/TX AC80 Mode_CH58

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH58	5290	-4.26	0.61	-3.65	9.00

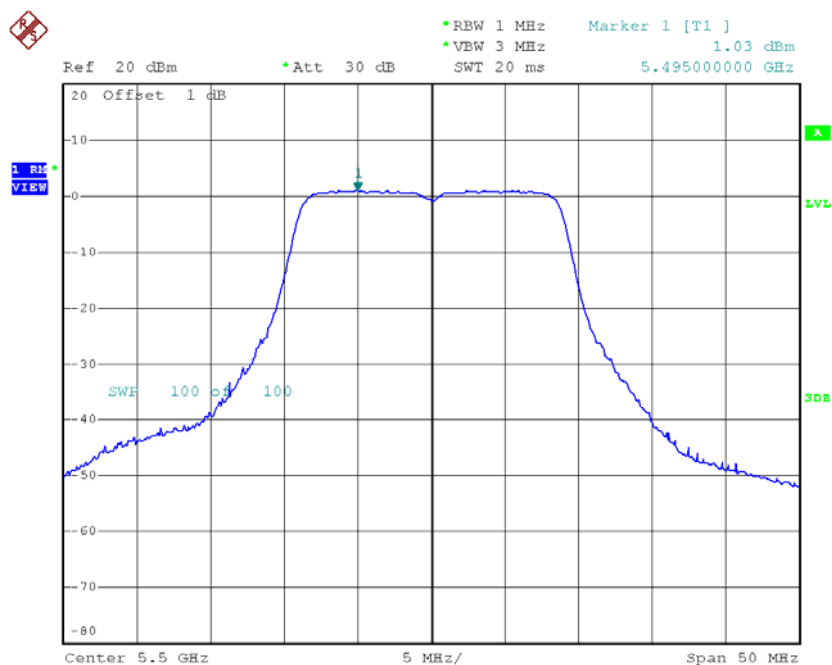


Date: 3.APR.2015 16:36:07

Test Mode: UNII-2C/TX AC20 Mode_CH100/CH116/CH140

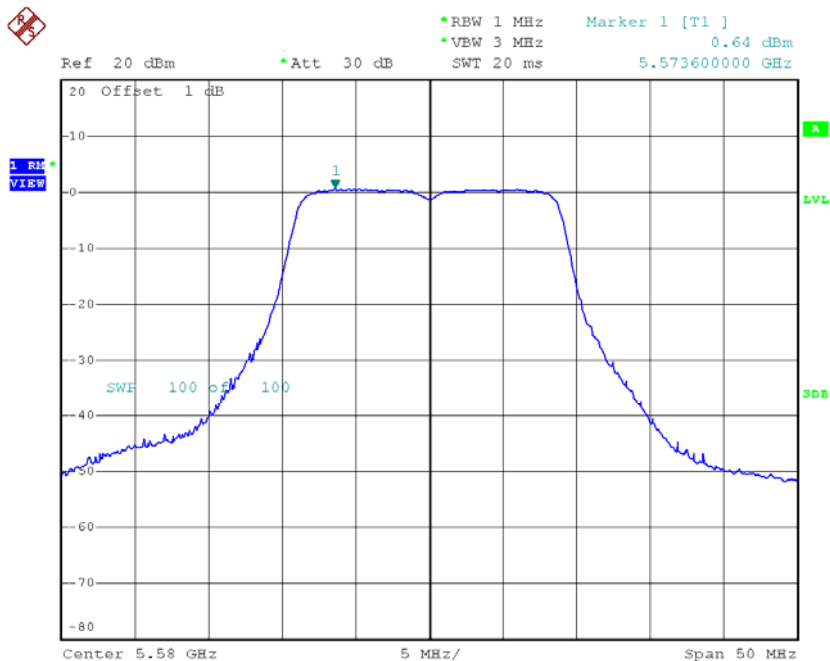
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH100	5500	1.03	0.07	1.10	9.00
CH116	5580	0.64	0.07	0.71	9.00
CH140	5700	1.13	0.07	1.20	9.00

CH100



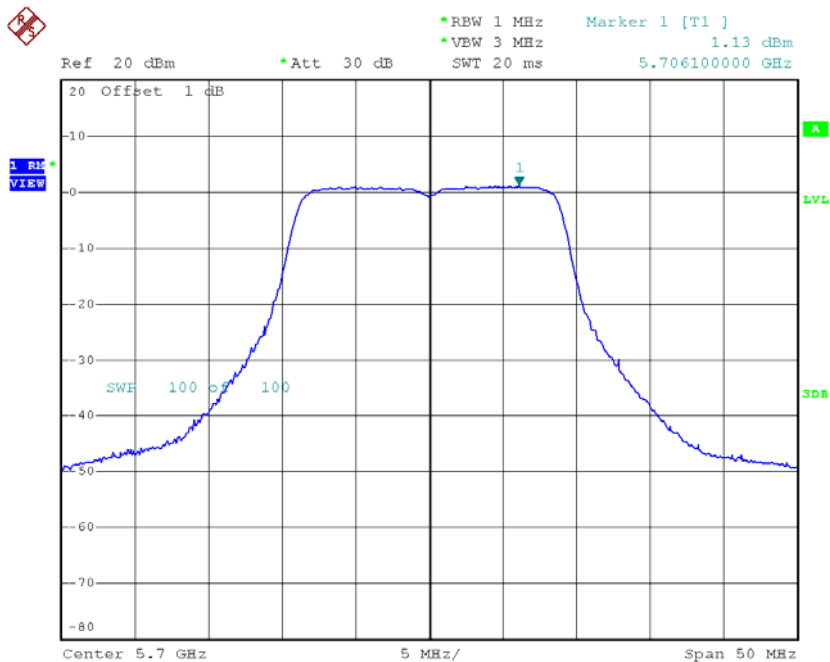
Date: 3.APR.2015 16:01:49

CH116



Date: 3.APR.2015 16:02:46

CH140

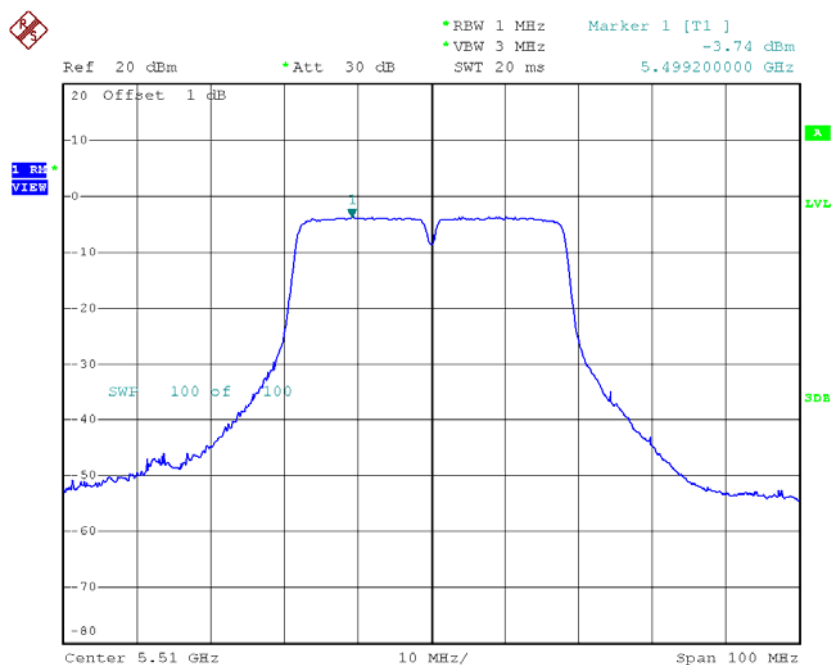


Date: 3.APR.2015 16:03:21

Test Mode: UNII-2C/TX AC40 Mode_CH102/CH110/CH134

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density+Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH102	5510	-3.74	0.26	-3.48	9.00
CH110	5550	-3.35	0.26	-3.09	9.00
CH134	5670	-4.06	0.26	-3.80	9.00

CH102



Date: 3.APR.2015 16:30:49