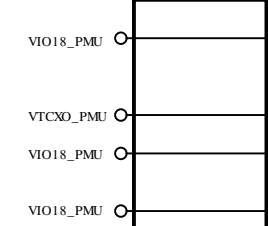
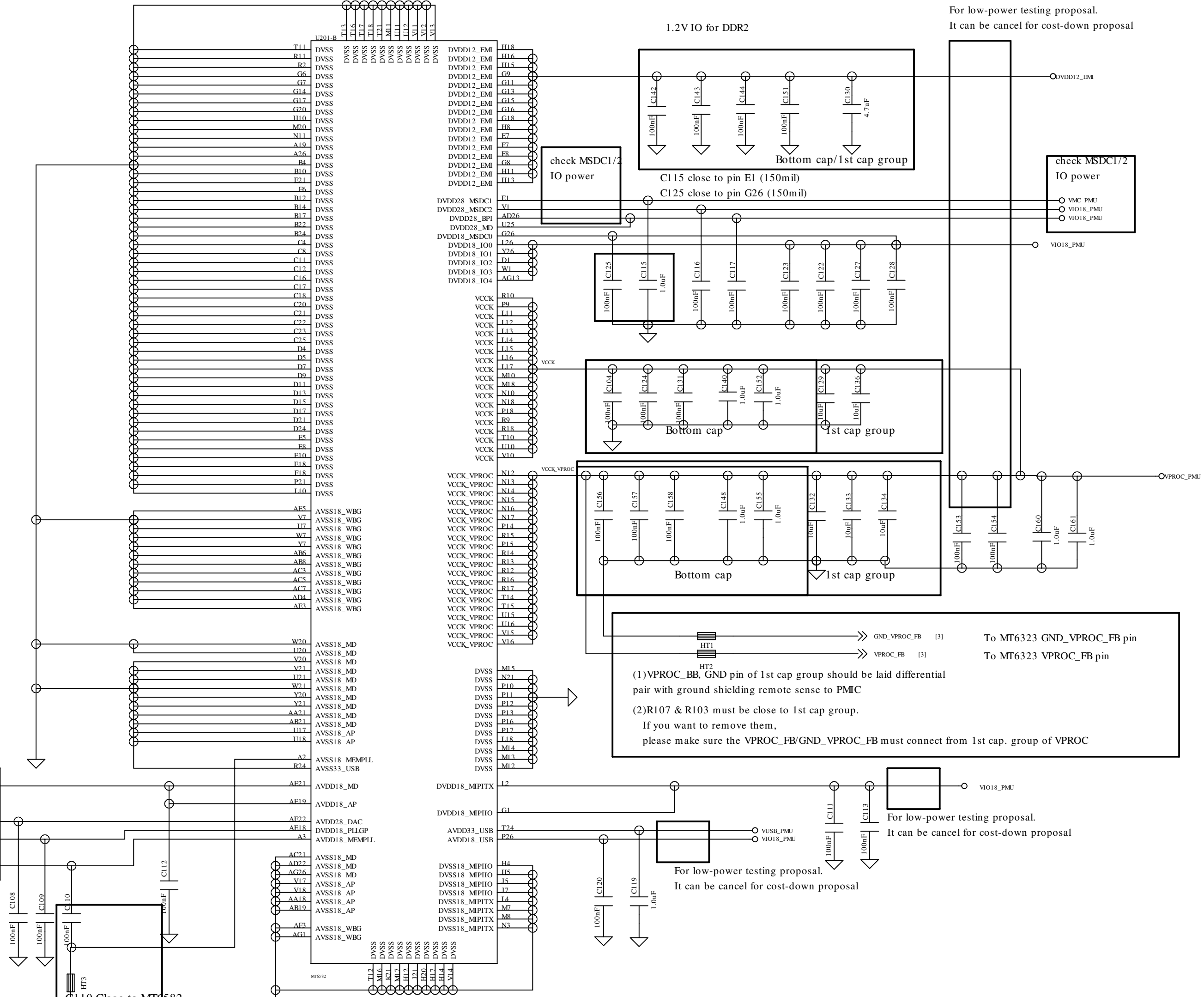


For low-power testing proposal.
It can be cancel for cost-down proposal



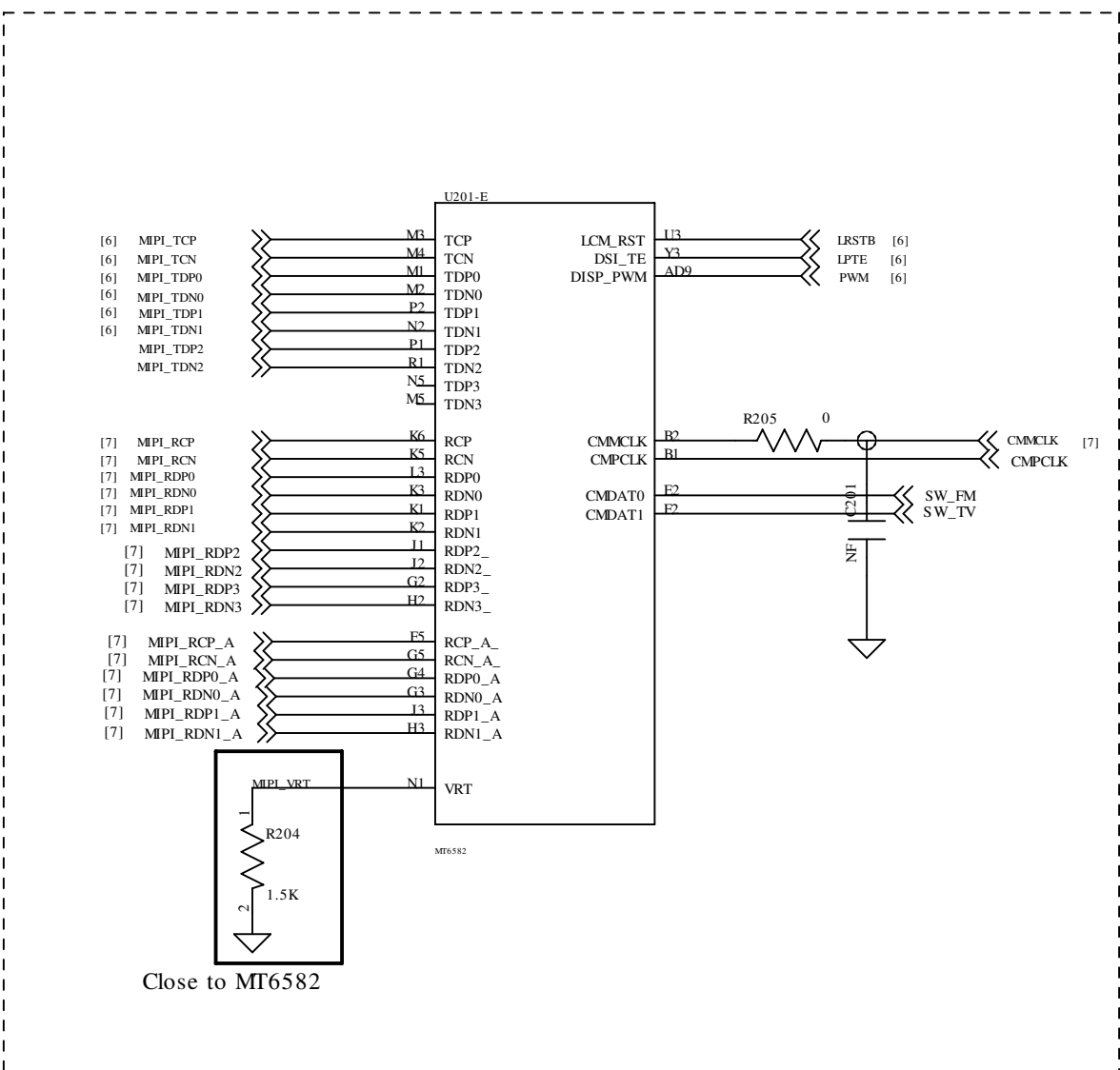
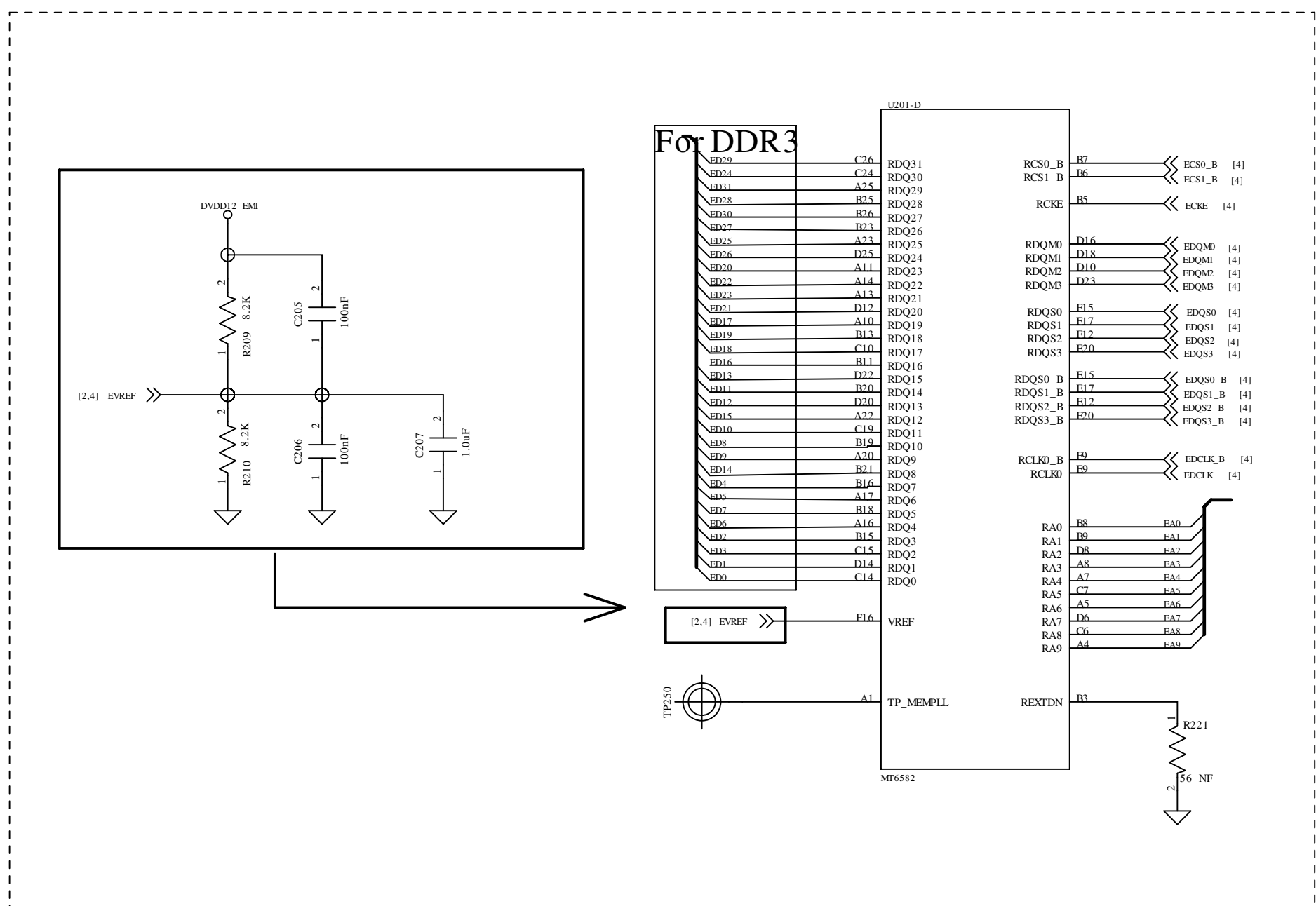
C110 Close to MT6582
A2 should connect to C110.GND pin first
then connect to GND by via

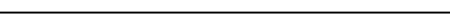


(1)VPROC_BR_GND pin of 1st cap group should be laid differential pair with ground shielding remote sense to PMC
(2)R107 & R103 must be close to 1st cap group.
If you want to remove them,
please make sure the VPROC_FB/GND_VPROC_FB must connect from 1st cap_group of VPROC

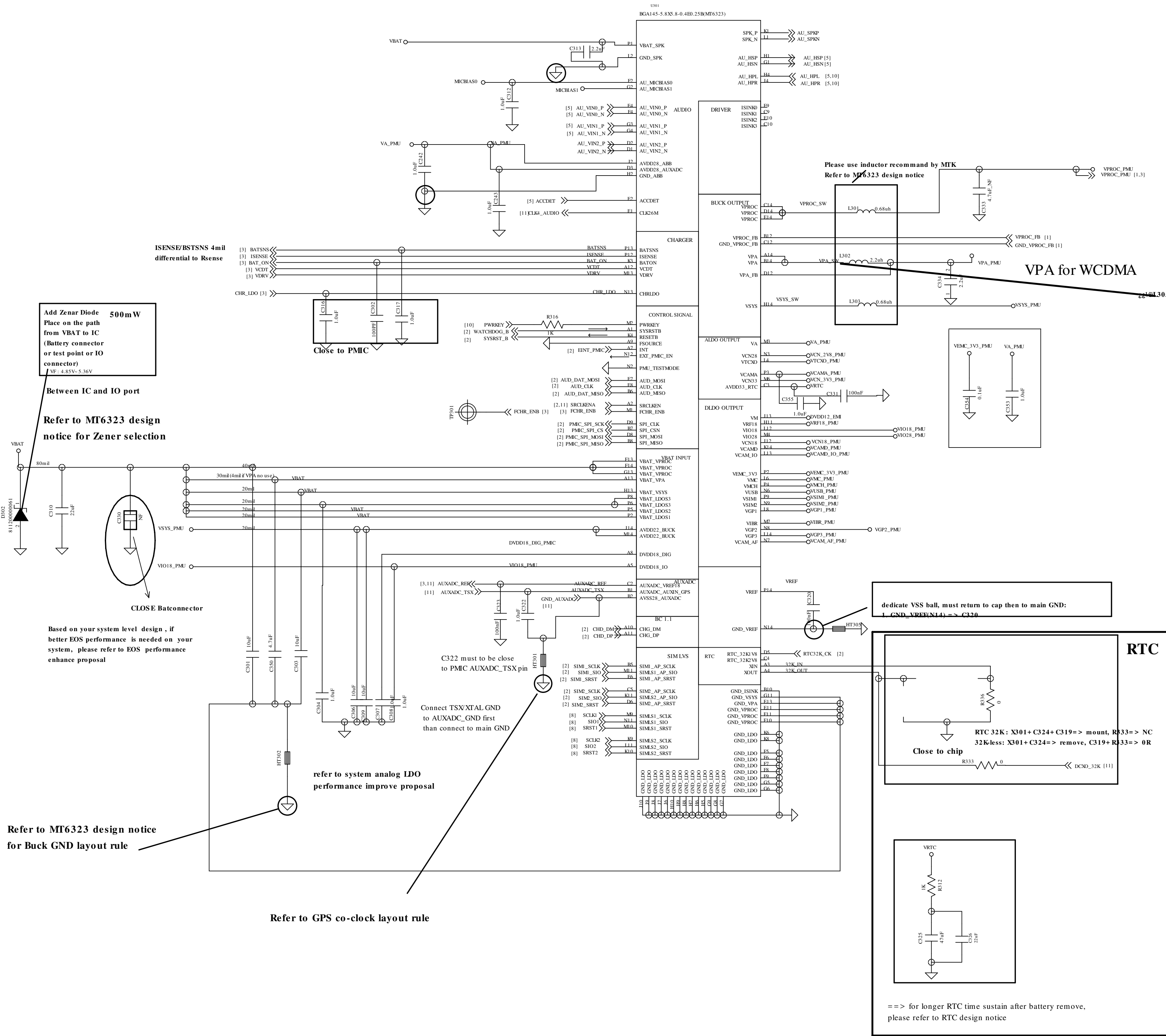
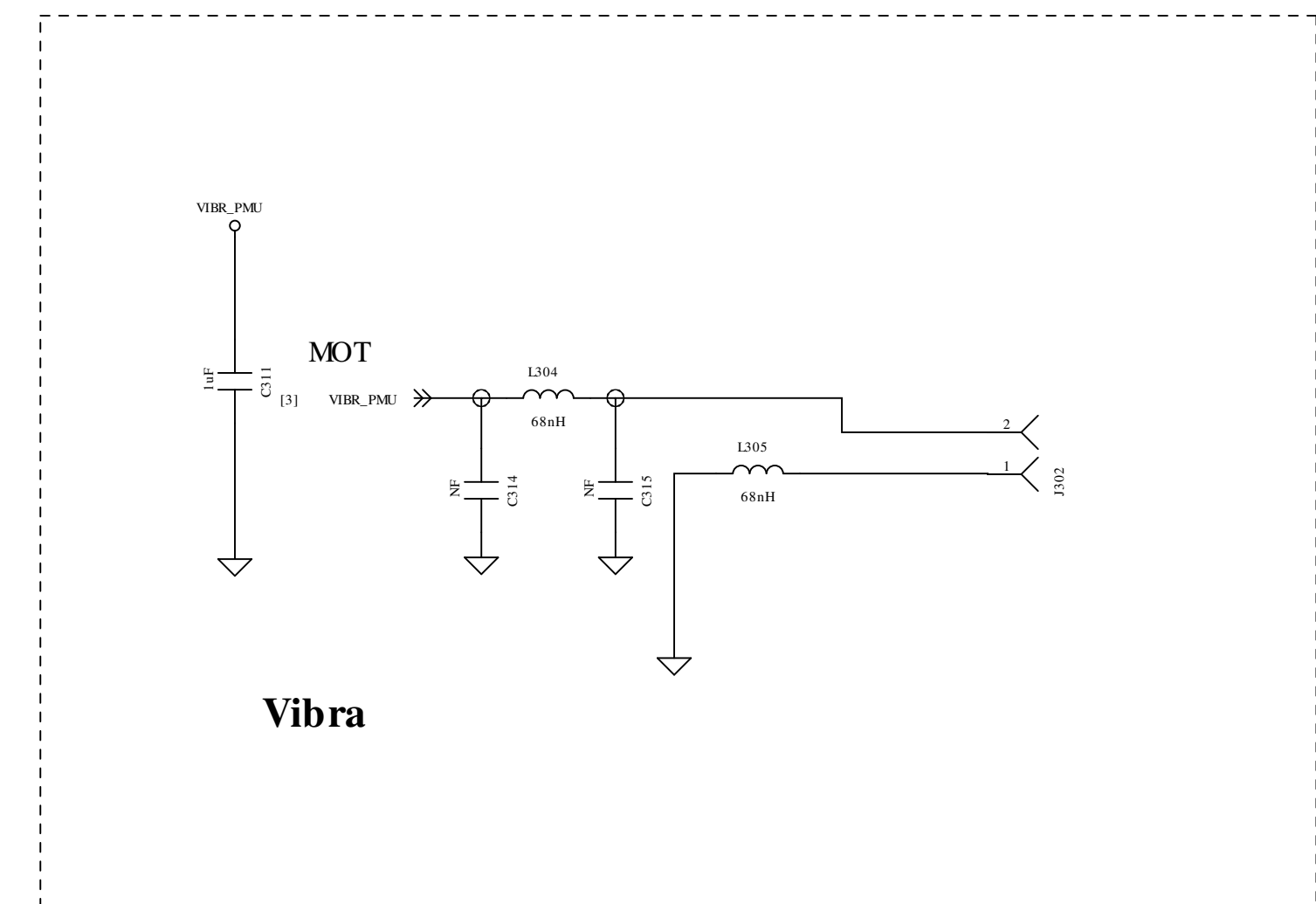
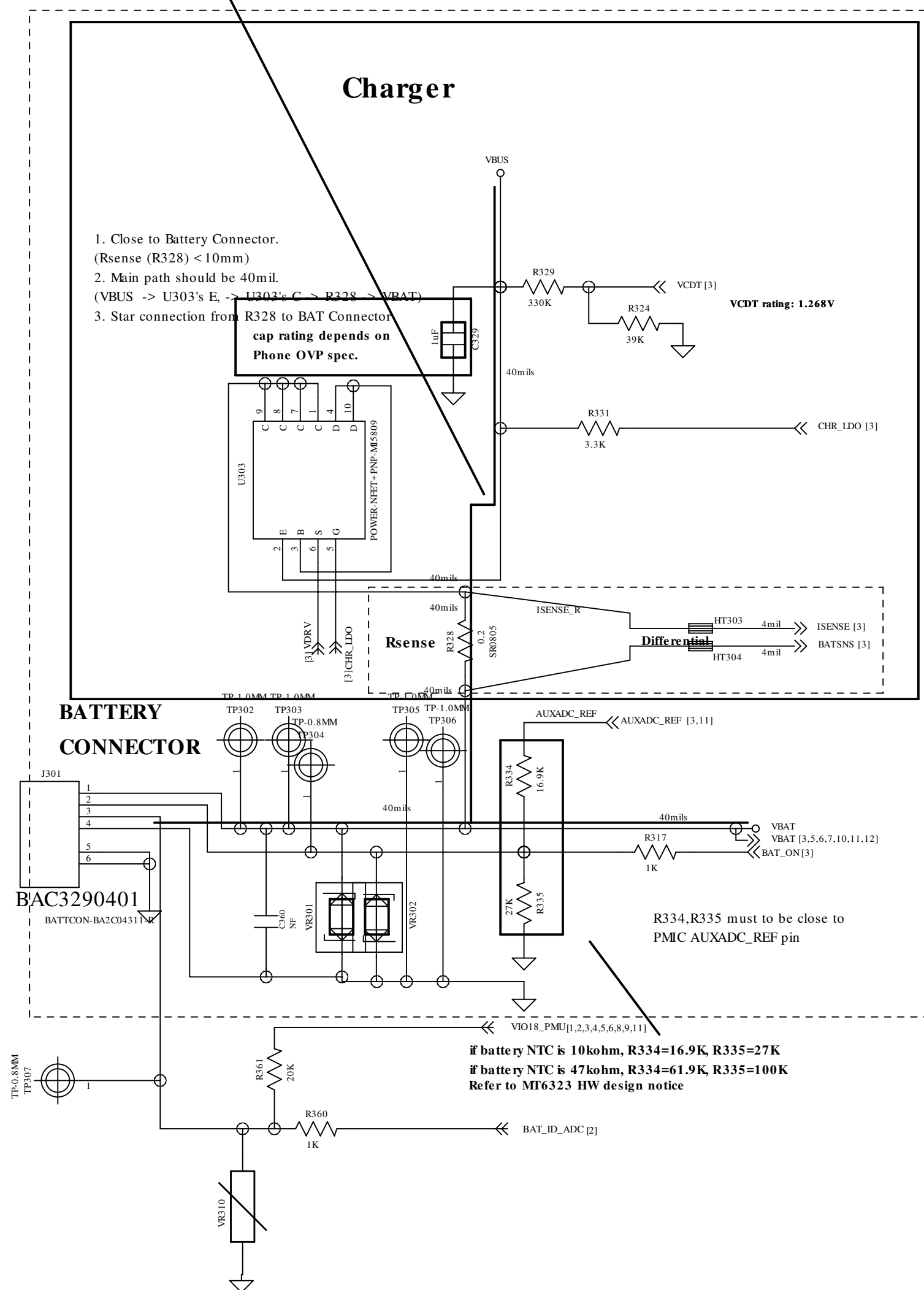
For low-power testing proposal.
It can be cancel for cost-down proposal

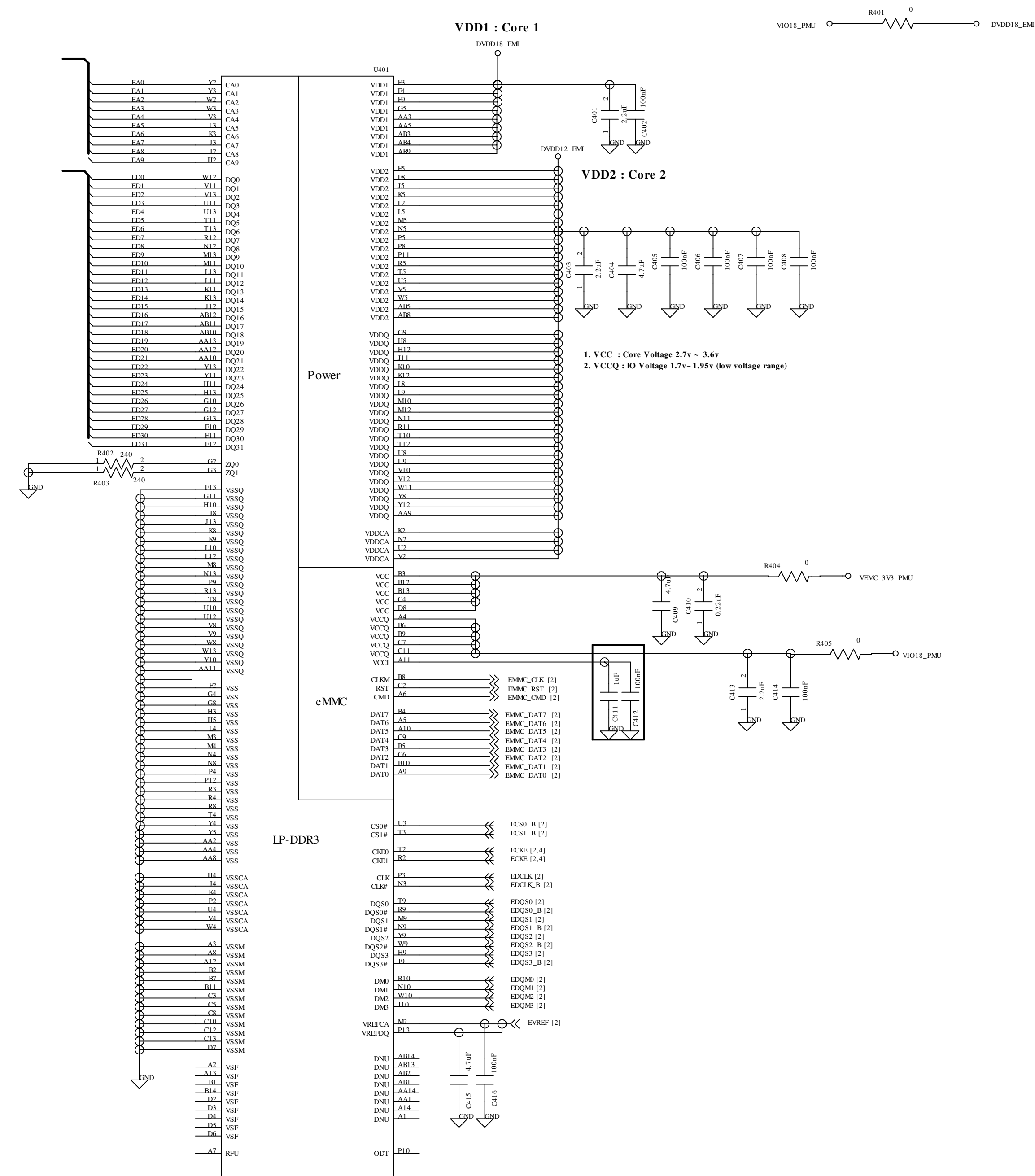
For low-power testing proposal.
It can be cancel for cost-down proposal



TITLE: LU/A-U23		REV: VI.3	
DOCUMENT NO.: 02_MT6582_BASEBAND		SIZED: A1	
DEPARTMENT: Hardware DEPT.			
COMPANY: 			
DESIGNER: <DESIGNER>	Last Saved Date: 2015/12/7	SHEET: 2	OF 13

Before you select BJT , please take power dissipation into consideration.
Refer to MT6323 design notice





Receiver

The diagram illustrates the receiver circuit. It starts with two input signals, [3] AU_ISS and [3] AU_ISSN, which pass through resistors R501 and R502. The signals then converge at a central node. This node is connected to a 100pF capacitor (C502) leading to ground and a 100pF capacitor (C503) leading to a bridge rectifier (BRD-TS4580MS5V 1A50V). The rectifier's output is fed into the REC501 (AUDIO-REC-MBED1206-AD11A18) module, which is connected to a speaker.

Handset Microphone

Handset Microphone

Diagram illustrating the Handset Microphone circuit, showing connections to the microphone (MICBIA50) and the main GND.

The circuit includes the following components and connections:

- Microphone (MICBIA50):** Connected to the input of the circuit.
- Resistors:**
 - R_{274} (10K) and R_{273} (10K) are connected in series to the microphone input.
 - R_{272} (10K) and R_{271} (10K) are connected in series to the microphone output.
 - R_{270} (10K) is connected to the output of the circuit.
- Capacitors:**
 - CS_{22} (100nF) and CS_{23} (100nF) are connected to the input of the circuit.
 - CS_{24} (100nF) and CS_{25} (100nF) are connected to the output of the circuit.
 - CS_{26} (100nF) and CS_{27} (100nF) are connected to the output of the circuit.
- Output Connections:**
 - The output of the circuit is connected to AU_VIN0_P-1 and AU_VIN0_N-1 .
 - The output is also connected to a common ground point labeled "together then single via to main GND".

Labels in the diagram include:

- Close to BB** (Base Band)
- Close to MIC** (Microphone)
- together then single via to main GND**

[illegible][illegible]

Analog Switch

The diagram illustrates an Analog Switch circuit using the U302 component. The switch is controlled by IN=0 and IN=1 signals. The output is connected to a 100K resistor and a 470Ω resistor, which are connected to a 1.5V source. The output is also connected to a 100K resistor and a 470Ω resistor, which are connected to a 1.5V source. The output is connected to a 100K resistor and a 470Ω resistor, which are connected to a 1.5V source.

Earphone MICPHONE

Earphone MICPHONE

Close to BB

Close to MIC

GND of C(4.7uF) and headset should tie together and single via to GND plane

Close to Eardack

together then single via to main GND

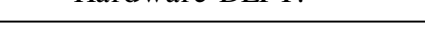
TITLE:	LUA-U23	REV:	V1.3
DOCUMENT NO.:	05_AUDIO	SIZED:	A1
DEPARTMENT:	Hardware DEPT.		
COMPANY:			
DESIGNER: < DESIGNER >	Last Saved Date:	2015/12/7	SHEET: 5 OF 13

CTP

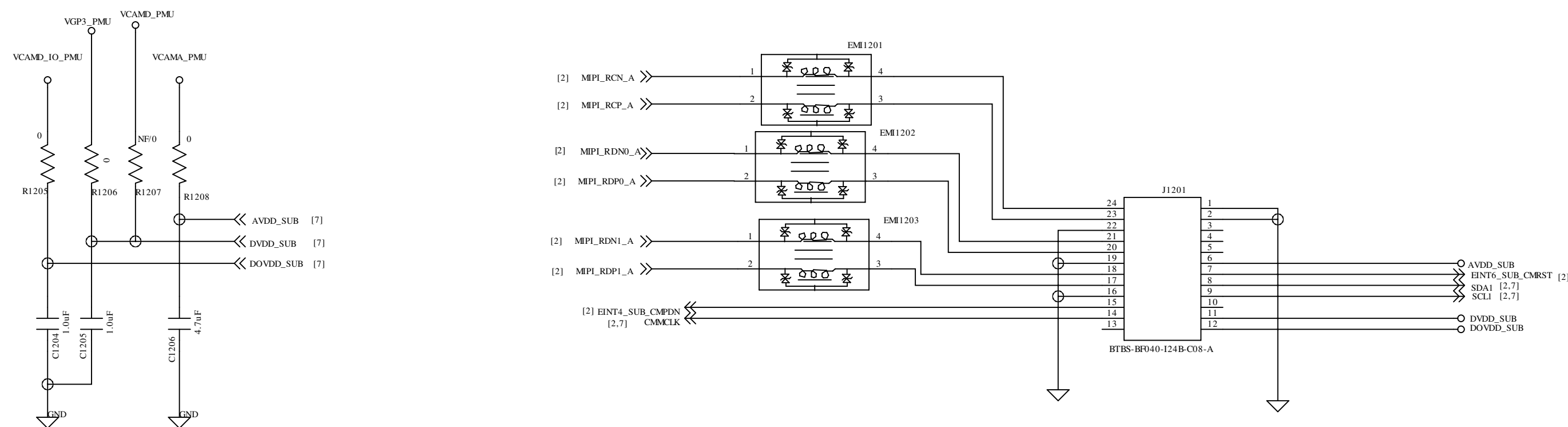
Backlight LED Driver

The schematic diagram illustrates the electrical connections for the BTR5-BF040-140B-C08-A component. Key components and connections include:

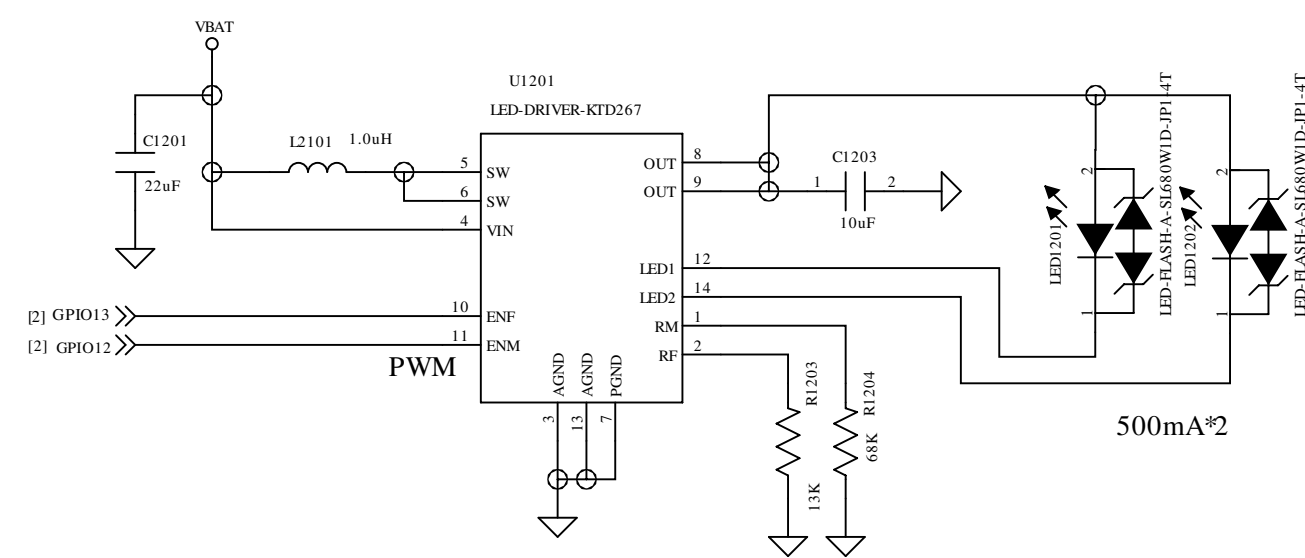
- Power and Grounding:**
 - VBUS is connected to the board's power input.
 - TP-0.5MM connectors (TP18, TP19, TP16, TP17) are used for power and ground connections.
 - TP-0.5MM connectors (TP18, TP19, TP16, TP17) are used for power and ground connections.
 - TP-0.5MM connectors (TP18, TP19, TP16, TP17) are used for power and ground connections.
 - TP-0.5MM connectors (TP18, TP19, TP16, TP17) are used for power and ground connections.
- Signal Connections:**
 - MP1_TDN0, MP1_TDN1, MP1_TDN2, MP1_TDN3, MP1_TCN, and MP1_TCP are connected to the component's pins.
 - USB_DP and USB_DM are connected to the component's pins.
 - SPK_N and SPK_P are connected to the component's pins.
 - SPK_N and SPK_P are connected to the component's pins.
 - SPK_N and SPK_P are connected to the component's pins.
 - SPK_N and SPK_P are connected to the component's pins.
- Component Details:**
 - ESM1101, ESM1102, ESM1103:** These are 10-pin connectors used for signal and power connections.
 - J1102:** A 40-pin connector used for signal and power connections.
 - TP18, TP19, TP16, TP17:** These are 0.5mm pitch connectors used for power and ground connections.
 - TP18, TP19, TP16, TP17:** These are 0.5mm pitch connectors used for power and ground connections.
 - TP18, TP19, TP16, TP17:** These are 0.5mm pitch connectors used for power and ground connections.
 - TP18, TP19, TP16, TP17:** These are 0.5mm pitch connectors used for power and ground connections.
- Other Components:**
 - ESM1101, ESM1102, ESM1103:** These are 10-pin connectors used for signal and power connections.
 - J1102:** A 40-pin connector used for signal and power connections.
 - TP18, TP19, TP16, TP17:** These are 0.5mm pitch connectors used for power and ground connections.
 - TP18, TP19, TP16, TP17:** These are 0.5mm pitch connectors used for power and ground connections.
 - TP18, TP19, TP16, TP17:** These are 0.5mm pitch connectors used for power and ground connections.
 - TP18, TP19, TP16, TP17:** These are 0.5mm pitch connectors used for power and ground connections.

TITLE: LUA-U23		REV: V1.3
DOCUMENT NO.: 11_LCD_CTP_SUB		SIZED: A1
DEPARTMENT: Hardware DEPT.		
COMPANY: 		
DESIGNER: <DESIGNER>	Last Saved Date: 2015/12/7	SHEET: 6 OF 13

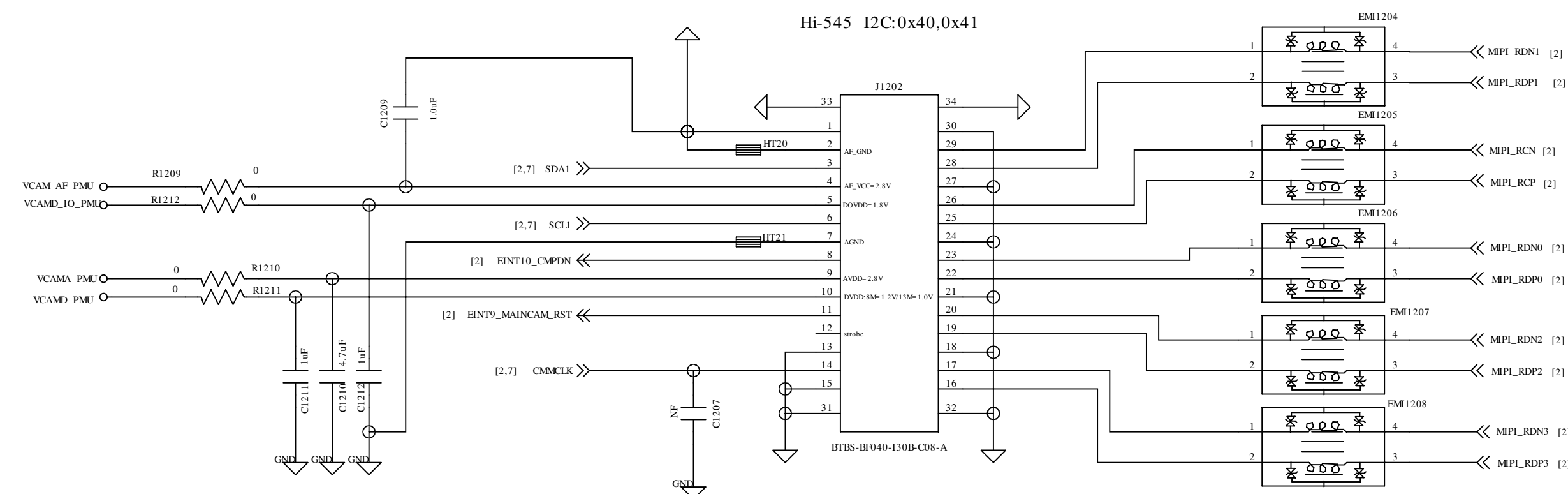
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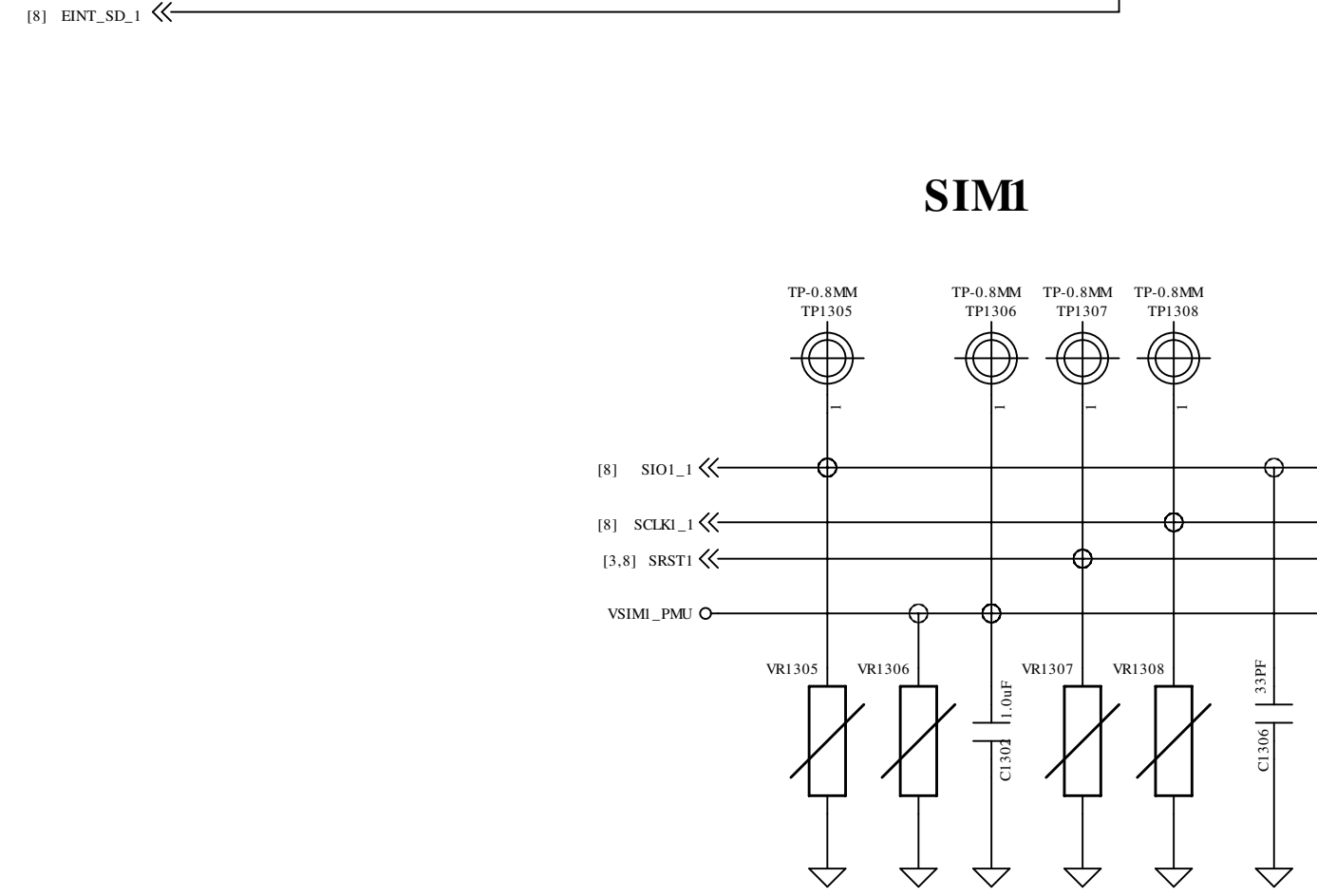
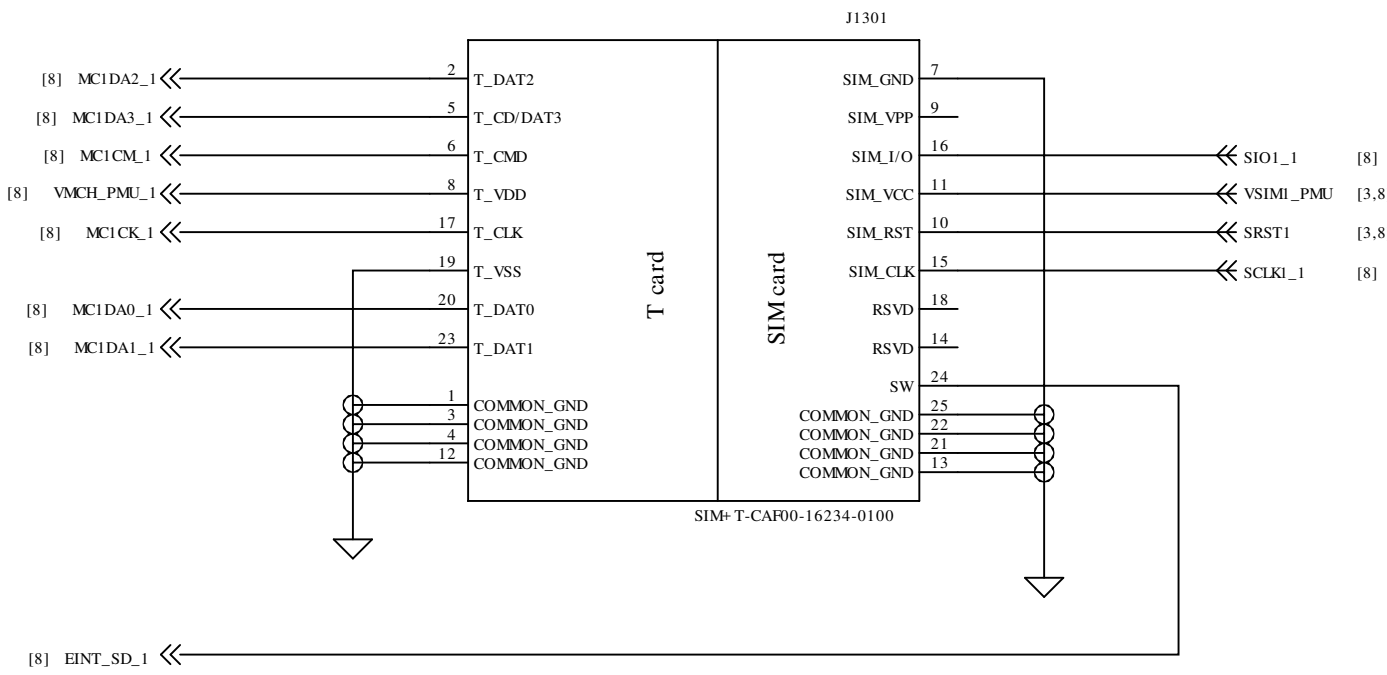
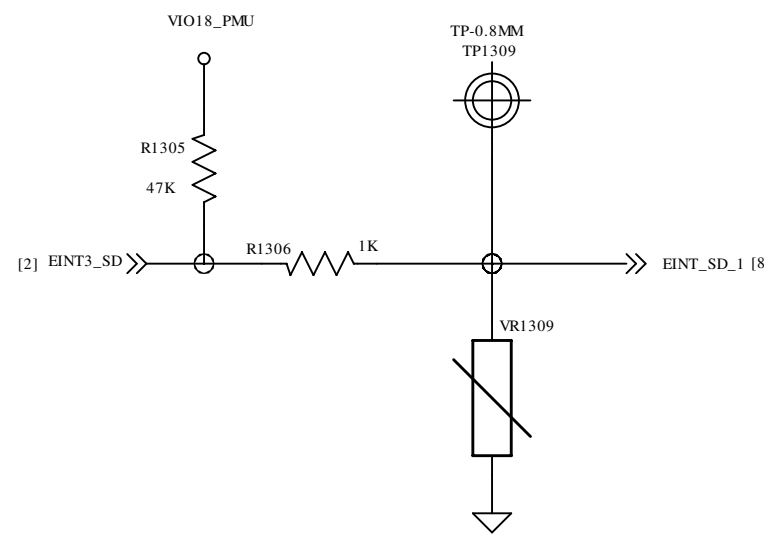
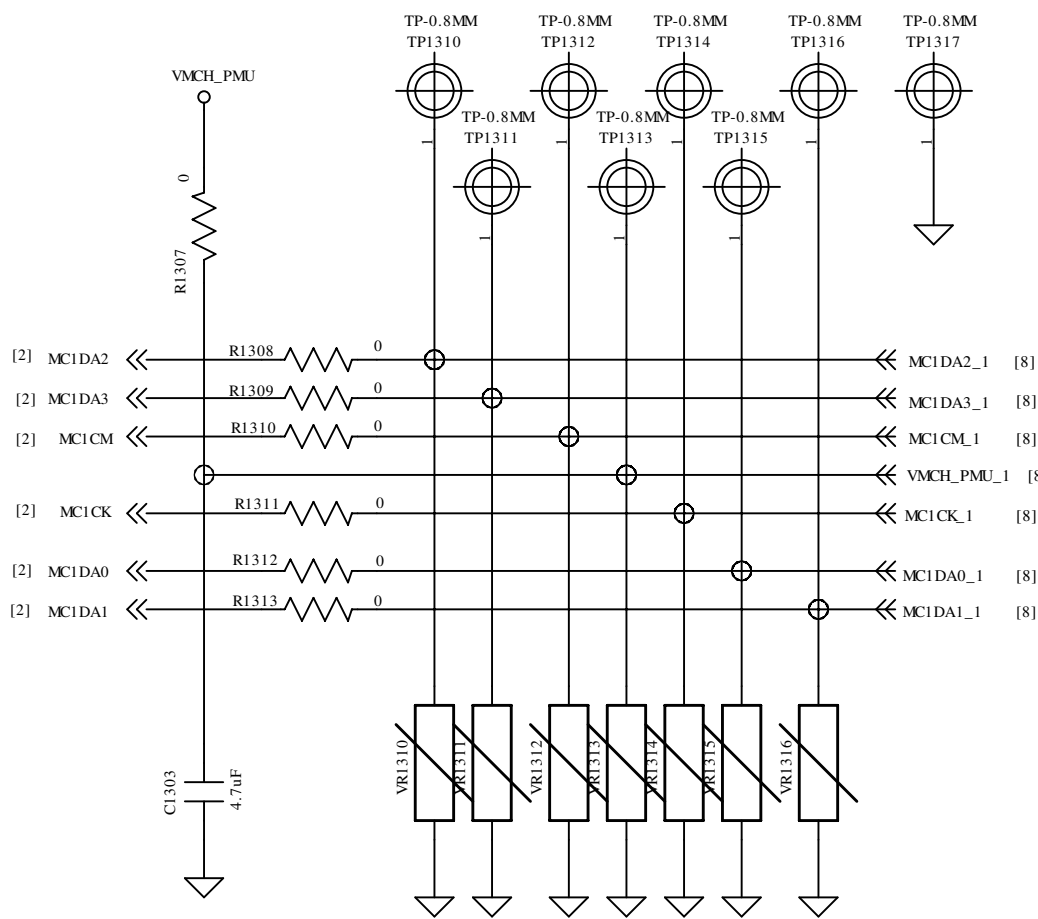
FLASH_LED



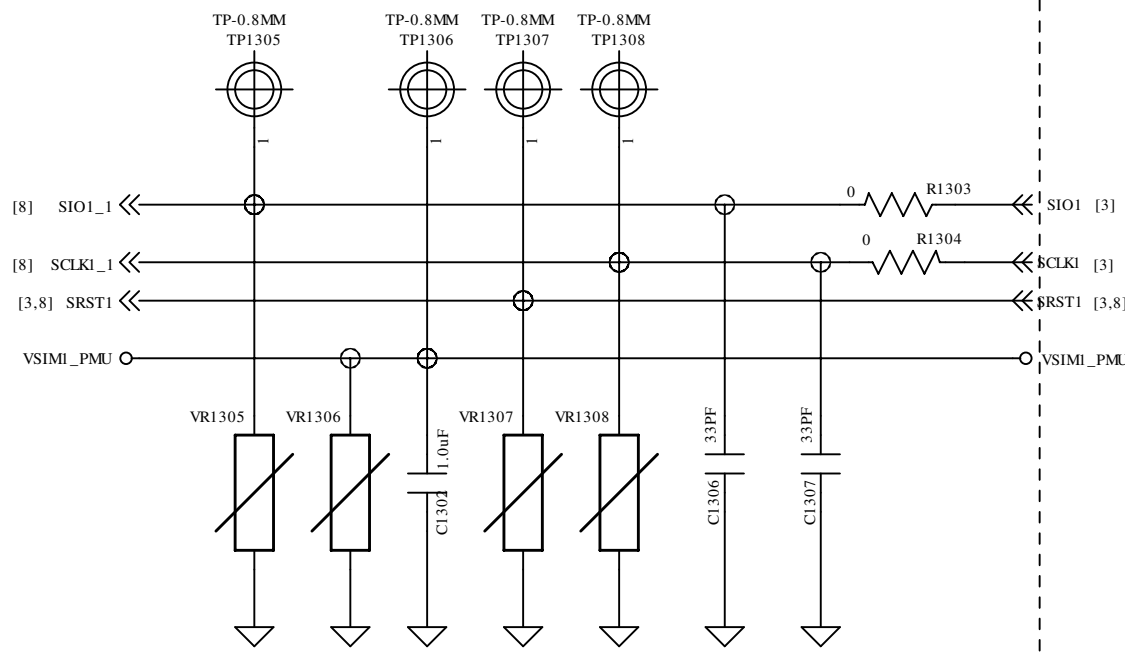
MAIN_camera



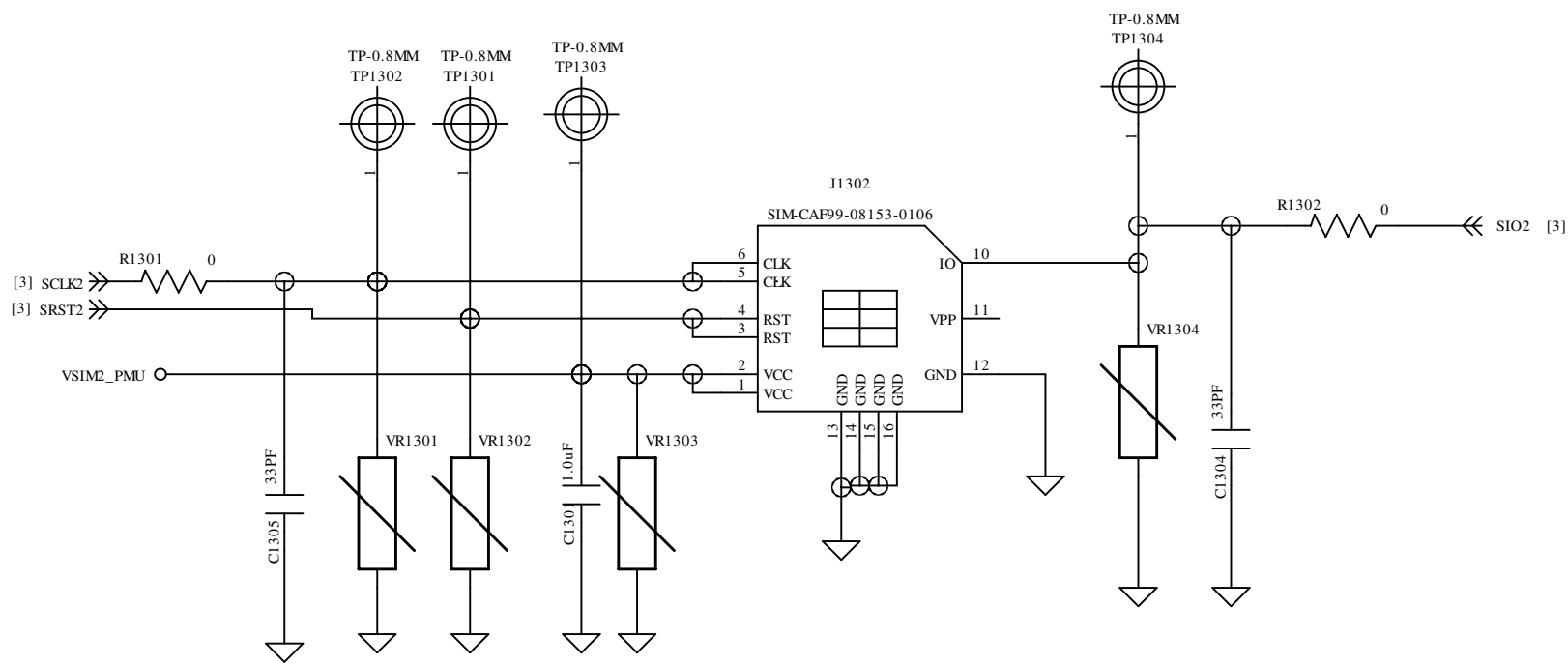
SD CARD

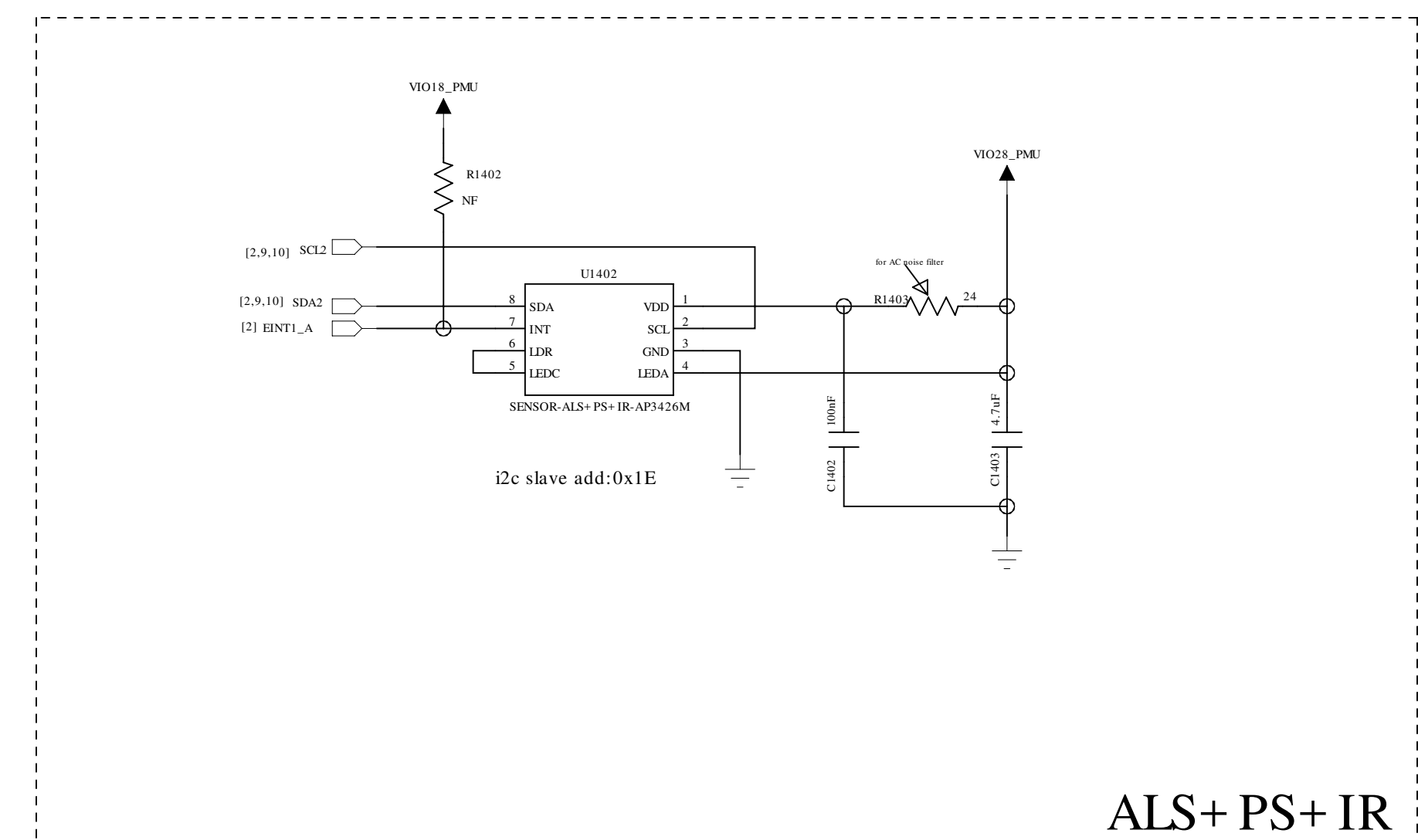
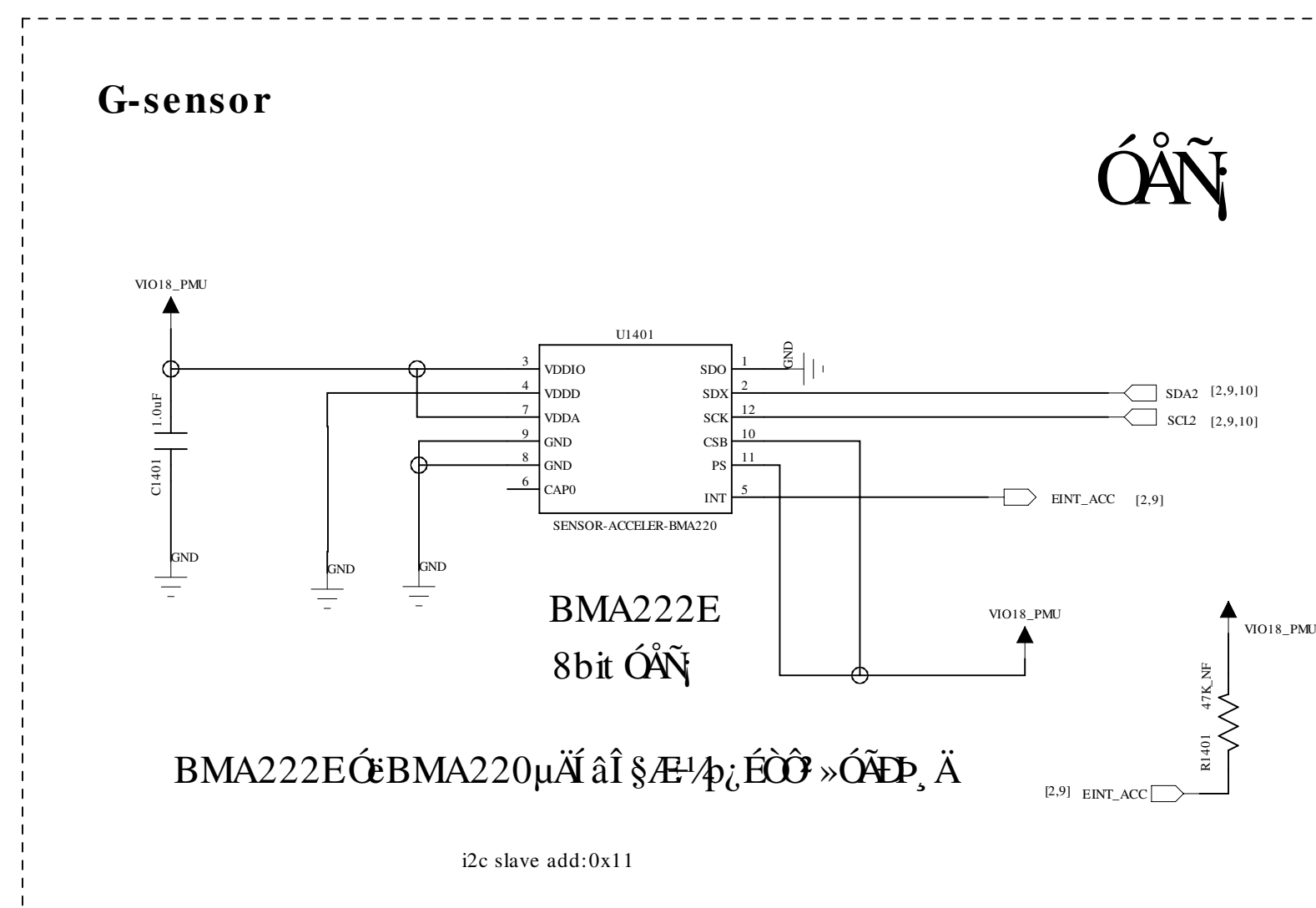


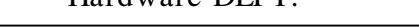
SIM1



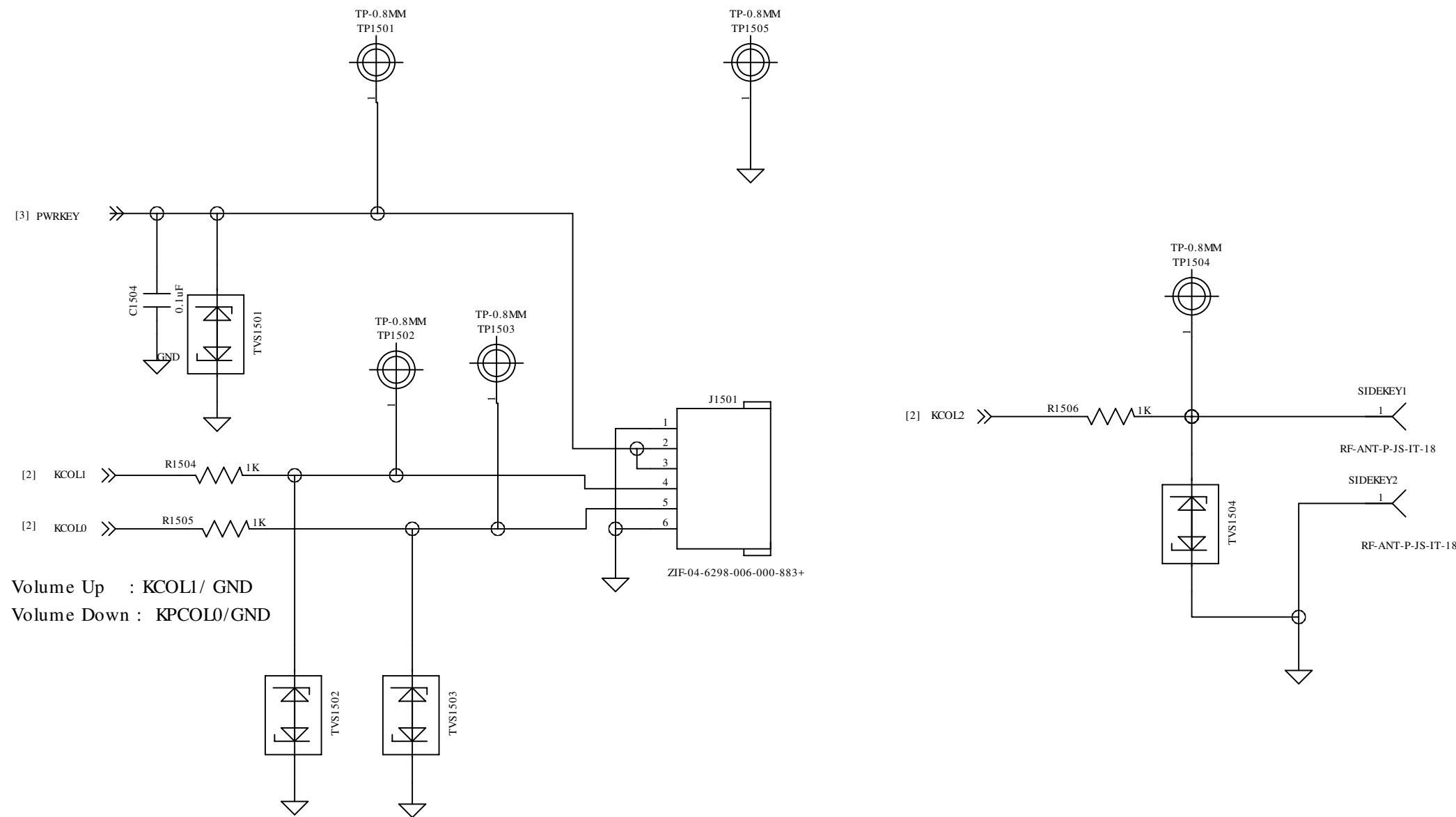
SIM2



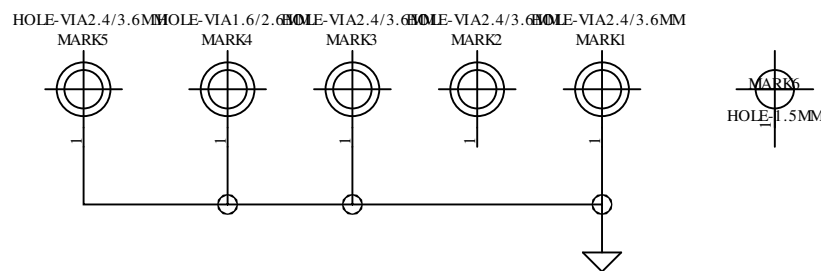
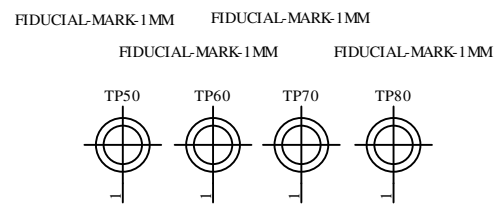
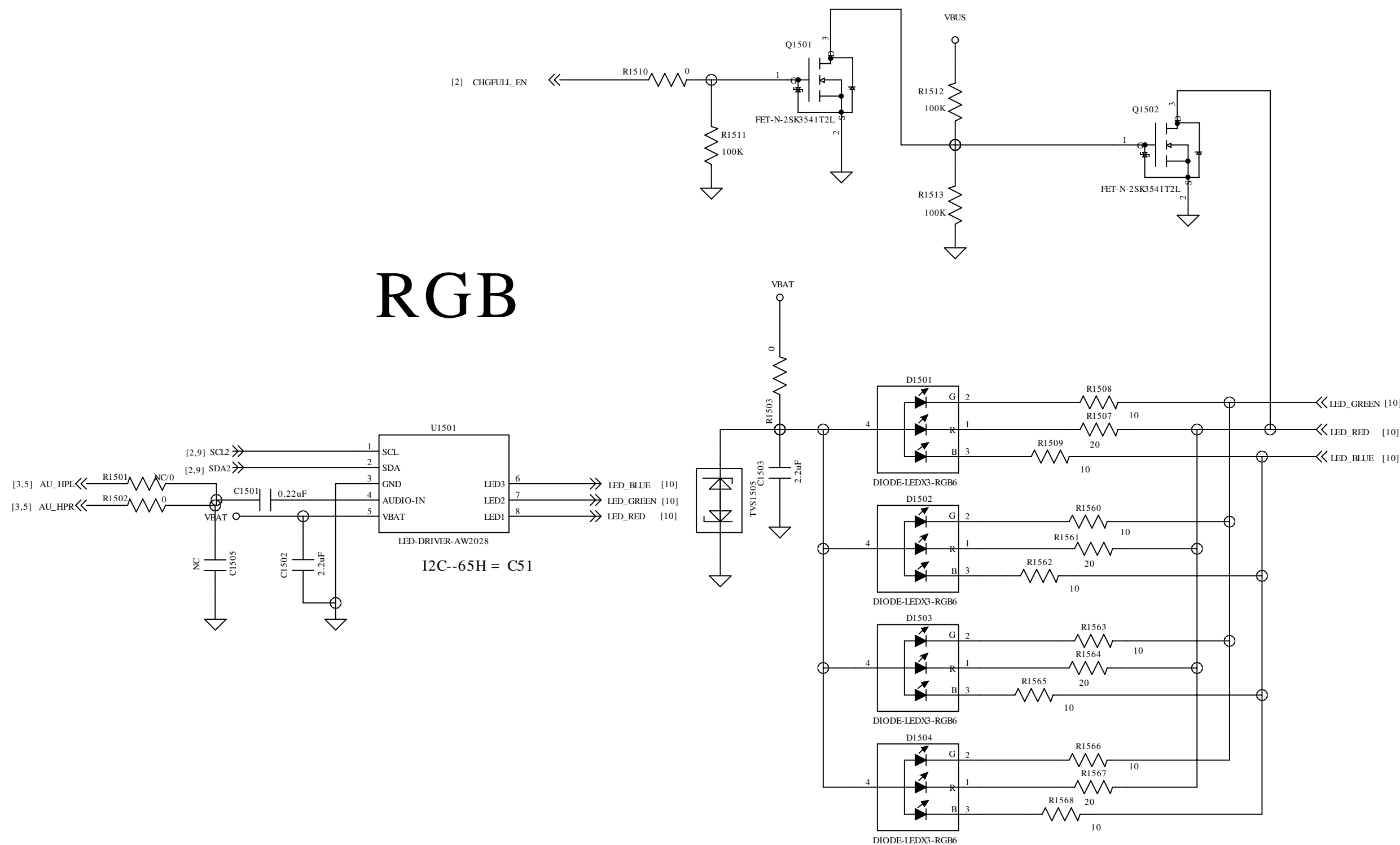
Gsensor¹ ©~ø² »¶·^{3/4} ÄË«lay,ÄËÇ°ÊÎîñÊÎ½ ÒÈÓÃÑ BMA222E

TITLE:		LUA-U23		REV:		V1.3	
DOCUMENT NO.:		14_SENSORS		SIZED:		A1	
DEPARTMENT:		Hardware DEPT.					
COMPANY:							
DESIGNER:		< DESIGNER >		Last Saved Date:		2015/12/7	
				SHEET:		9 OF 13	

KEY



RGB



TITLE	LUA-U23	REV	V1.3
DOCUMENT NO.	15_KEY_LEDS	SIZED	A1
DEPARTMENT	Hardware DEPT.		
COMPANY	WINGTECH		
DESIGNER	< DESIGNER >	Last Saved Date	2015/12/7
SHEET	10	OF	13

RF9810 control logic table

Enable VetC VetB VetA				
LB_GMSK_TX	H	L	H	L
HB_GMSK_TX	H	L	H	H
LB_EDGE_TX	H	L	L	L
HB_EDGE_TX	H	L	L	H
RX1	L	H	L	L
RX2	L	L	H	L
RX3	L	L	H	H
RX4	L	L	L	H

VBAT
VBAT [3]R: 7,10,11,12]

BP10~4 and 10~11 are 2G+3G mode both
BP15~9 and 12~14 are 3G mode only
(suggest BP15~9 = 1.8V)

[2] (2,1) ASPM_VREF1A << ASM_VCTRLA [2] [2]
[2] (2,1) ASPM_VREF1B << ASM_VCTRLB [2] [2]
[2] (2,1) ASPM_VREF1C << ASM_VCTRLC [2] [2]
[2] (2,1) VREF1 << W0_G0RPA10R0R0E

[2] (2,1) PVS0GE_PA_VRAM << W0_G0RPA10R0R0E [2,11,12]

[3] (3,1) DCXD_32K << DCXD_32K [10R]

VTCXD_PMI << R633 << VTCXD28-1 [11]
VREF18_PMI << R634 << VREF18-1 [11]

V018_PMI << V018_PMI

[2] BSI-A_EN [2,11] << BSI-A_EN [2,20]
[2] BSI-A_CLK [2,11] << BSI-A_CLK [2,20]
[2] BSI-A_DATA1 [2,11] << BSI-A_DATA1 [2,20]
[2] BSI-A_DATA2 [2,11] << BSI-A_DATA2 [2,20]

[2] DCDC [10R] << DCDC [10R] [2]
[2,3] SRC1EN2A [10R,3] << SRC1EN2A [10R,3]
[2,3] SRC1EN2B [10R,3] << SRC1EN2B [10R,3]

[10] SYSC1A_WEN [11,13] << SYSC1A_WEN [11,14R]
[10] CLK1_CM0B [11] << CLK1_CM0B [11]
[11] CLK1_AUDIO [11] << CLK1_AUDIO [11,11]

[2] RX_BRQ2 [2,11] << RX_BRQ2 [2,11]
[2] RX_BRQ2 [2,11] << RX_BRQ2 [2,11]
[2] RX_BRQ2 [2,11] << RX_BRQ2 [2,11]
[2] RX_BRQ2 [2,11] << RX_BRQ2 [2,11]

[2] TX_BRQ2 [2,11] << TX_BRQ2 [2,11]
[2] TX_BRQ2 [2,11] << TX_BRQ2 [2,11]
[2] TX_BRQ2 [2,11] << TX_BRQ2 [2,11]
[2] TX_BRQ2 [2,11] << TX_BRQ2 [2,11]

Two Application Circuit Conditions,

- 1.TSX Circuit : X600= TSX, R653= R656= NC, R654= 100K+ -1%, R655= R657= 0ohm
- 2.XTAL Circuit : X600= Mobile XTAL, R653= R656= 0ohm, R654= R655= R657= NC

[3] AUXADC_REF <<Route AUXADC_REF with 4mil trace width

[3] AUXADC_TSX <<Route AUXADC_TSX with 4mil trace width

[5] GND_AUXADC <<Route AUXADC_GND with 24mil trace width under AUXADC_REF/AUXADC_TSX trace

Connect TSX/XTAL GND to GND_AUXADC first than connect to main GND

Route AUXADC_REF/AUXADC_TSX as differential trace with well GND shielding and route AUXADC_GND with 24mil trace width under AUXADC_TSX/AUXADC_REF trace to provide return current path.

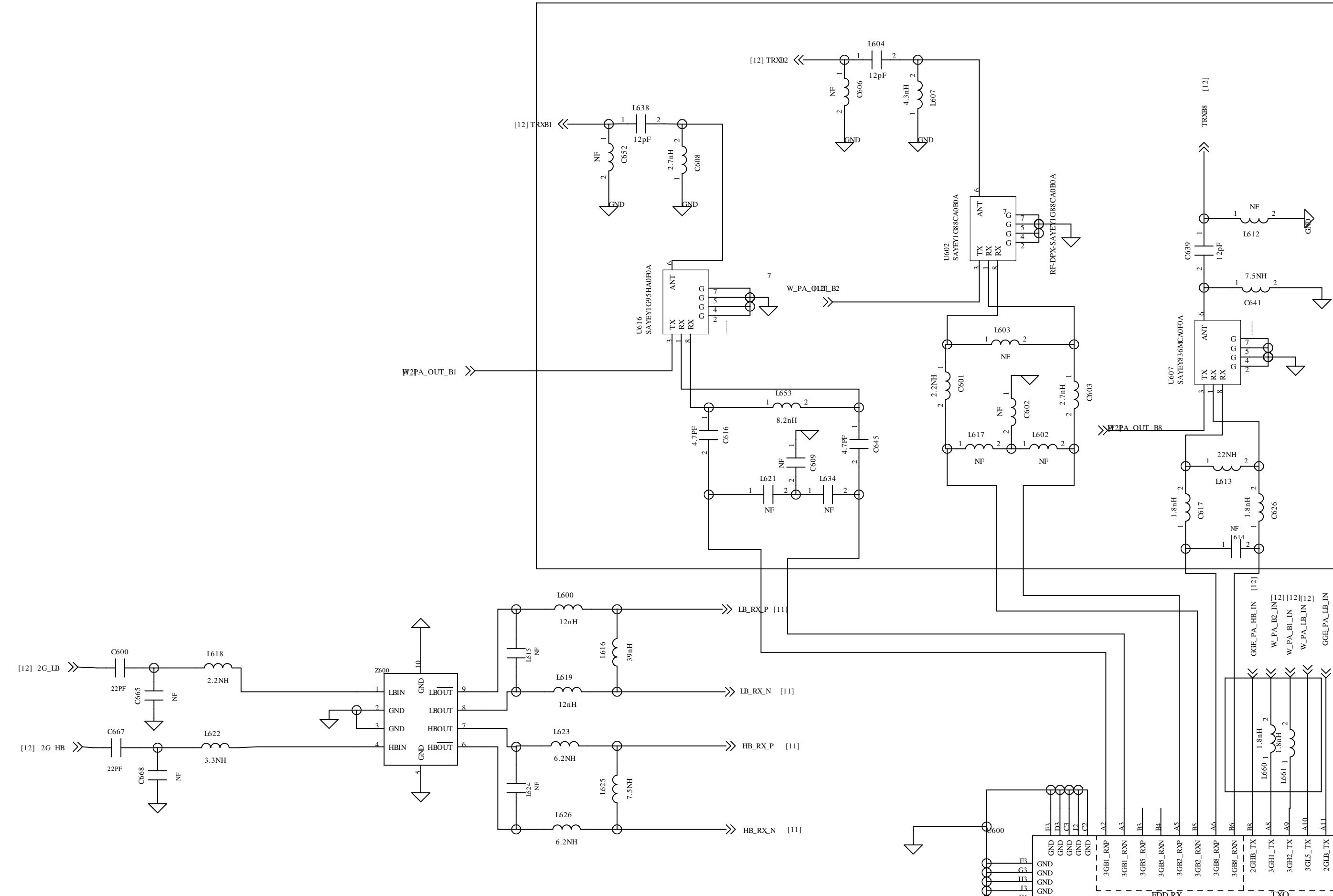
MODE	Logic	DCXD_32K_EN	XMODE	VXDIG
DCXD + 32K XO	0/GND	1(VT018)	1(VT018)	1(VT018)
DCXD + 32K Less	1(VT02028)	1(VT02028)	1(VT02028)	1(VT02028)

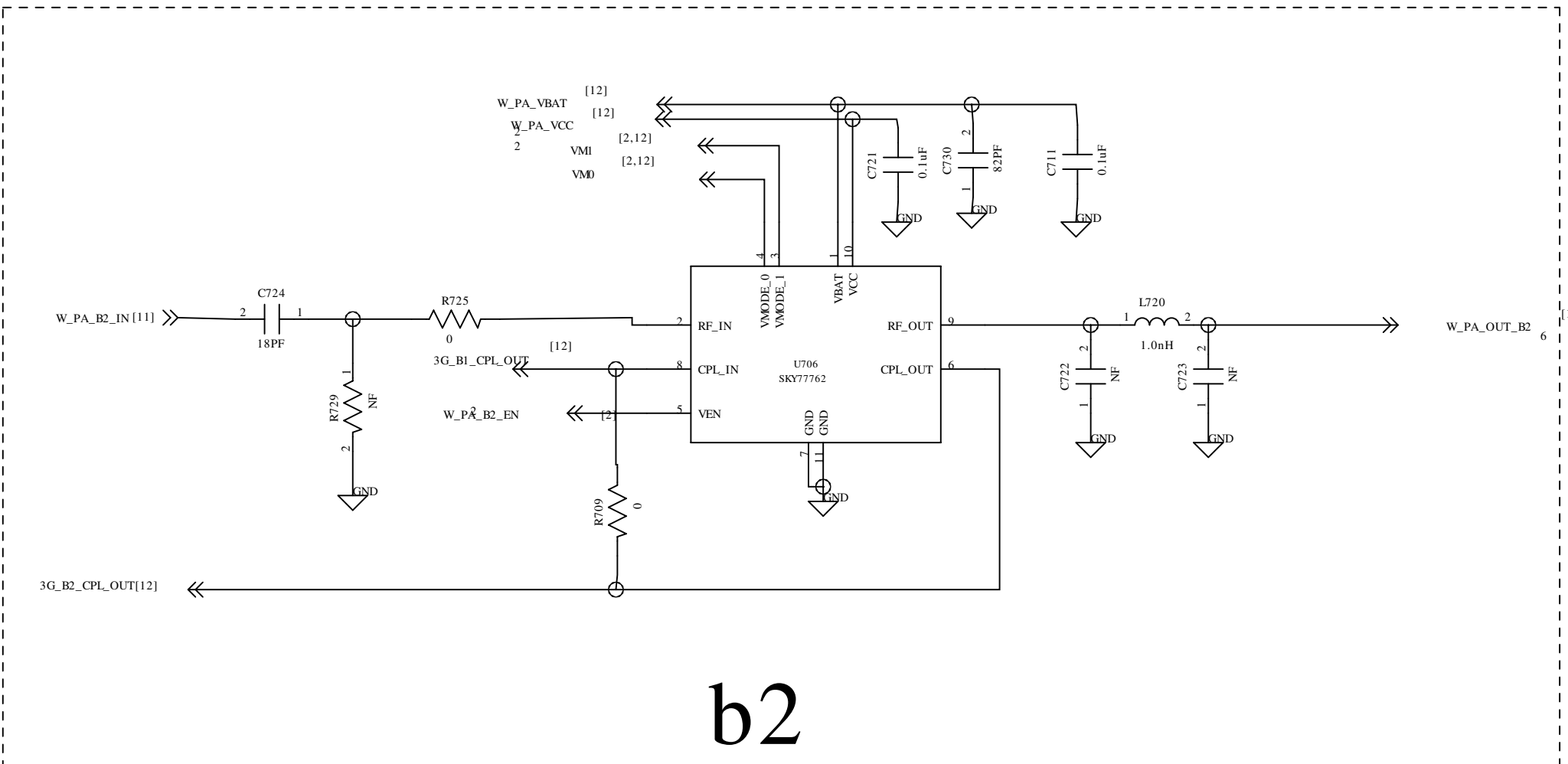
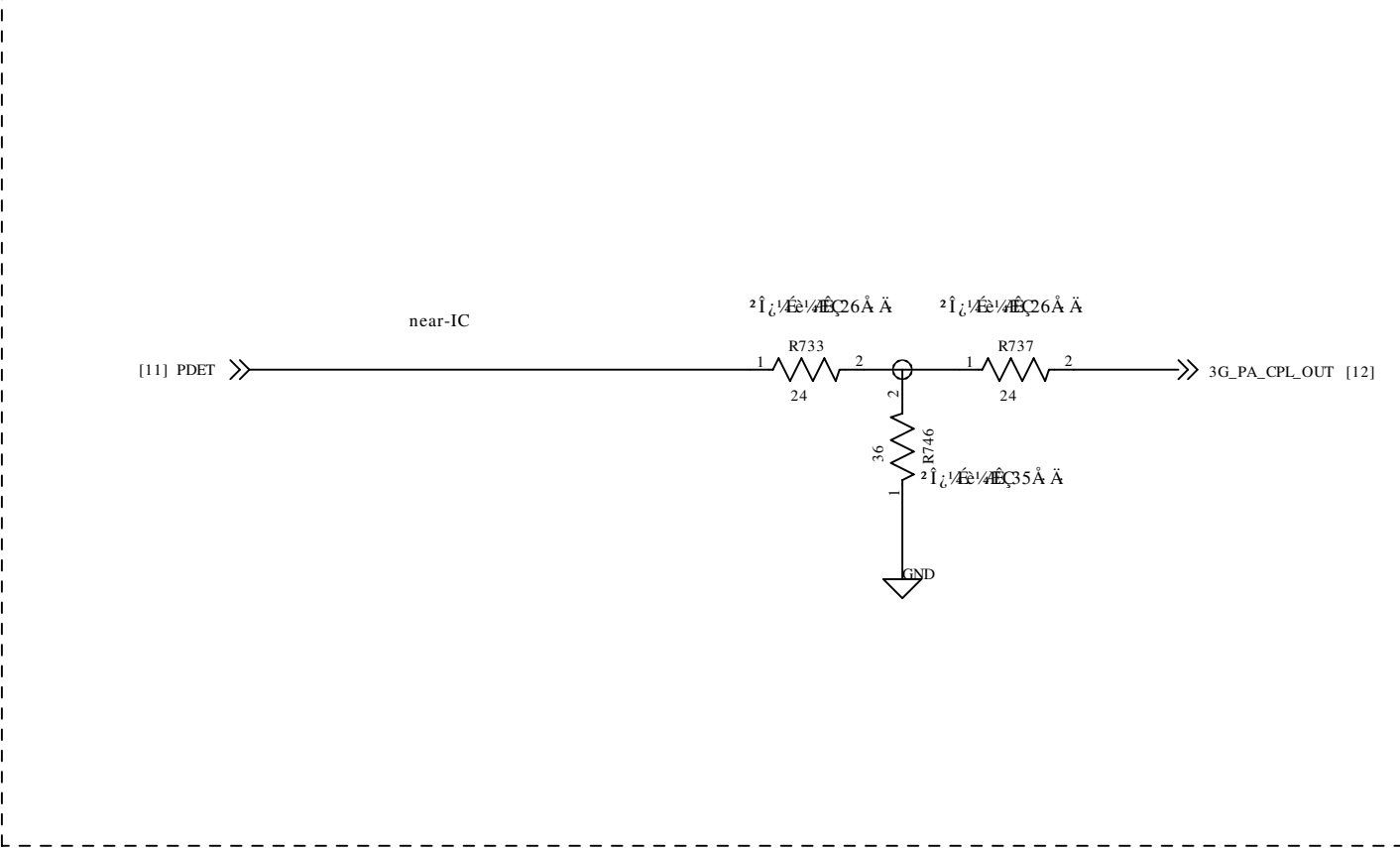
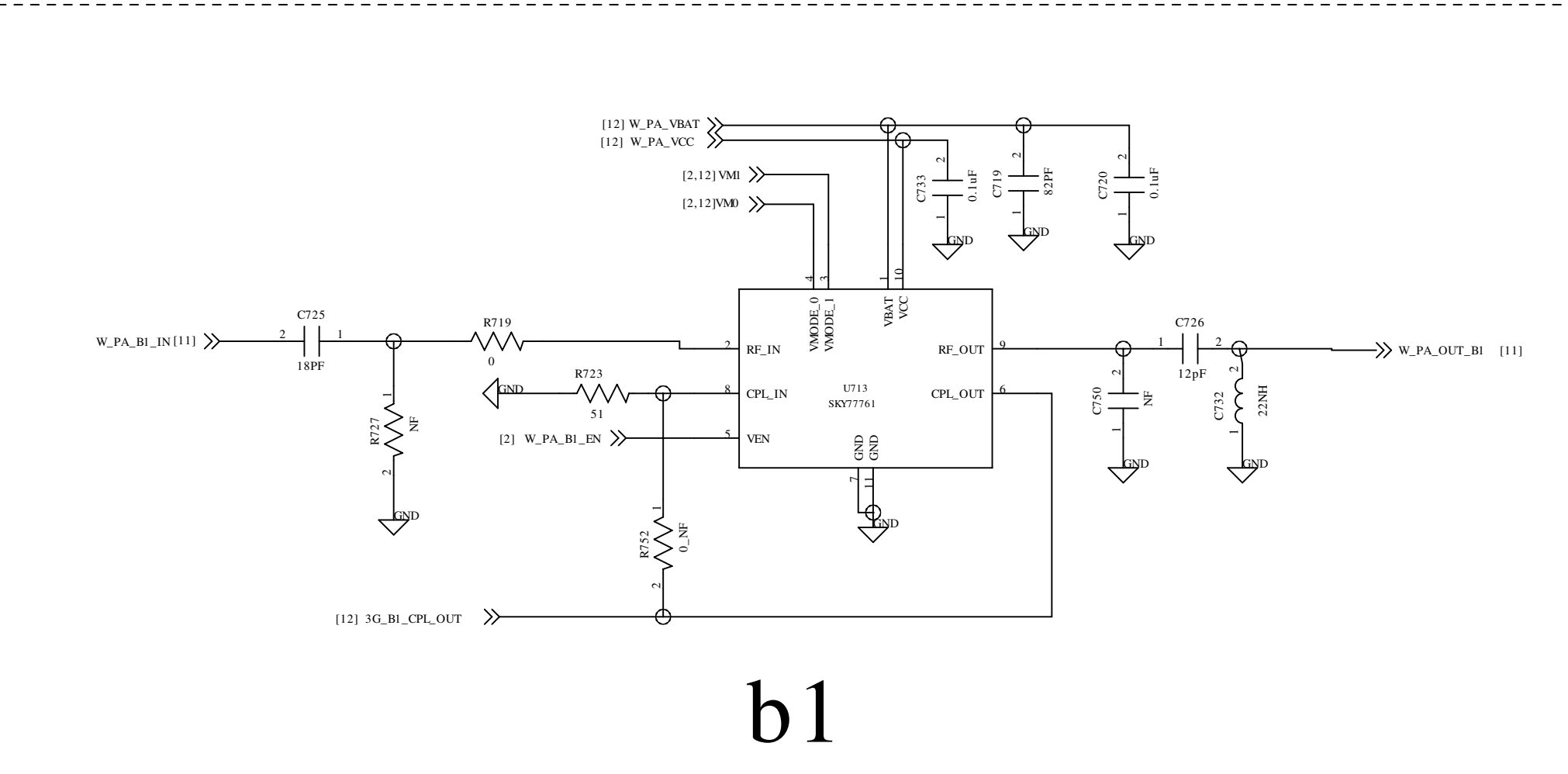
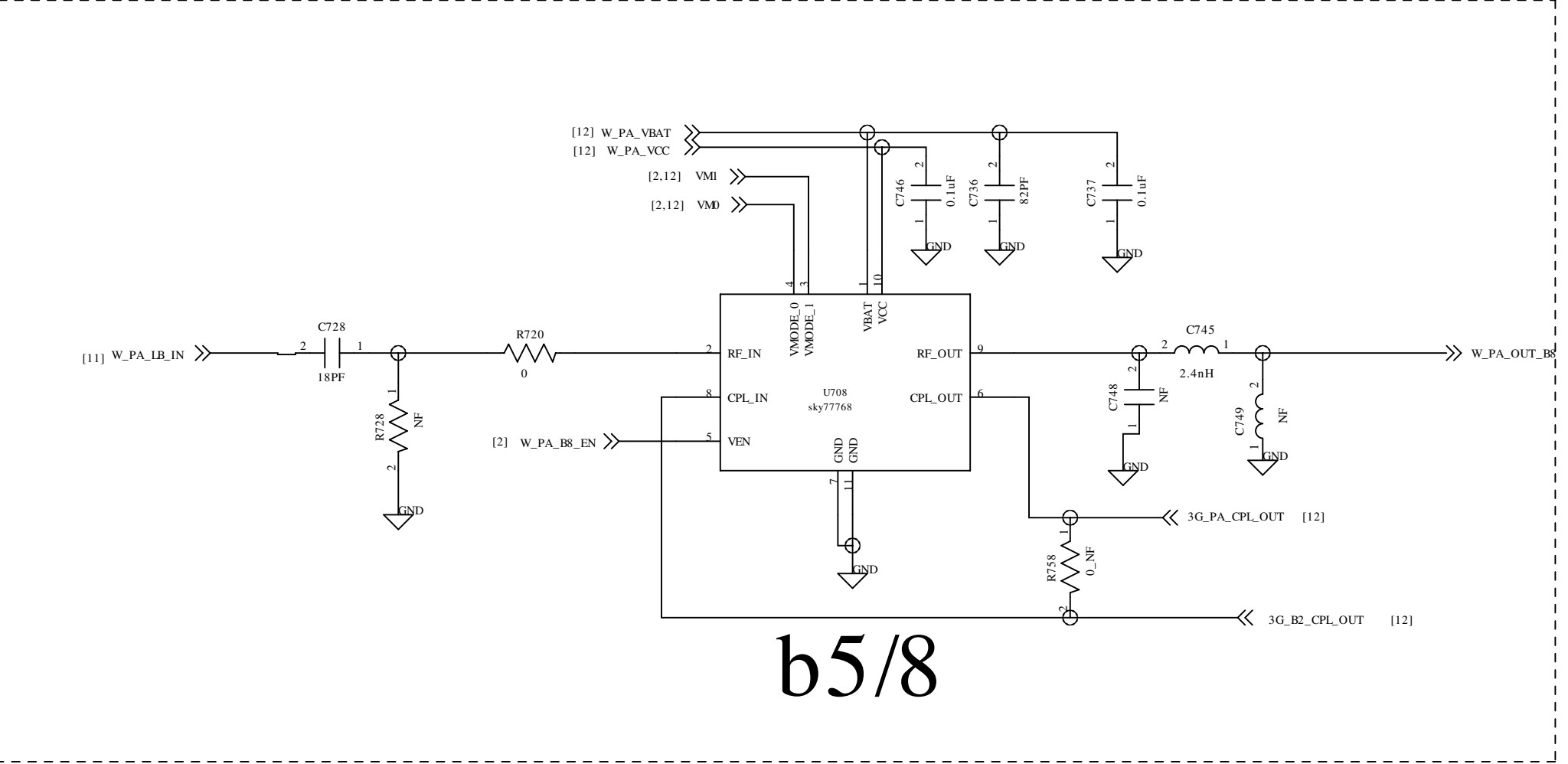
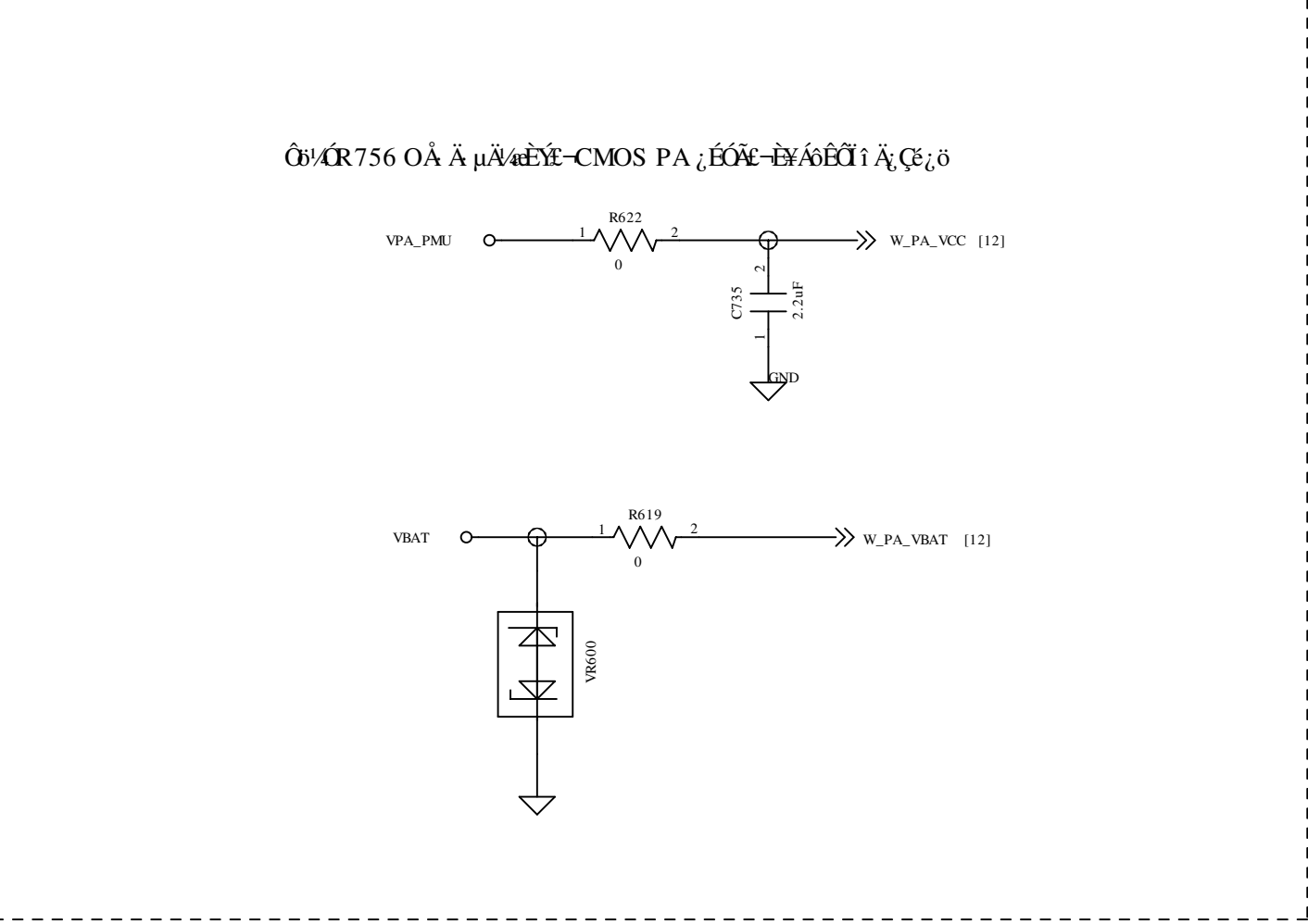
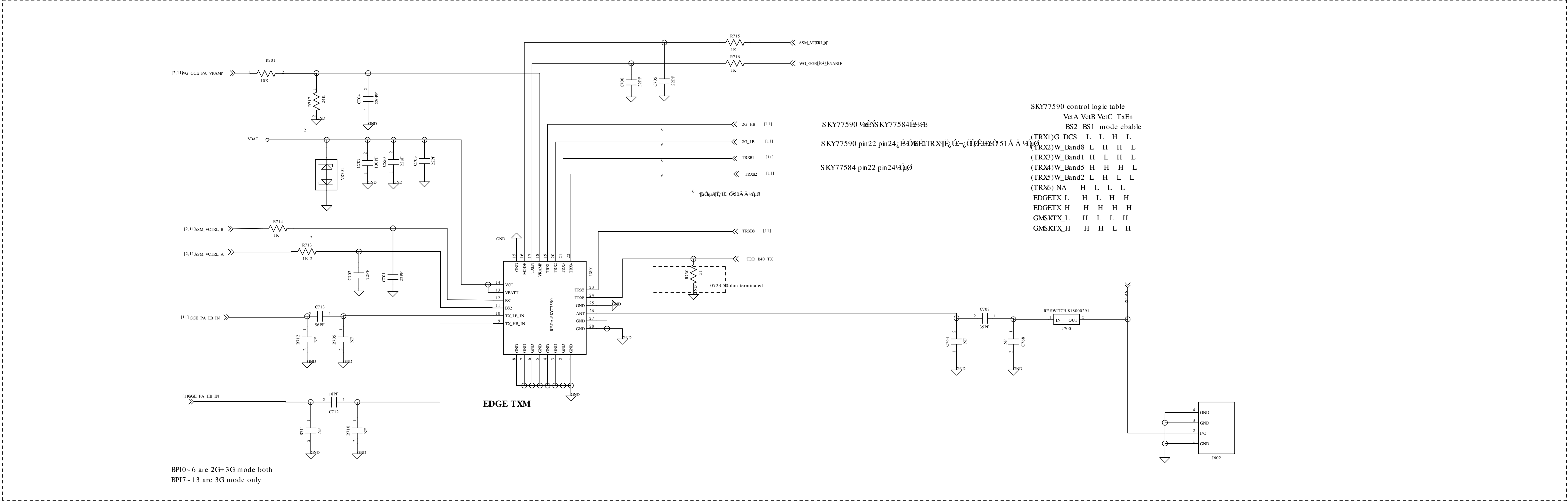
[11] VTCXD28-1 << R616 << DCXD_32K_EN [11]

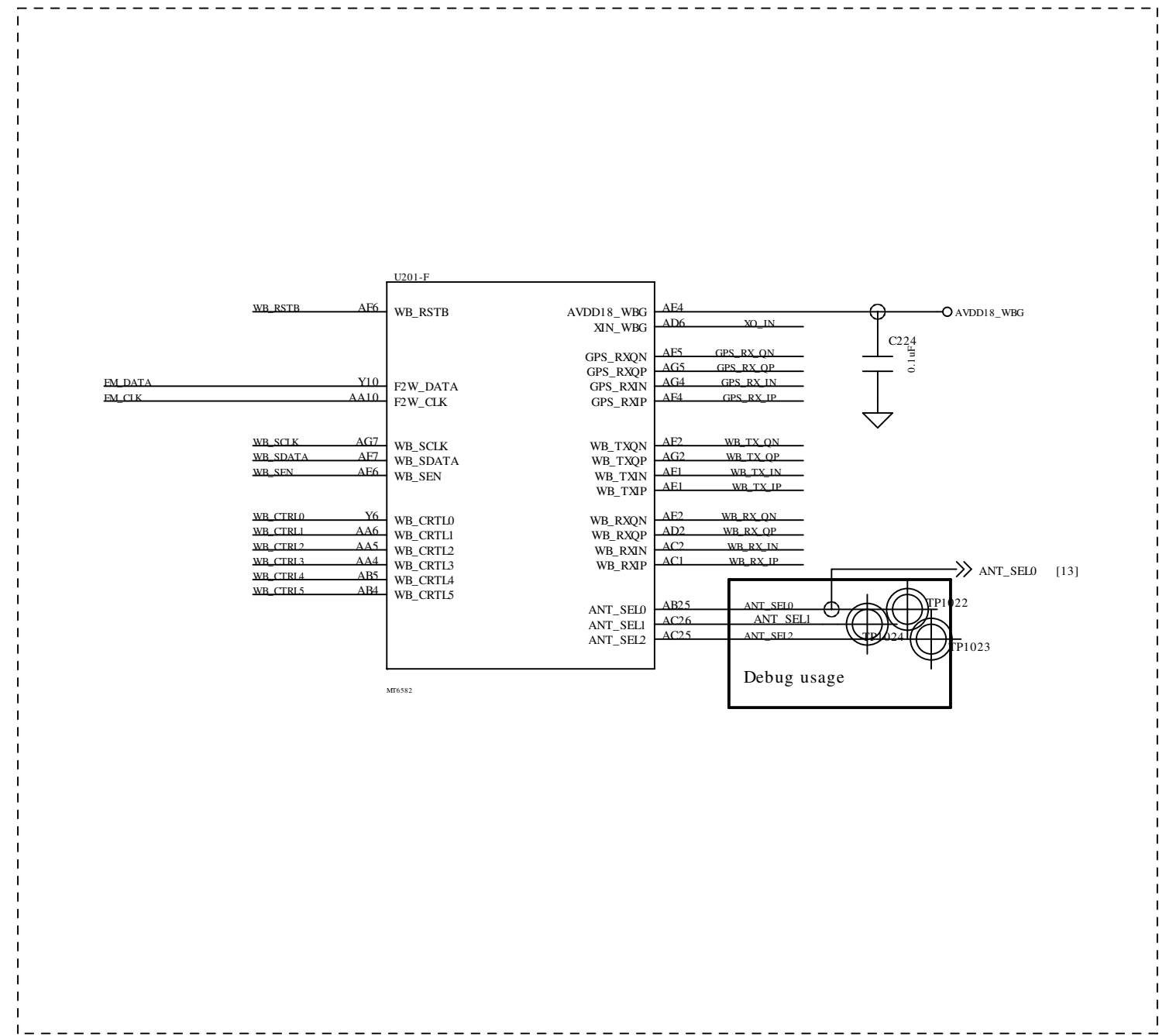
[11] VTCXD28-1 << R618 << XMODE [11]

[11] VTCXD28-1 << R617 << VXDIG [11]

[11] VTCXD28-1 << C870 << 470nF







TITLE: LU/A-U23		REV: VI.3	
DOCUMENT NO.: 10_WIFN_BT_GPS_FM_MIT6627		SIZED: A1	
DEPARTMENT: Hardware DEPT.			
COMPANY: 			
DESIGNER: <DESIGNER>	Last Saved Date: 2015/12/7	SHEET: 13 OF 13	