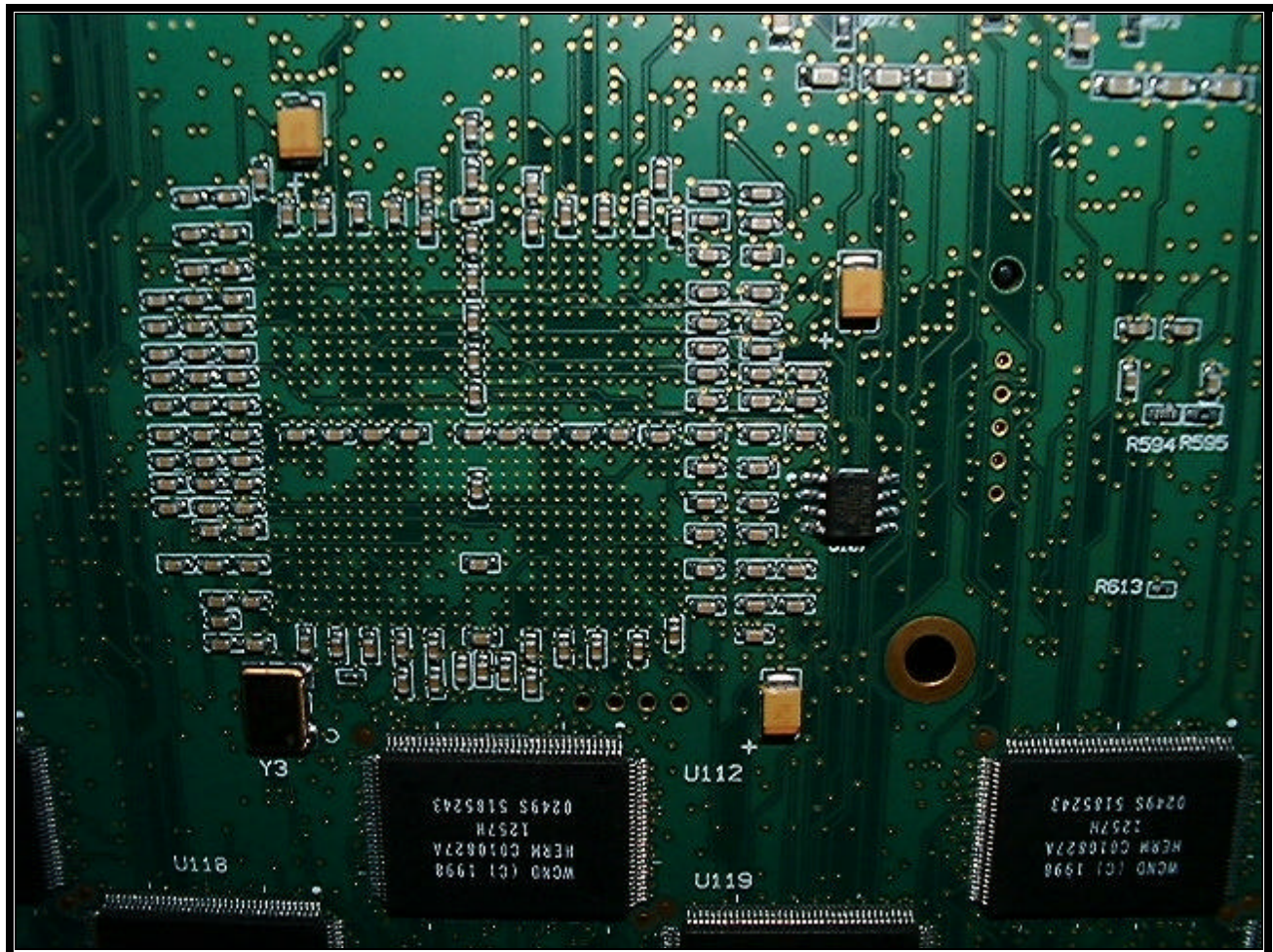
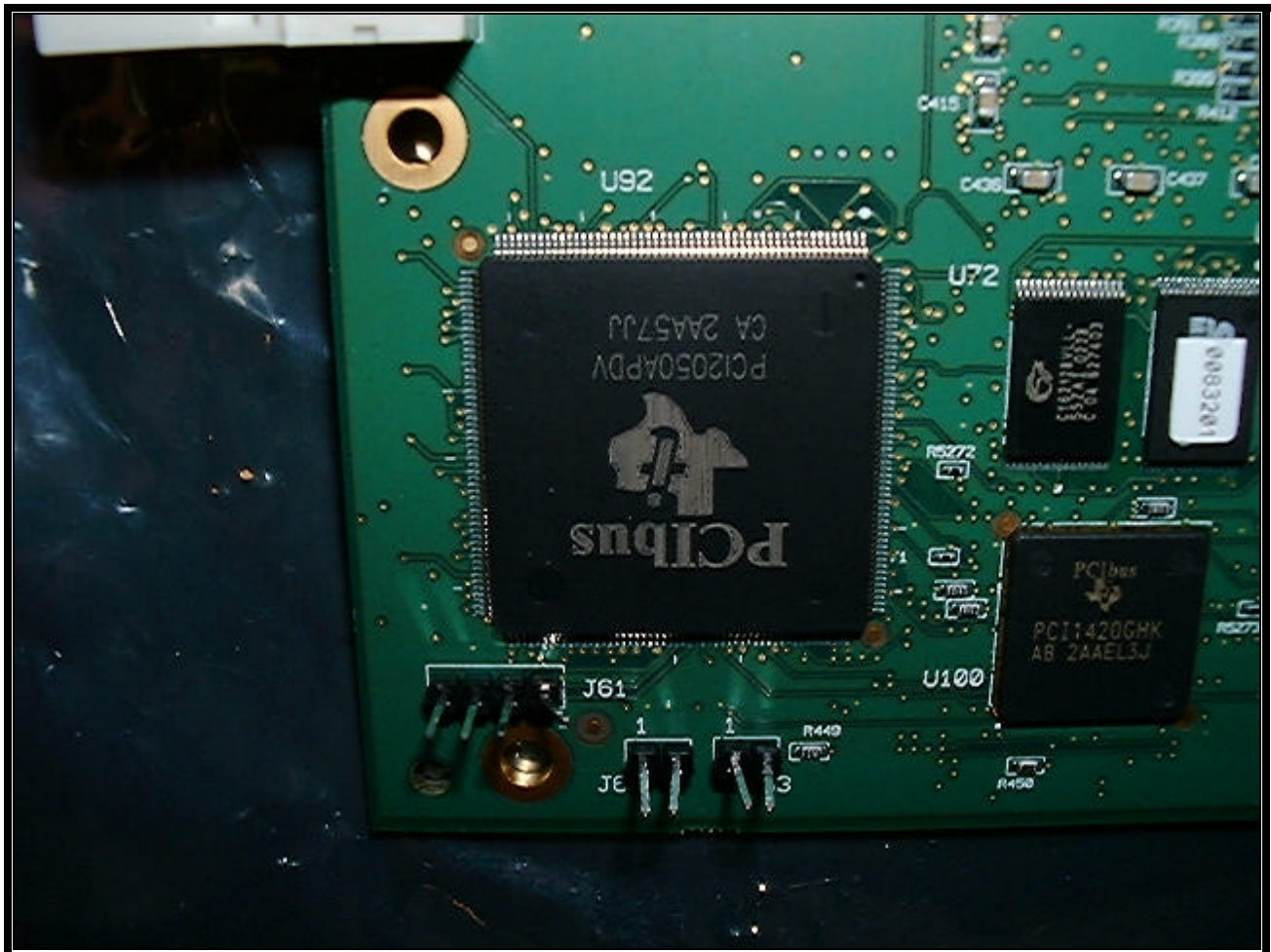




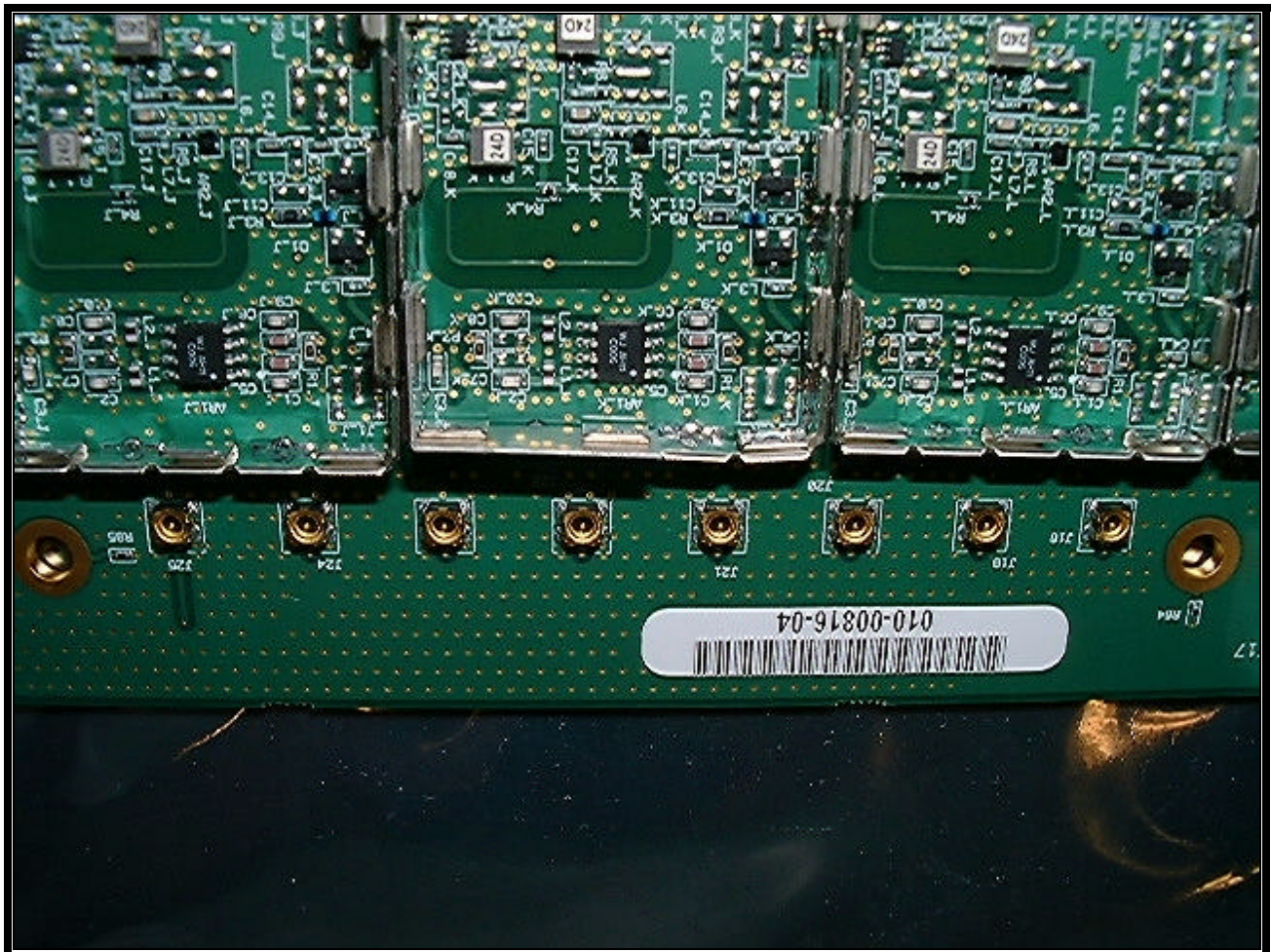
PHOTOGRAPH 49: SIGNAL PROCESSING BOARD FPGA



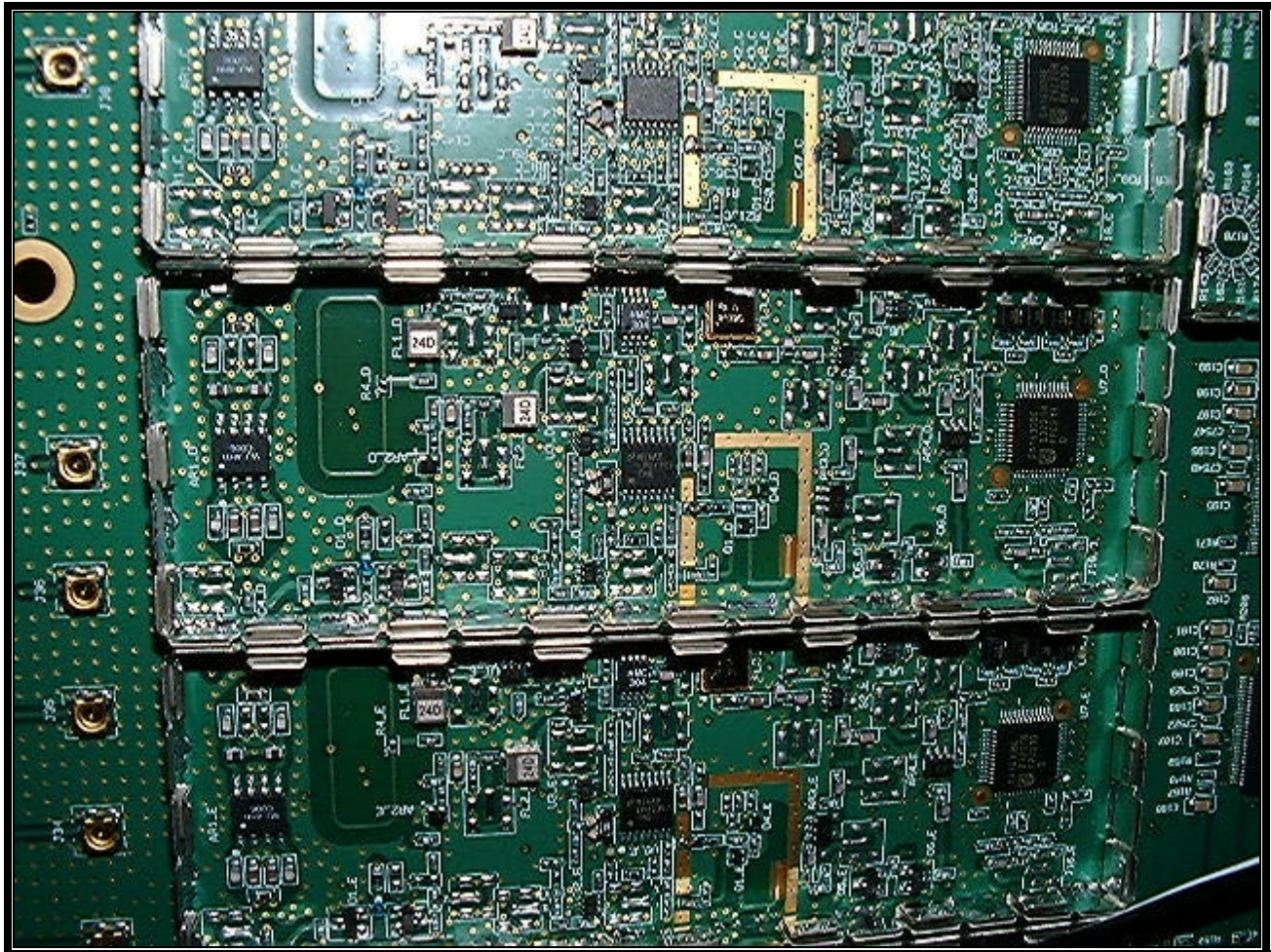
PHOTOGRAPH 50: REAR VIEW OF FPGA



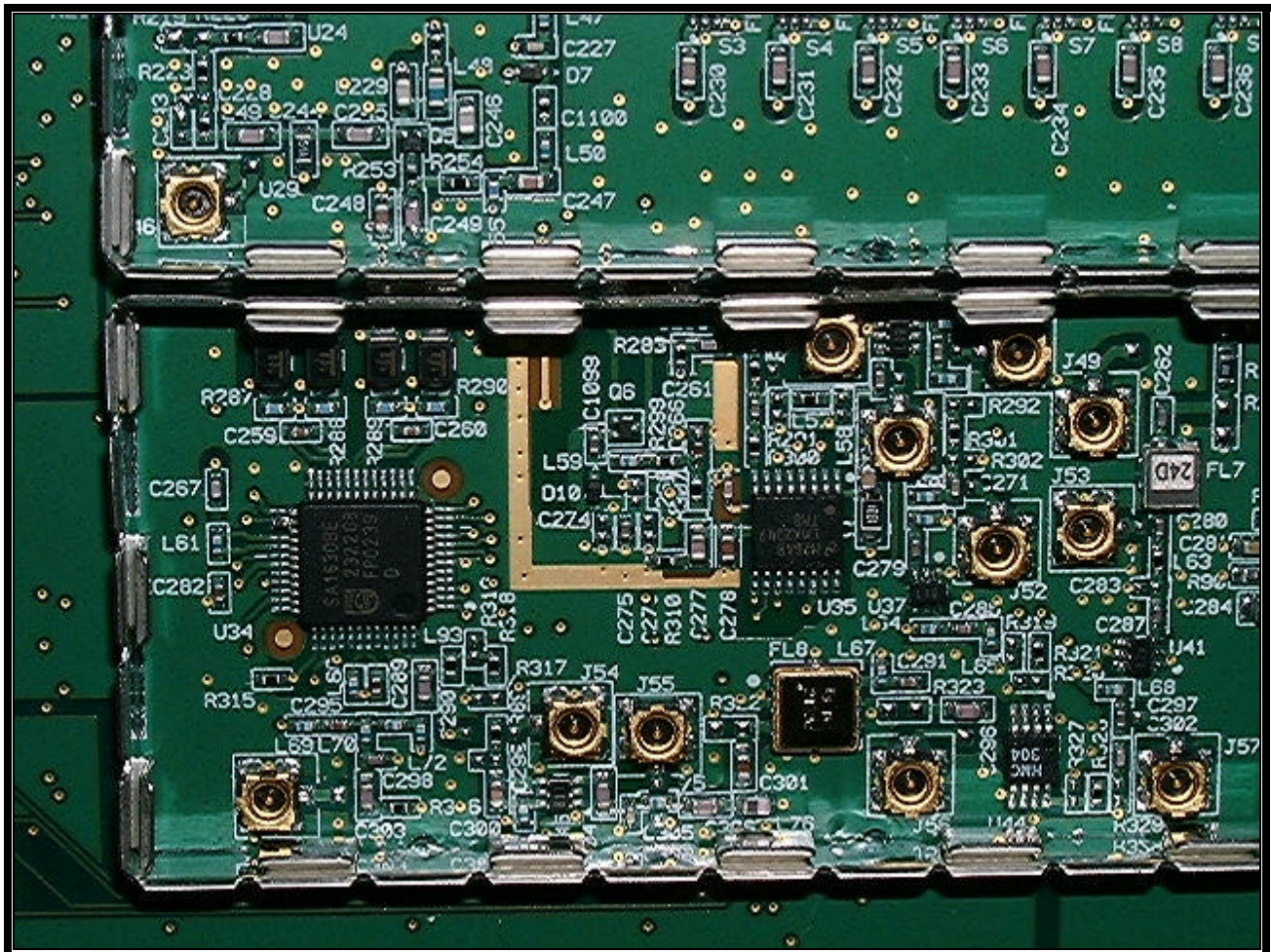
PHOTOGRAPH 51: SIGNAL PROCESSING BOARD PCI BRIDGE CHIP & PCMCIA BRIDGE CHIP



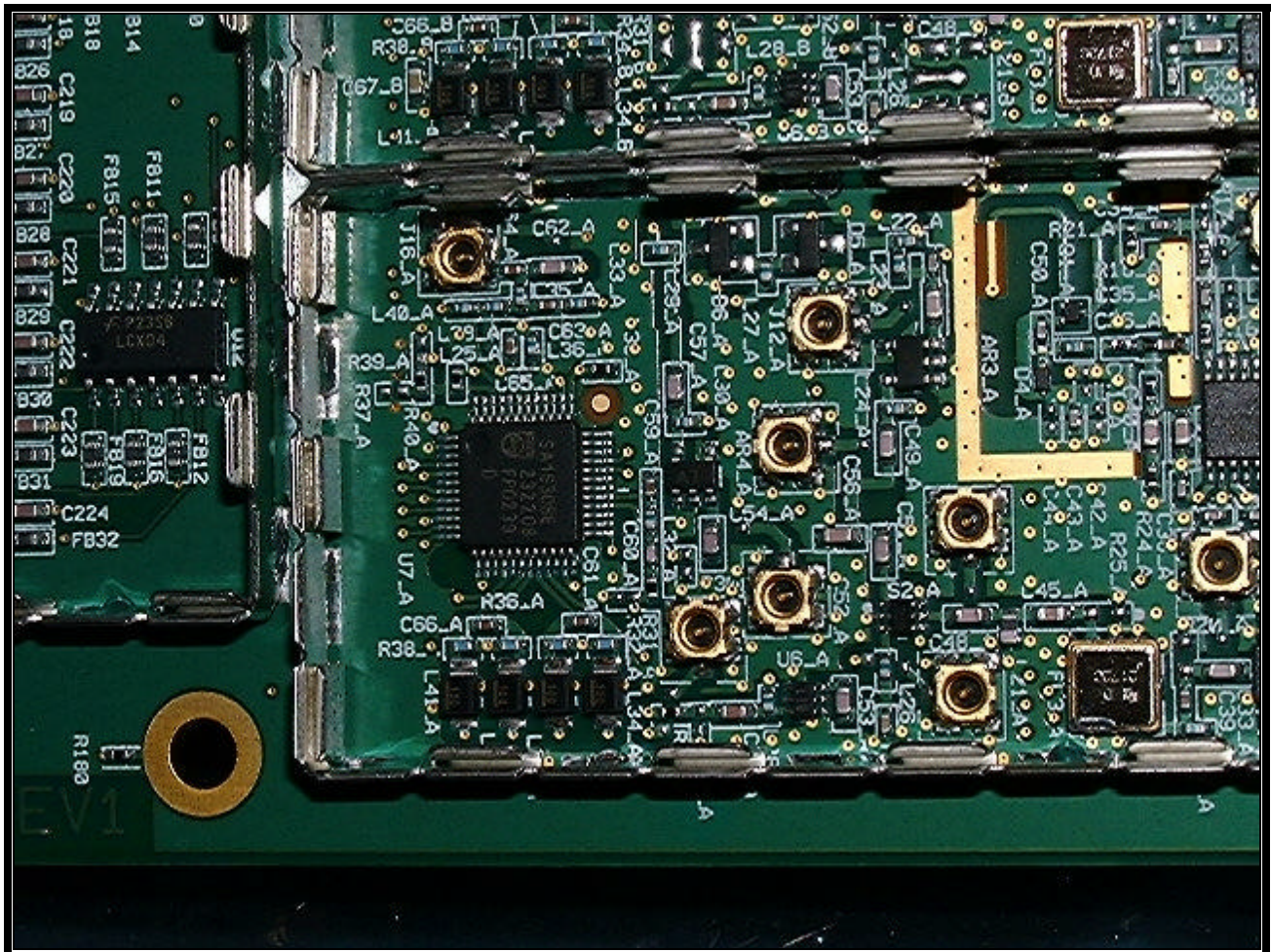
PHOTOGRAPH 52: SIGNAL PROCESSING BOARD RF CONNECTORS



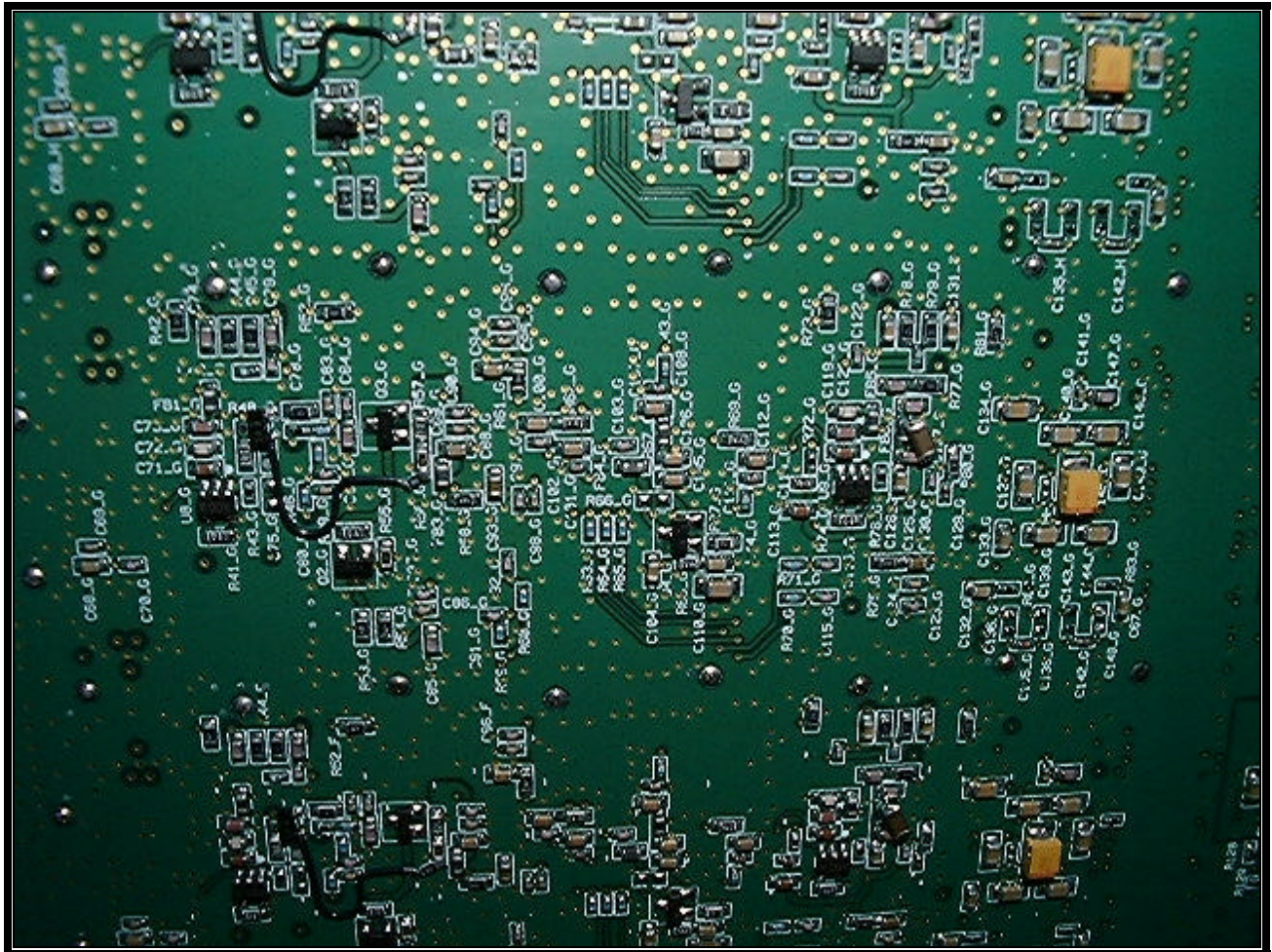
PHOTOGRAPH 53: SIGNAL PROCESSING CIRCUIT



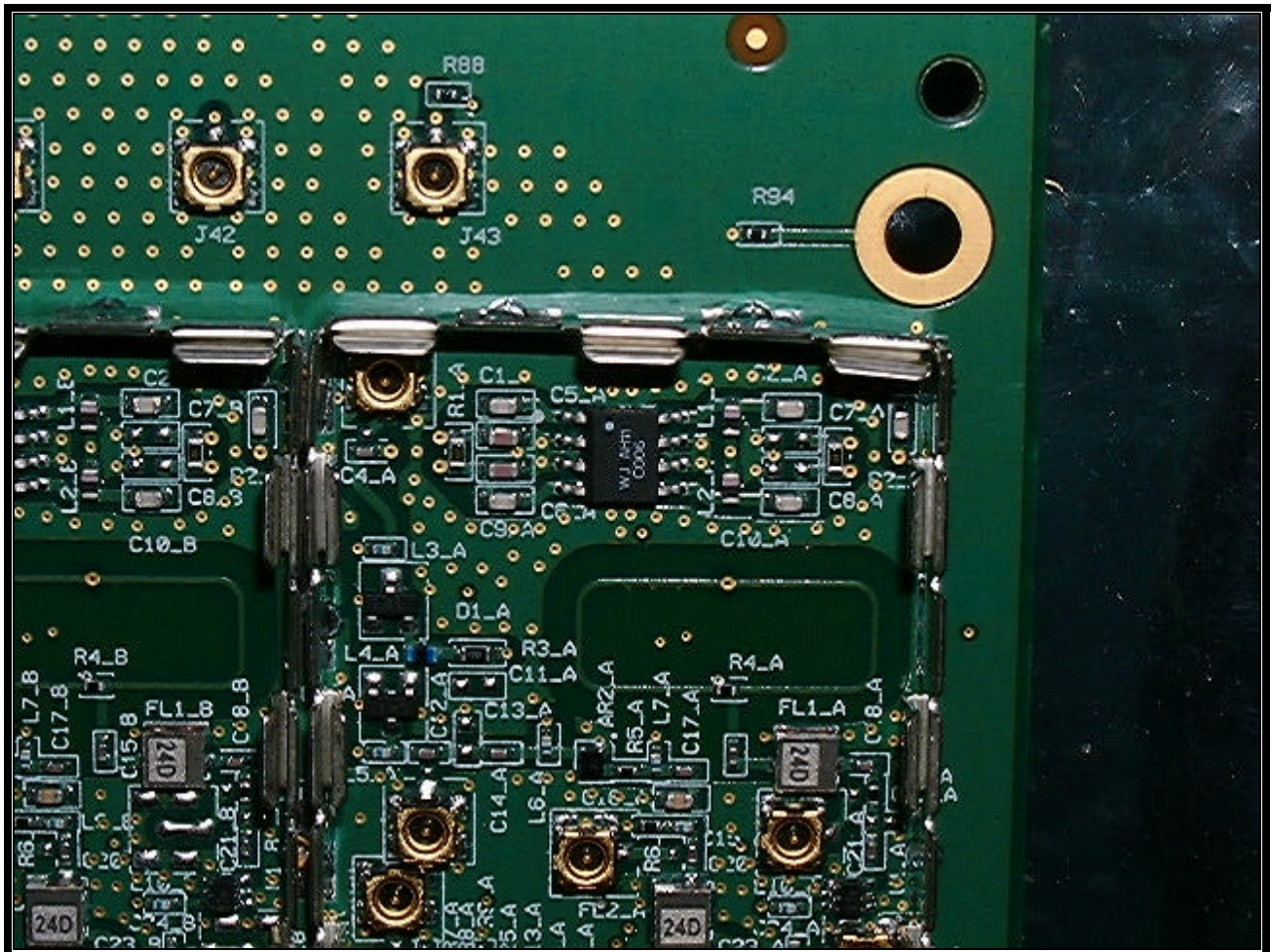
PHOTOGRAPH 54: SIGNAL PROCESSING CIRCUIT 1



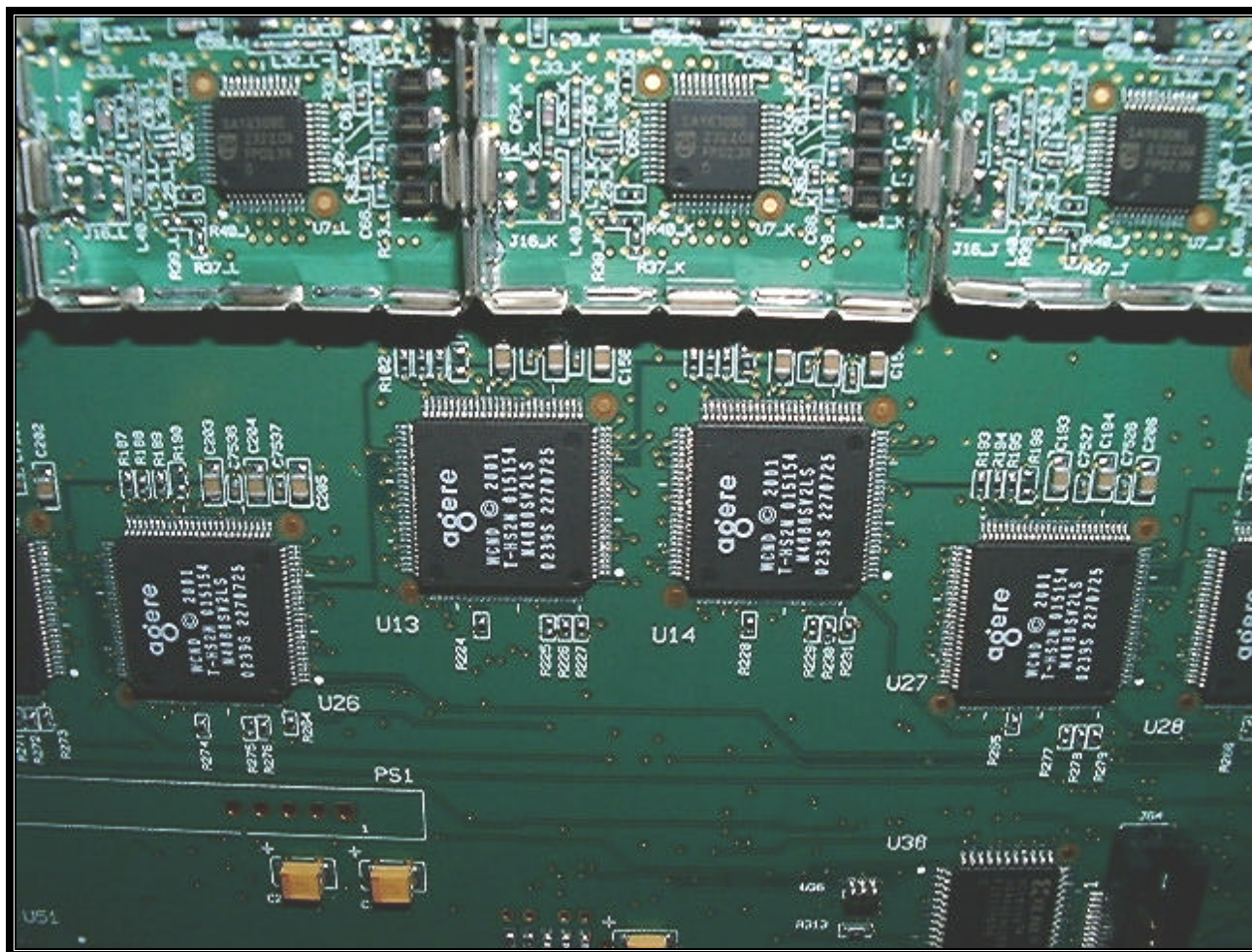
PHOTOGRAPH 55: SIGNAL PROCESSING CIRCUIT 2

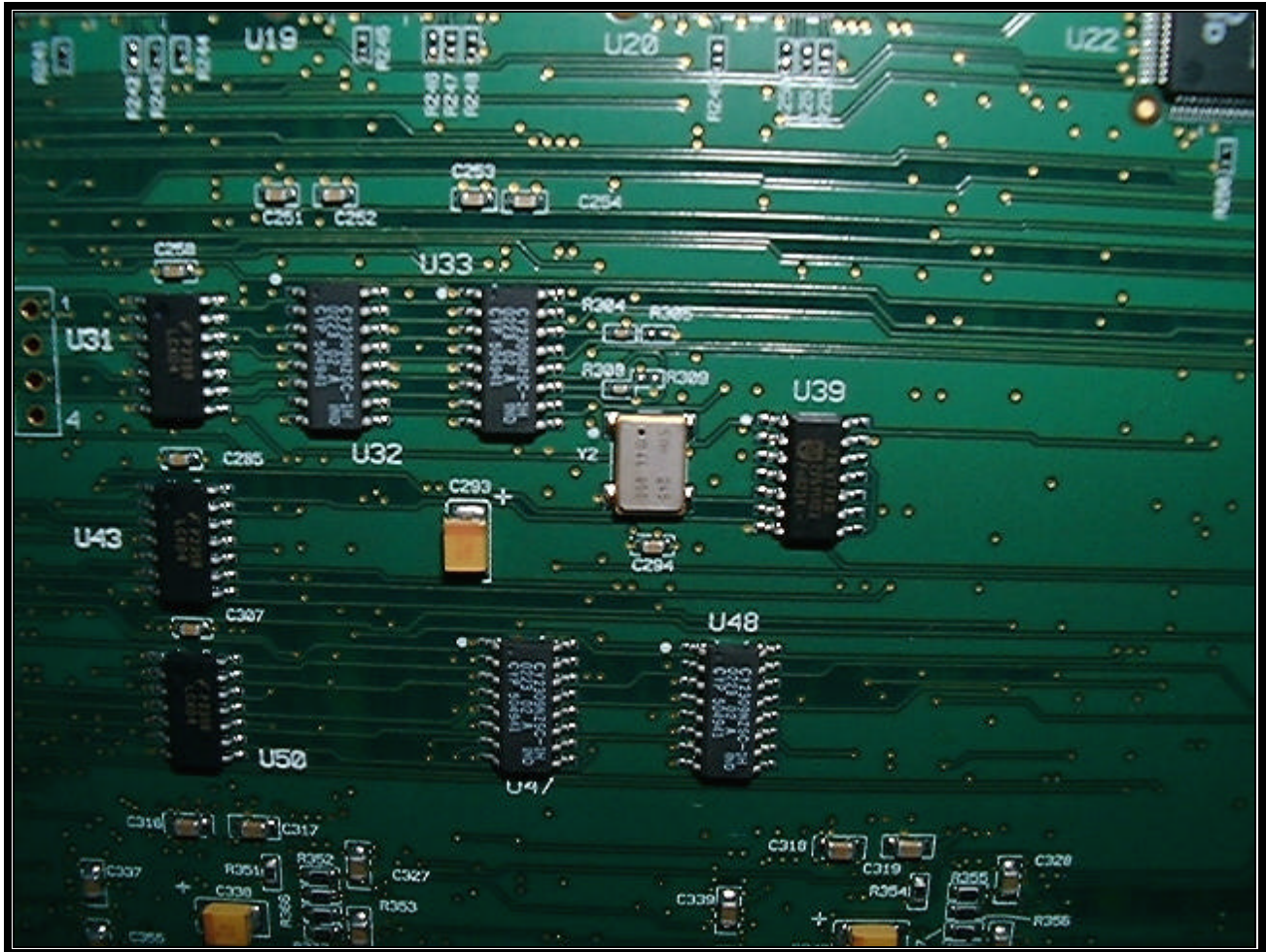


PHOTOGRAPH 56: REAR VIEW OF SIGNAL PROCESSOR CIRCUIT

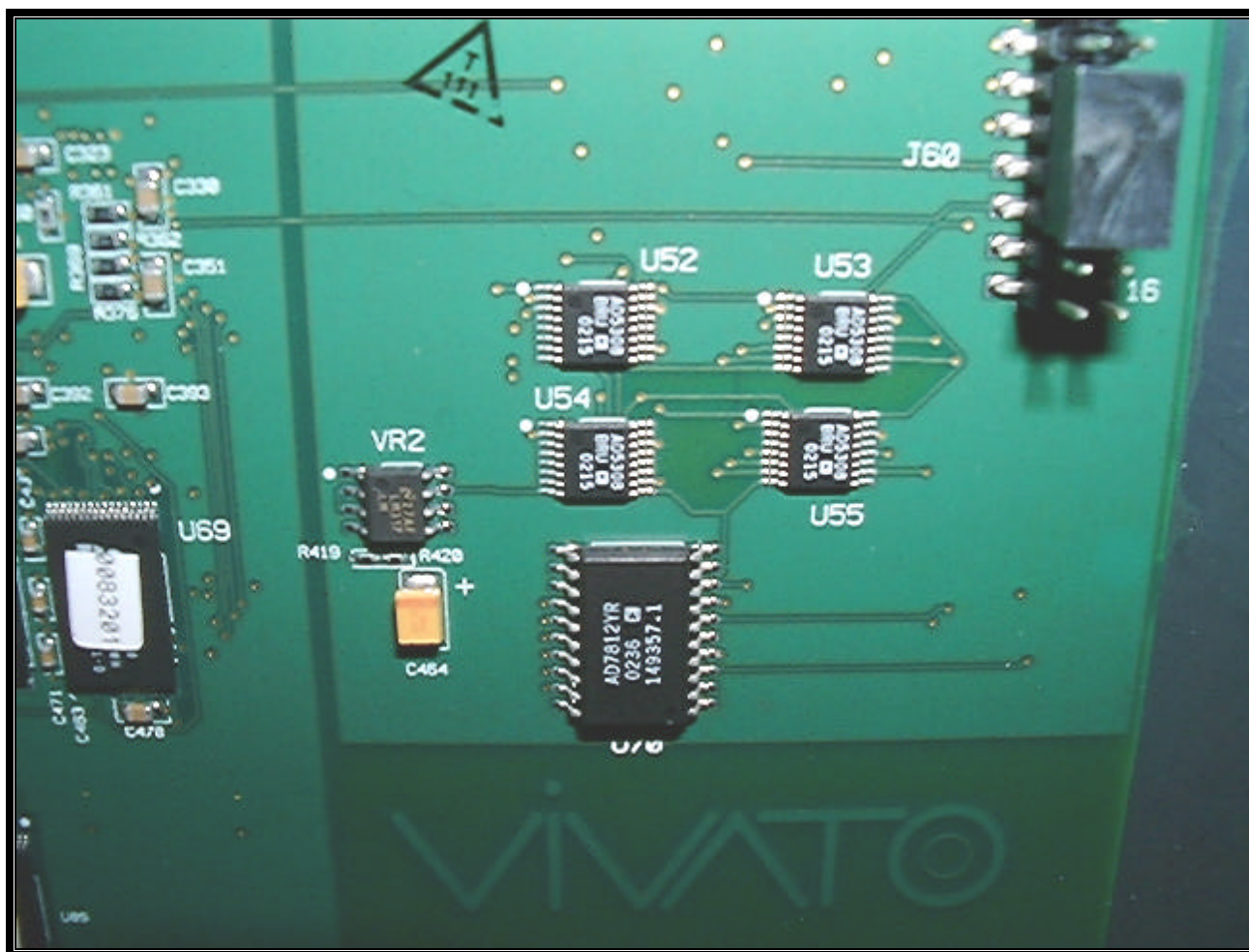


PHOTOGRAPH 57: SIGNAL PROCESSOR PA

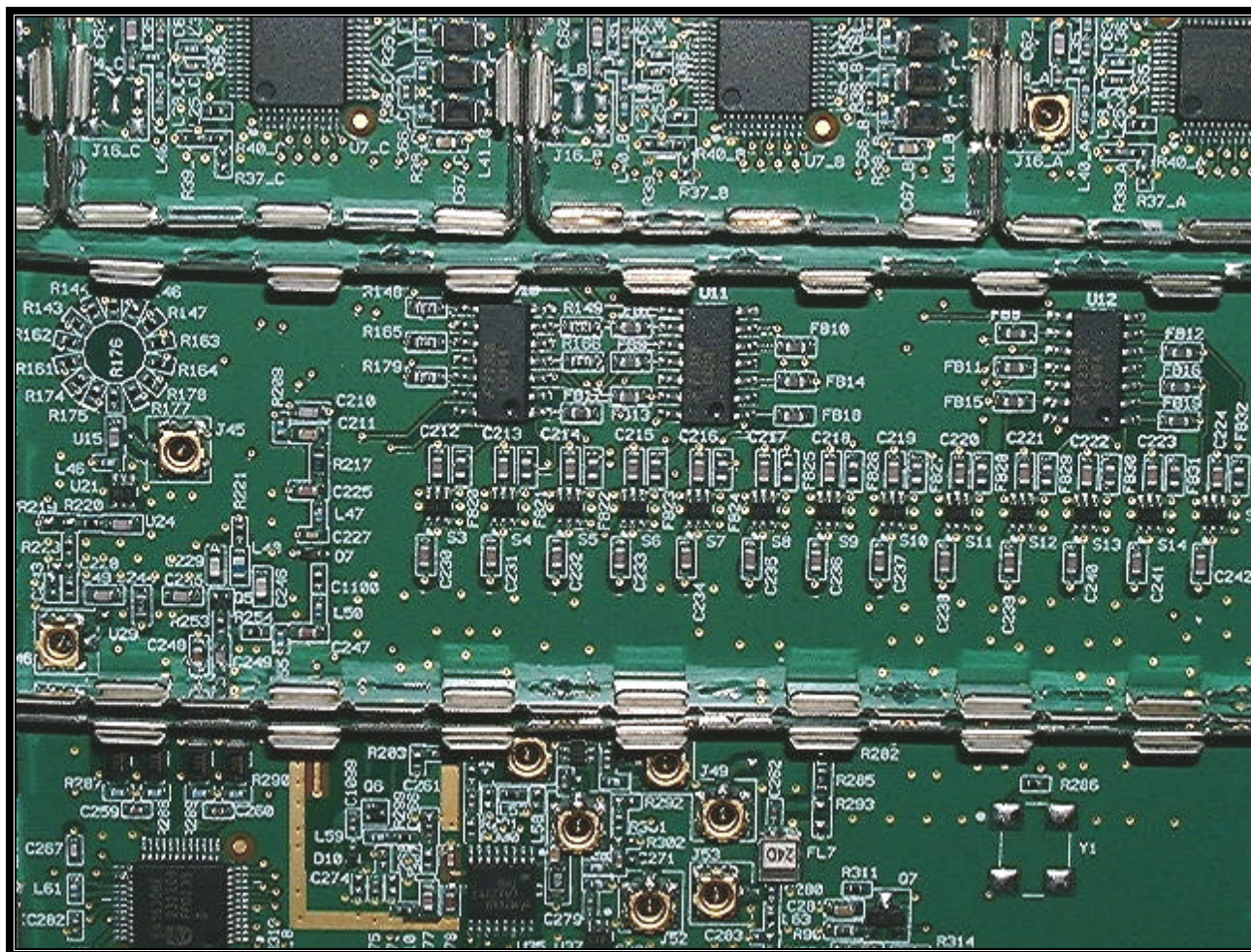




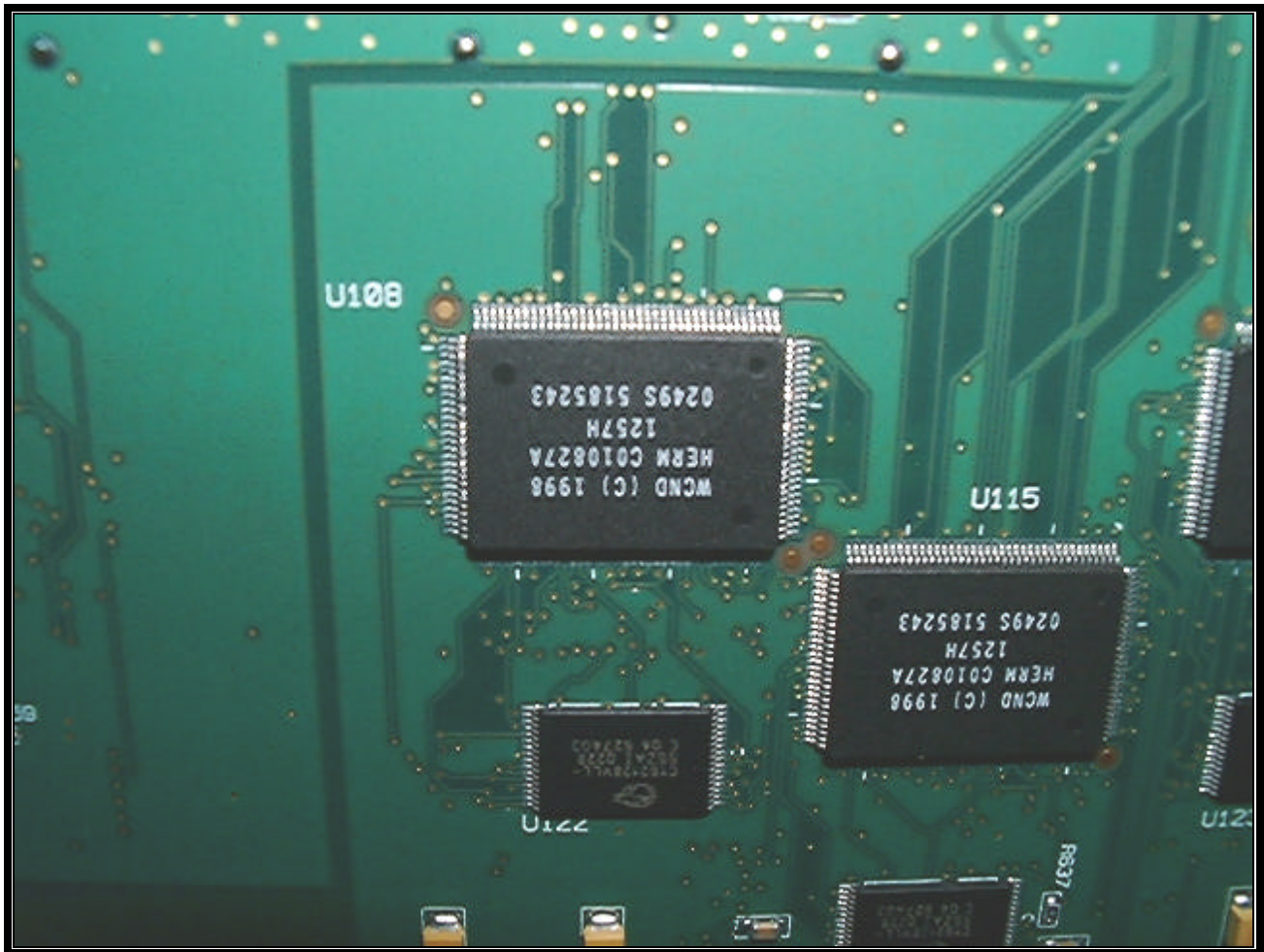
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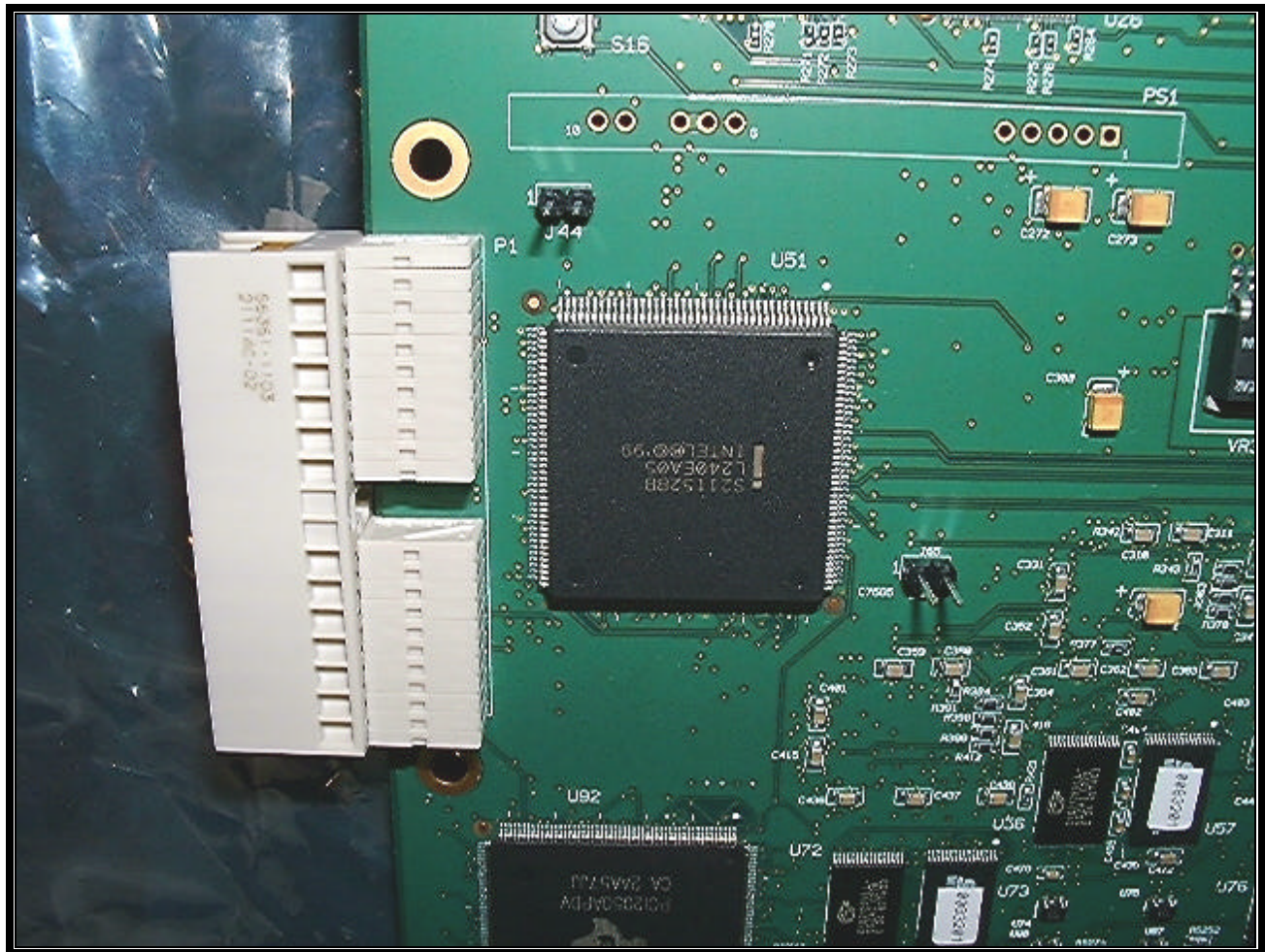
PHOTOGRAPH 60: DAC'S AND ADC



PHOTOGRAPH 61: IF VCO AND SCANNING SWITCHES



PHOTOGRAPH 62: MAC CHIP AND FLASH



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