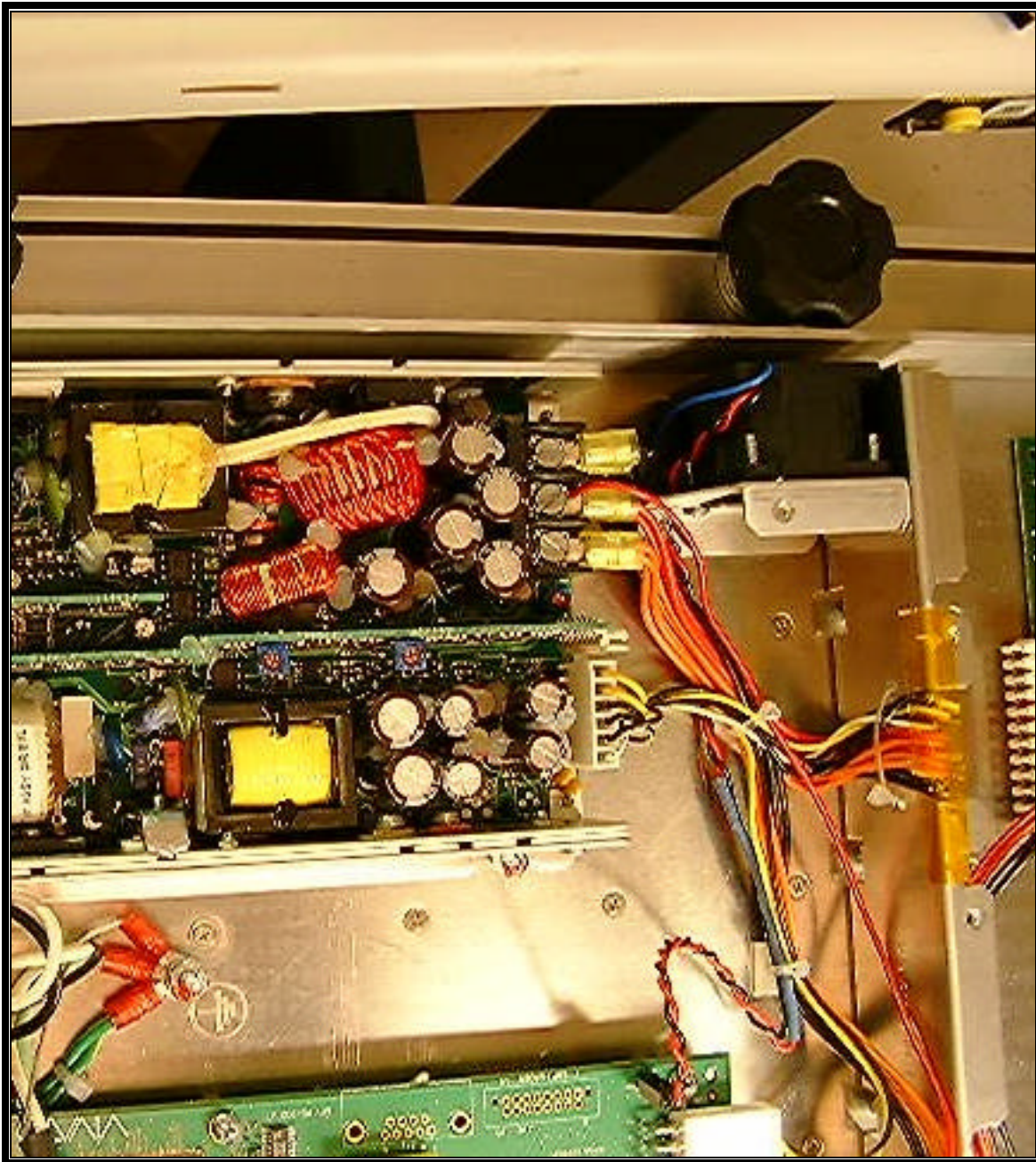
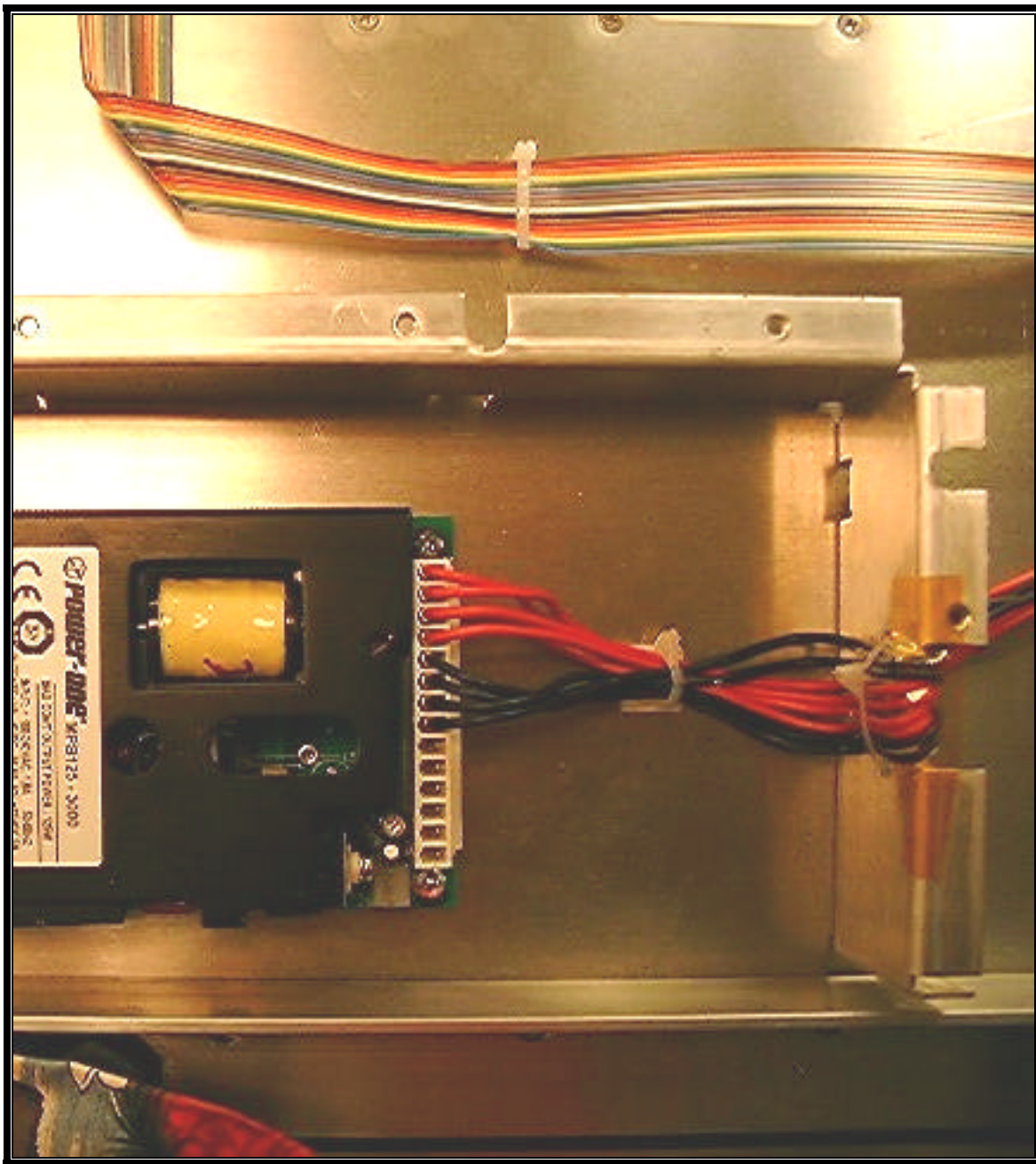
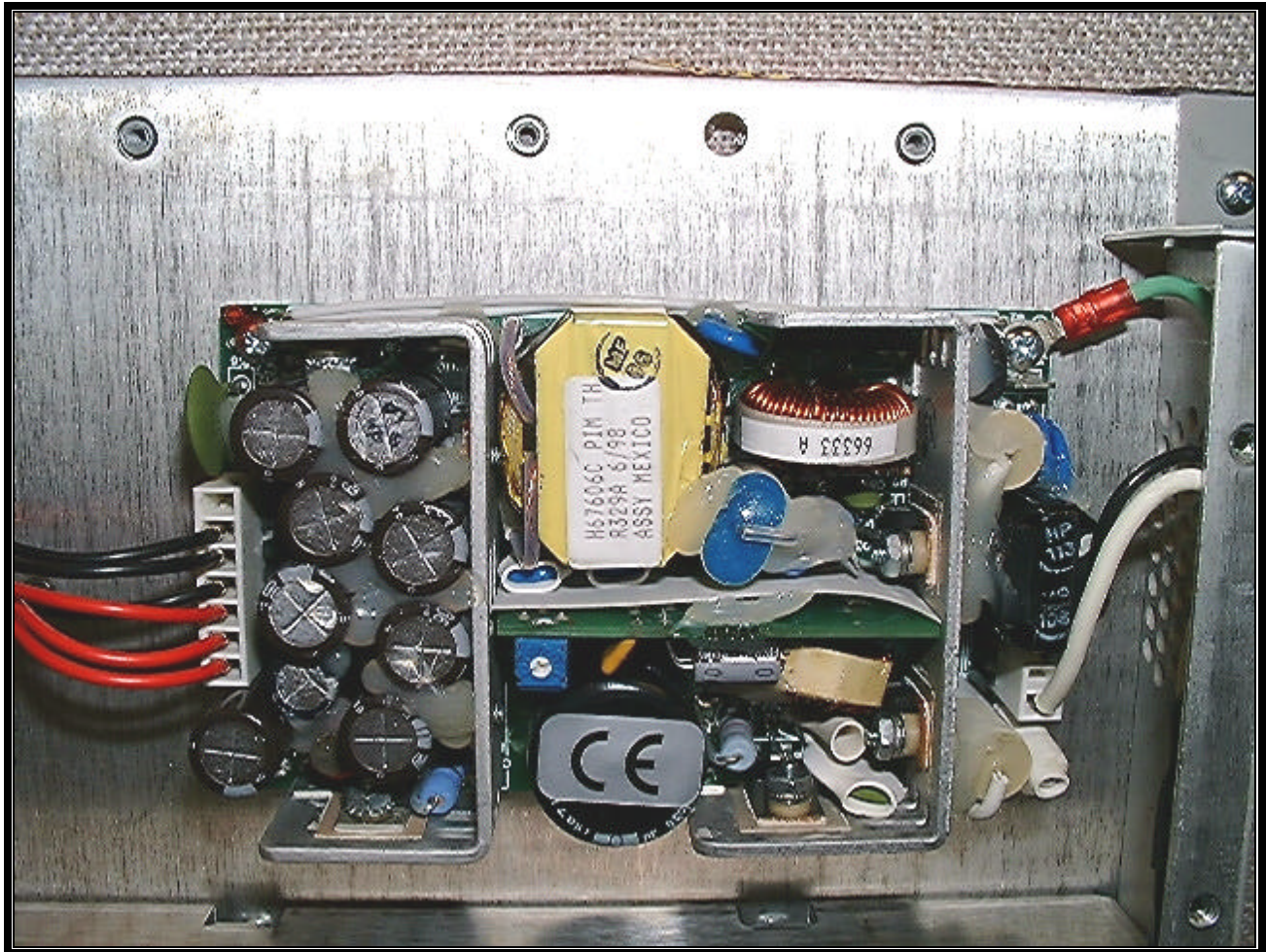




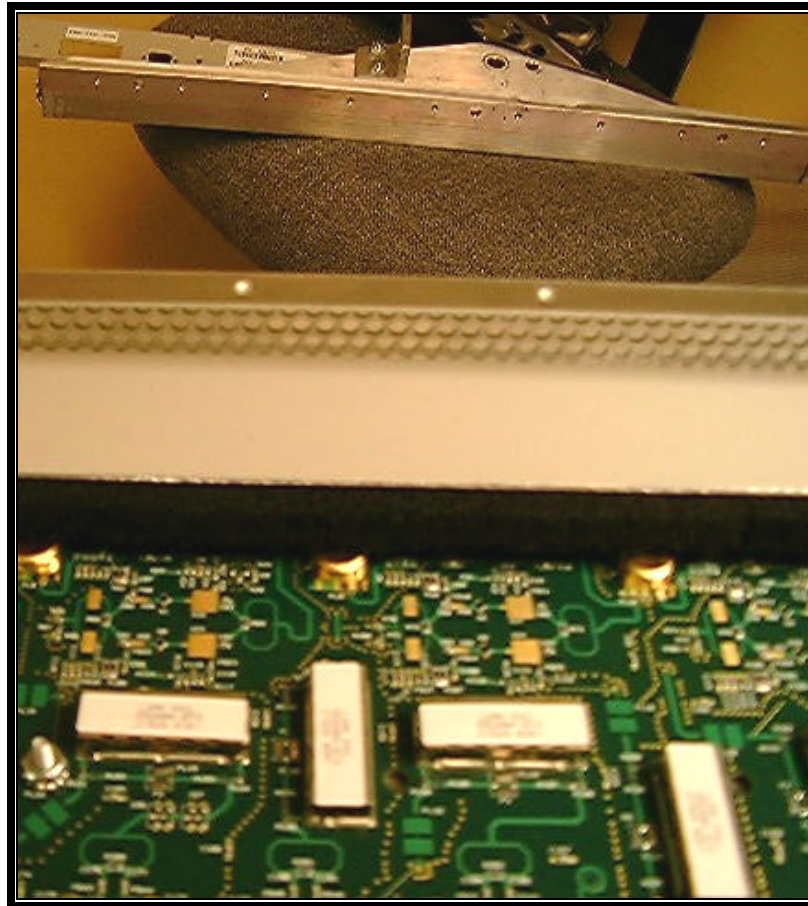
PHOTOGRAPH 64: MAIN POWER SUPPLY, LINE MODULE, AND SECONDARY POWER SUPPLY



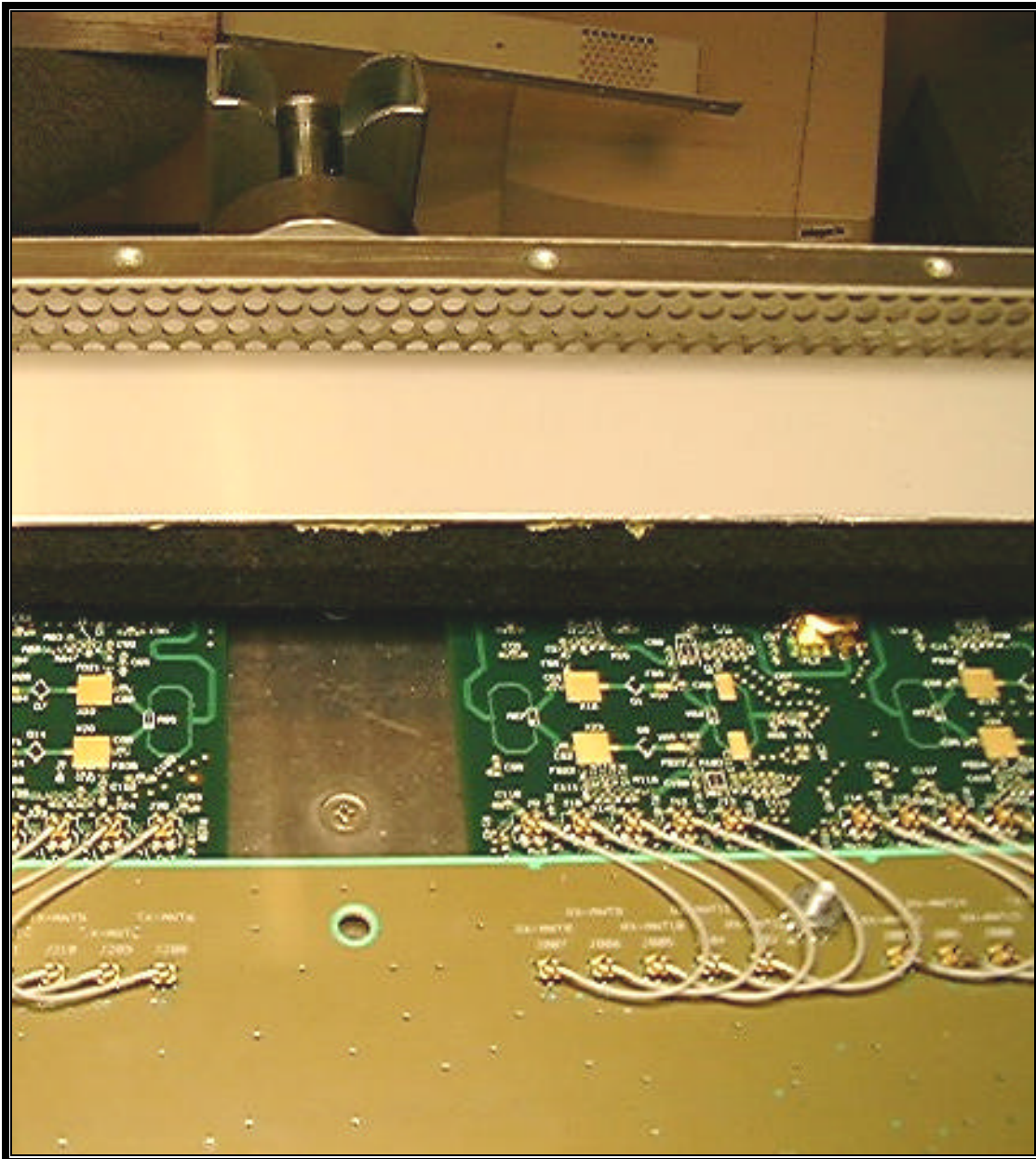
PHOTOGRAPH 65: SECONDARY POWER SUPPLY



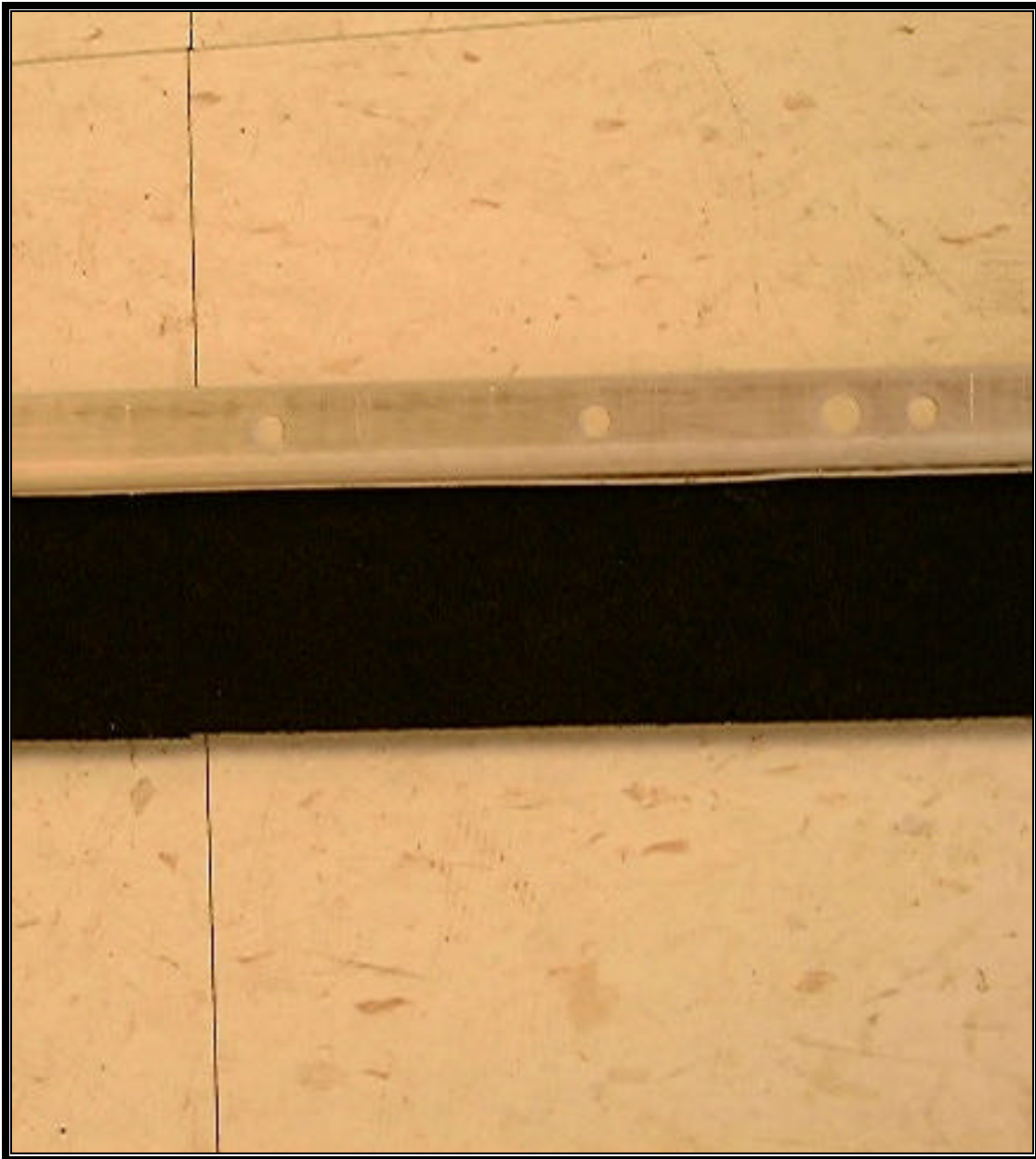
PHOTOGRAPH 66: ALTERNATE SECONDARY POWER SUPPLY



PHOTOGRAPH 67: INTERNAL BRACKET WITH ABSORBING FOAM-LEFT HALF



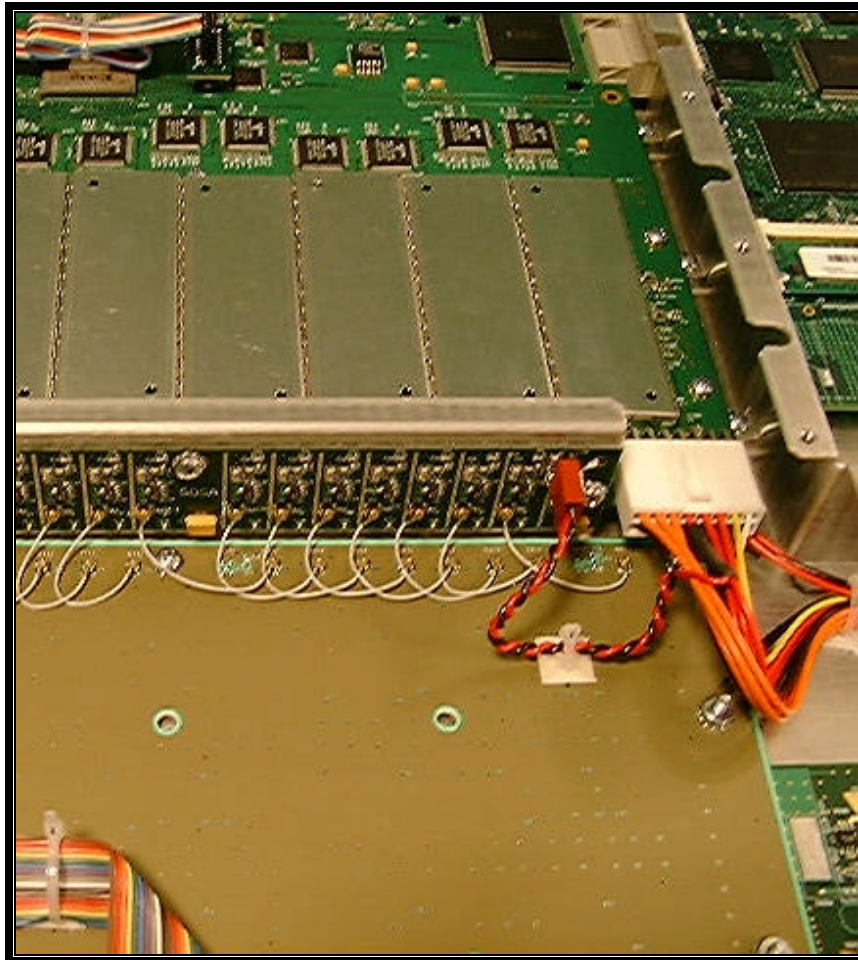
PHOTOGRAPH 68: INTERNAL BRACKET WITH ABSORBING FOAM-RIGHT HALF



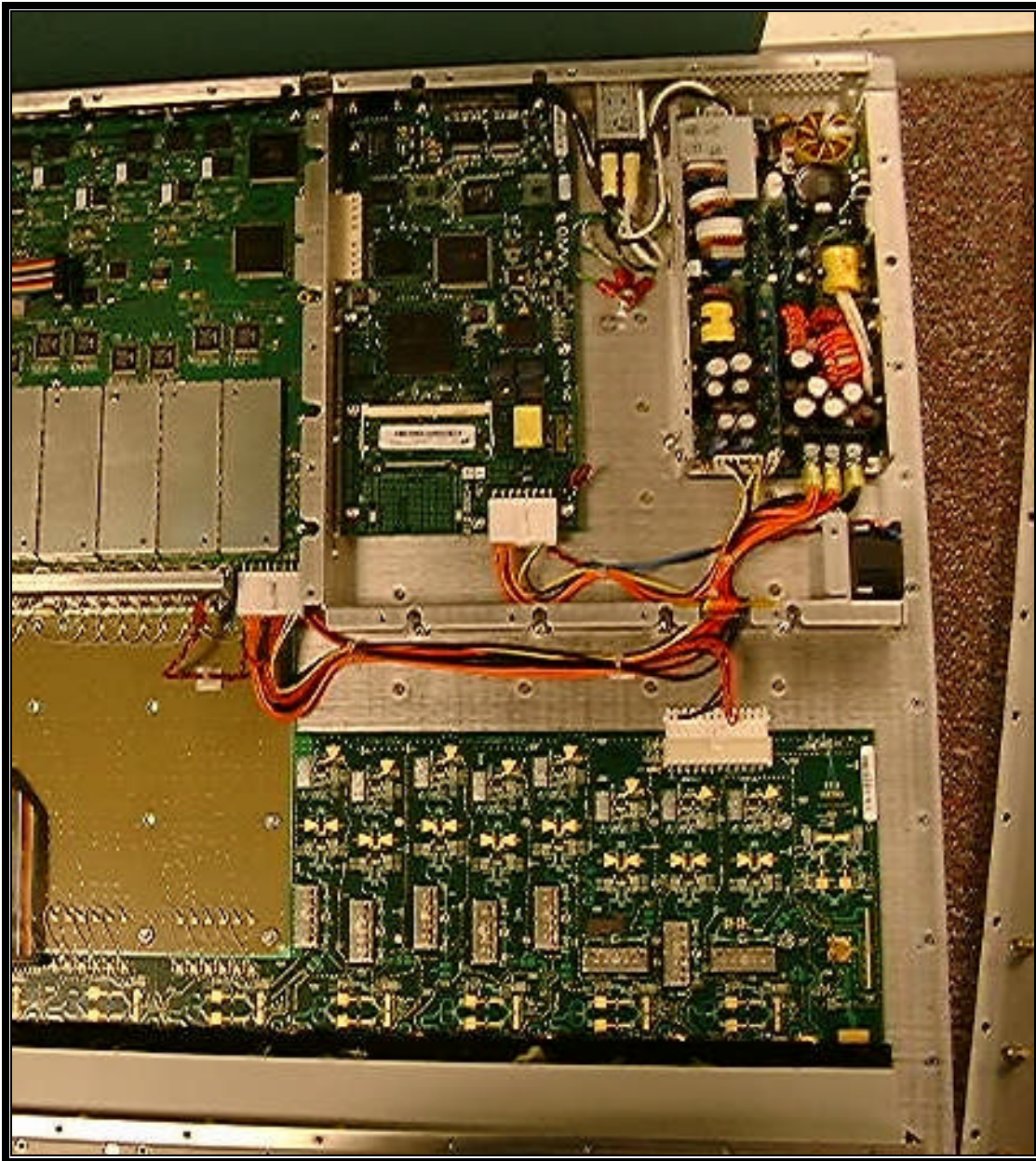
PHOTOGRAPH 69: INTERNAL BRACKET FOAM SIDE-LEFT HALF



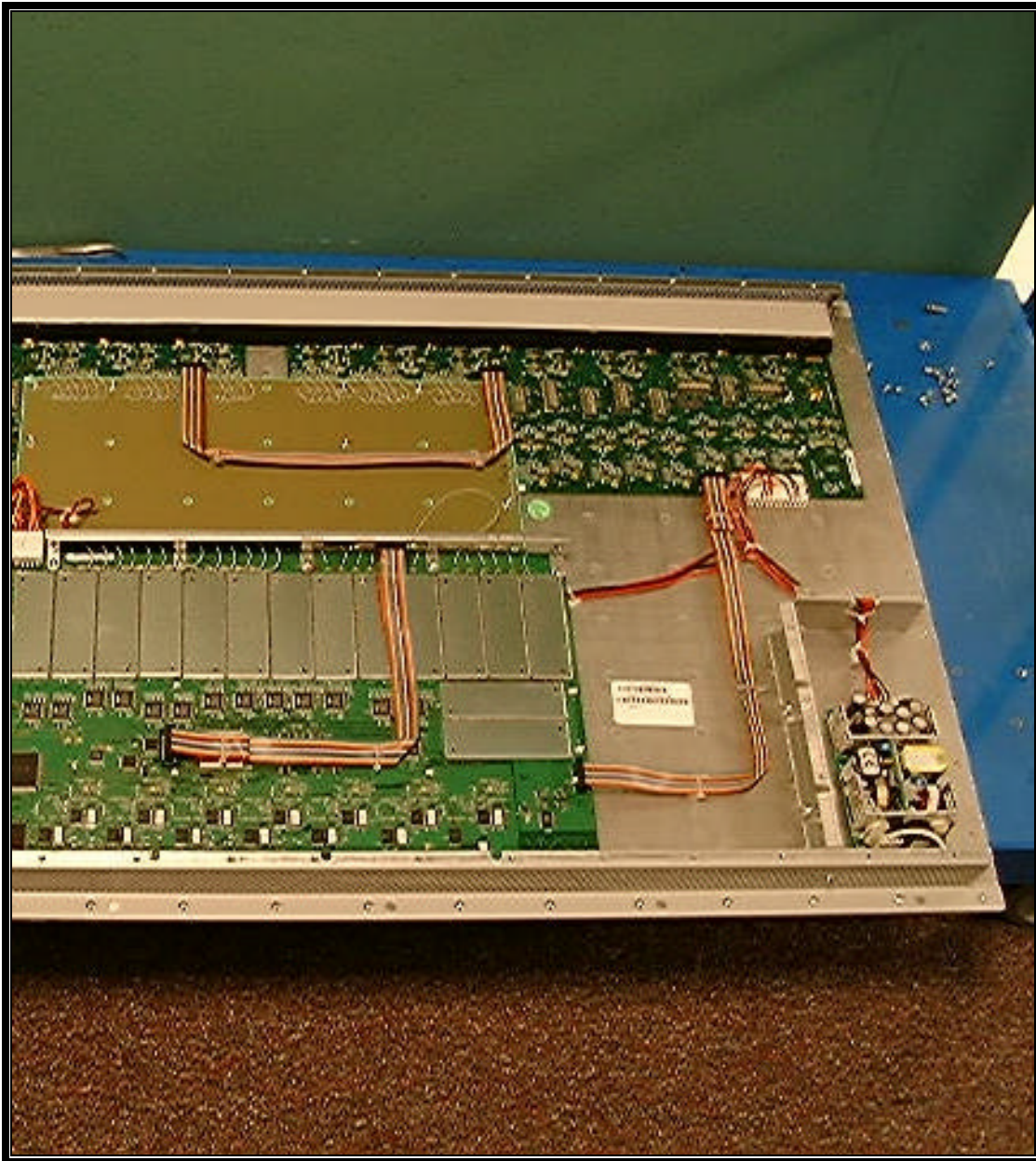
PHOTOGRAPH 70: INTERNAL BRACKET FOAM SIDE-RIGHT HALF



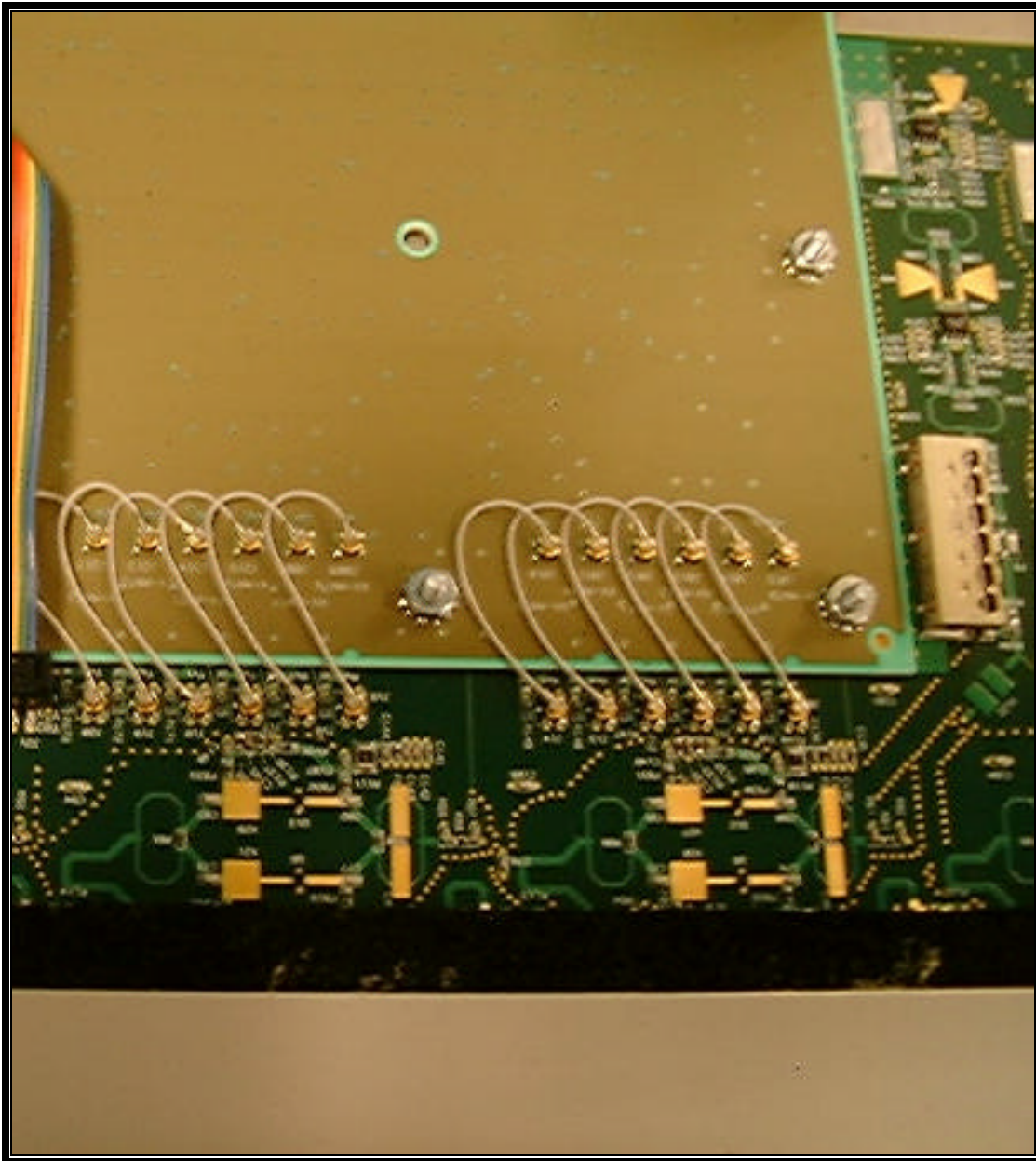
PHOTOGRAPH 71: SOSA BOARD CONNECTION BETWEEN SIGNAL PROCESSING BOARD AND ALFRED BOARD, AND POWER CONNECTIONS



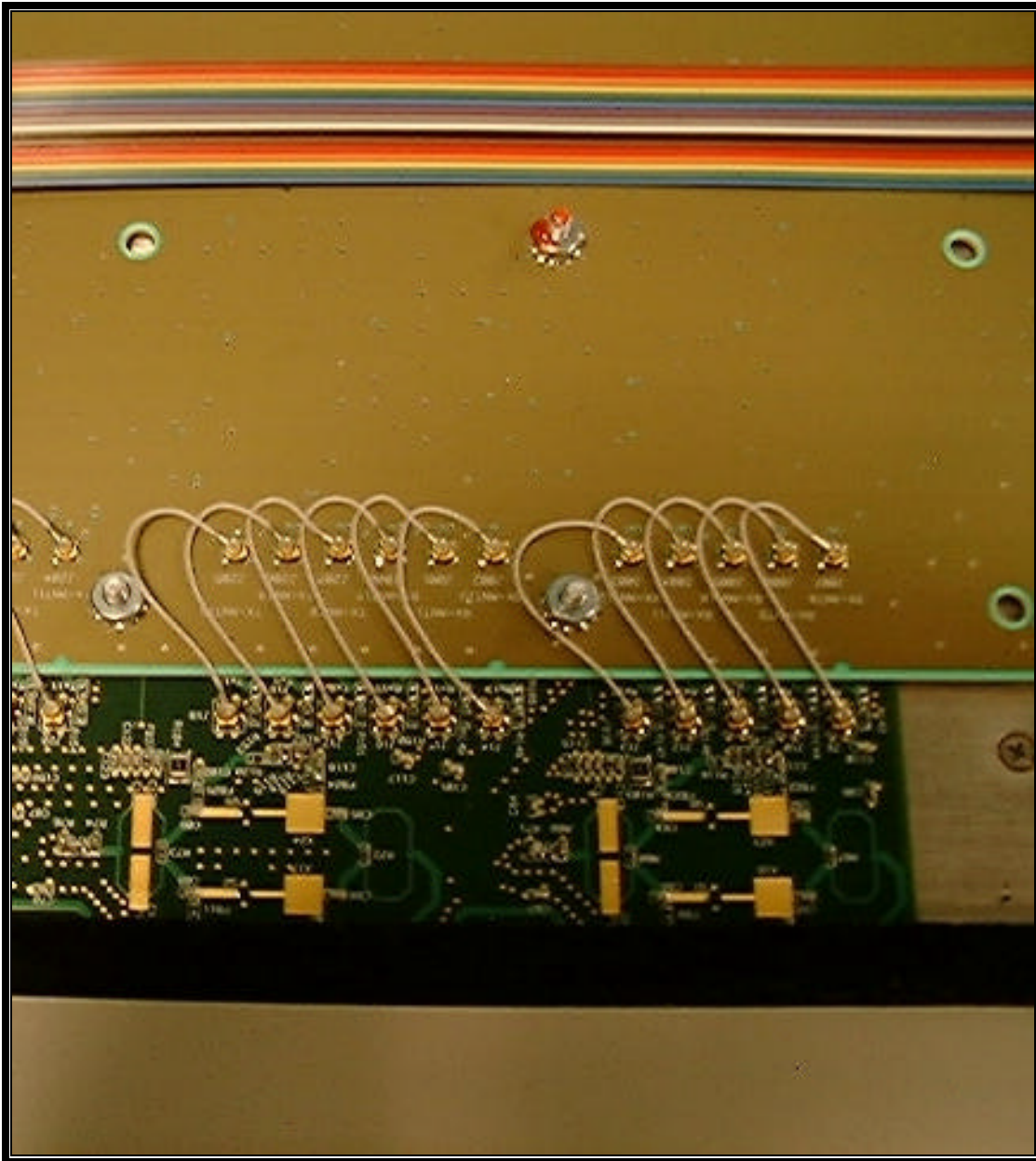
PHOTOGRAPH 72: LEFT HALF INTERIOR OF WI-FI SWITCH



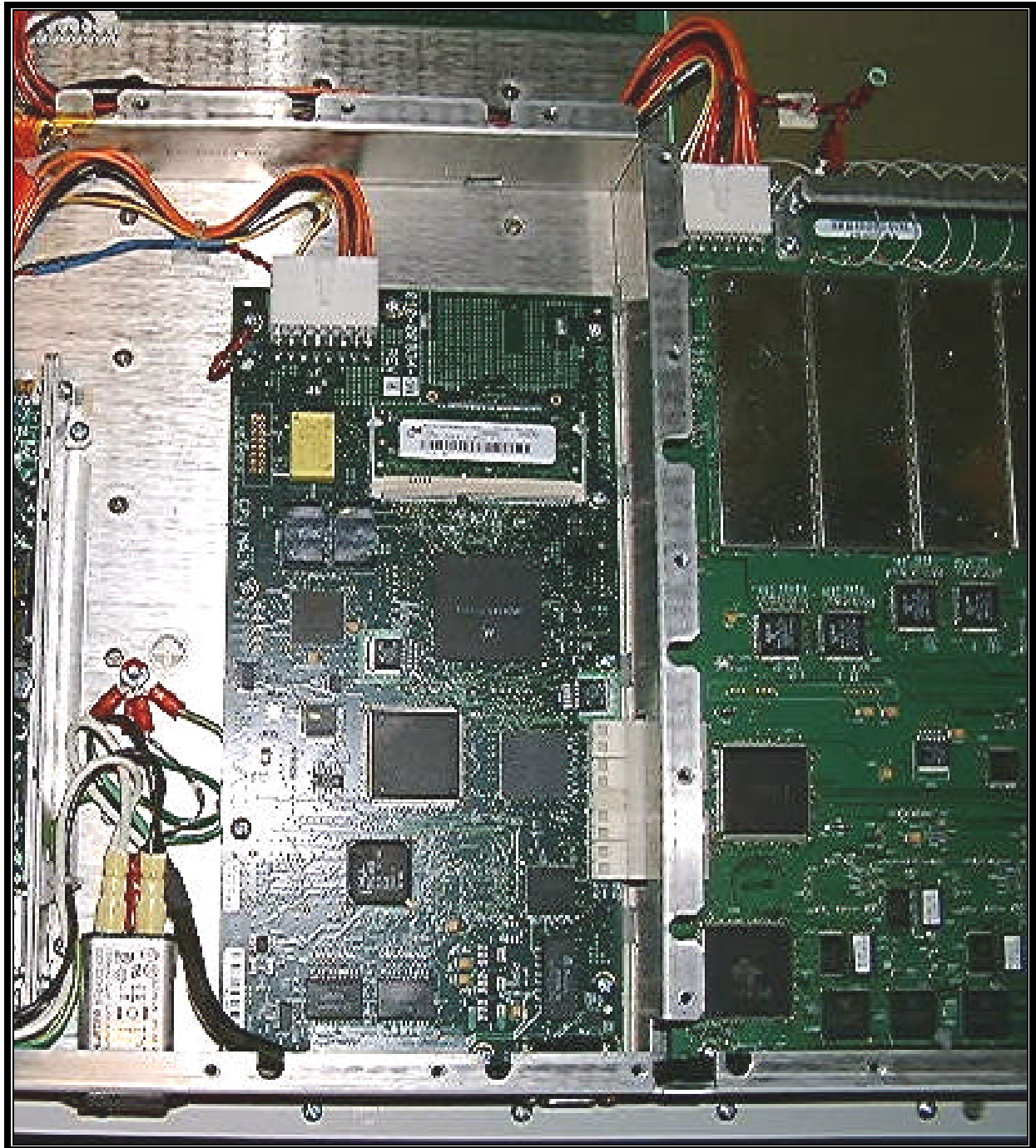
PHOTOGRAPH 73: RIGHT HALF INTERIOR OF WI-FI SWITCH



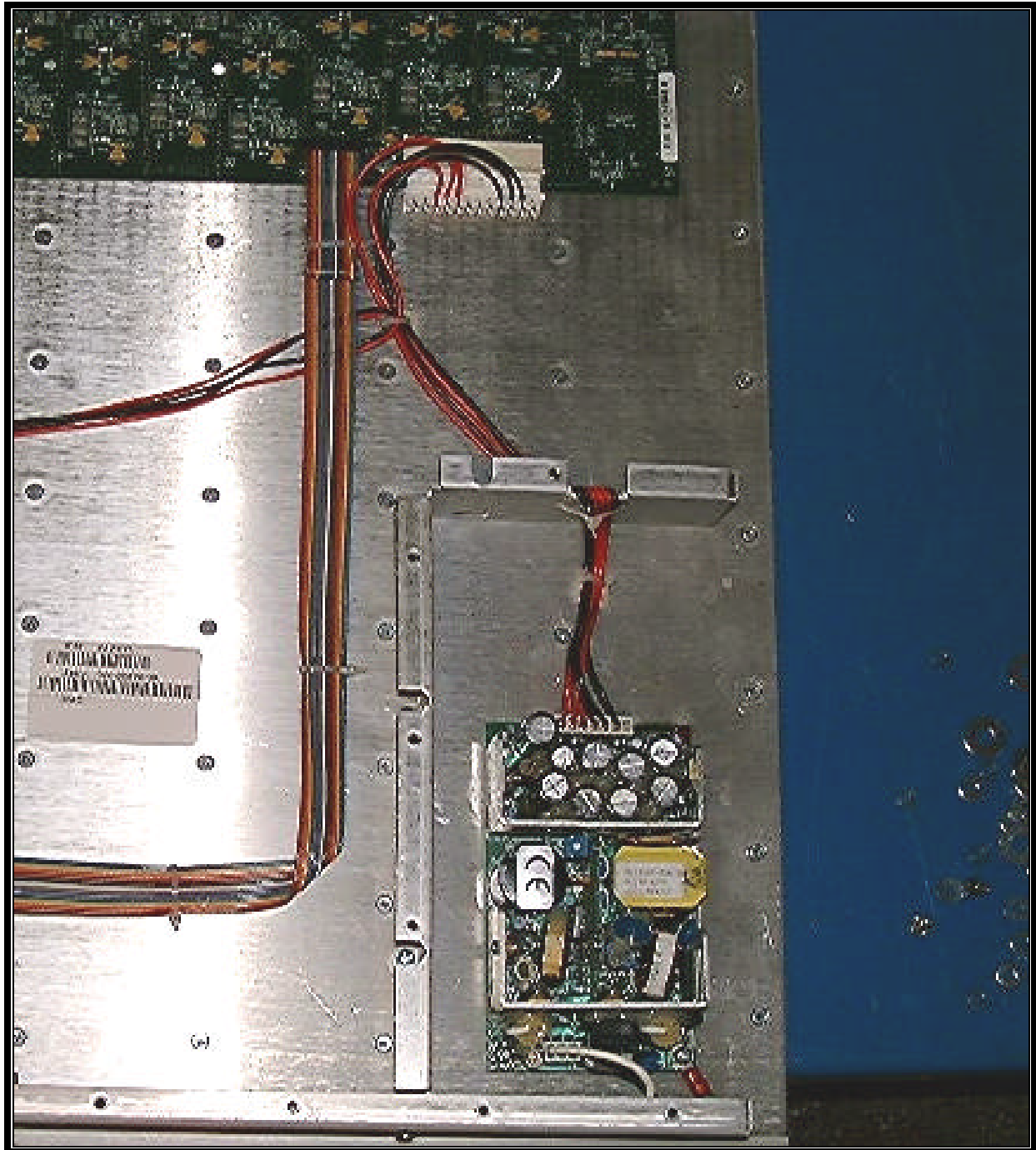
PHOTOGRAPH 74: CONNECTIONS BETWEEN ALFRED BOARD AND LEFT ANTENNA BOARD



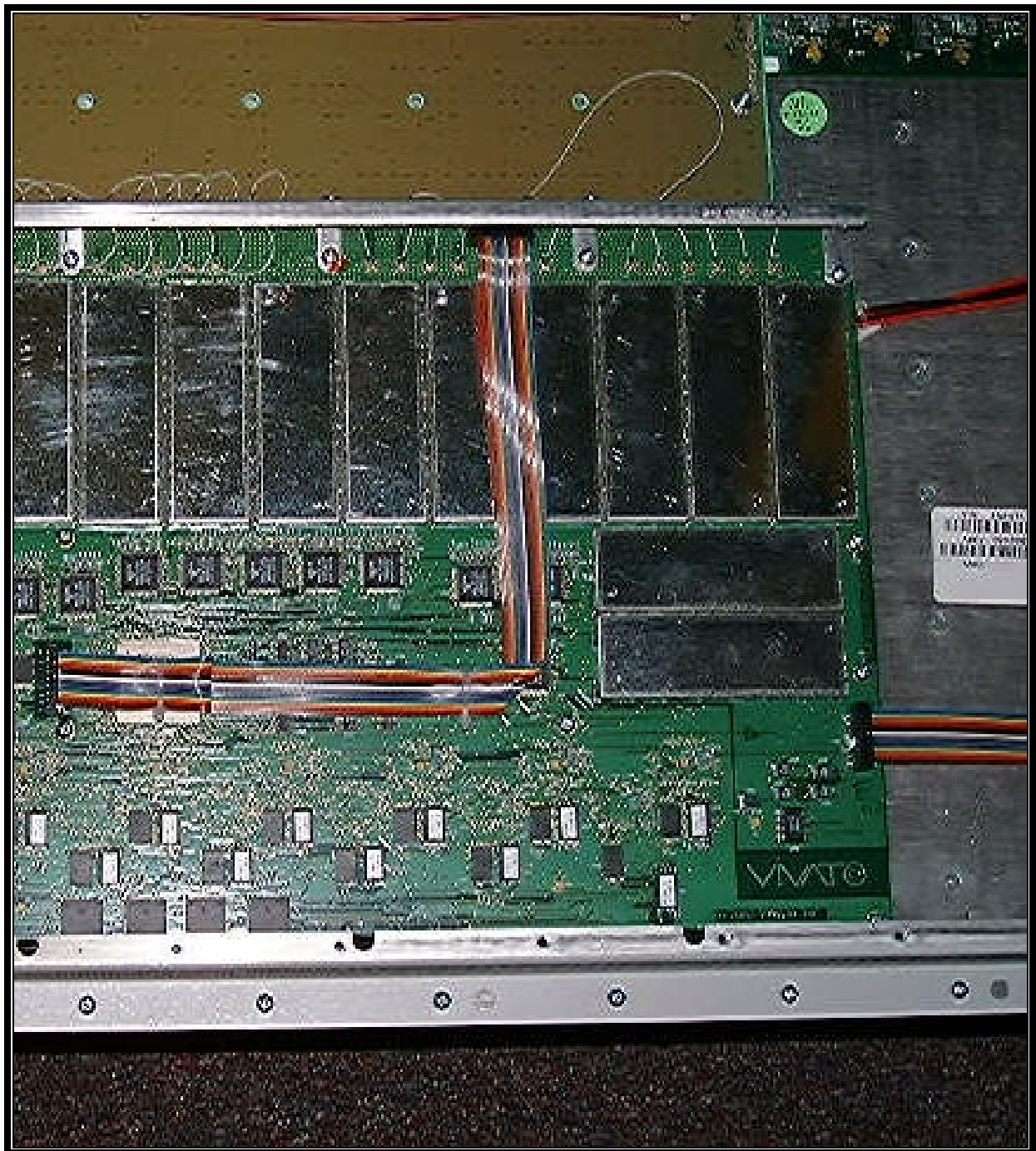
PHOTOGRAPH 75: CONNECTIONS BETWEEN ALFRED BOARD AND RIGHT ANTENNA BOARD



PHOTOGRAPH 76: INTERIOR CONNECTION OF THE CPU BOARD AND THE SIGNAL PROCESSING BOARD



PHOTOGRAPH 77: ALTERNATIVE SECONDARY POWER SUPPLY LOCATION



PHOTOGRAPH 78: CONTROL CABLE BETWEEN FPGA AND SOSA BOARD