

This manual is made as documentation for Danphone's R4 Transceiver used for Saab Transponder Tech's R4 UAIS transponder.

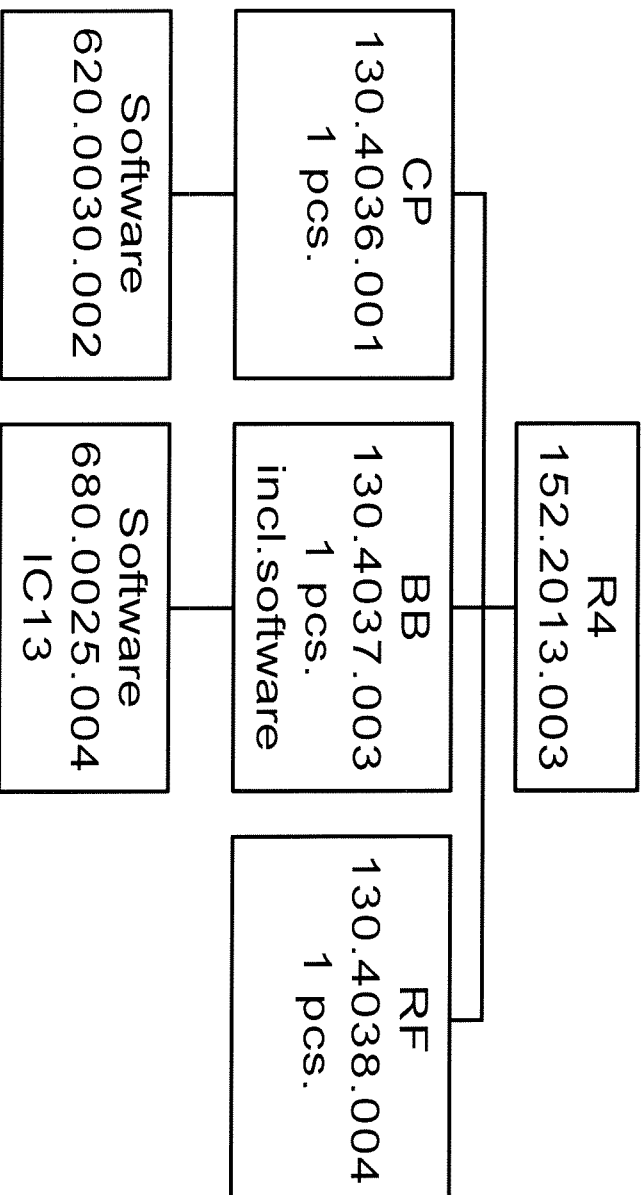
The manual contains the following:

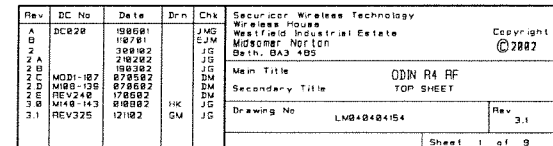
- Modifications on R4 RF version 3
- Overview of the complete transponder (Structure Diagram)
- Diagram for each transceiver module.
- Component placement for each transceiver module.
- Partslist for each transceiver module

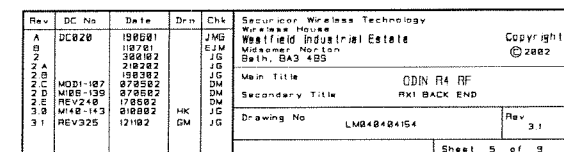
Modification to Documentation.

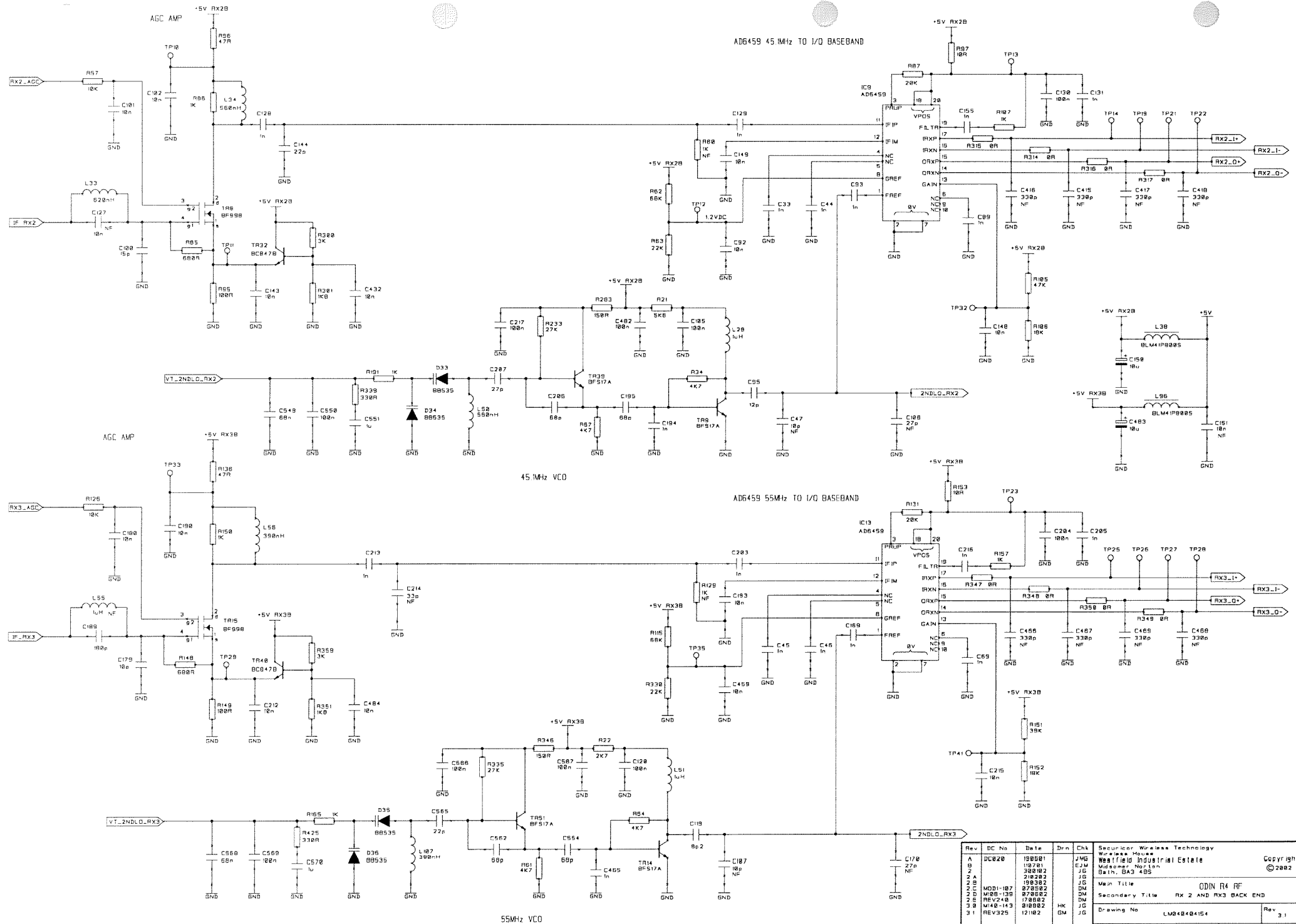
Since approval measurements at Telefication (Telefication Report number 99368530) there is added a gate in the power ramping cicuity. The gate is inserted to turn off the Tx-modulator when the Tx switch is in receiver mode. The gate ensures that the Tx modulator is turned on before power ramp up, and turned off after power ramp down. It is checked that the ramping behavior is un-altered compared to the measurement at Telefication. For this modification a Quad NOR gate is used. In future a single NOR gate shall be implemented in the layout. Also the daughterboard that contains the ramping shall be implemented in the layout.

R4 Structure Diagram



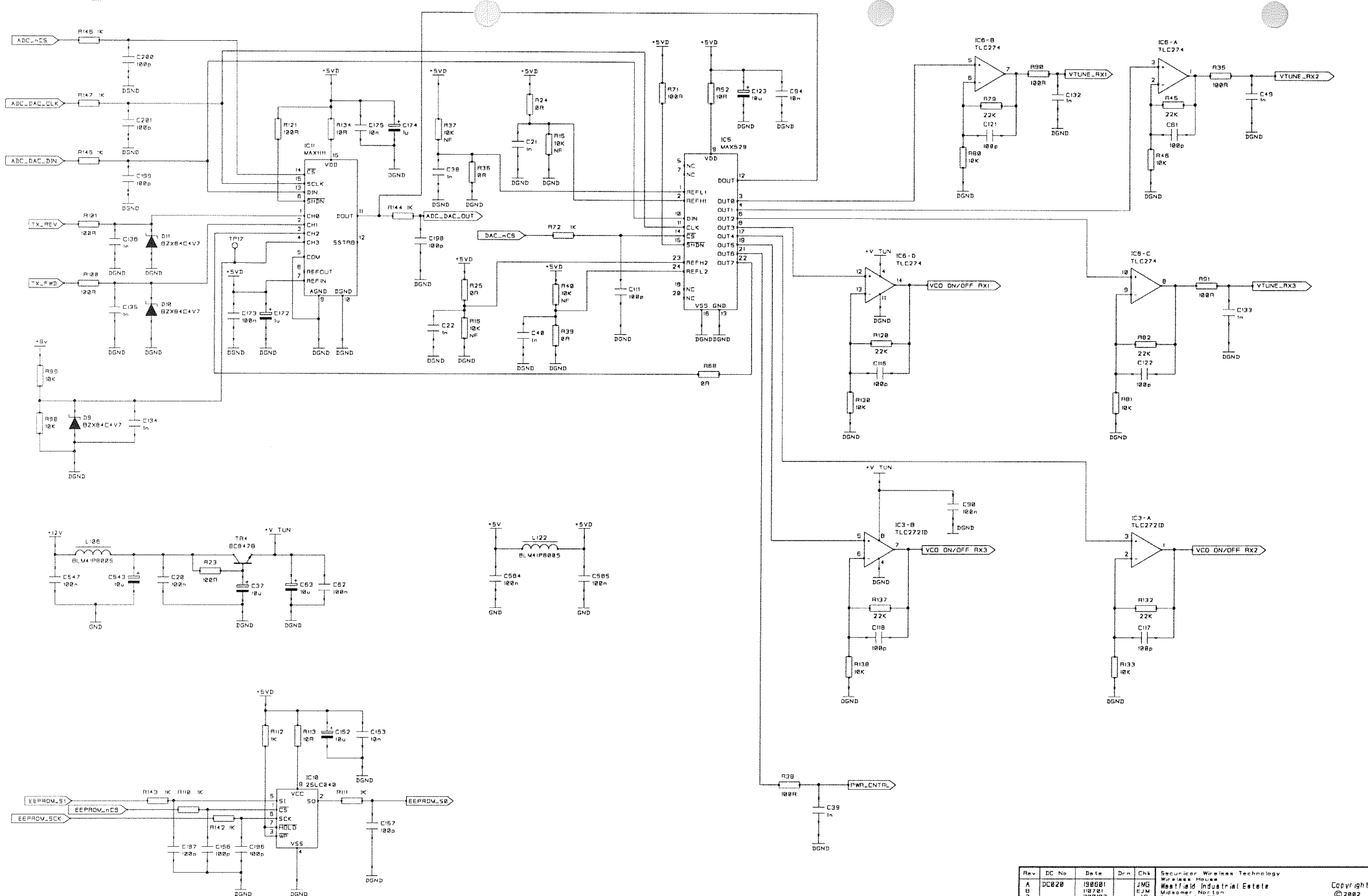


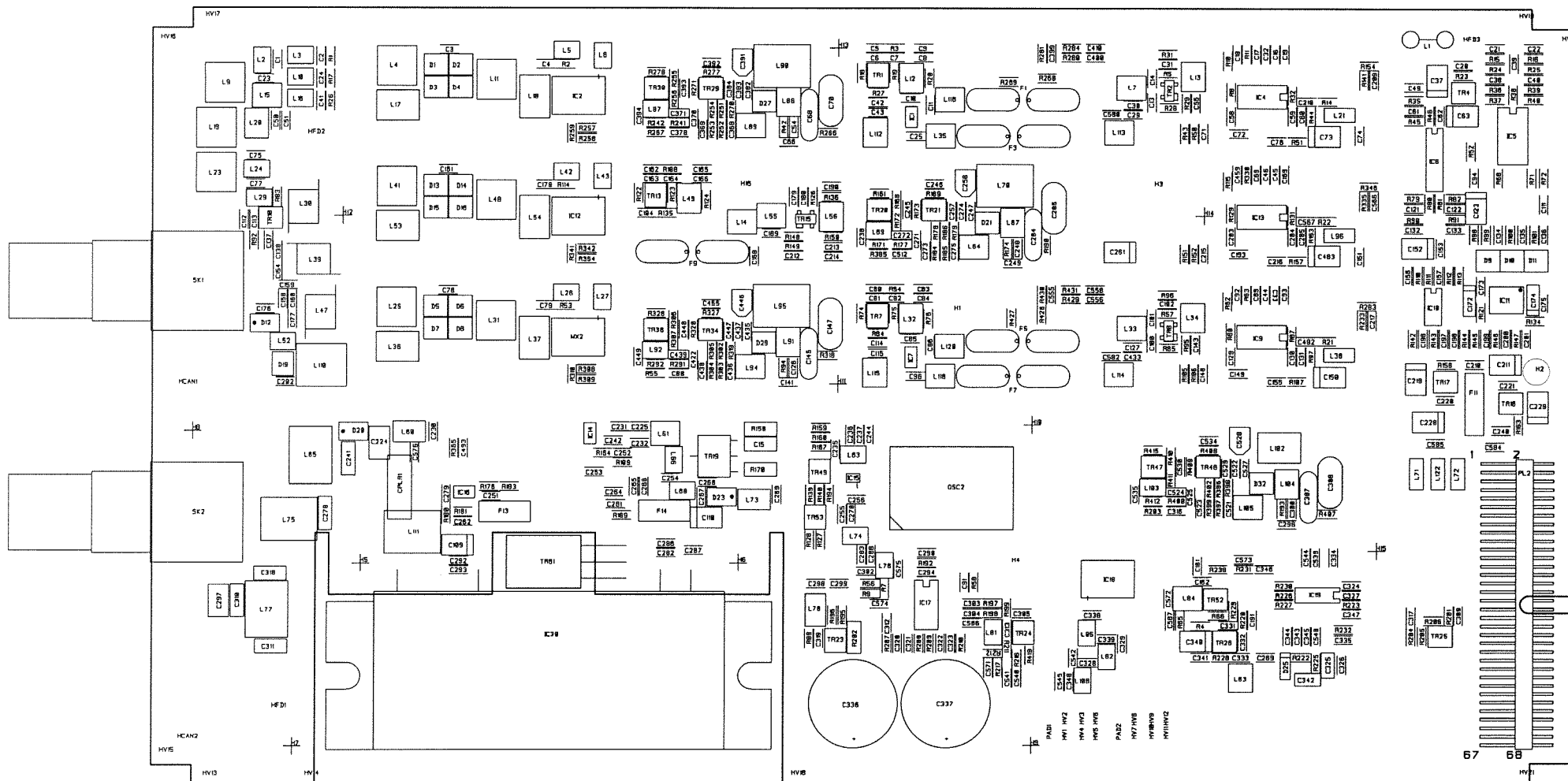




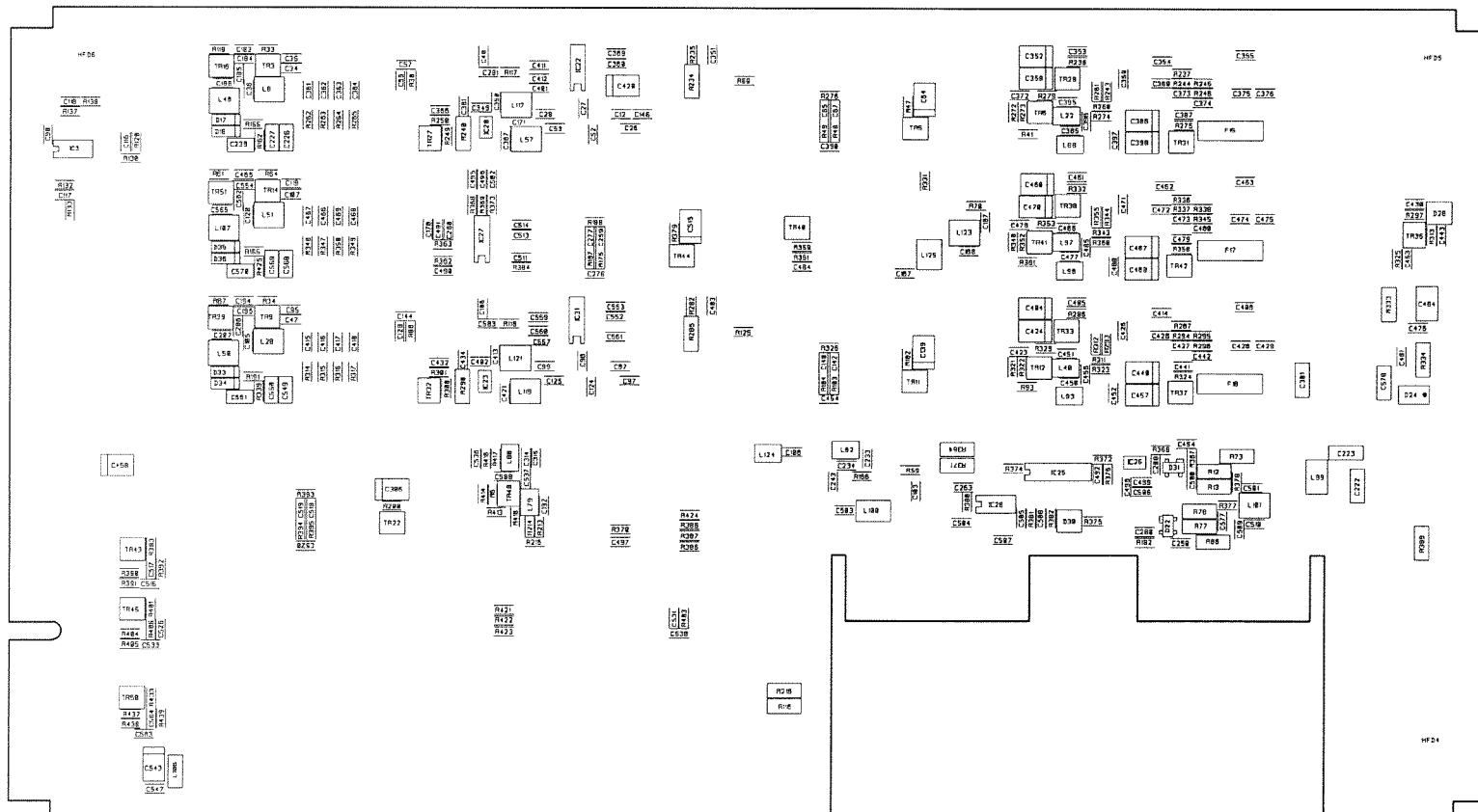
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A	DC820	199801	JMG	JMG	Wireless House	© 2002
B		19701	JMG	JMG	Westfield Industrial Estate	
C		210202	JMG	JMG	Midsummer, Norlton	
D		199302	JMG	JMG	Bath, BA3 4BS	
E		070502	JMG	JMG		
F		070502	JMG	JMG		
G		170602	JMG	JMG		
H		010002	JMG	JMG		
I		121102	JMG	JMG		

Main Title: ODIN R4 RF
Secondary Title: RX 2 AND RX3 BACK END
Drawing No: LV0404154
Rev: 3.1

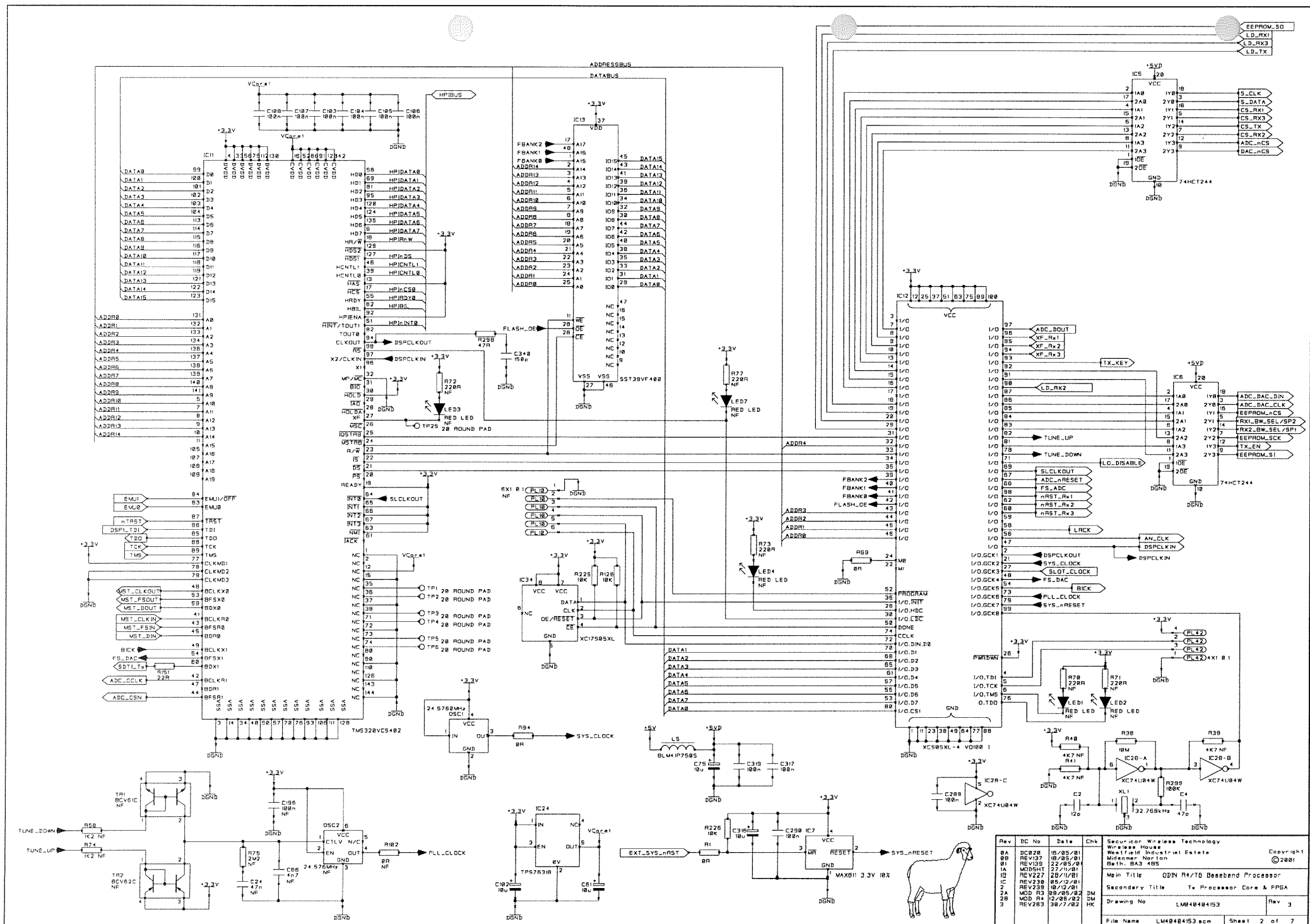




SECURICOR WIRELESS TECHNOLOGY LTD
 ODIN R4 RF PCB - V3
 L0010DINR4RC0
 LAYER 1 ASSEMBLY

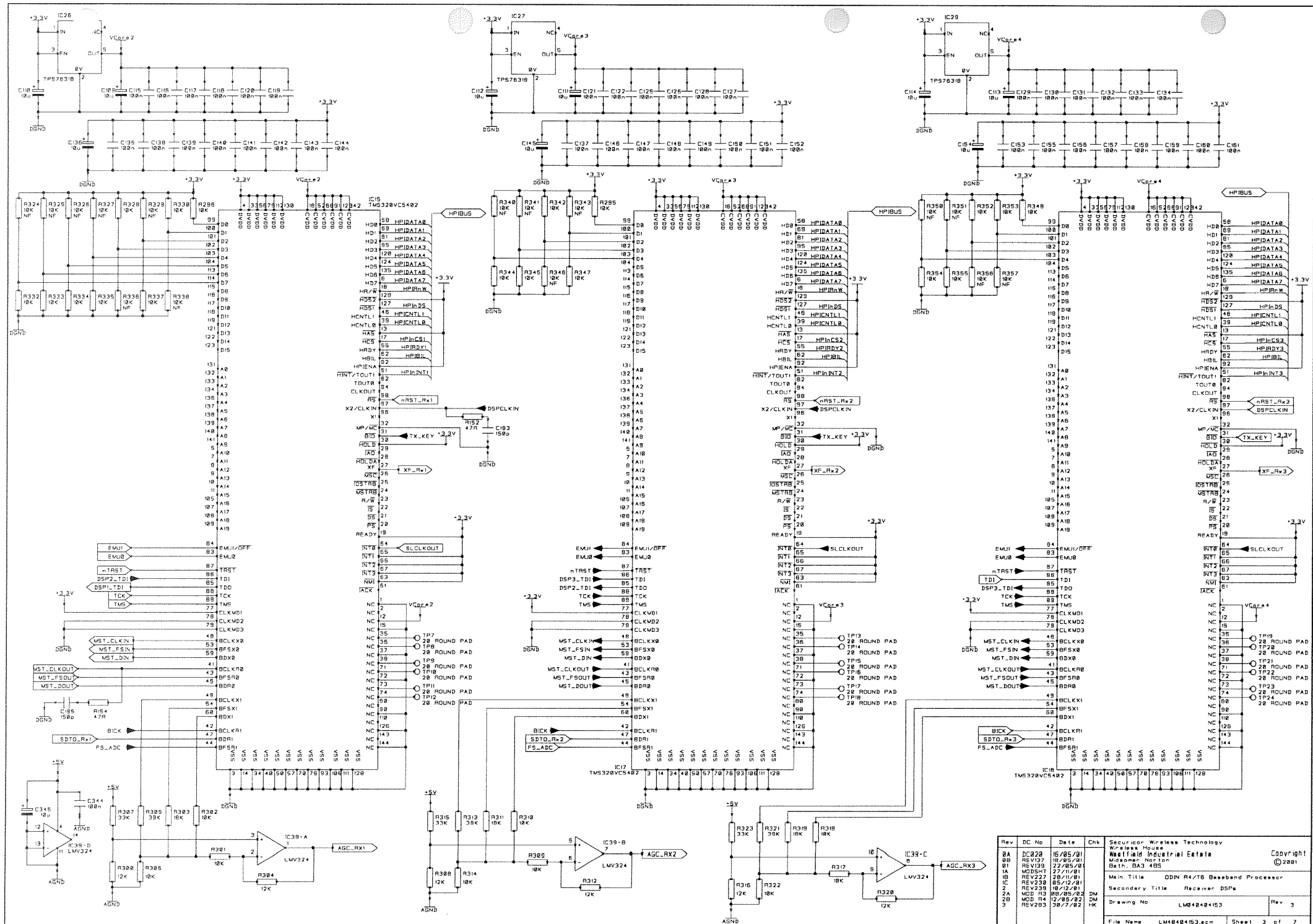


LAYER 8 ASSEMBLY
 LONODIN+RCD
 ODIN R4 RF PCB - V3
 SECURICOR WIRELESS TECHNOLOGY LTD



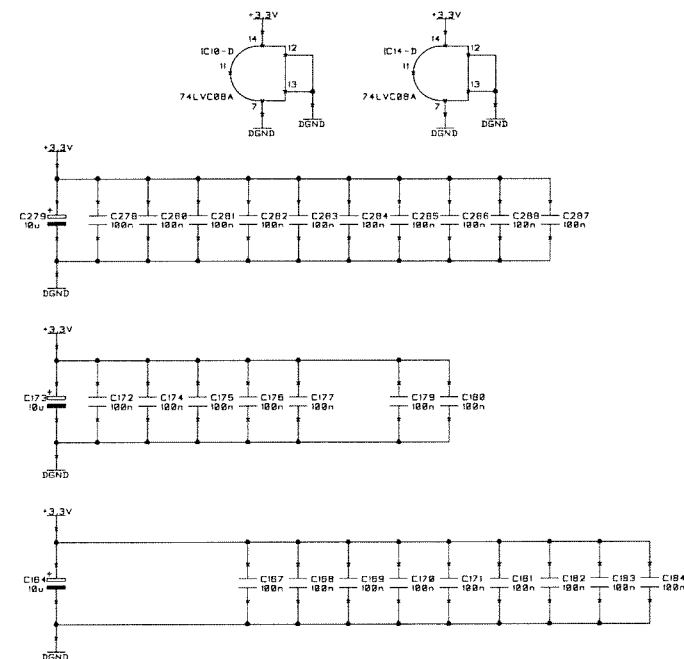
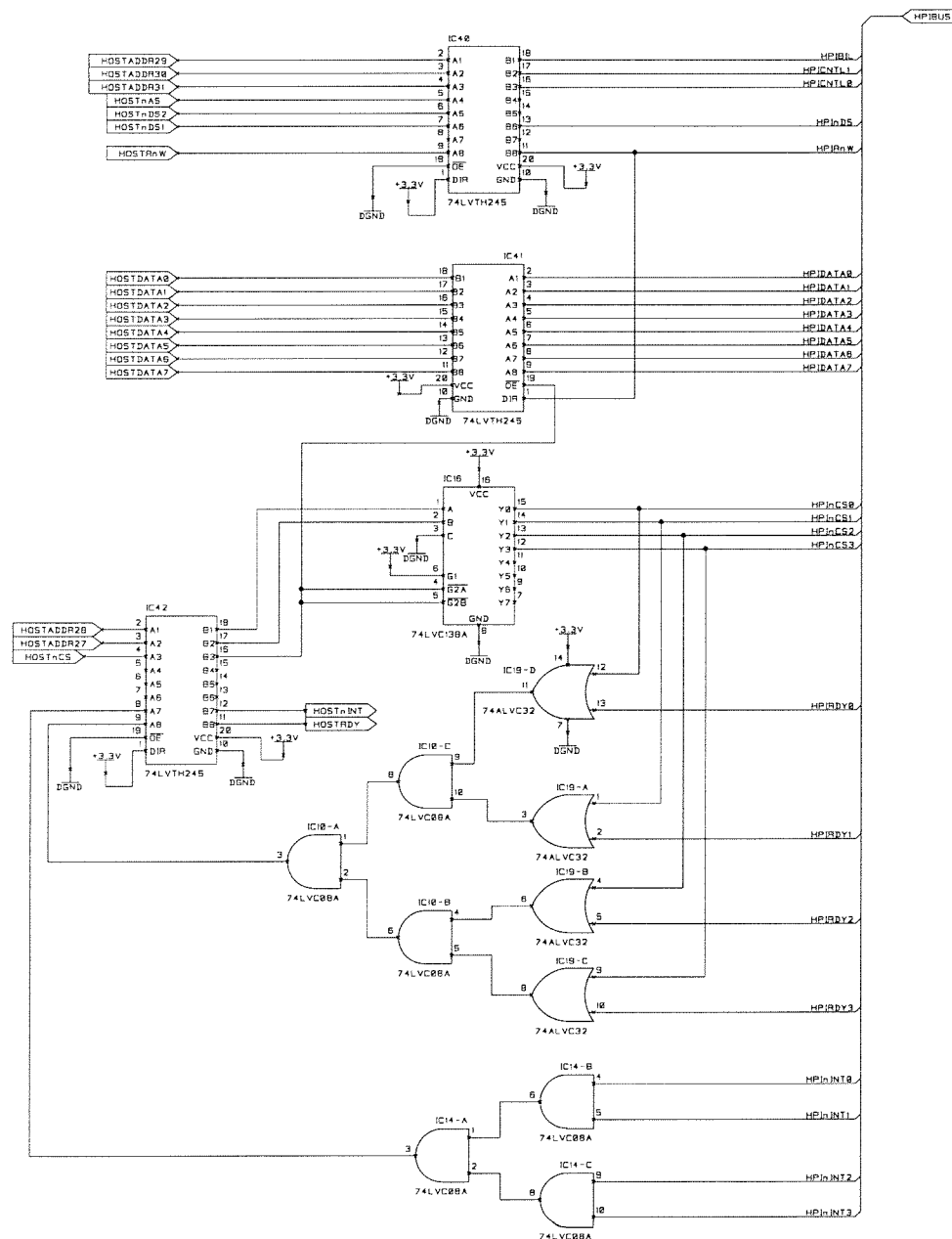
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0A	DC028	15/05/01		Wireless House	
0B	REV137	18/05/01		Westfield Industrial Estate	
0C	REV228	22/05/01		Midsummer Norton	
1A	MOD501	27/11/01		Devon, BA3 4BS	© 2001
1B	REV227	28/11/01			
1C	REV230	05/12/01			
1D	REV238	18/12/01			
2A	MOD R3	08/05/02	DM		
2B	MOD R4	12/05/02	DM		
3	REV263	30/7/02	HK		

Main Title		QDN R4/T6 Baseband Processor	
Secondary Title		Tx Processor Core & FPGA	
Drawing No		LMB484153	
Rev		3	
File Name		LMB484153.scm	
Sheet		2 of 7	



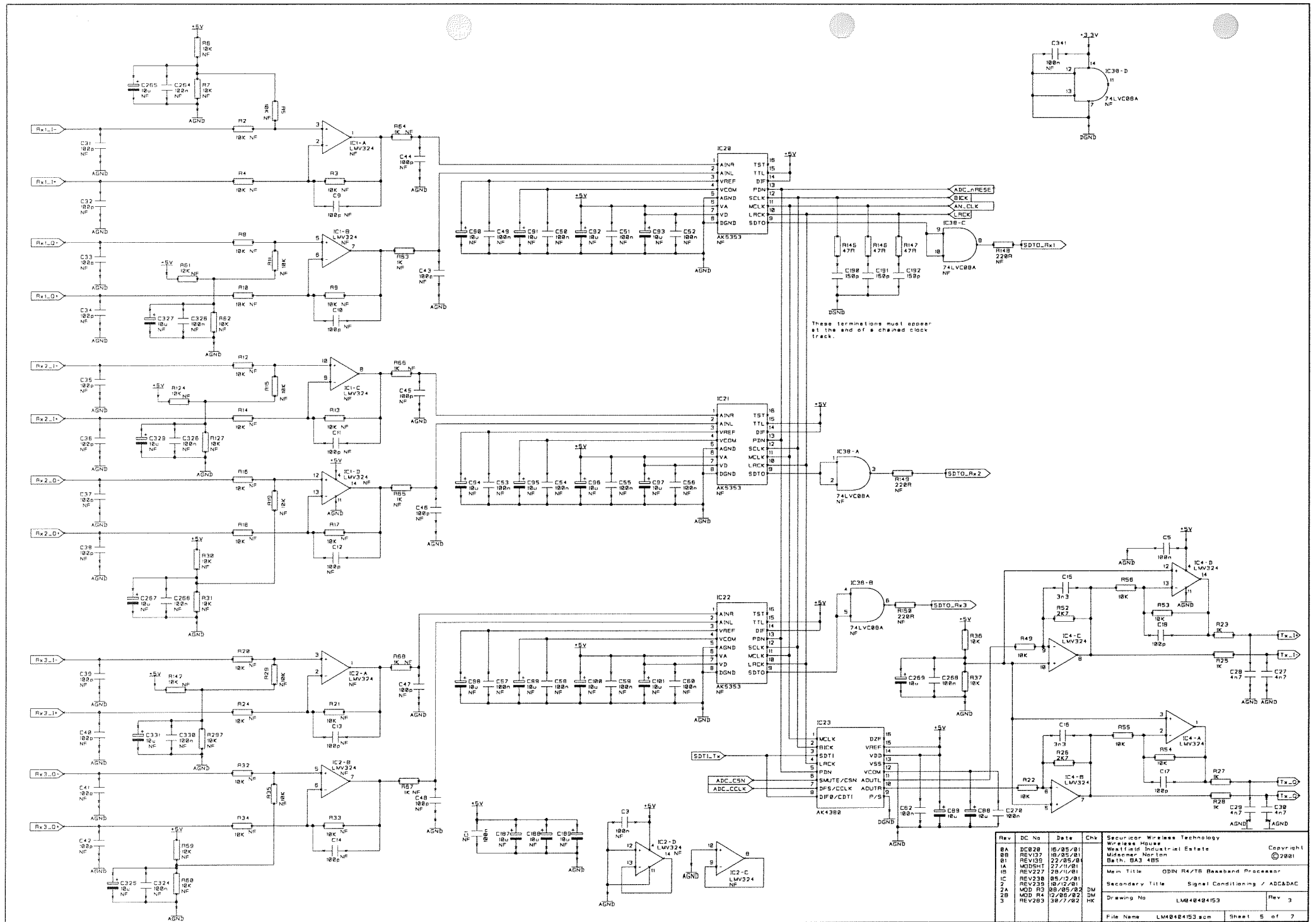
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0A	DC020	16/05/01			
0B	REV137	17/05/01			
01	REV138	22/05/01			
1A	MOD137	27/11/01			
1B	REV227	28/11/01			
1C	REV238	05/12/01			
2	REV238	19/12/01			
2A	MOD R3	09/05/02	DW		
3	MOD R4	12/05/02	DM		
3	REV203	30/7/02	HK		

Main Title	ODN R4/T6 Baseband Processor
Secondary Title	Receiver DSPs
Drawing No	LM0404153
Rev	3
File Name	LM0404153.acm
Sheet	3 of 7



Rev	DC No	Date	Chk	Securicon Wireless Technology Wireless House Westfield Industrial Estate Middx. Nott'n Bath, BA3 4BS	Copyright © 2001
0A	DC028	15/05/01			
0B	REV137	15/05/01			
01	REV139	22/05/01			
1A	MOD54T	27/11/01			
1B	REV227	26/11/01			
1C	REV230	05/12/01			
2	REV238	05/12/01			
2A	MOD R3	05/05/02	DM		
2B	MOD R4	12/08/02	DM		
3	REV283	30/7/02	HK		

Main Title	ODIN R4/T6 Baseband Processor
Secondary Title	Host Port Interface
Drawing No	LM0404153
Rev	3
File Name	LM0404153.wcm
Sheet	4 of 7



Rev	DC No	Date	Chg	Securicor Wireless Technology
0A	DC020	15/05/01		Wireless House
0B	REV137	18/05/01		Wireless House
01	REV130	22/05/01		Wireless House
1A	MOD511	27/11/01		Wireless House
1B	REV227	28/11/01		Wireless House
1C	REV230	05/12/01		Wireless House
2	REV238	18/12/01		Wireless House
2A	MOD R3	08/05/02	DM	Wireless House
2B	MOD R4	12/05/02	DM	Wireless House
3	REV263	30/7/02		Wireless House

Mem Title	ODN R4/TB Baseband Processor
Secondary Title	Signal Conditioning / ADC&DAC
Drawing No	LM40404153
Rev	3
File Name	LM40404153.scm
Sheet	5 of 7

