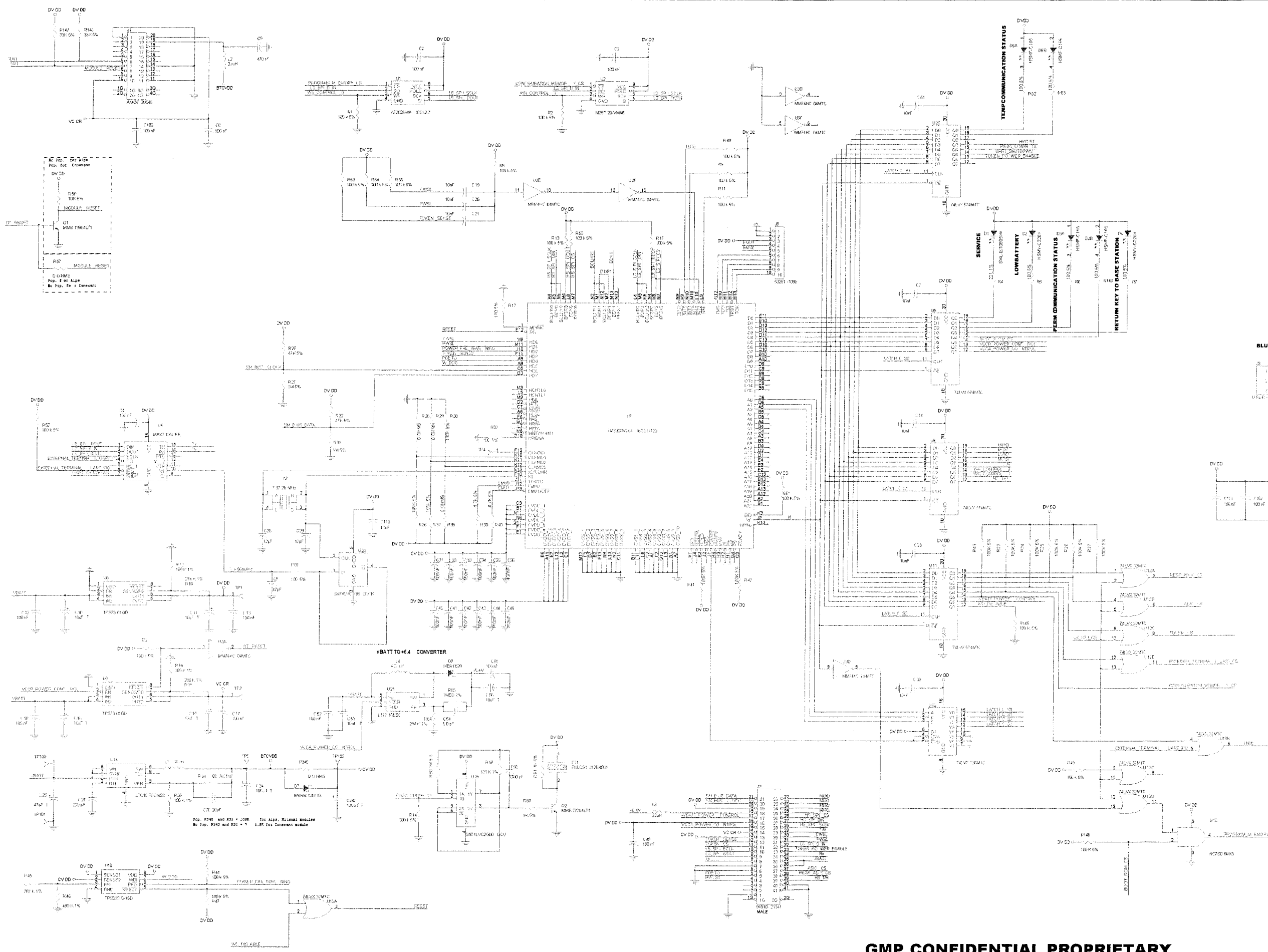
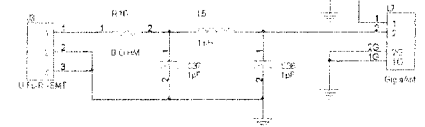


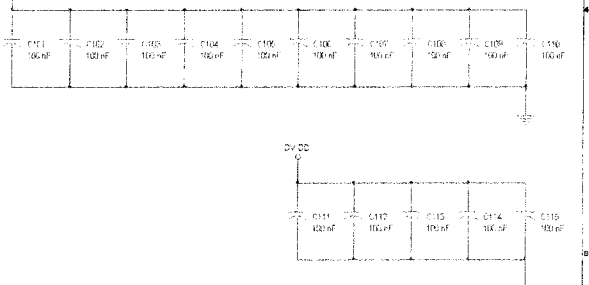


- 07-09-02 V3.0**
Change package for Y2 to be same as Y1
Changed R23 from 1M to 100K
- 07-23-02 V3.1**
Added 5.4 V Boost Circuit, Added HWTST signal
- 08-19-02 V3.2**
Revised J2 connector pinout
- 11-04-02 V4.0**
Added Bluetooth Antenna L-match and matching impedance network. Increased current to LEDs. Eliminated Y1 and replaced with U22. New EEPROM, U1.
Added GND pins to J2. Fixed W_DCG reset circuit. Added bulk decoupling caps. Changed R1 & R2 connection to GND.

BLUETOOTH ANTENNA AND MATCHING NETWORK

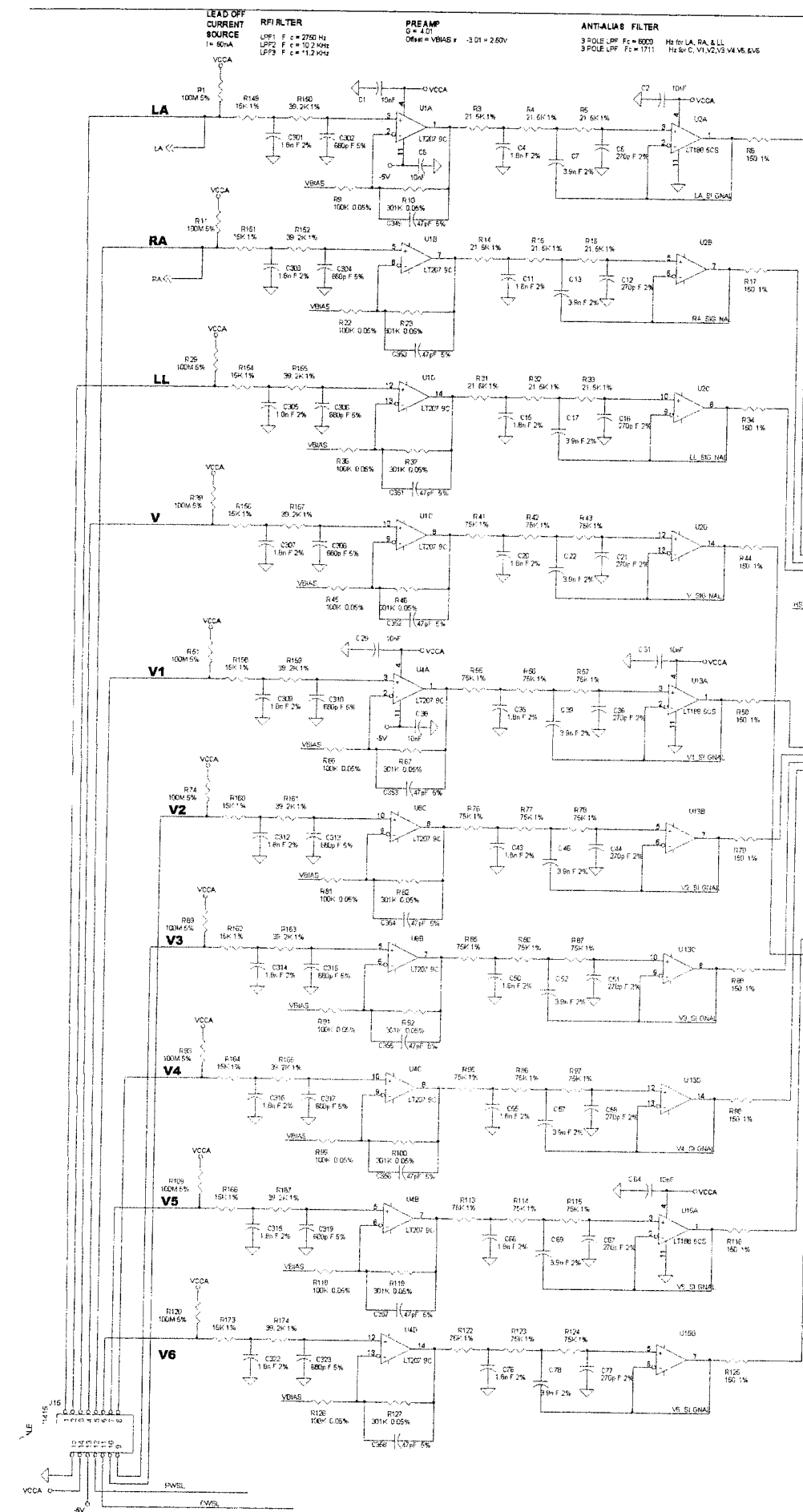


BULK DECOUPLING CAPS



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BCF
11/10/02



07-2-02 v3.2

Changed A/D reference to $+5.0$ VDC. Added RF filtering. Changed signal gain in 1st stage and at A/D buffer stage. Added HS TH line. Changed J2 to 4-pin. Added new L.S. A/D MAX 1110. Changed supplies from -3.0 V to -5.0 V. Added Resp. circuit on sheet 2. Added V/A ECG ASIC. Changed compo for low noise.

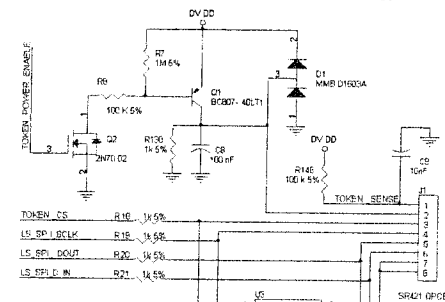
07-3-1-02 v3.3

Changed LL, LA, and R A filters. Replaced U27 with LMH6645. Corrected J2 pin

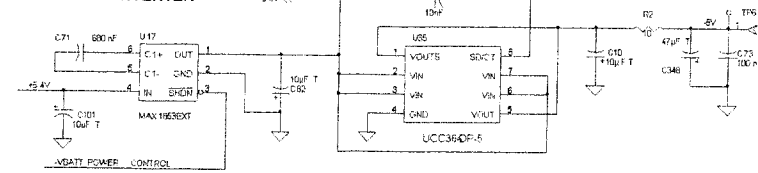
11-1-1-02 V4.0

Added 10 buffer amp. U32 & U33. Deleted ECG ASIC. Added 47 pF rollout on U1, U4, U6. Numerous changes to resp. demo circuit. Added -5V regulator, U35. Changed RF filter to 2750 & 10.2 kHz LPF. Separate op amp for

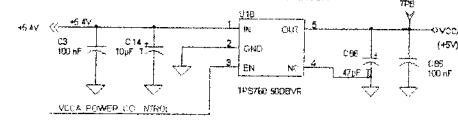
DATAKEY INTE RFACE CKT



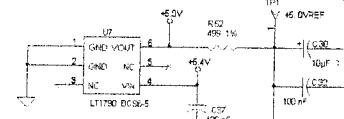
-5.4 V CONVERTER



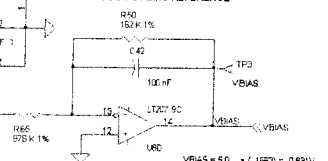
+5V PWR REGULATOR



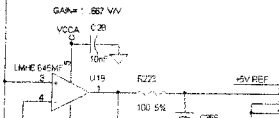
A/D REFERENCE



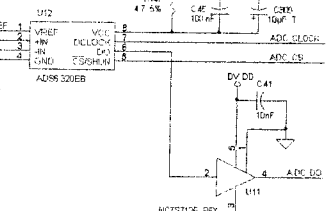
ECG INPUT BIAS REFERENCE



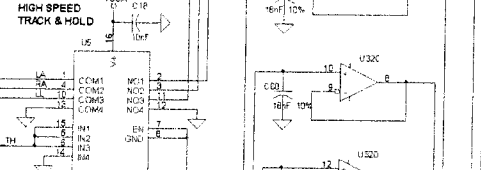
A/D BUFFER AMP



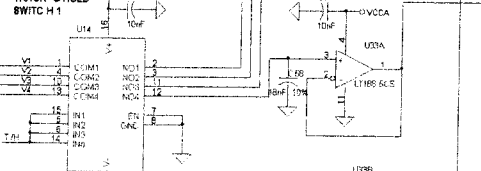
A/D CONVERTER



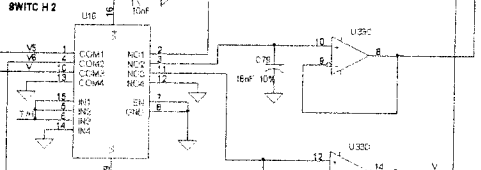
HIGH SPEED TRACK & HOLD



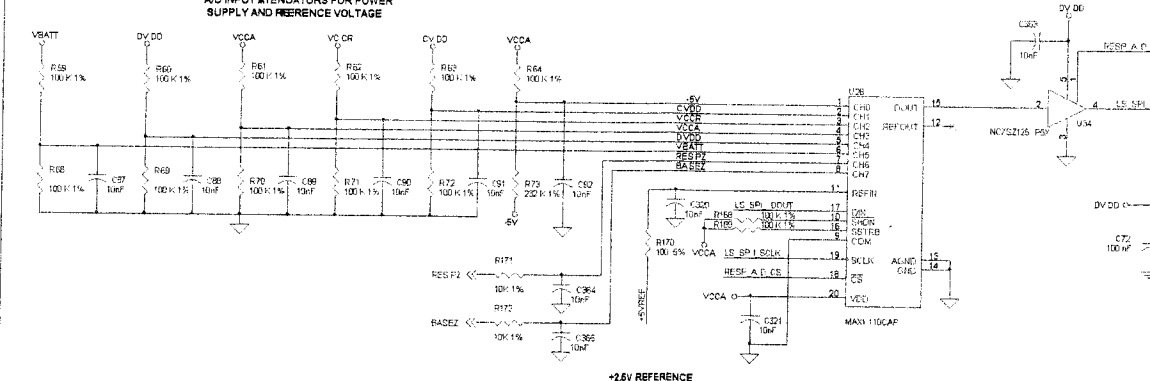
LOW SPEED TRACK & HOLD SWITC H 1



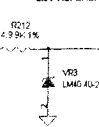
LOW SPEED TRACK & HOLD SWITC H 2



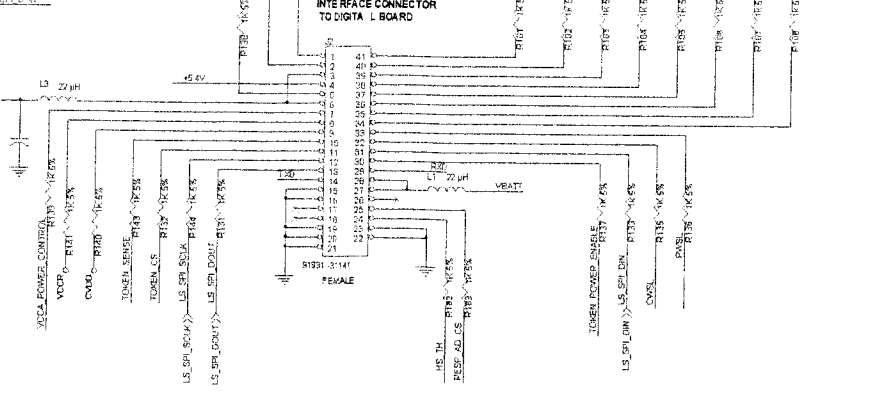
A/D INPUT ATTENUATORS FOR POWER SUPPLY AND REFERENCE VOLTAGE



+2.5V REFERENCE



INTERFACE CONNECTOR TO DIGITAL BOARD



NOTES:
 UNLESS OTHERWISE SPECIFIED, ALL RESISTOR
 PACKAGES WITH VA. USES IN OHMS
 RS ARE 1% TOLERANCE IN 0402

