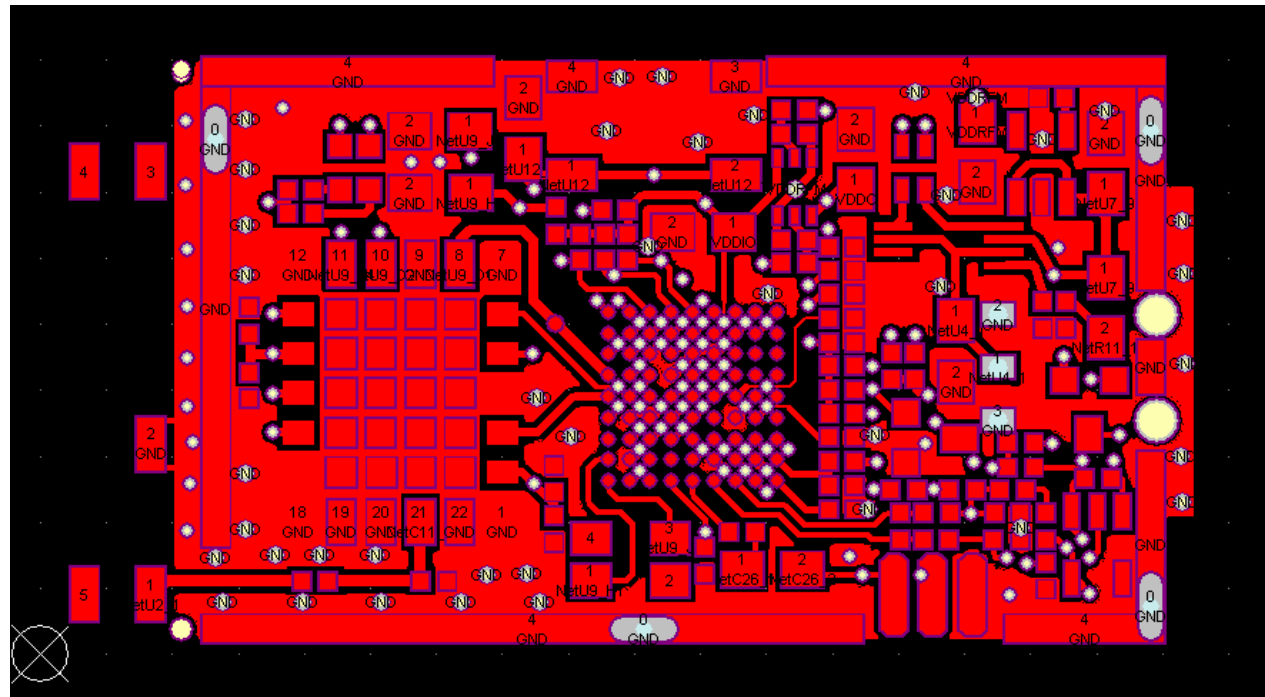
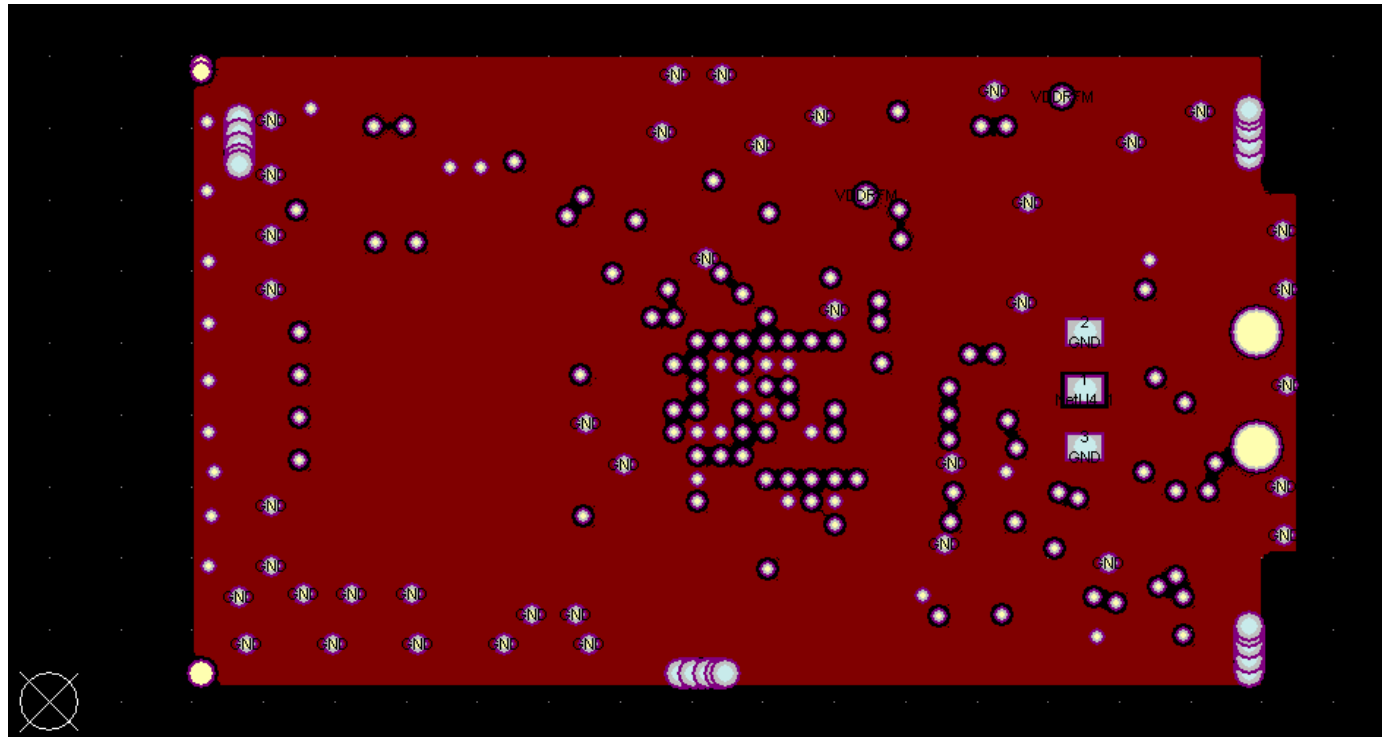


8. PCB LAYOUT

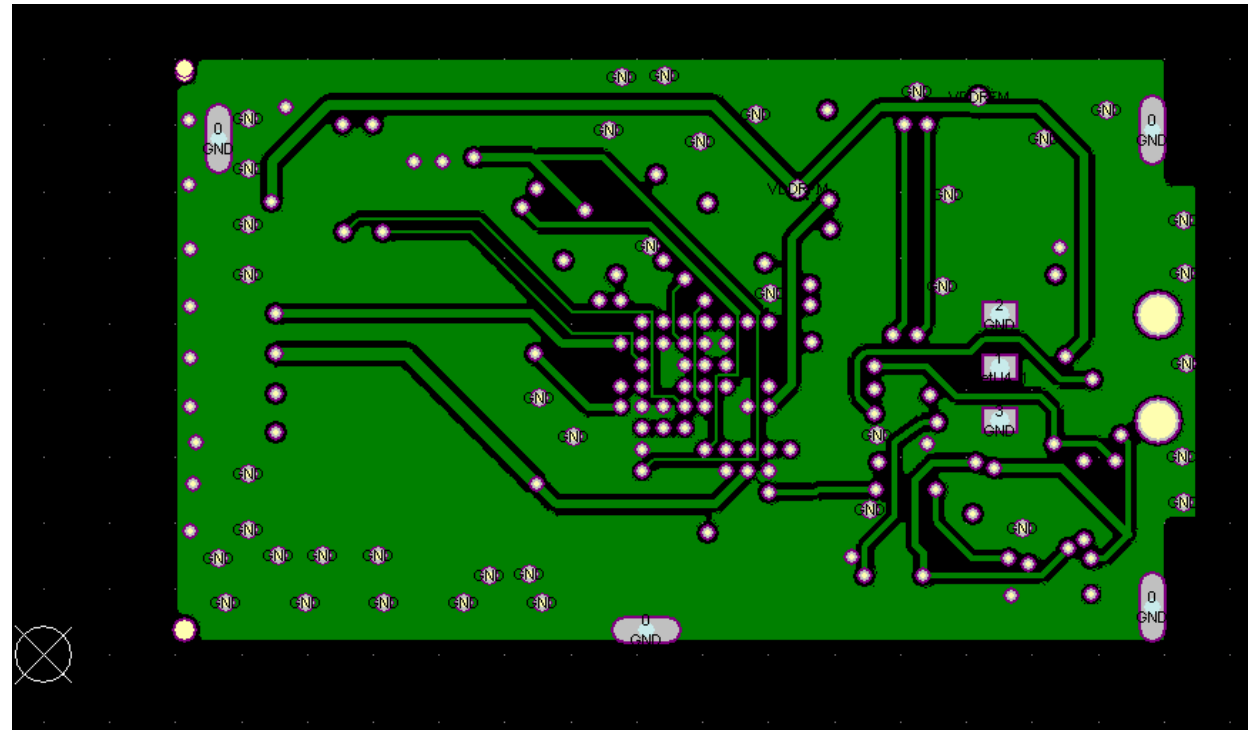
Top Layer



Mid Layer1



Mid Layer30



Bottom Layer

