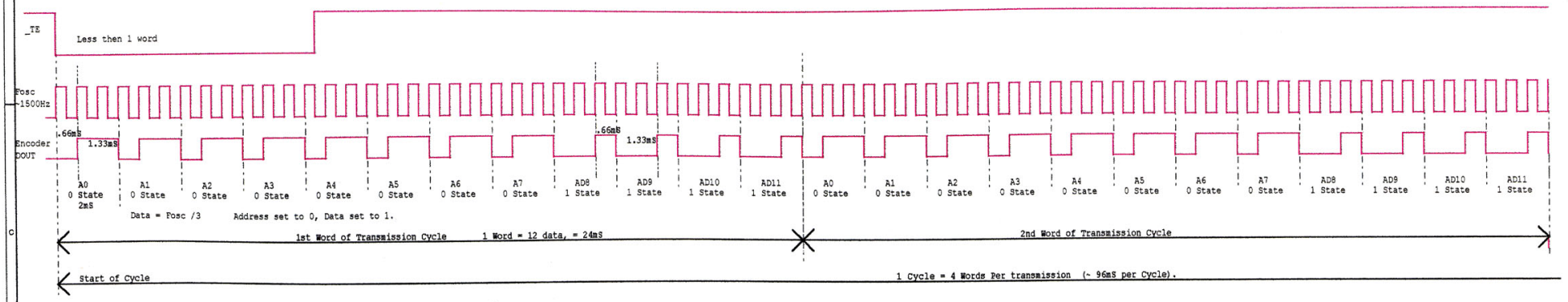

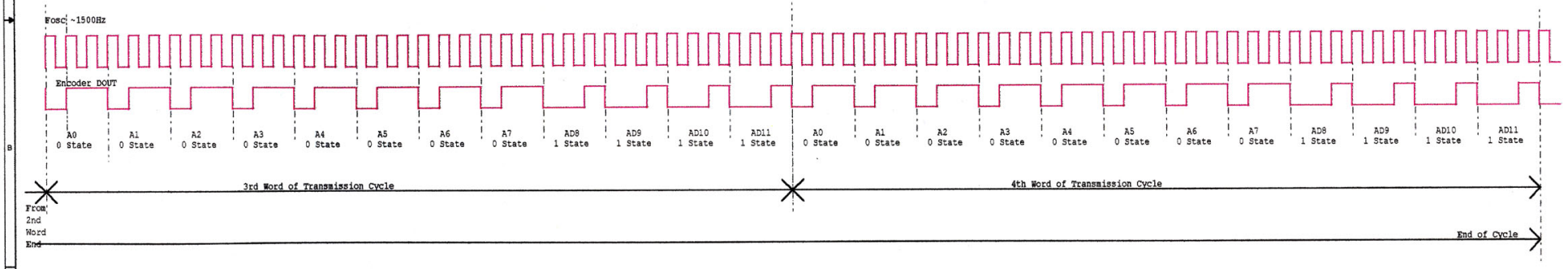

A HOLTEK HT12E 212 Encode device controls data encoding. The encoder address/ data lines are pre-selected on the printed circuit boards. There is no user selection available. The four data lines (AD8-AD11) are internally pulled high. Address lines A0-A7 are tied to the Vss potential for address 0.
The 212 encoder begins a four work transmission cycle upon receipt of a transmission enable signal (_TE Pin 15 U8). This cycle will repeat itself as long as the transmission enable (_TE) is held low by the output of Q2 U1 Pin 13. Once the transmission enable returns high, the encoder will output the balance of the four-word cycle. A minimum transmission of four words from the encoder is generated when the _TE line is drive low. For a valid TX cycle the _TE U8 Pin 15 must be low for less than 1 word. The encoded waveform for the address/data bits are based on Fosc 1/3. Three Fosc cycles are required to generate one bit pattern/state. For a zero pattern, DOUT is low for one Fosc cycle, then high for two cycles. For a one pattern, DOUT is low for two Fosc cycles, then high for the last cycle.The encoder frequency will be dependant on the supply voltage levels, Fosc range from ~1Khz at 2VDC to approximately 1.5KHz at 4.4VDC.

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