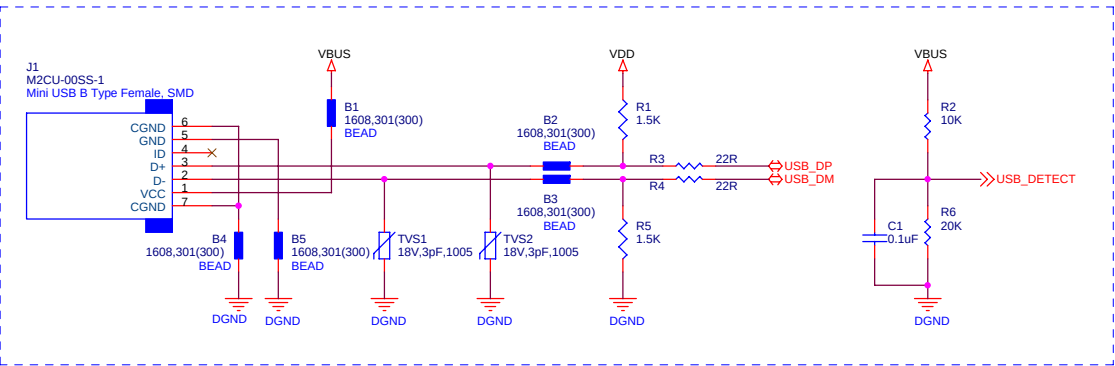
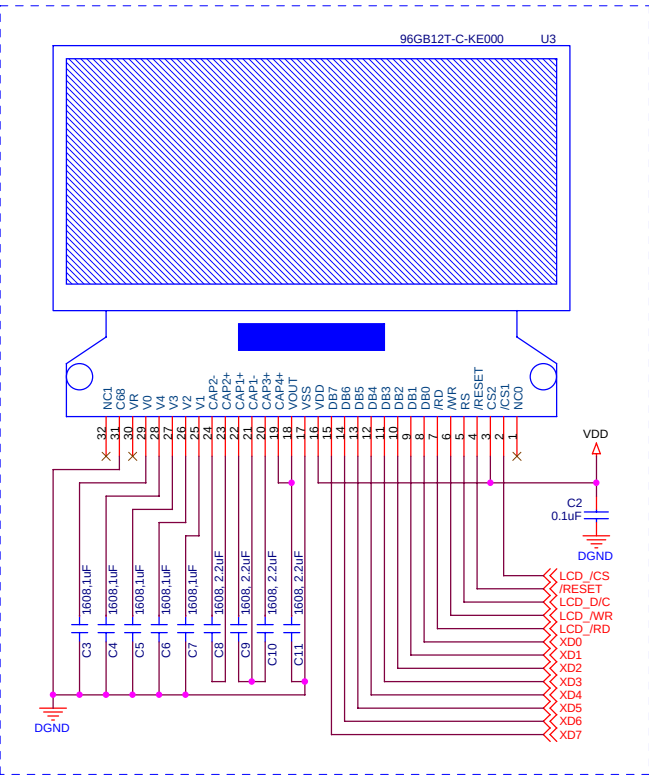


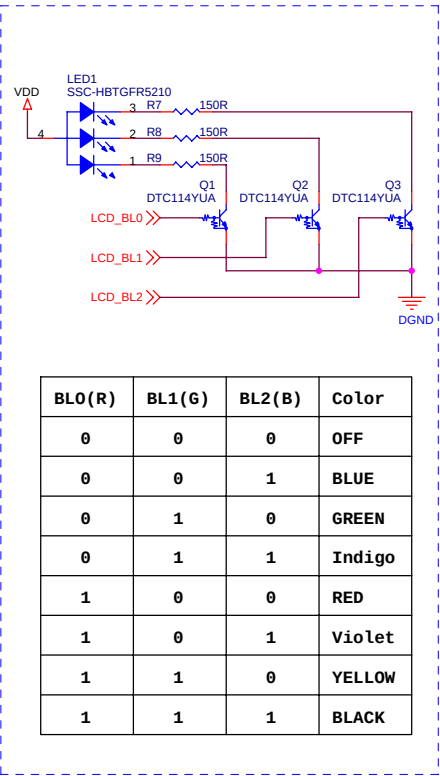
USB Interface



LCD(Top Veiw)

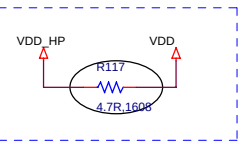
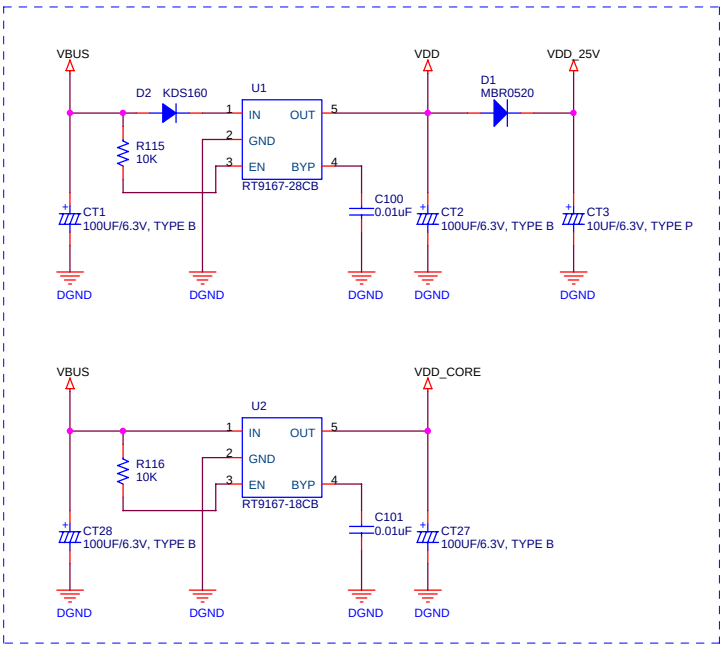


LCD Backlight

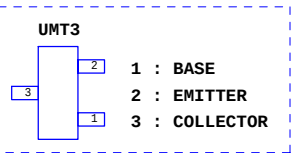


BLO(R)	BL1(G)	BL2(B)	Color
0	0	0	OFF
0	0	1	BLUE
0	1	0	GREEN
0	1	1	Indigo
1	0	0	RED
1	0	1	Violet
1	1	0	YELLOW
1	1	1	BLACK

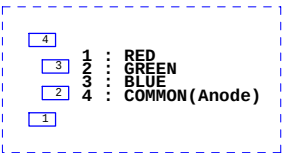
Main Power(LDO) Circuit



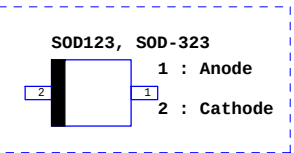
TR Top View



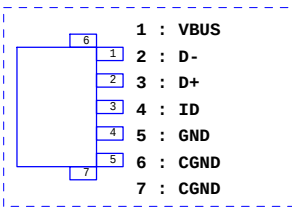
LED Top View



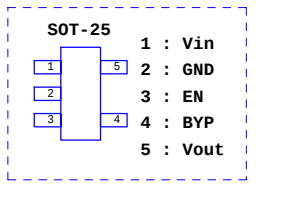
MBR0520 Top View



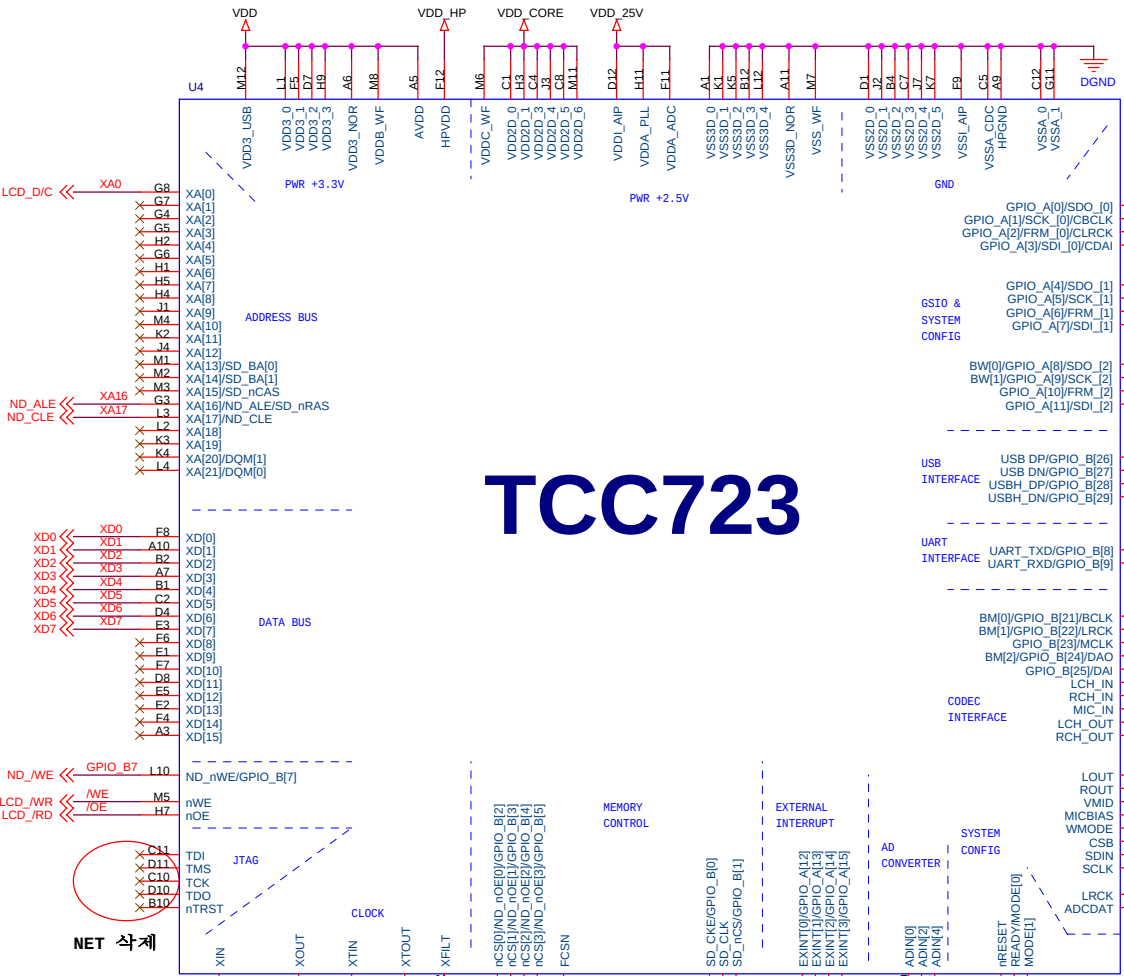
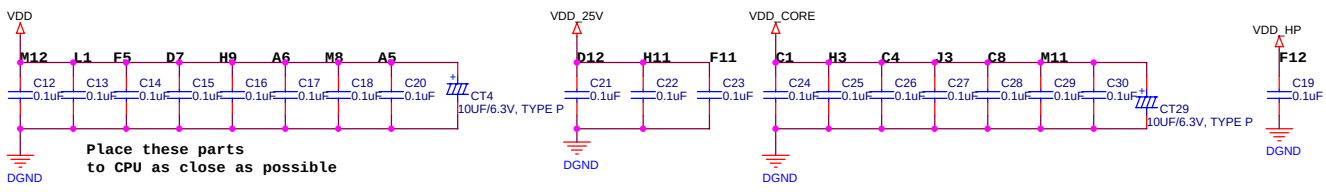
Mini USB-B Top View



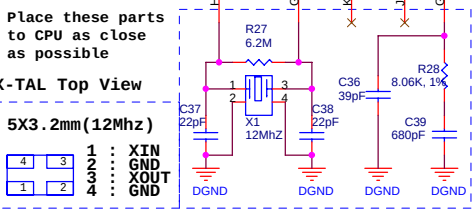
RT9167 Top View



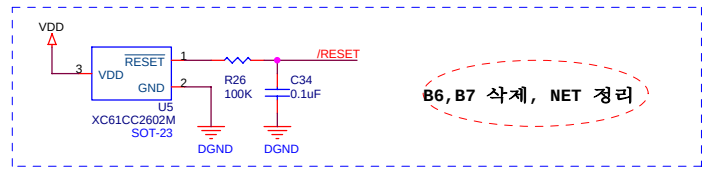
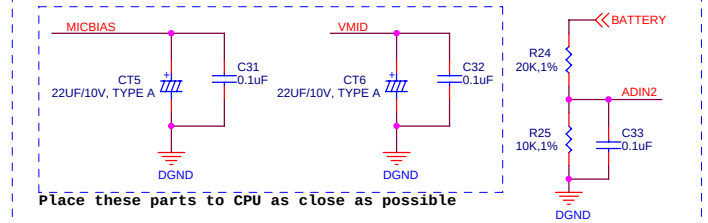
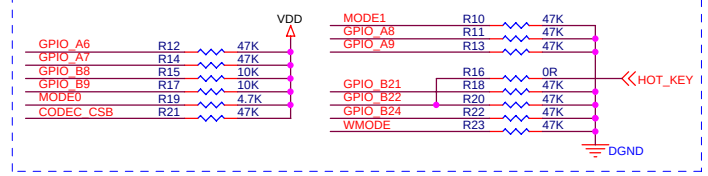
By-pass Capacitors Design Note.



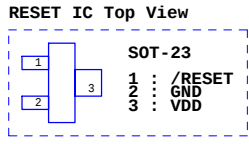
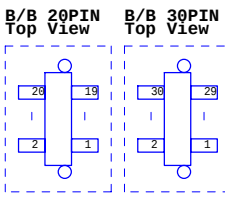
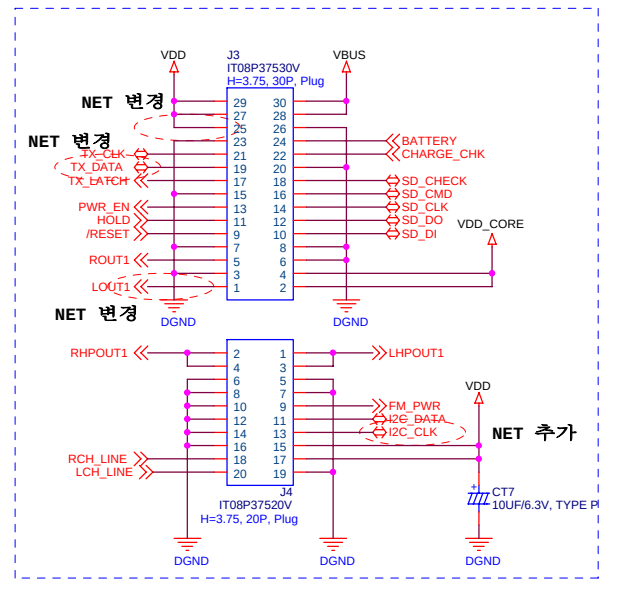
TCC723



CONFIG(PULL-UP, PULL-DN)



B6, B7 삭제, NET 정리



MURO ELECTRONICS

LDP-600 MAIN_Lexar

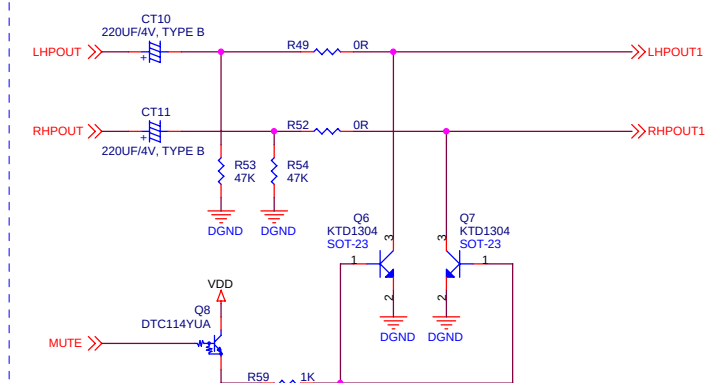
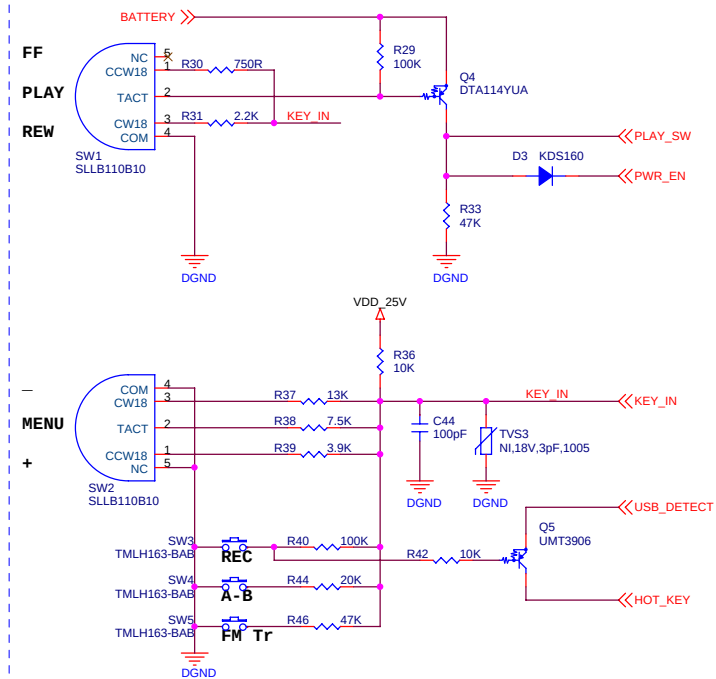
Title

03. Main Chip, Connector

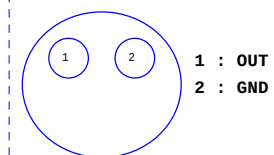
Size A3 Document Number 2004. 09. 05 Edited By Seung-Ryong, Ha Rev 0.5

Date: Saturday, September 11, 2004 Sheet 3 of 7

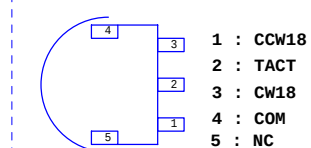
KEY



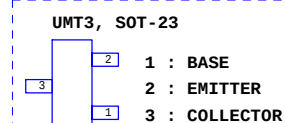
MIC Top View



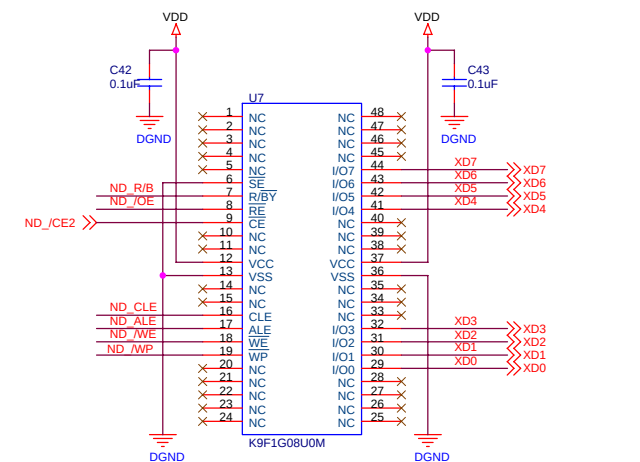
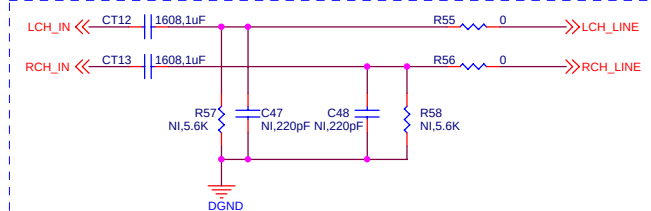
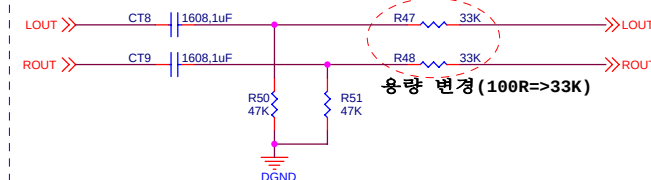
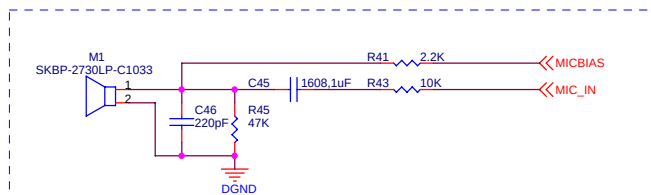
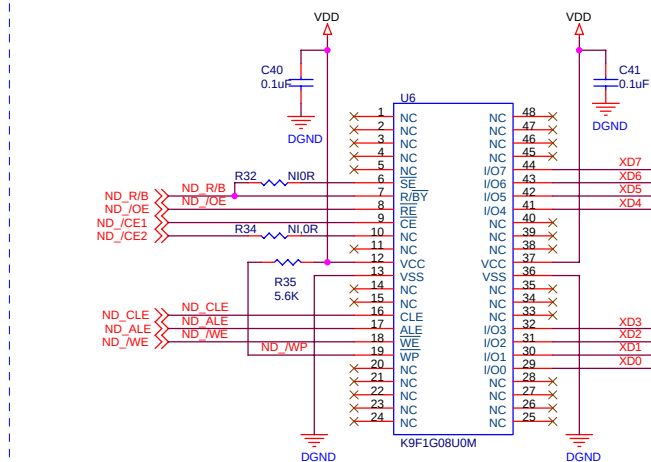
Jog Top View



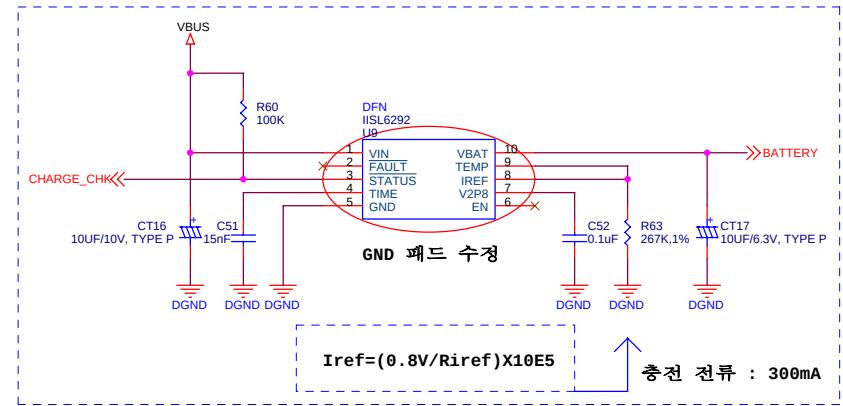
TR Top View



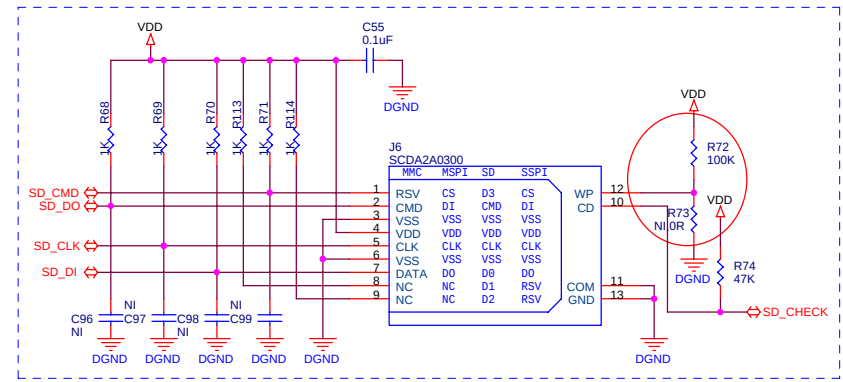
NAND FLASH MEMORY



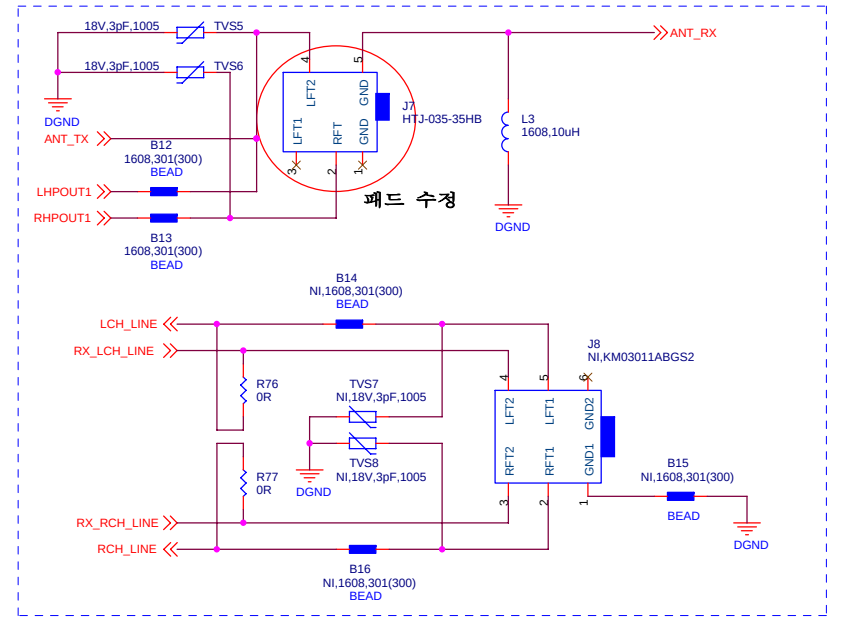
Charge Circuit



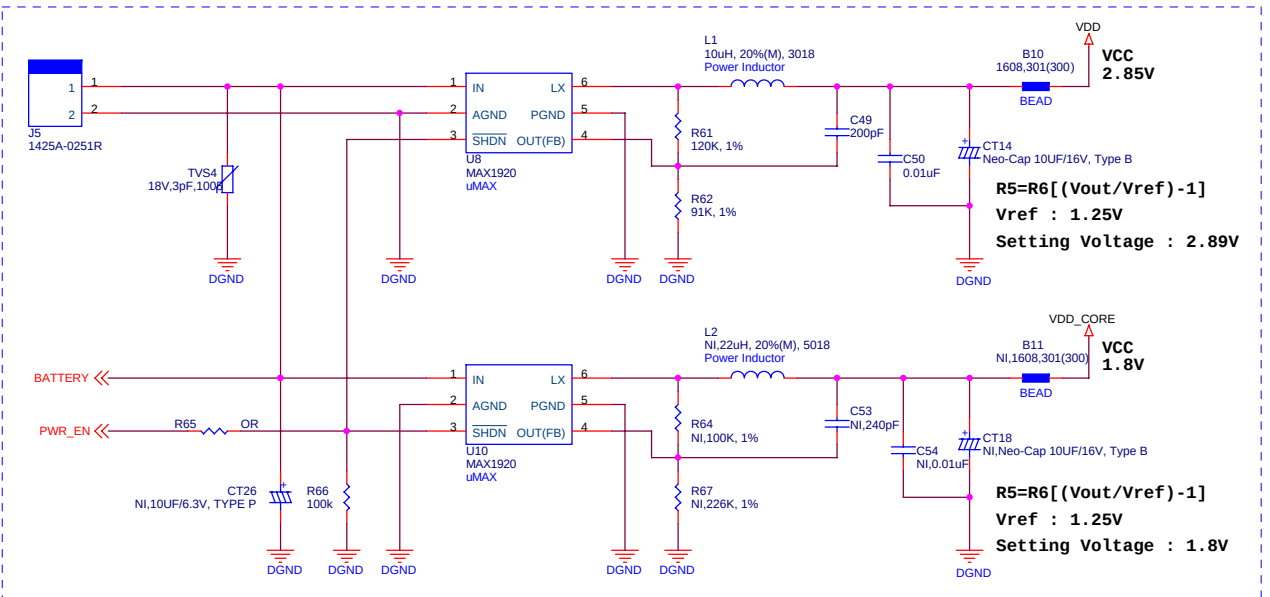
SD Card Interface



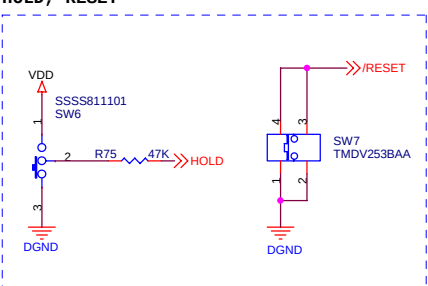
Earphone, Line-In Interface



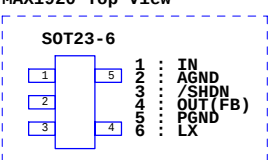
Main Power(DC/DC Step-Down) Circuit



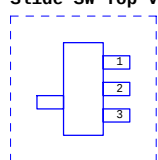
HOLD, RESET



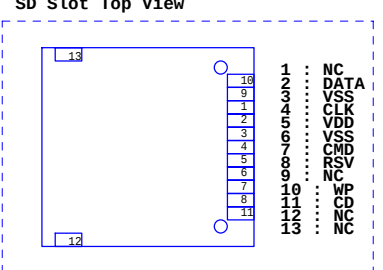
MAX1920 Top View



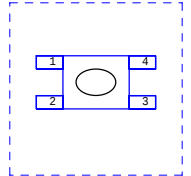
Slide SW Top View



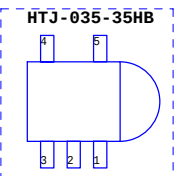
SD Slot Top View



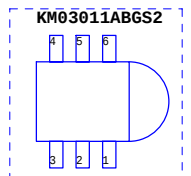
TACT SW



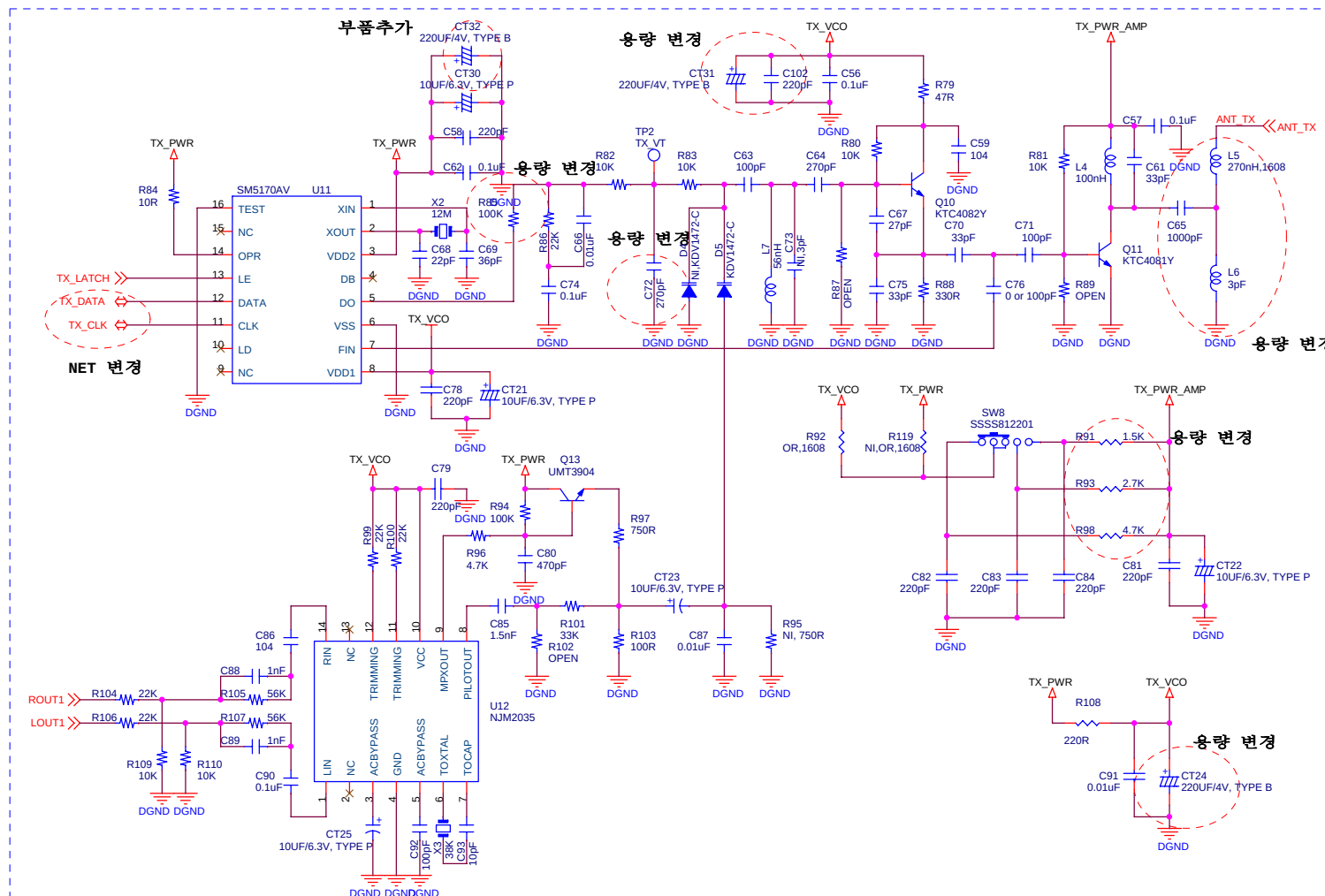
EAR JACK



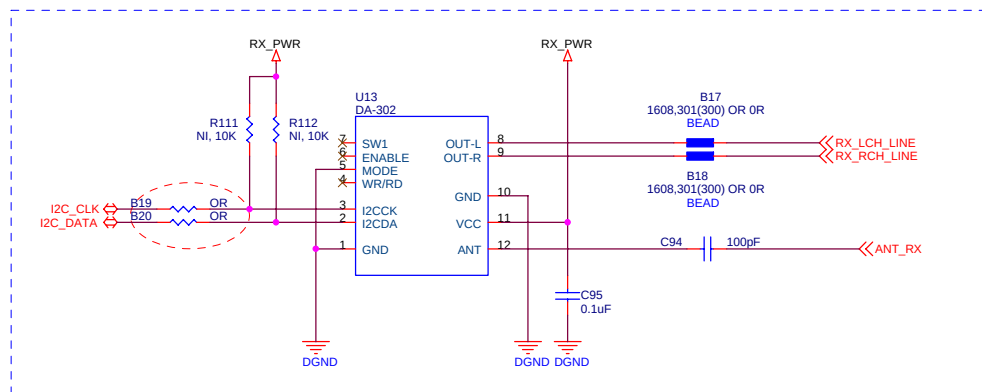
LINE JACK



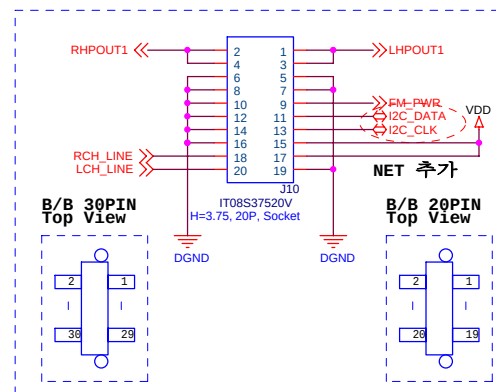
FM Transmitter Circuit



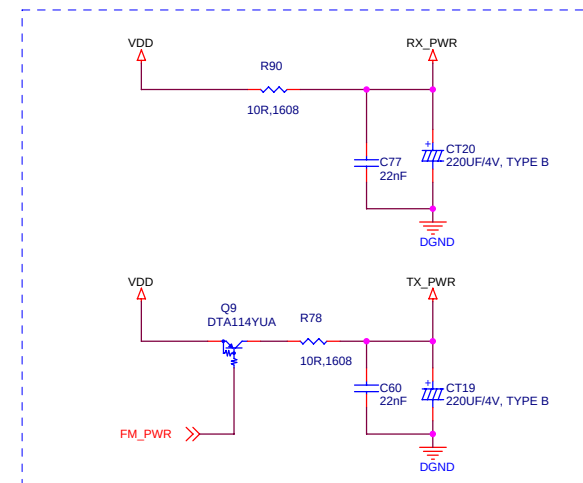
FM Radio Circuit



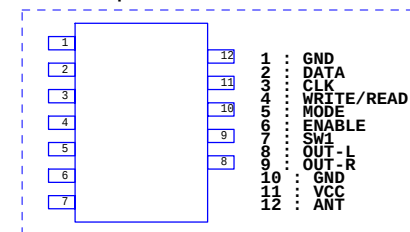
B/B Connector



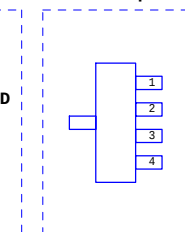
FM RX/TX Power Circuit



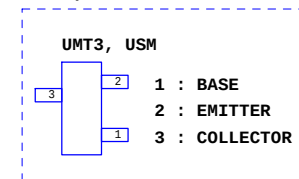
DA-302 Top View



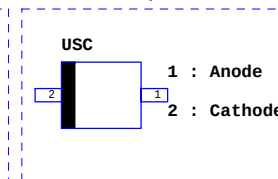
Slide SW Top View



TR Top View



KDV1472-C Top View



B/B Connector

