

WP1200 REFERENCE DESIGN

(D02-RM-00A)

PAGE1 RF & PA (RF5189)

PAGE2 RF2958

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PREVIOUS VERSION: DB-1-R0-F-AC DB611123.DSN

Rev1.0

08/04/2003: correct the pin names of RF2958
correct the pin names of RF5189
add C11, CA27, DA2
change values: C2, C12, C13, C14, C15, C20, C21
del: net GPIO_0
del: R8, C26

Rev1.0+

08/08/2003: change footprint of L4
08/13/2003 change value: C2 4.7uF.

NOTES :

1. EEPROM SHOULD BE PROGRAMMED BEFORE LOADED.
2. RESISTER ARRAYS SHOULD BE ARRANGED FOR LAYOUT CONVINIENCE.
3. COMPONENTS LOADED WHEN ASSEMBLING WILL FOLLOW BOM.
4. PCMCIA CARD PACKAGE USE LOW VOLTAGE KEY

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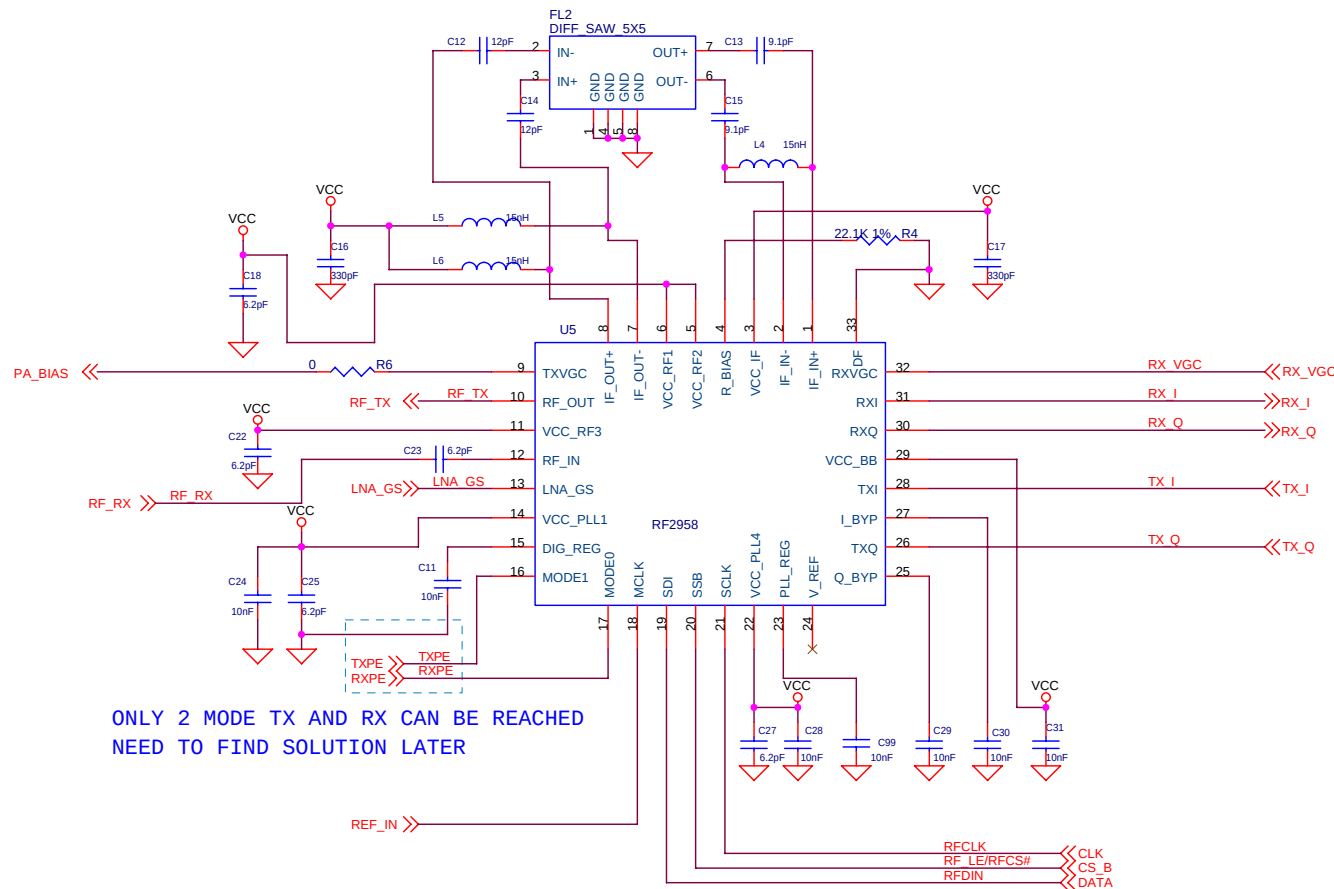
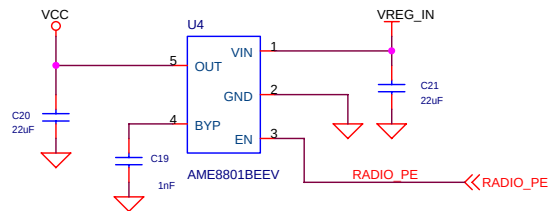
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Title

PCMCIA Reference Board

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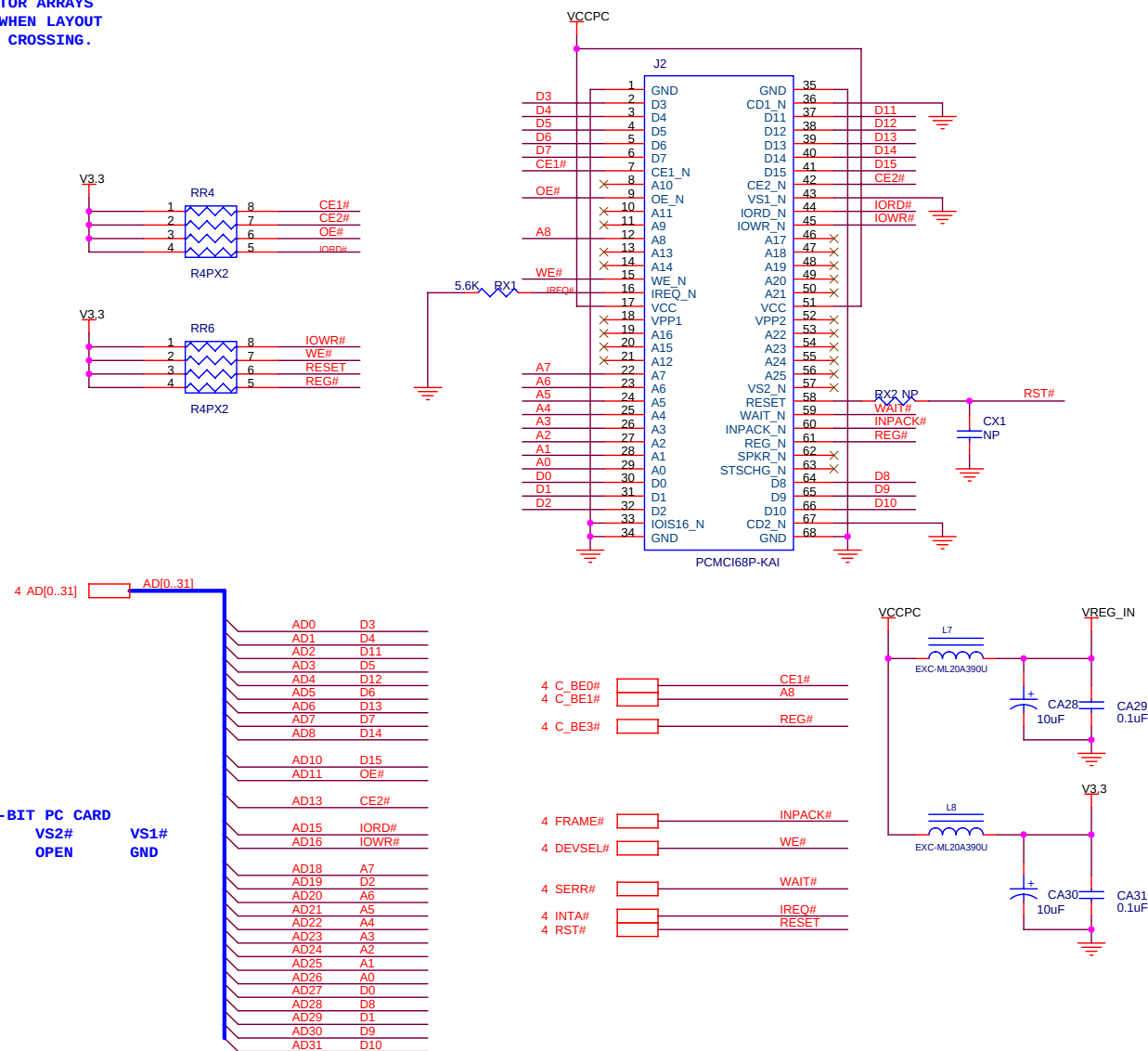


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CONNECTION TO RESISTOR ARRAYS
SHOULD BE ADJUSTED WHEN LAYOUT
TO ENSURE NO TRACES CROSSING.



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