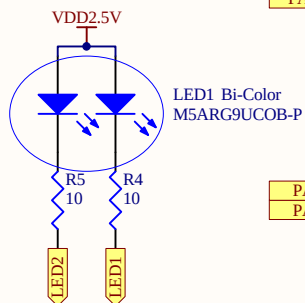


- 1). All electrolytic capacitors are 16V or above, $\pm 20\%$ except specified.
- 2). All ceramic capacitors are 12V or above, $\pm 20\%$ except specified.
- 3). All resistors are $\pm 5\%$ except specified.
- 4). In PCB layout, the trace separation between all VCCs and GND should be minimum 2.0mm.
- 5). The surrounding area of all Fuse/polyswitch should be free of traces within 2.0mm of the component.
- 6). * Means adjusted by Factory.
- 7). ** The parts is close to IC as possible.



REVISION HISTORY

1	Final release for production
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BY	DATE
	2 MAY 2014



