

Device Overall Description

The LR802URN3 is designed to provide wireless LAN function on a small form factor with USB interface. The wireless LAN function is based on REALTEK RTL8188ETV CMOS MAC, Baseband PHY, RF single chip and fully comply with current IEEE 802.11n and IEEE 802.11 b/g standards.

Features

- Realtek RTL8188ETV CMOS MAC, Baseband PHY, RF single chip
- Comply with 802.11n specification for 2.4GHz band, 1T1R Modes
- 20MHz and 40MHz bandwidth transmission; Short Guard Interval(400ns)
- DSSS with DBPSK and DQPSK, CCK modulation with long and short preamble
- OFDM with BPSK, QPSK, 16QAM, and 64QAM modulation, Convolutional Coding Rate: 1/2, 2/3, 3/4, and 5/6
- 72.2Mbps receive/transmit PHY rate using 20MHz bandwidth
- 150Mbps receive/transmit PHY rate using 40MHz bandwidth
- Backward compatible with 802.11b/g devices while operating in 802.11n mode
- Frame aggregation for increased MAC efficiency (A-MSDU, A-MPDU)
- Low latency immediate High-Throughput Block Acknowledgement (HT-BA)
- PHY-level spoofing to enhance legacy compatibility
- Power saving mechanism
- Channel management and co-existence
- Transmit Opportunity (TXOP) Short Inter-Frame Space (SIFS) bursting for higher multimedia bandwidth
- 11b: 1,2,5.5,11Mbps
- 11g: 6,9,12,18,24,36,48,54Mbps
- 11n: Legacy and High Throughput Modes, Support 20/40MHz Bandwidth MCS0~7(150Mbps PHY Rate Rate)
- One antenna connector for external antenna, and wire to board host connector for flexible assembly