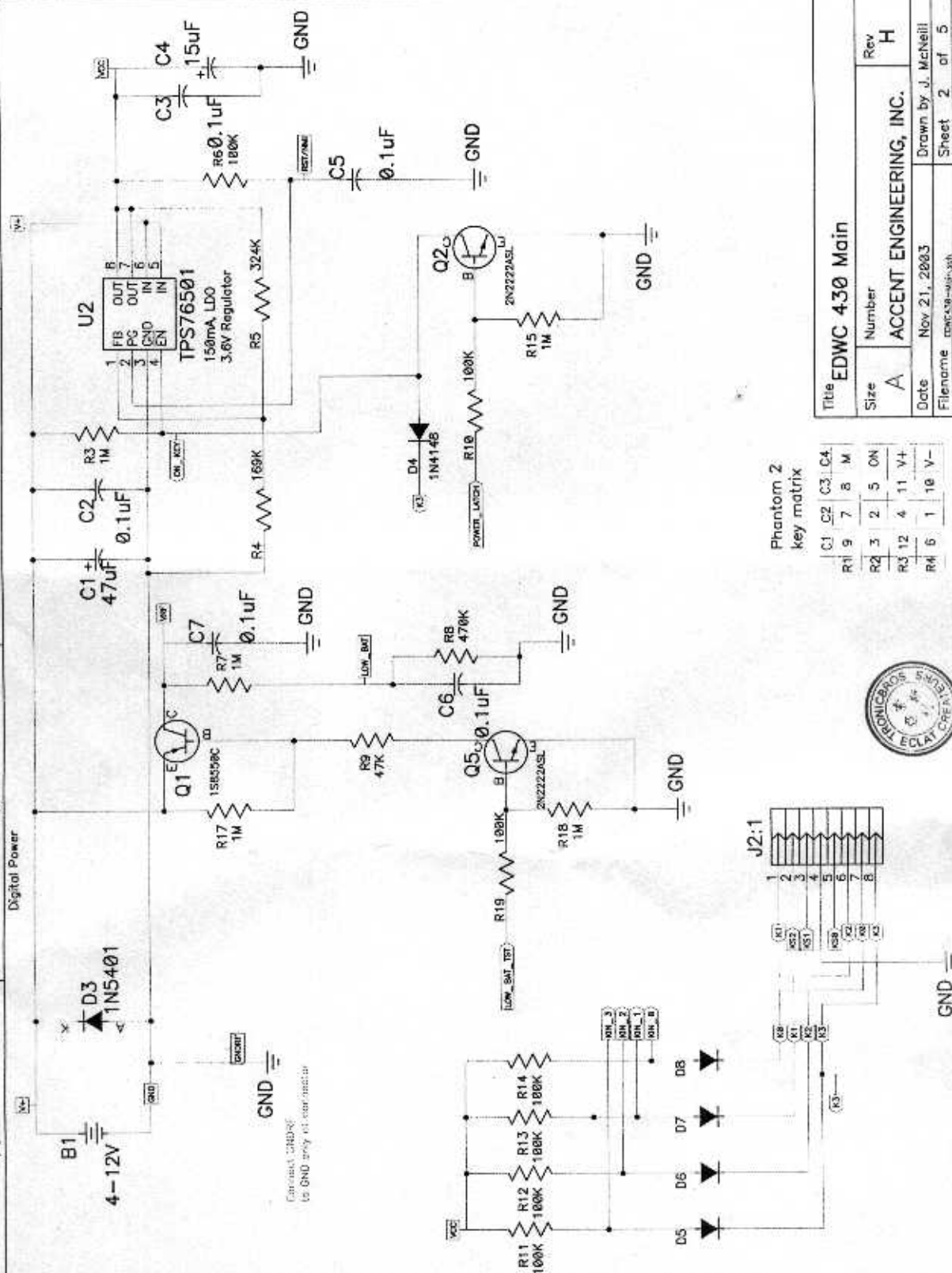
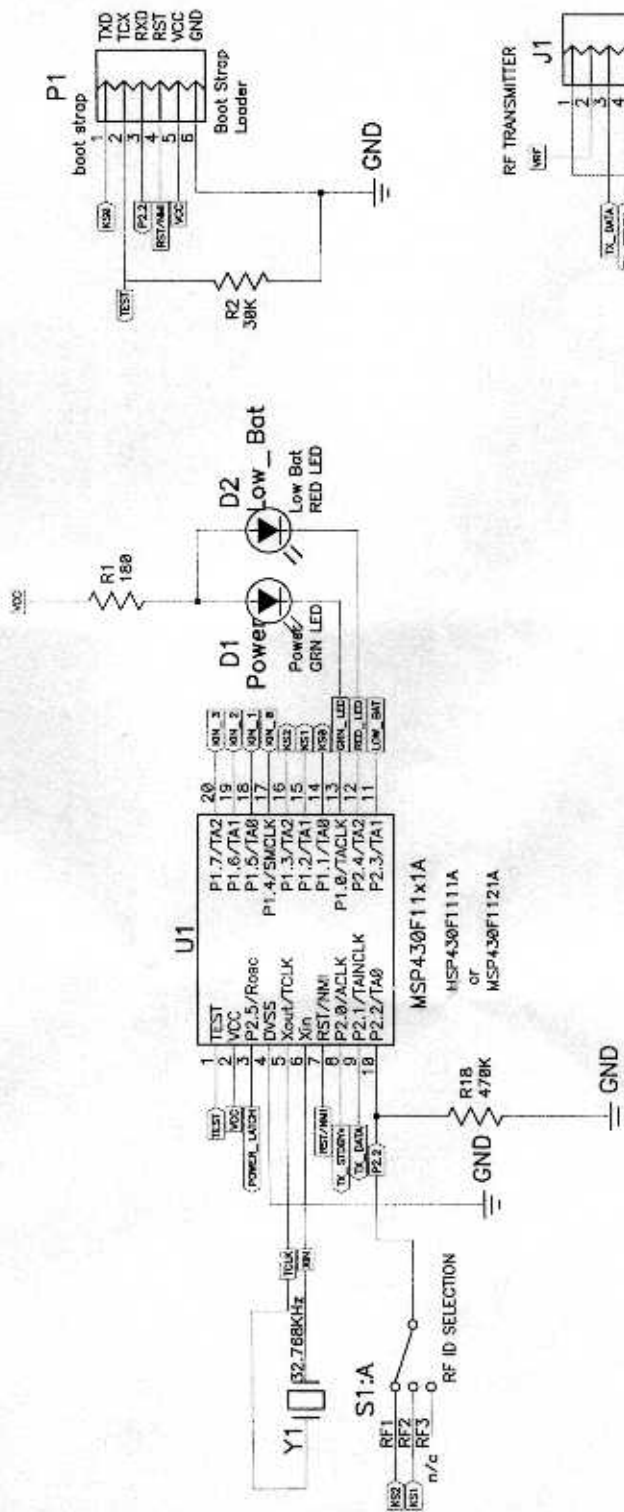








- Rev A: - Mar 25, 2003 - added R35,R36,R37,R4 for battery monitor  
 - added R38,D10,D11 for LED circuit.  
 - Apr 04, 2003 - added S1 for RF ID selection  
 Rev B: - Jun 05, 2003 - changed to 20 pin MSP430F11x1  
 Jun 06, 2003 - updated RF connector, low battery test  
 Rev C: - Aug 01, 2003 - changed Q1 - C from VCC to VRF to eliminate DC-DC converter  
 - added R18 pull-down resistor on U1, pin 18  
 - Sep 01, 2003 - added R19 to keep Q1 off  
 - Oct 02, 2003 - corrected J1(RF TX) pinout  
 Rev D: - Oct 24, 2003 - date/rev clarification  
 Rev E: - Nov 21, 2003 - added D8, C7, changed R8 to 33K, R7 to 100K



Title EDWC 430 TX

| Size     | Number                   | Rev                 |
|----------|--------------------------|---------------------|
| A        | ACCENT ENGINEERING, INC. | E                   |
| Date     | Nov 21, 2003             | Drawn by J. McNeill |
| Filename | EDWC430-TX.sch           | Sheet 1 of 2        |

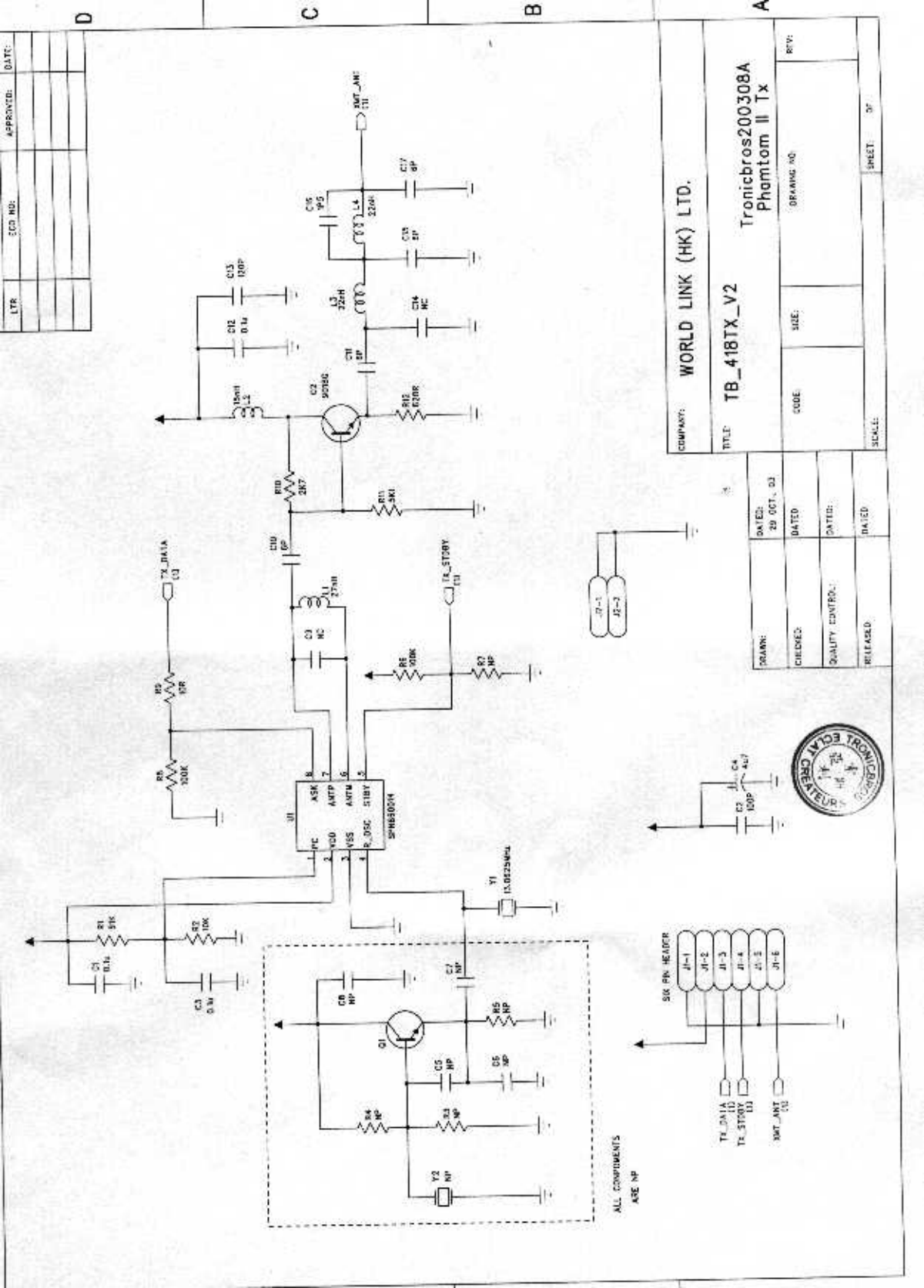
A

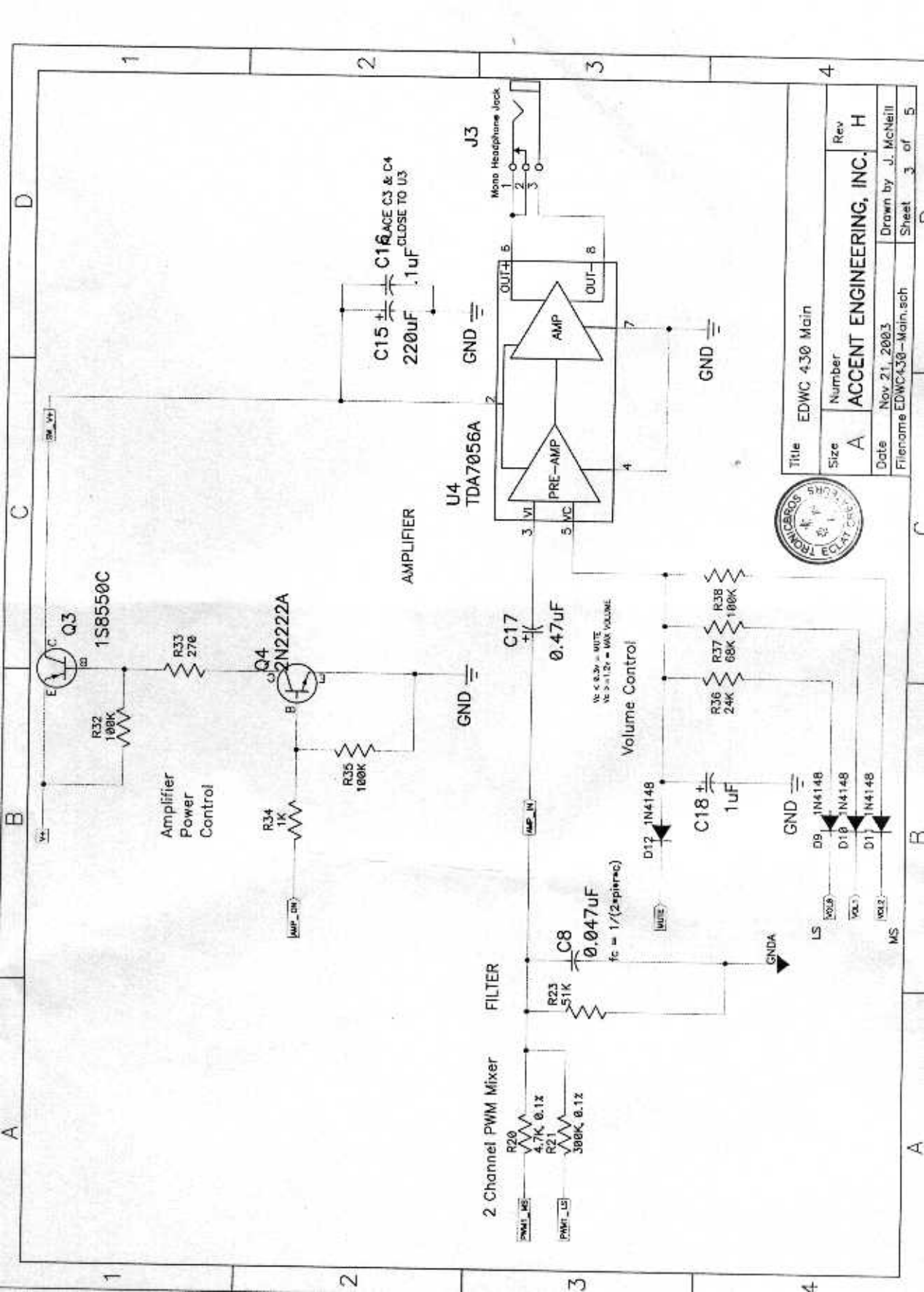
B

C

D







|                           |  |                          |        |       |
|---------------------------|--|--------------------------|--------|-------|
| Title EDWC 430 Main       |  | Size A                   | Number | Rev H |
| Date Nov 21, 2003         |  | Accent Engineering, Inc. |        |       |
| Filename EDWC430-Main.sch |  | Drawn by J. McNeill      |        |       |
|                           |  | Sheet 3 of 5             |        |       |