Appendix for 5A_N77A

Product Name: 5G Indoor CPE
Model No: TITAN 5000

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power（dBm）	Power Class	Verdict
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	23.28	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	24.30	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.39	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.39	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	22.36	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	23.29	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.28	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.29	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	22.81	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	23.78	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	22.95	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	22.72	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	21.77	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	22.82	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	21.85	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	21.52	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	22.28	PC2	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	17.96	PC2	PASS

DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	22.29	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	22.33	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	21.32	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	22.35	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	21.08	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	21.09	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	23.43	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	24.43	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.47	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	23.53	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	22.46	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	23.35	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.52	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.44	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	22.88	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	23.84	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.11	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	22.76	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	21.91	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	22.83	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	22.07	PC2	PASS

DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	21.74	PC2	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	22.33	PC2	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	23.28	PC2	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	22.39	PC2	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	22.43	PC2	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	21.28	PC2	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	22.20	PC2	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	21.17	PC2	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	18.01	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	23.51	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	24.49	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.49	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.43	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	22.45	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	23.50	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.51	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.48	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	22.93	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	23.93	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.18	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.79	PC2	PASS

DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	21.91	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	22.93	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	22.21	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	21.74	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	22.27	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	23.27	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.35	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.34	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	21.29	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	18.15	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.37	PC2	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	18.06	PC2	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	23.41	PC2	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Inner_Full	24.37	PC2	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.60	PC2	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	23.45	PC2	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	22.45	PC2	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Inner_Full	23.44	PC2	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	22.51	PC2	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	22.39	PC2	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	22.99	PC2	PASS

DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+M	Inner_Full	23.99	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	23.43	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	22.78	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	22.10	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+M	Inner_Full	23.02	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	22.40	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	21.77	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	22.41	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+H	Inner_Full	23.45	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	22.66	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	22.46	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	21.46	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+H	Inner_Full	22.41	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	21.53	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	21.28	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	23.53	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	24.53	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.67	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.17	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	22.46	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	23.55	PC2	PASS

DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.46	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.04	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	23.04	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	23.96	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.67	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	22.77	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	22.20	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	23.01	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	22.68	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	21.73	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	22.58	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	23.53	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.90	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	22.53	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	21.58	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	22.43	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	21.92	PC2	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	21.52	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	23.10	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	24.19	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.45	PC2	PASS

DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	22.57	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	22.19	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	23.29	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.27	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	21.30	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	22.81	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	23.69	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.50	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	22.36	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	21.80	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	22.74	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	22.24	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	21.29	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	22.33	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	23.32	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.74	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	22.20	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	21.30	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	22.27	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	21.62	PC2	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	21.03	PC2	PASS

DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	23.05	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	24.21	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.32	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	22.42	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	22.05	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	23.22	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.20	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	21.08	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	22.77	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	23.80	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.47	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	22.26	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	21.78	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	22.78	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	22.39	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	21.18	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	22.44	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	23.52	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.00	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.13	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	21.53	PC2	PASS

DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	22.57	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	21.85	PC2	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	20.93	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	22.88	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	23.55	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.22	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	22.27	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	21.88	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	22.91	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.31	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	21.31	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	22.84	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	23.67	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.30	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	21.63	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	21.73	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	22.68	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	22.27	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	20.96	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	22.44	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	23.34	PC2	PASS

DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.11	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	22.05	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	21.44	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	22.40	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	21.98	PC2	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	20.88	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	22.80	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	23.84	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.31	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	22.14	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	21.88	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	22.83	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.23	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	21.08	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	22.65	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	23.70	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.28	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.07	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	21.68	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	22.63	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	22.36	PC2	PASS

DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	20.98	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	22.52	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	23.42	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.30	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	22.09	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	21.56	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	22.43	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.36	PC2	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	21.11	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	22.74	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	23.79	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.33	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	22.08	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	21.70	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	22.75	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.07	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	20.94	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	22.67	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	23.67	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.31	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	22.12	PC2	PASS

DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	21.68	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	22.70	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	22.27	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	21.07	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	22.62	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	23.60	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.27	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.12	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	21.55	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	22.62	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.20	PC2	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	21.08	PC2	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	22.72	PC2	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	23.72	PC2	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.14	PC2	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	22.11	PC2	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	21.68	PC2	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	22.69	PC2	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.09	PC2	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	20.98	PC2	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	22.69	PC2	PASS

DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	23.70	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.25	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.09	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	21.70	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	22.69	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.05	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.03	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	22.68	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	23.76	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.15	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.07	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	21.71	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	22.69	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.17	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	21.06	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	6.01	≤13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	6.44	≤13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	6.12	≤13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	6.38	≤13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	4.86	≤13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	5.90	≤13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	5.84	≤13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	6.74	≤13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	5.86	≤13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	6.69	≤13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	5.00	≤13	PASS
DC_5A_n7	30	5+15	DFT-16QAM	M+H	Outer_Full	5.87	≤13	PASS

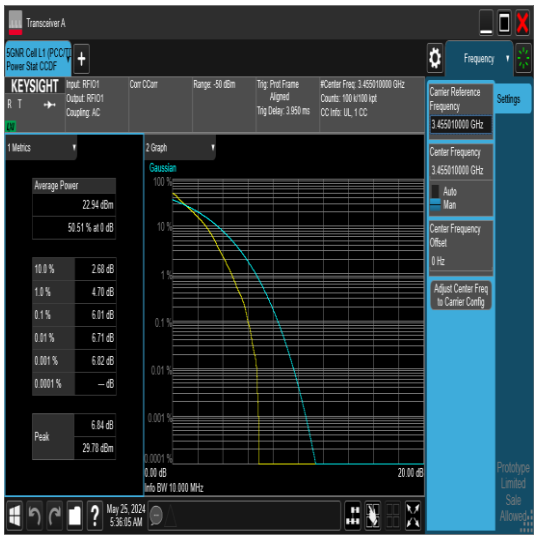
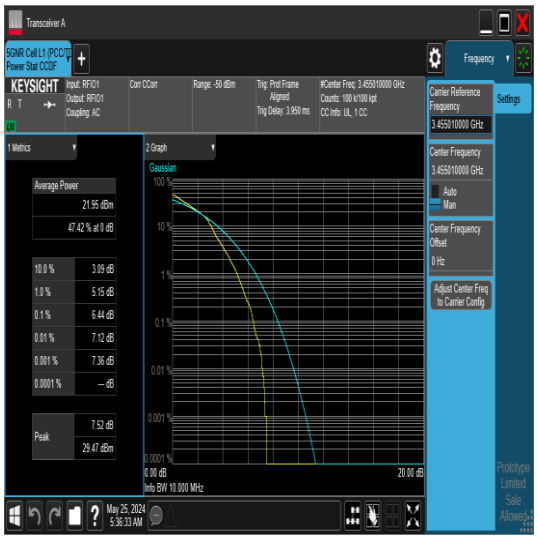
7A-3450-35 50								
DC_5A_n7 7A-3450-35 50	30	5+20	DFT-QPSK	M+L	Outer_Full	5.92	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+20	DFT-16QAM	M+L	Outer_Full	6.78	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+20	DFT-QPSK	M+M	Outer_Full	5.88	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+20	DFT-16QAM	M+M	Outer_Full	6.62	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+20	DFT-QPSK	M+H	Outer_Full	5.27	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+20	DFT-16QAM	M+H	Outer_Full	6.16	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+30	DFT-QPSK	M+L	Outer_Full	5.93	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+30	DFT-16QAM	M+L	Outer_Full	6.58	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+30	DFT-QPSK	M+M	Outer_Full	5.96	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+30	DFT-16QAM	M+M	Outer_Full	6.70	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+30	DFT-QPSK	M+H	Outer_Full	5.11	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+30	DFT-16QAM	M+H	Outer_Full	6.25	≤13	PASS
DC_5A_n7 7A-3450-35 50	30	5+40	DFT-QPSK	M+L	Outer_Full	6.05	≤13	PASS
DC_5A_n7 7A-3450-35	30	5+40	DFT-16QAM	M+L	Outer_Full	6.69	≤13	PASS

50								
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	5.99	≤13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	6.66	≤13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	5.54	≤13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	6.25	≤13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	6.06	≤13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	6.63	≤13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	6.08	≤13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	6.67	≤13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	5.79	≤13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	6.49	≤13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6.05	≤13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	6.74	≤13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6.09	≤13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	6.71	≤13	PASS

DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	5.99	≤13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	6.67	≤13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	6.09	≤13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	6.88	≤13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	6.10	≤13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	6.86	≤13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	6.08	≤13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	6.80	≤13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	5.09	≤13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	6.74	≤13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6.07	≤13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	6.69	≤13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6.04	≤13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	6.76	≤13	PASS
DC_5A_n7	30	5+90	DFT-QPSK	M+L	Outer_Full	5.06	≤13	PASS

7A-3450-3550								
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	6.79	≤13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	5.14	≤13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	6.26	≤13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	5.07	≤13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	6.72	≤13	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	5.13	≤13	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	6.78	≤13	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	5.20	≤13	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	6.80	≤13	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	5.13	≤13	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	6.76	≤13	PASS

Test Graphs

NTNV_DC_5A_n77A-3450-3550_PC2_30_5+10_M+L_TID2_#1	NTNV_DC_5A_n77A-3450-3550_PC2_30_5+10_M+L_TID3_#1
	
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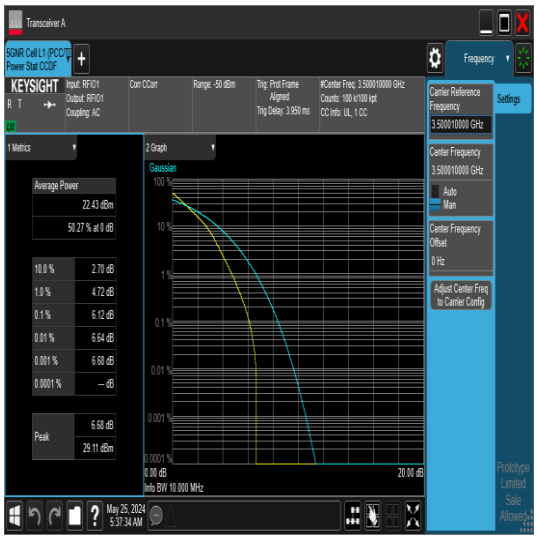	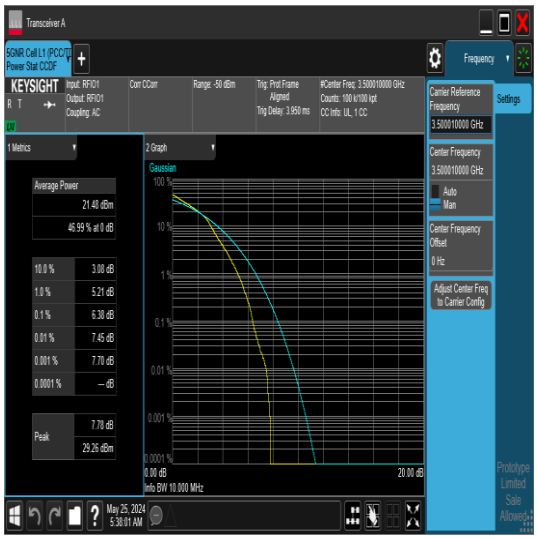
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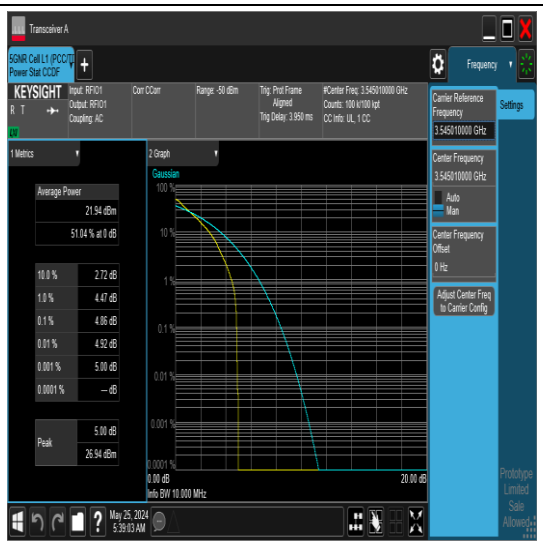

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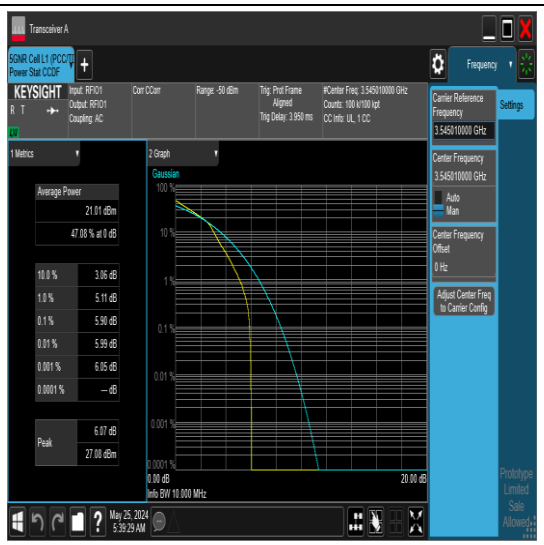

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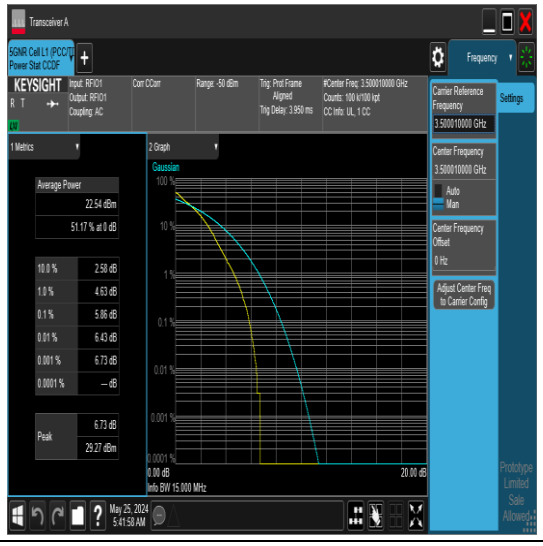

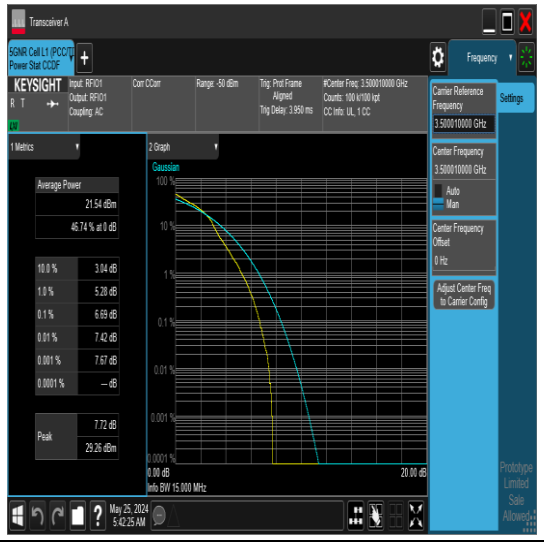

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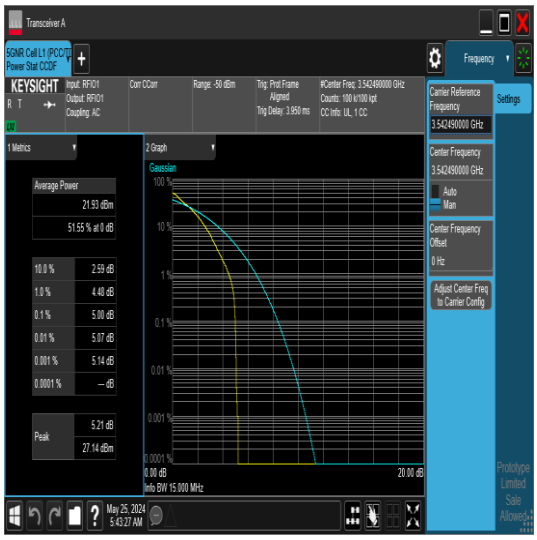	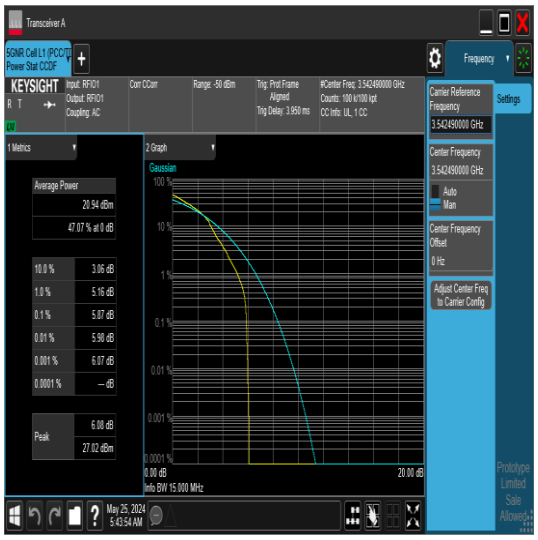
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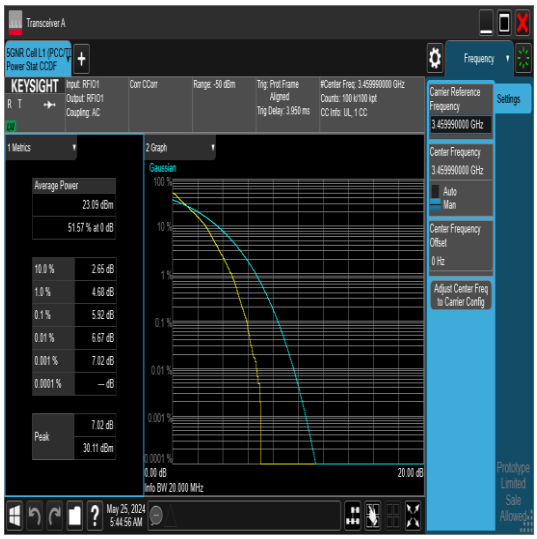	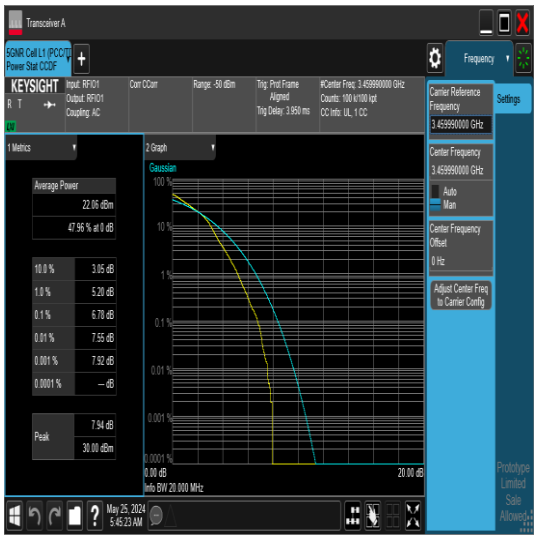
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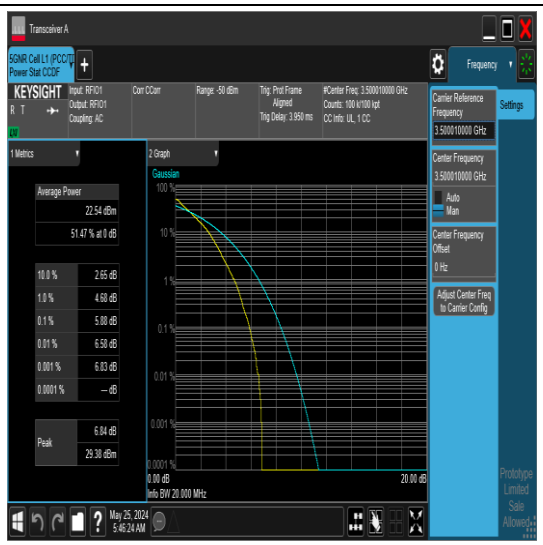

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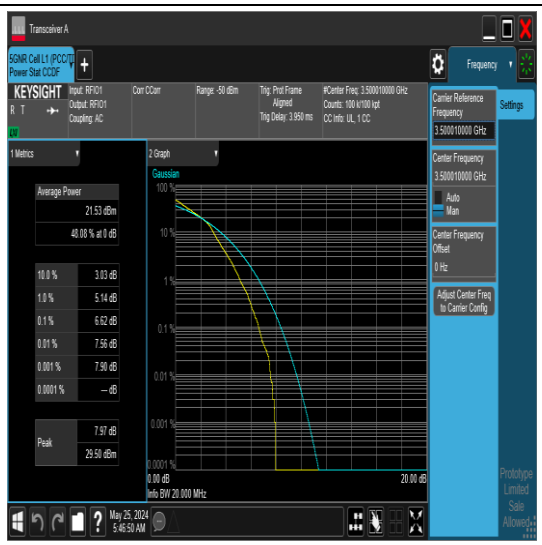

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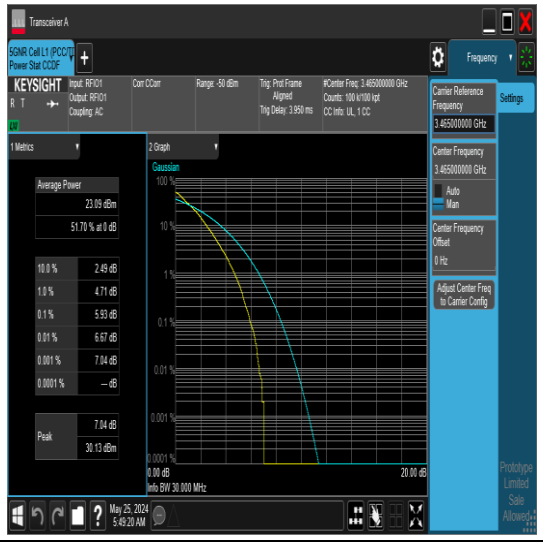

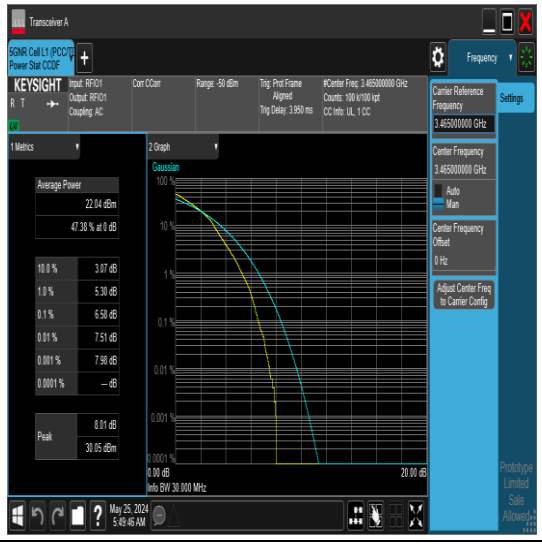

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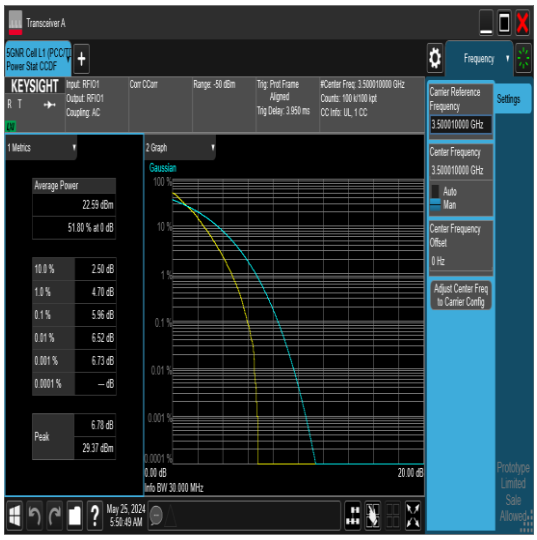	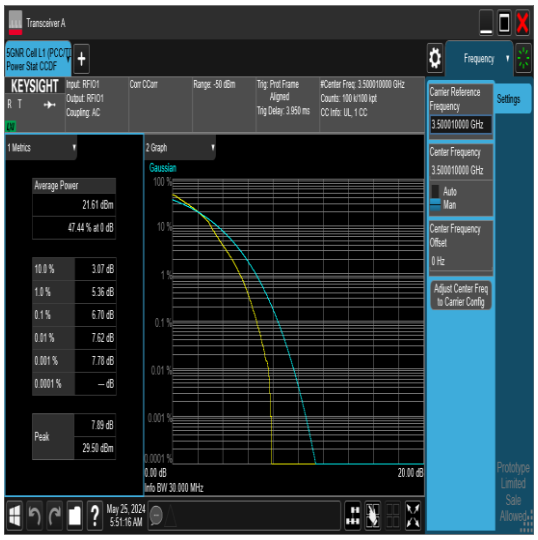
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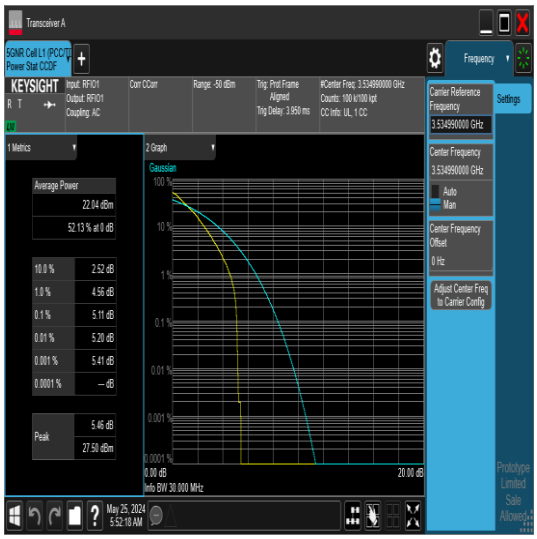	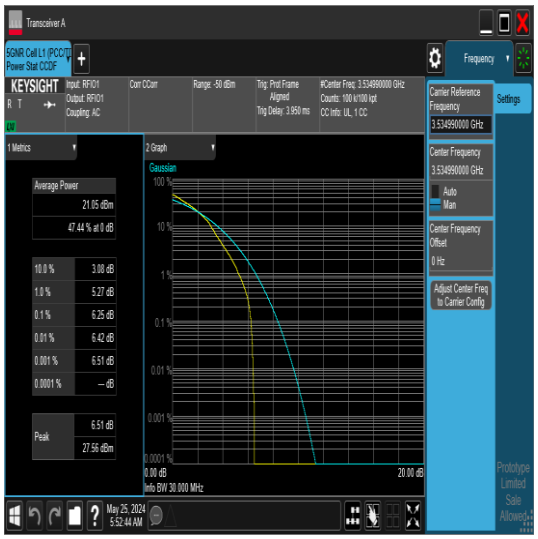
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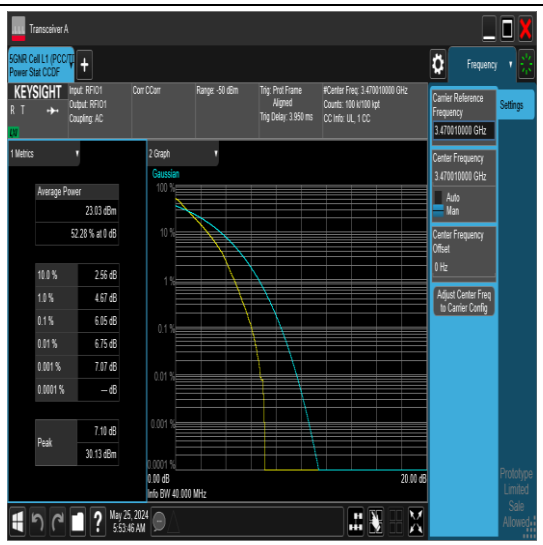

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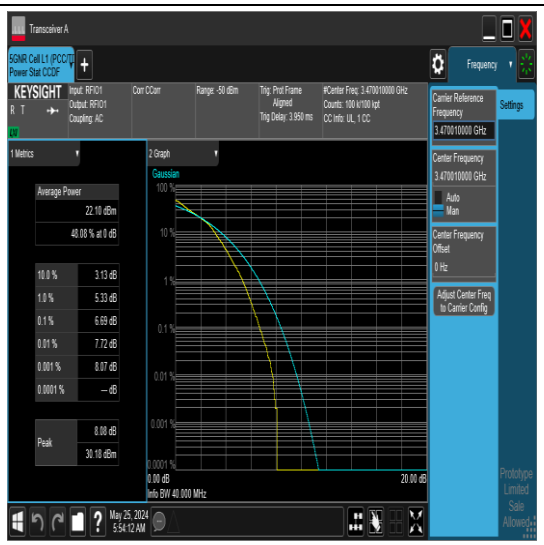

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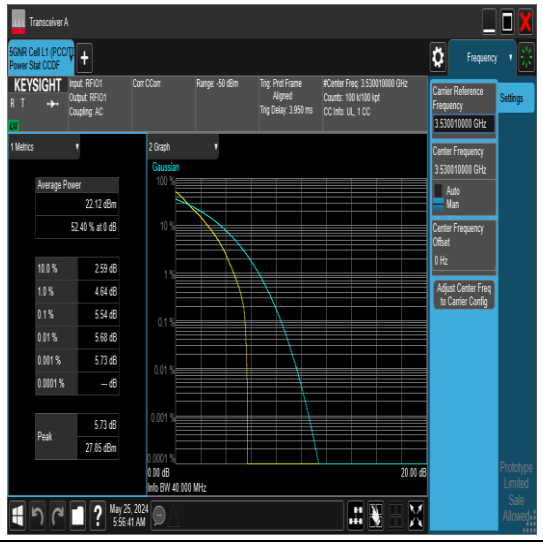

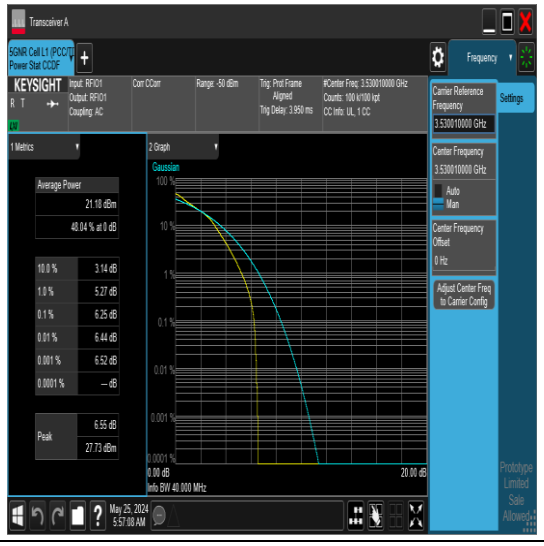

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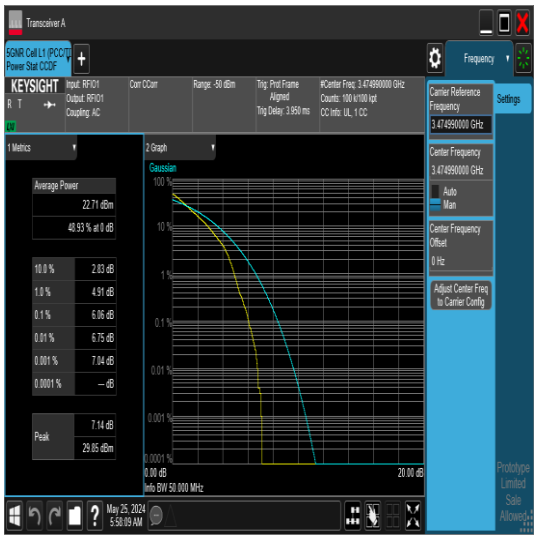	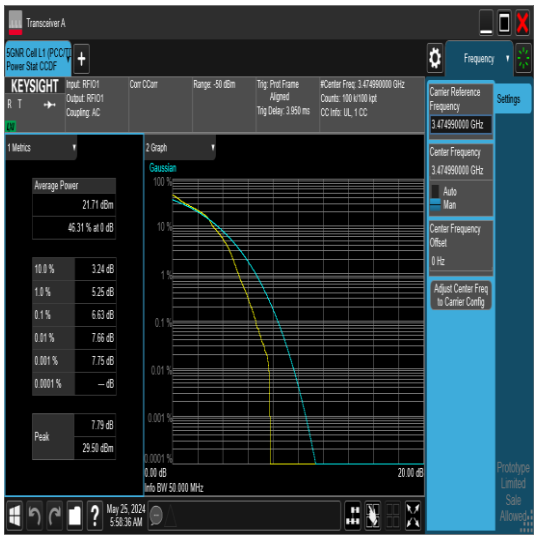
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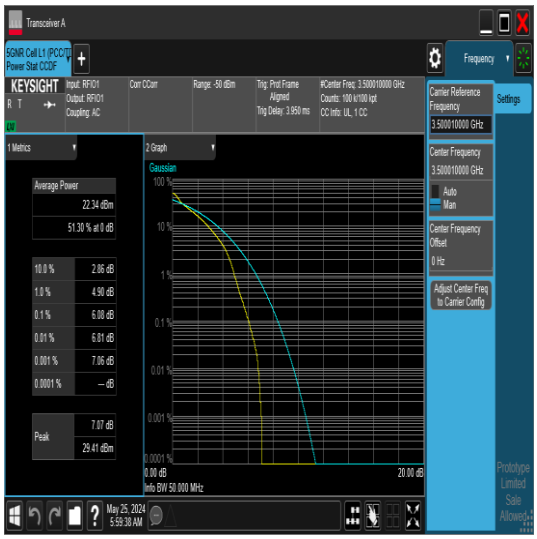	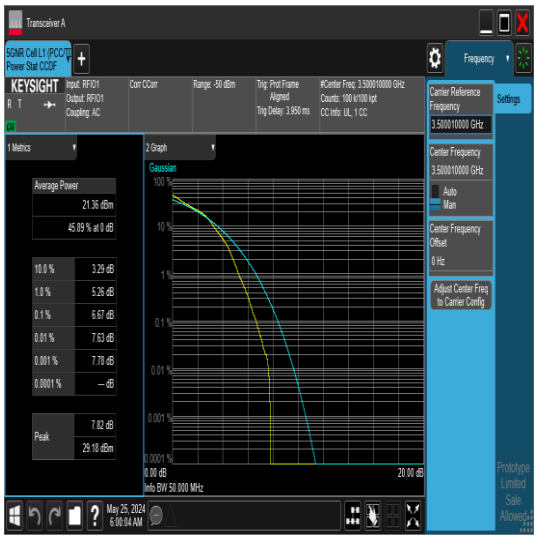
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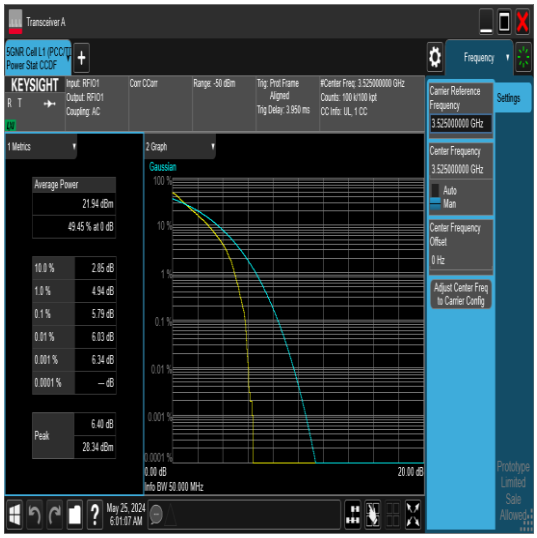

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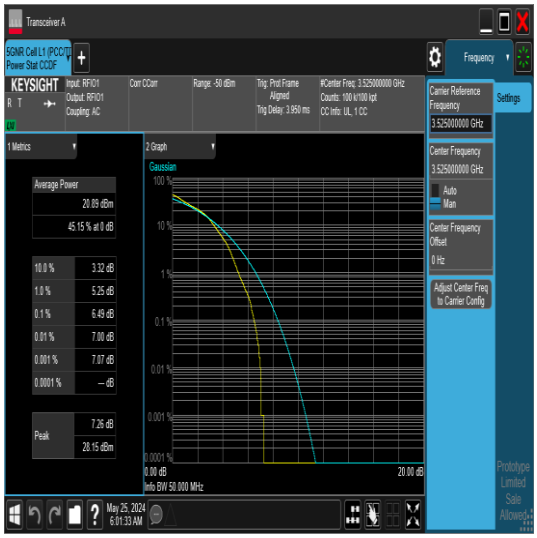

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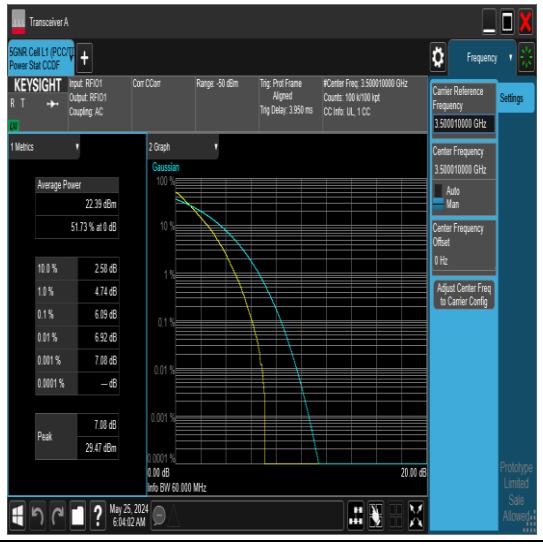

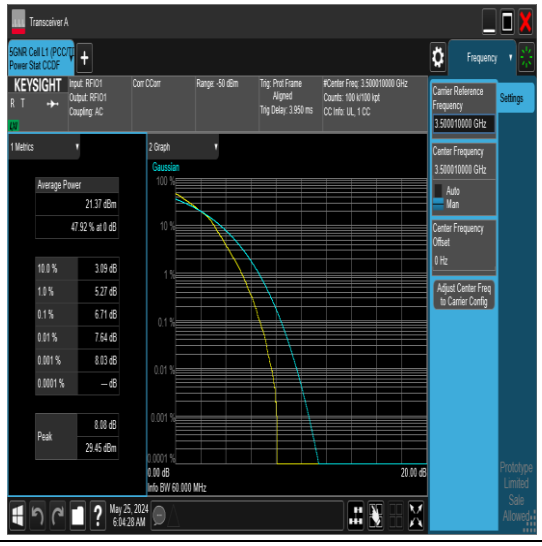

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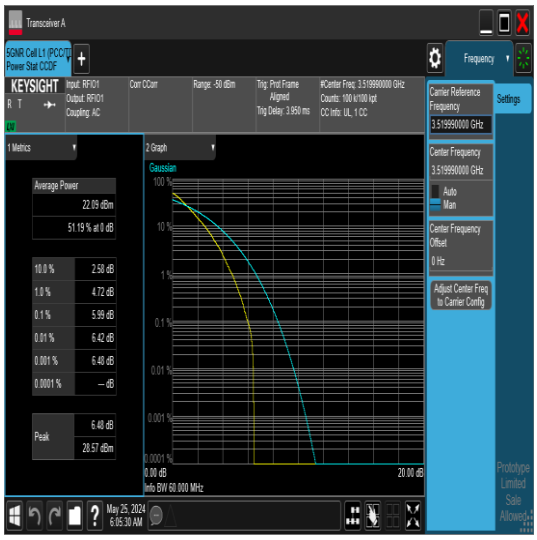	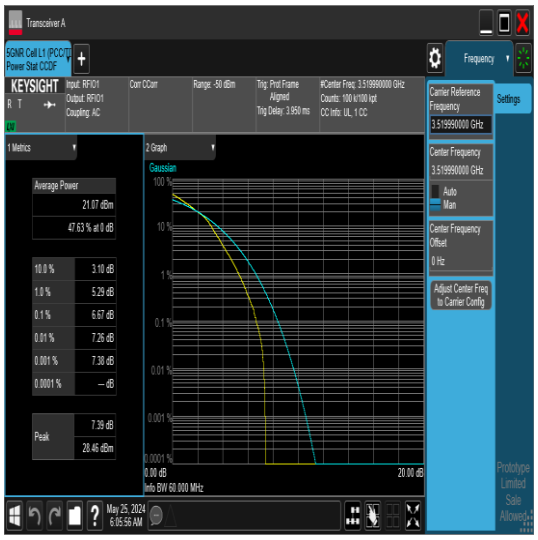
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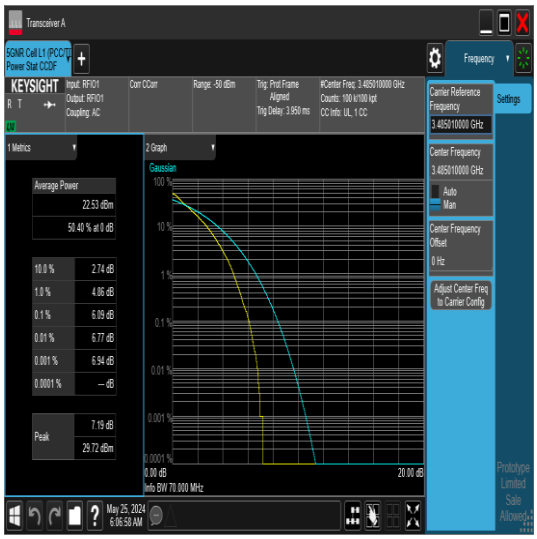	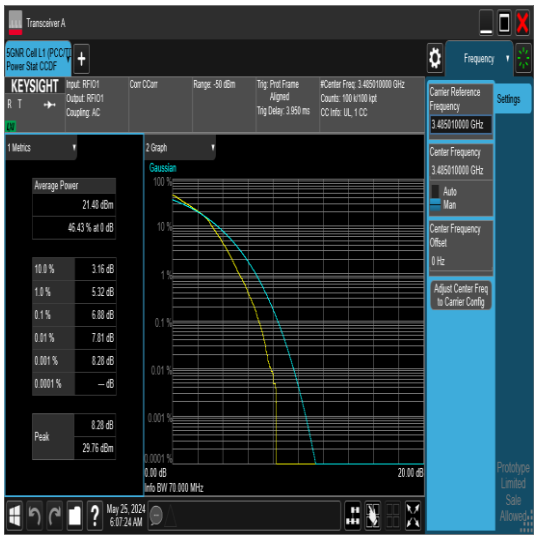
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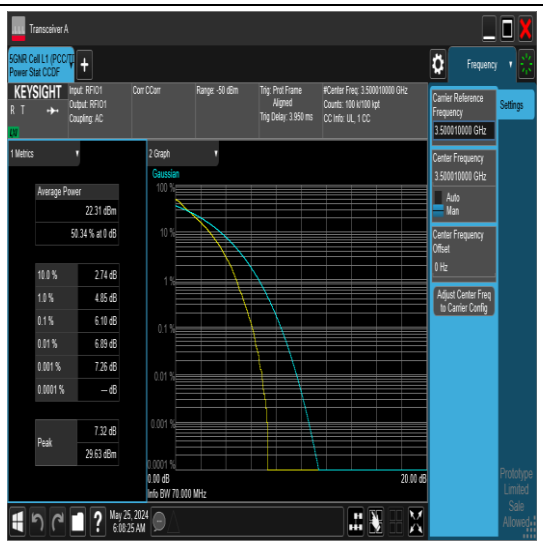

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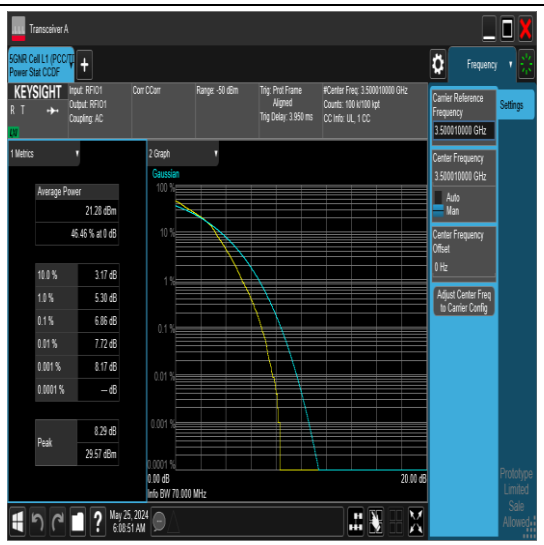

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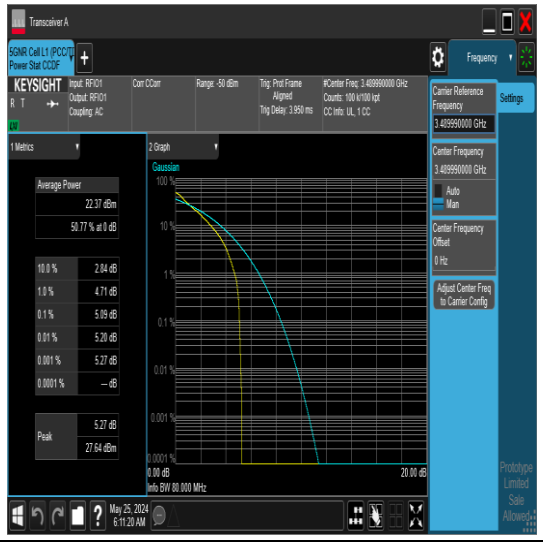

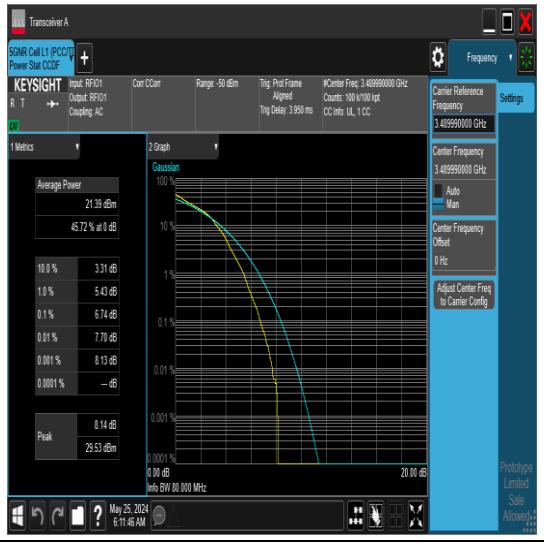

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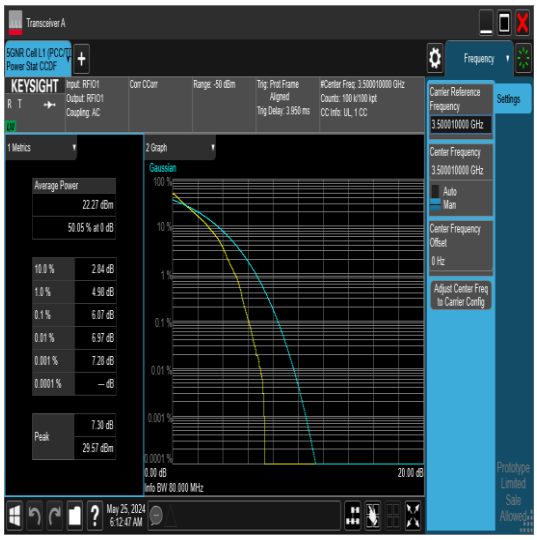	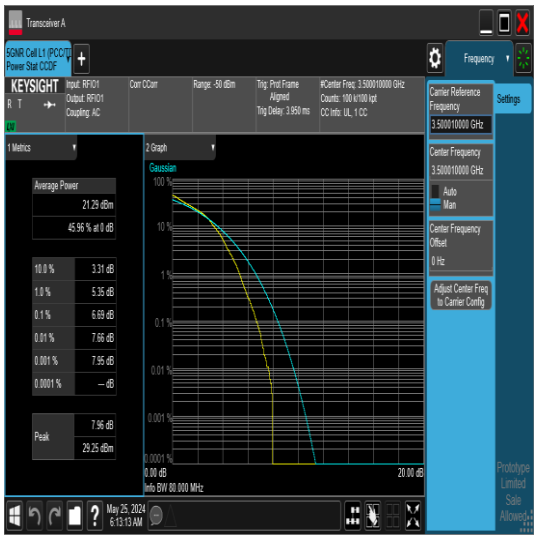
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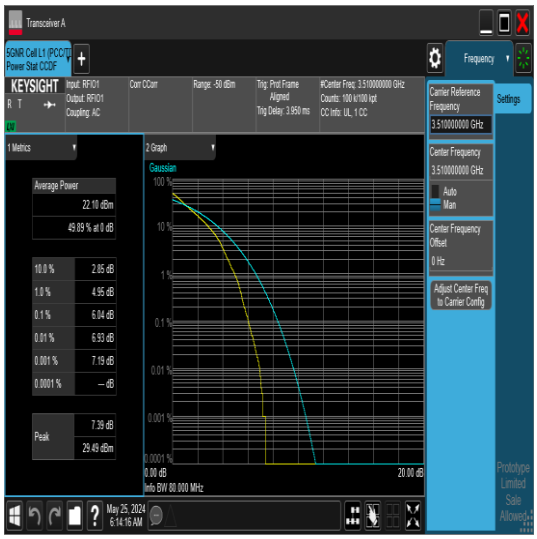	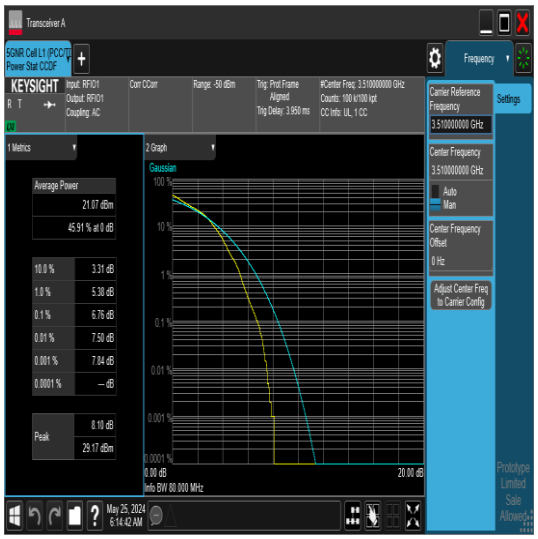
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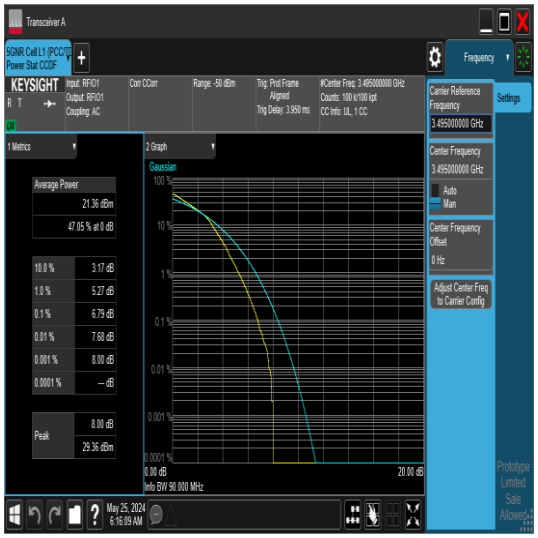

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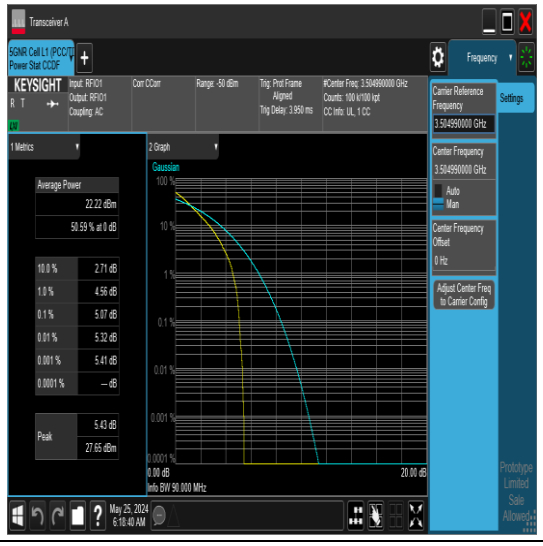

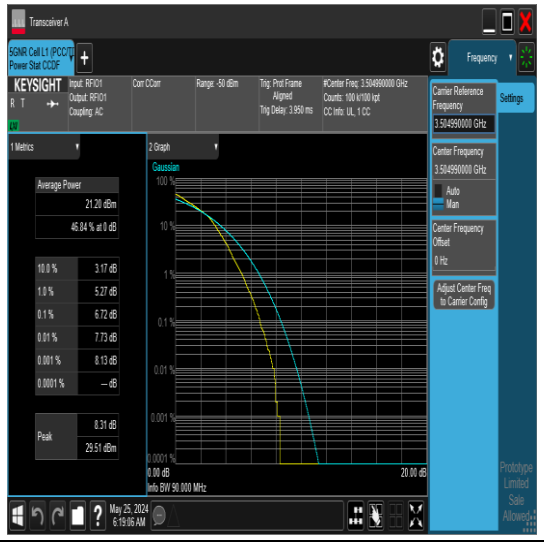

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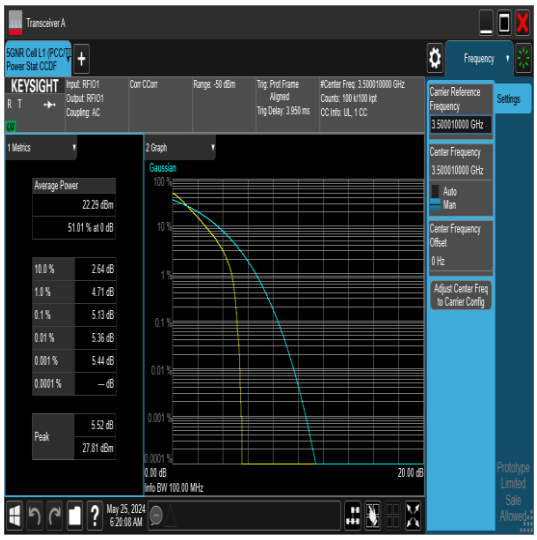	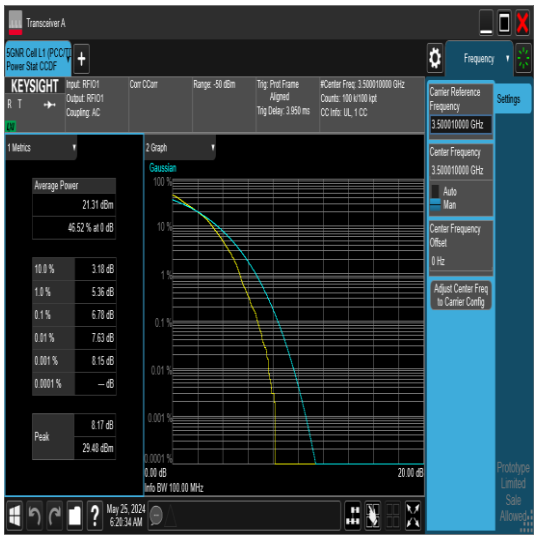
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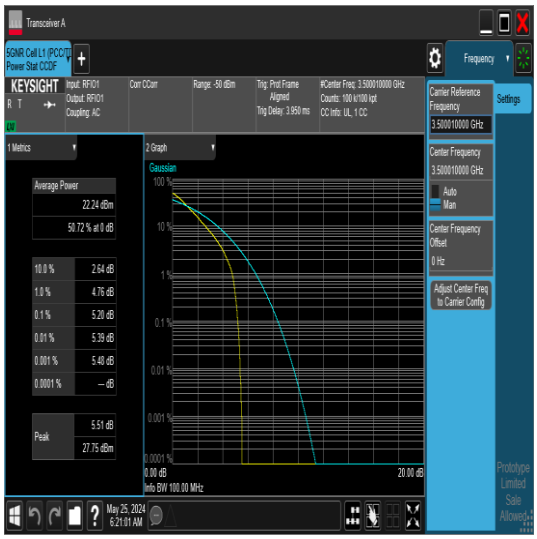	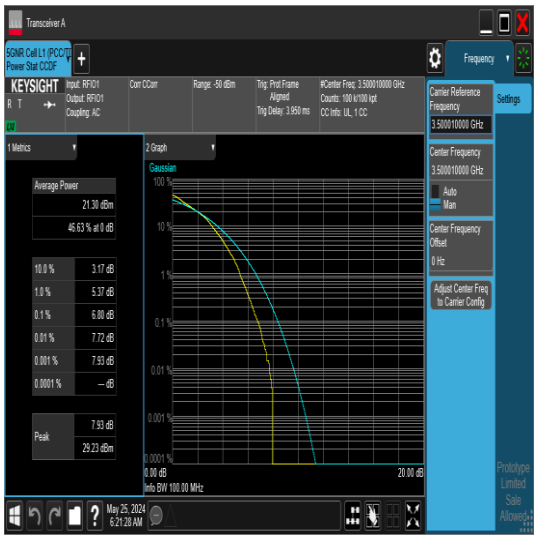
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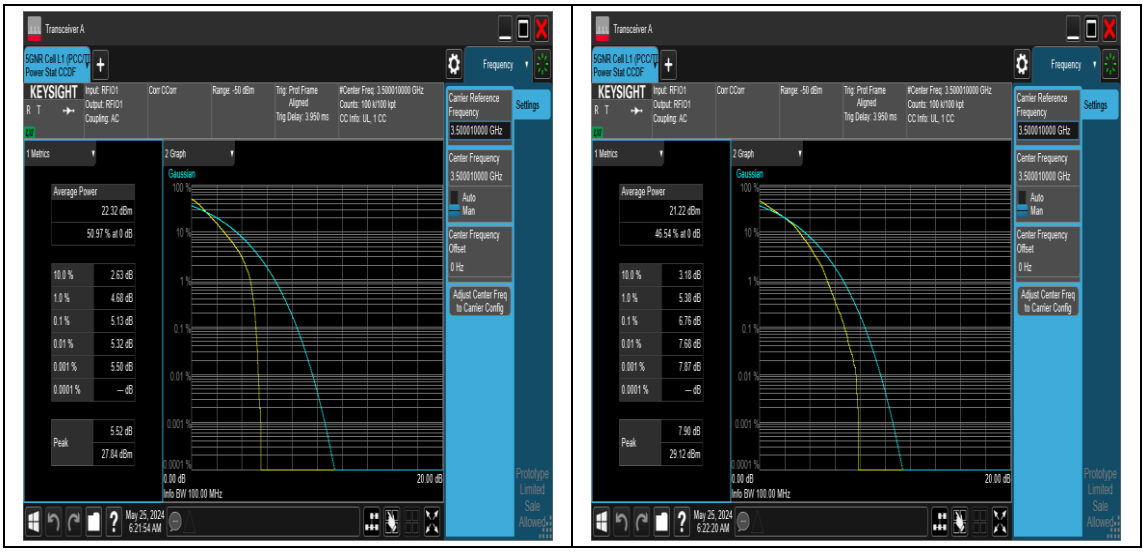

Transceiver A
5GNR Cell L1 (PCC/T)
Power Stat CCDF
Frequency
KEYSIGHT
Input: RF/D1
Output: RF/D1
Coupling: AC
Corr CCorr
Range: -50 dBm
Trig: Prot Frame Aligned
Trig Delay: 3.950 ms
fCenter Freq: 3.500010000 GHz
Counts: 100 k/100 kpt
CC Info: UL, 1 CC
Carrier Reference Frequency
3.500010000 GHz
Settings
1 Metrics
2 Graph
Gaussian
Average Power
22.32 dBm
50.97 % at 0 dB
10.0 % 2.63 dB
1.0 % 4.68 dB
0.1 % 5.13 dB
0.01 % 5.32 dB
0.001 % 5.50 dB
0.0001 % – dB
Peak 5.52 dB
27.84 dBm
100 %
10 %
1 %
0.1 %
0.01 %
0.001 %
0.0001 %
0.00 dB
20.00 dB
Info BW 100.00 MHz
Center Frequency
3.500010000 GHz
Auto
Man
Center Frequency Offset
0 Hz
Adjust Center Freq to Carrier Config
May 25, 2024
6:21:54 AM
Prototype Limited Sale Allowed
Transceiver A
5GNR Cell L1 (PCC/T)
Power Stat CCDF
Frequency
KEYSIGHT
Input: RF/D1
Output: RF/D1
Coupling: AC
Corr CCorr
Range: -50 dBm
Trig: Prot Frame Aligned
Trig Delay: 3.950 ms
fCenter Freq: 3.500010000 GHz
Counts: 100 k/100 kpt
CC Info: UL, 1 CC
Carrier Reference Frequency
3.500010000 GHz
Settings
1 Metrics
2 Graph
Gaussian
Average Power
21.22 dBm
46.54 % at 0 dB
10.0 % 3.18 dB
1.0 % 5.30 dB
0.1 % 6.76 dB
0.01 % 7.60 dB
0.001 % 7.87 dB
0.0001 % – dB
Peak 7.90 dB
29.12 dBm
100 %
10 %
1 %
0.1 %
0.01 %
0.001 %
0.0001 %
0.00 dB
20.00 dB
Info BW 100.00 MHz
Center Frequency
3.500010000 GHz
Auto
Man
Center Frequency Offset
0 Hz
Adjust Center Freq to Carrier Config
May 25, 2024
6:22:20 AM
Prototype Limited Sale Allowed

Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

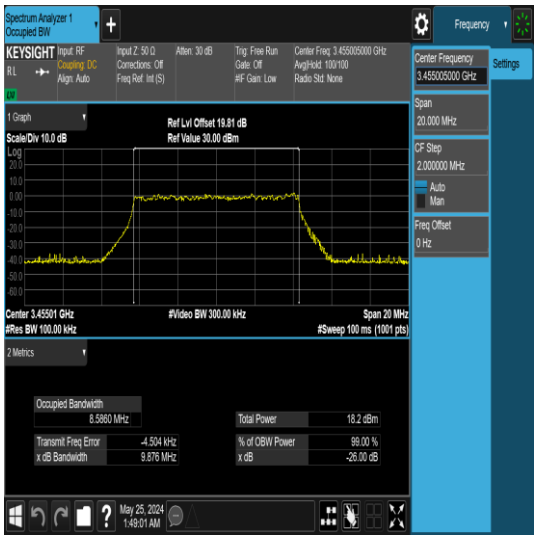
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	8.6036	9.746	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	8.5860	9.876	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	8.6249	9.932	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	8.5854	9.803	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	8.5973	9.683	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	8.5783	9.692	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	12.897	14.29	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	12.897	14.39	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	12.888	14.26	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	12.887	14.15	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	12.890	14.26	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	12.891	14.37	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	17.854	19.21	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	17.833	19.55	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	17.882	19.29	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	17.798	19.40	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	17.837	19.47	PASS

DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	17.818	19.23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	26.840	28.66	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	26.788	28.70	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	26.818	28.52	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	26.792	28.71	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	26.760	28.57	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	26.788	28.72	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	35.751	37.91	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	35.743	37.77	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	35.714	37.80	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	35.681	37.71	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	35.759	37.66	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	35.666	37.72	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	45.754	47.96	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	45.682	47.85	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	45.741	47.94	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	45.625	48.01	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	45.640	47.92	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	45.611	47.85	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	57.804	60.47	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	57.909	60.37	PASS

DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	57.737	60.39	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	57.885	60.53	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	57.669	60.48	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	57.695	60.55	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	64.390	66.97	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	64.247	67.00	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	64.371	66.94	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	64.167	66.80	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	64.215	67.02	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	64.051	66.72	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	77.150	80.13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	77.129	80.08	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	77.095	80.12	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	77.134	80.16	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	76.943	80.19	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	77.116	80.14	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	86.897	90.12	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	86.669	90.04	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	86.762	90.09	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	86.615	122.8	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	86.784	109.6	PASS

DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	86.524	90.07	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	96.243	99.98	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	96.388	123.0	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	96.370	99.87	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	96.385	123.4	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	96.239	99.91	PASS
DC_5A_n77A-3450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	96.354	99.99	PASS

Test Graphs

NTNV_DC_5A_n77A-3450-3550_PC2_30_5+10_M+L_TID1_Ant1#1	NTNV_DC_5A_n77A-3450-3550_PC2_30_5+10_M+L_TID3_Ant1#1
	
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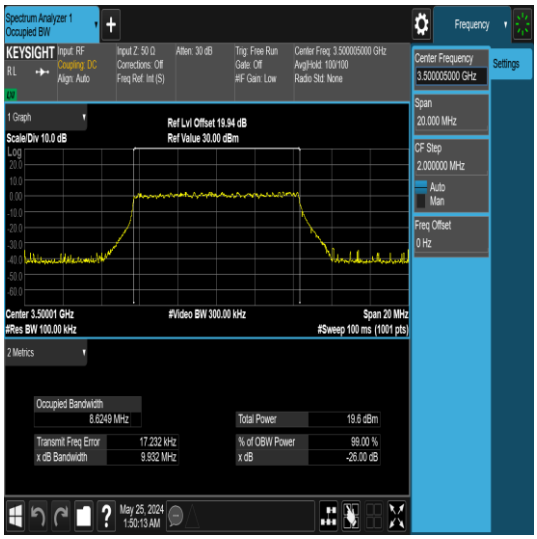	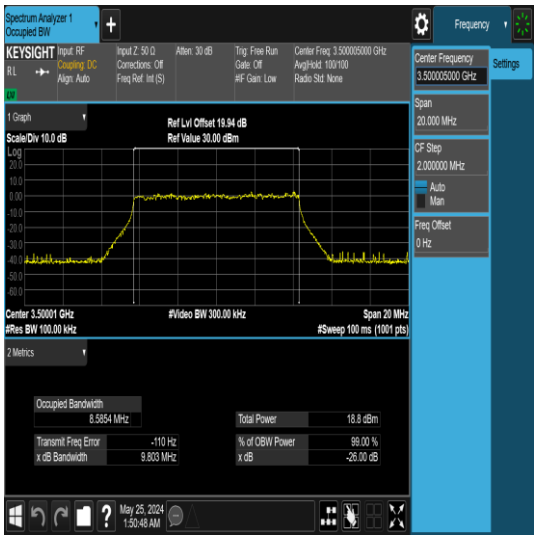
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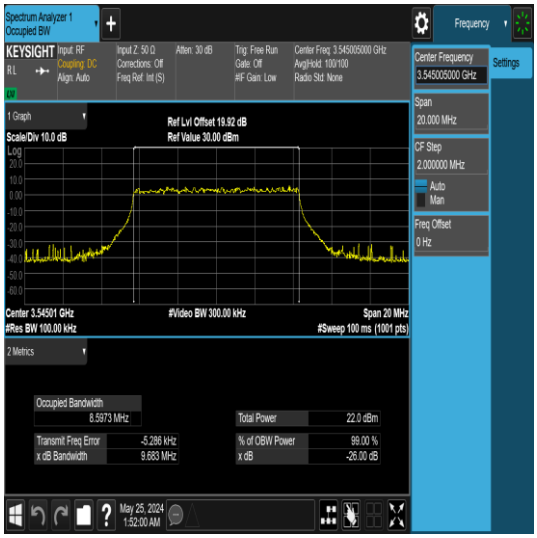

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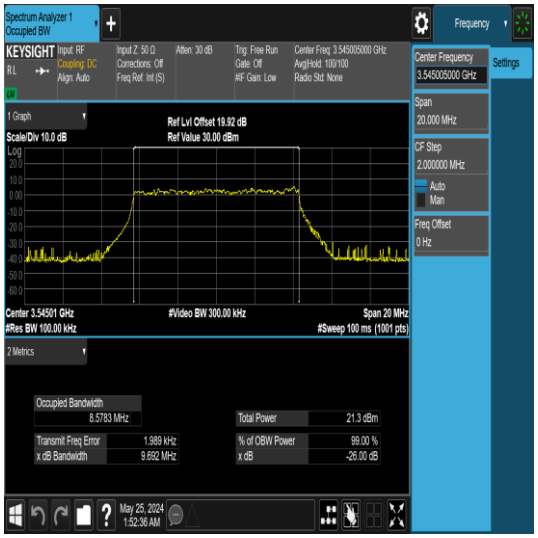

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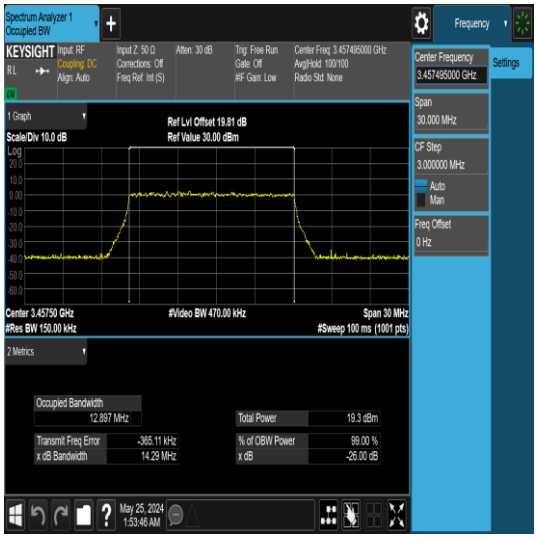

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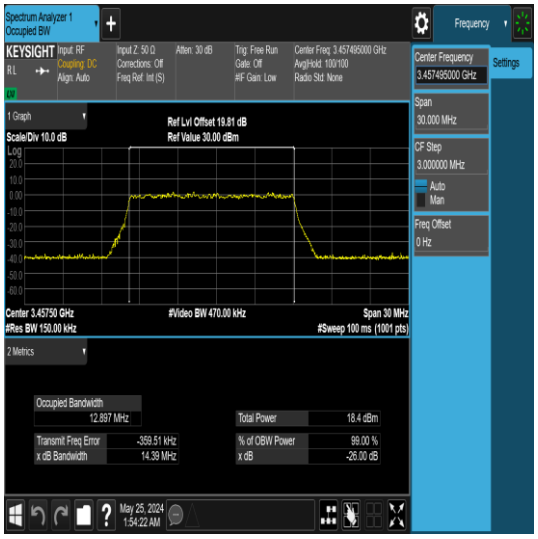

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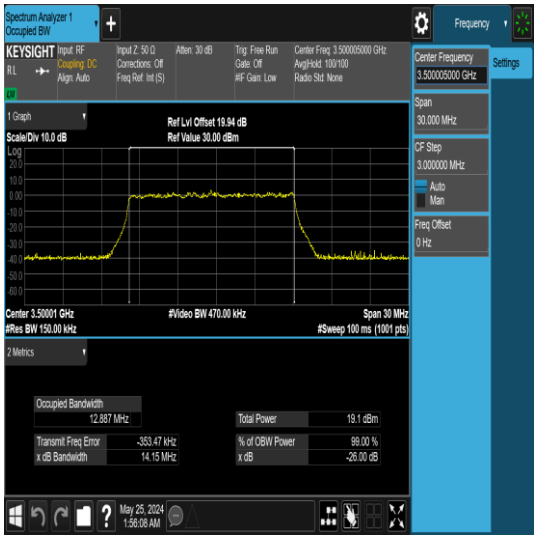

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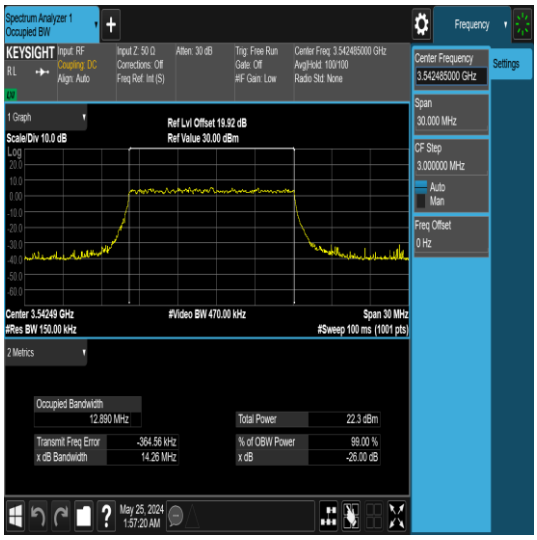	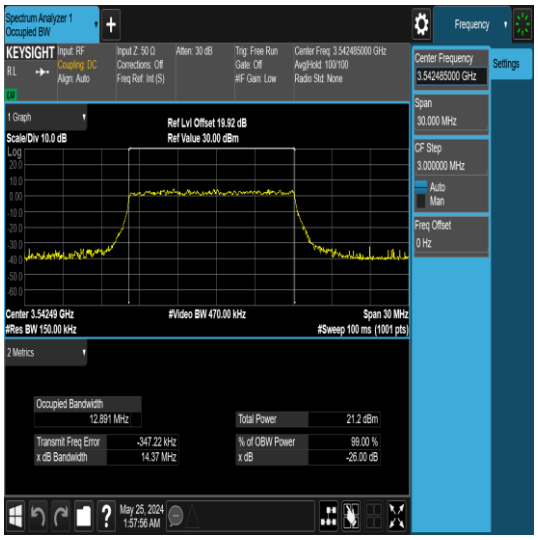
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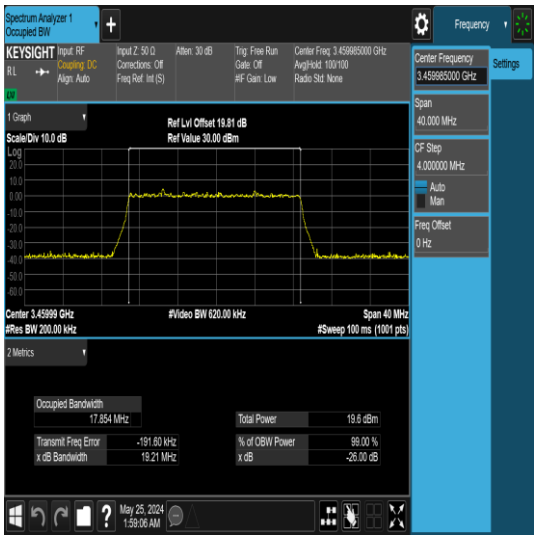	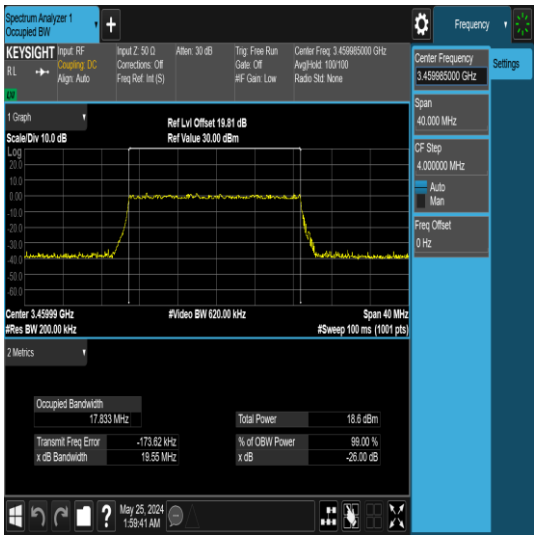
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