Appendix for 5A_N77A

Product Name: 5G Indoor CPE
Model No: TITAN 5000

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	23.33	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Inner_Full	24.30	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.24	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.32	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	22.24	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Inner_Full	23.41	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.15	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.05	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	23.36	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Inner_Full	24.35	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	23.45	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	23.35	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	22.44	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Inner_Full	23.40	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	22.44	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	22.27	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	24.99	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Inner_Full	25.99	PC2	PASS

DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	24.94	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	24.96	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	20.31	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Inner_Full	24.95	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	23.89	PC2	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	23.84	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	23.43	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Inner_Full	24.36	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.29	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	23.52	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	22.39	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Inner_Full	23.38	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.34	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.44	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	23.46	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Inner_Full	24.44	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.50	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	23.55	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	22.43	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Inner_Full	23.38	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	22.52	PC2	PASS

DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	22.47	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	24.85	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Inner_Full	25.81	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	24.80	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	25.04	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	23.84	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Inner_Full	24.80	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	23.72	PC2	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	23.81	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	23.43	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Inner_Full	24.37	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.23	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.57	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	22.42	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Inner_Full	23.43	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.30	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.53	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	23.44	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Inner_Full	24.44	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.60	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.51	PC2	PASS

DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	22.48	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Inner_Full	23.51	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	22.46	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	22.29	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	24.86	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Inner_Full	25.89	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	24.78	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	25.30	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	23.85	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Inner_Full	24.93	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	23.59	PC2	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	23.87	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	23.45	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Inner_Full	24.43	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.32	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	23.61	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	22.51	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Inner_Full	23.47	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	22.35	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	22.32	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	23.41	PC2	PASS

DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Inner_Full	24.41	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	23.58	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	23.36	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	22.49	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Inner_Full	23.44	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	22.65	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	22.44	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	24.81	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Inner_Full	25.77	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	24.80	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	24.90	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	23.86	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Inner_Full	24.83	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	23.77	PC2	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	23.97	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	23.60	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Inner_Full	24.60	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.66	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.66	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	22.64	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Inner_Full	23.63	PC2	PASS

DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.75	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.74	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	23.49	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Inner_Full	24.42	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.78	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.50	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	22.50	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Inner_Full	23.35	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	22.83	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	22.41	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	24.79	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Inner_Full	20.14	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	24.52	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	25.11	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	23.75	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Inner_Full	24.72	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	23.52	PC2	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	23.99	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	23.35	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Inner_Full	24.37	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.26	PC2	PASS

DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	23.48	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	22.40	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Inner_Full	23.35	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.22	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	22.32	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	23.41	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Inner_Full	24.37	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.71	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.26	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	22.41	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Inner_Full	23.38	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	22.54	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	22.13	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	24.39	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Inner_Full	25.51	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	24.22	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	24.74	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	23.48	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Inner_Full	24.47	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	23.18	PC2	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	24.16	PC2	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	23.38	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Inner_Full	24.44	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.30	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.32	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	22.42	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Inner_Full	23.37	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.18	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.40	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	23.36	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Inner_Full	24.35	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.59	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.00	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	22.41	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Inner_Full	23.29	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	22.40	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	21.95	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	24.40	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Inner_Full	25.37	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	24.16	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	24.74	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	23.47	PC2	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Inner_Full	24.43	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.94	PC2	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	23.50	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	23.31	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Inner_Full	24.45	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.18	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	23.10	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	22.30	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Inner_Full	23.38	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.29	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.08	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	23.18	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Inner_Full	24.20	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.67	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	22.92	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	22.18	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Inner_Full	23.17	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	22.60	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	22.00	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	23.94	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Inner_Full	25.00	PC2	PASS

DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.57	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	24.52	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	22.98	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Inner_Full	23.91	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	22.62	PC2	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	23.49	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	23.58	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Inner_Full	24.69	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.58	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.26	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	22.56	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Inner_Full	23.71	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.47	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	22.35	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	23.23	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Inner_Full	24.18	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.68	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	23.02	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	22.21	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Inner_Full	23.16	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	22.33	PC2	PASS

DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	21.81	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	23.83	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Inner_Full	24.94	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.46	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	24.60	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	22.88	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Inner_Full	23.92	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.28	PC2	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	23.45	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	23.59	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Inner_Full	24.84	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.50	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	18.46	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	22.61	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Inner_Full	23.73	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.54	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	22.31	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	18.64	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Inner_Full	24.15	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.56	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	23.13	PC2	PASS

DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	22.23	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Inner_Full	23.25	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	22.37	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	22.00	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	23.75	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Inner_Full	24.75	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.47	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	24.66	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	22.84	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Inner_Full	23.72	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.28	PC2	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	23.53	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	23.62	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Inner_Full	24.71	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.49	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.22	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	22.69	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Inner_Full	23.69	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.50	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.20	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	23.20	PC2	PASS

DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Inner_Full	24.25	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.90	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.98	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	22.17	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Inner_Full	23.17	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.49	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	22.05	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	23.71	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Inner_Full	24.71	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.33	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	24.57	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	22.77	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Inner_Full	23.53	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.16	PC2	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	23.38	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.11	≤13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.57	≤13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.14	≤13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.88	≤13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.14	≤13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.00	≤13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.76	≤13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.73	≤13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.46	≤13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.72	≤13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.89	≤13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.61	≤13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.71	≤13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.19	≤13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.75	≤13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.11	≤13	PASS
DC_5A_n77A-3	30	5+20	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.70	≤13	PASS

700-3980										
DC_5A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.42	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.08	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.96	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.97	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.70	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.06	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.99	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.06	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.04	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.64	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.04	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.22	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.32	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.41	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.05	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.97	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.12	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.07	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.15	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_5A_n77A-3	30	5+60	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.14	≤13	PASS

700-3980										
DC_5A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.36	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.19	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.03	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.27	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.44	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.19	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.14	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.35	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.89	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.11	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.93	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.10	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.27	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.32	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.15	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.23	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.47	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.22	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.07	≤13	PASS
DC_5A_n77A-3	30	5+90	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.39	≤13	PASS

700-3980										
DC_5A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.32	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.07	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.19	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.20	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.40	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.80	≤13	PASS
DC_5A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.17	≤13	PASS

Test Graphs

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+10_M+L_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+10_M+L_TID3_#1
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+10_M+M_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+10_M+M_TID3_#1
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+10_M+H_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+10_M+H_TID3_#1

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+15_M+L_TID2_#1

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+15_M+L_TID3_#1

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+15_M+M_TID2_#1

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+15_M+M_TID3_#1

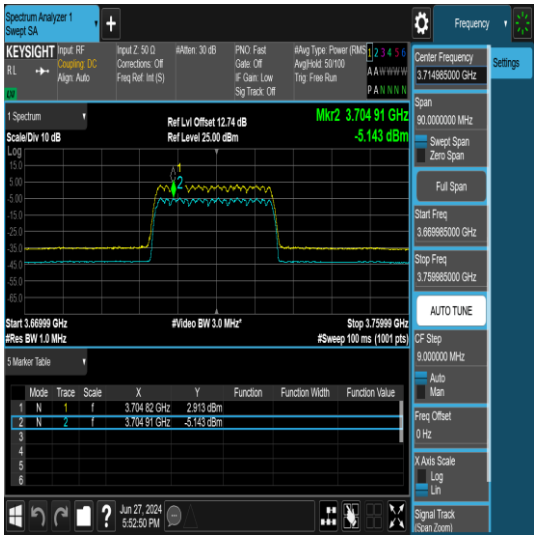
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+15_M+H_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+15_M+H_TID3_#1
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+20_M+L_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+20_M+L_TID3_#1
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+20_M+M_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+20_M+M_TID3_#1

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+20_M+H_TID2_#1

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+20_M+H_TID3_#1

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+30_M+L_TID2_#1

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+30_M+L_TID3_#1



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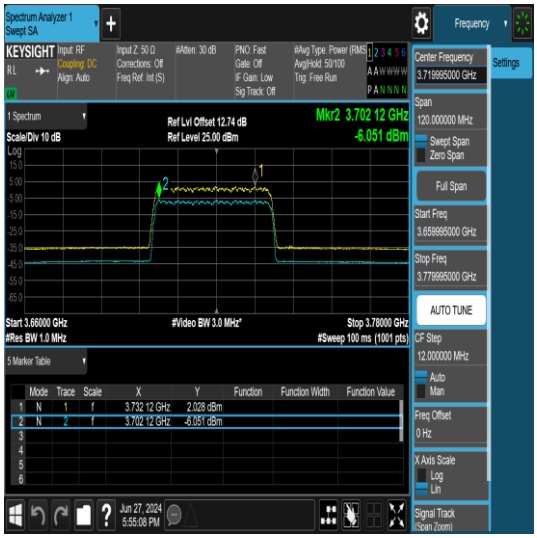
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+30_M+H_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+30_M+H_TID3_#1
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+40_M+L_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+40_M+L_TID3_#1



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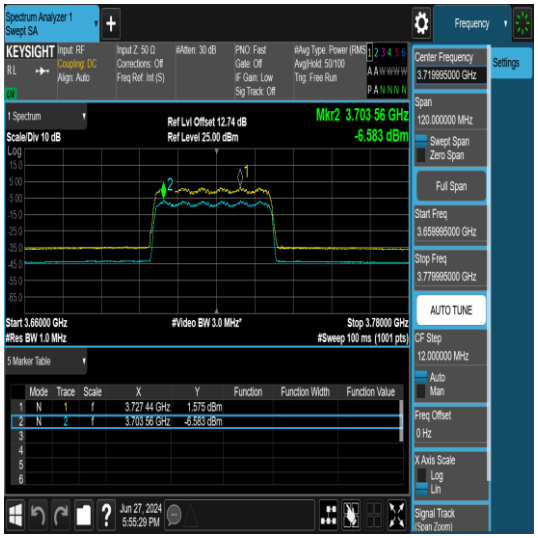

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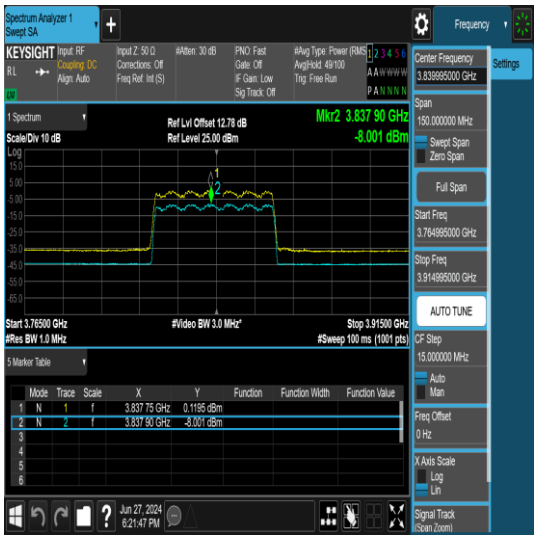
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+40_M+H_TID3_#1

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+50_M+L_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+50_M+L_TID3_#1
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+50_M+M_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+50_M+M_TID3_#1
	
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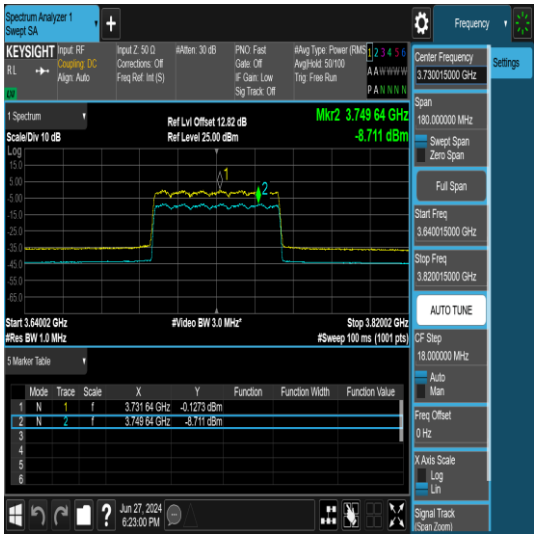
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+60_M+L_TID2_#1

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+60_M+L_TID3_#1



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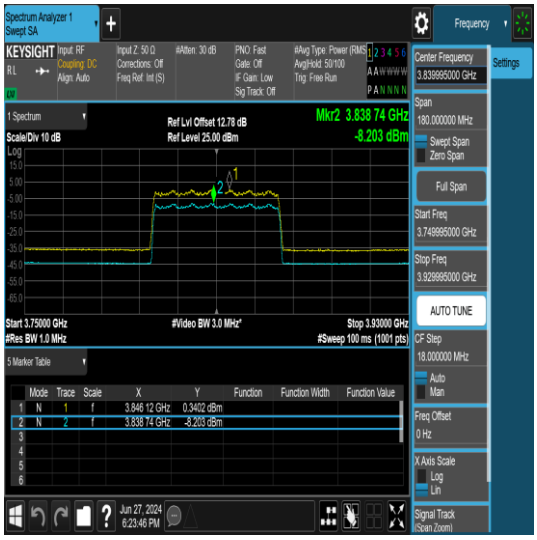

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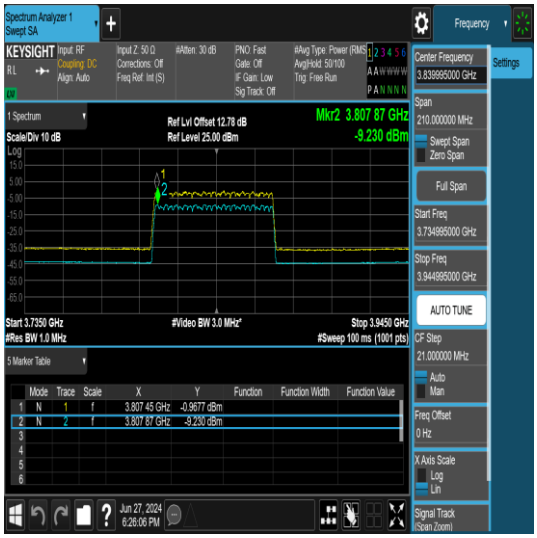
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+70_M+L_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+70_M+L_TID3_#1
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+70_M+M_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+70_M+M_TID3_#1



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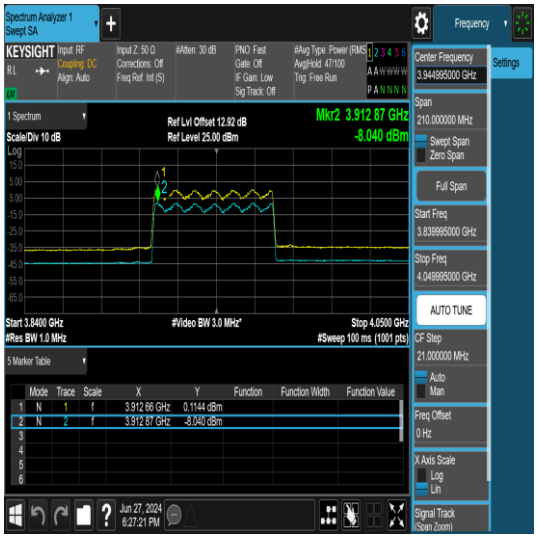
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+70_M+H_TID3_#1



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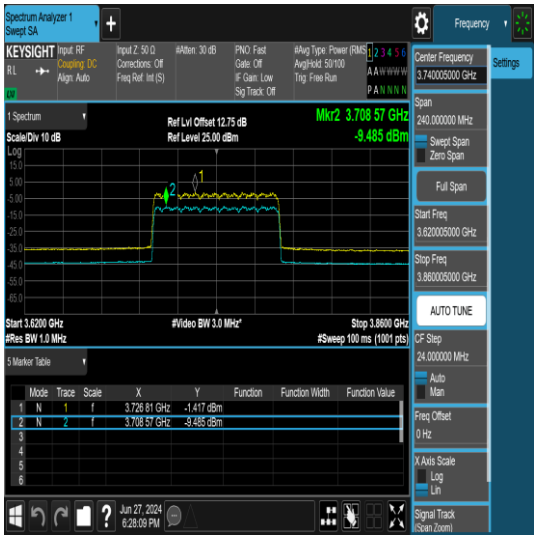
NTNV_DC_5A_n77A-3700-3980_PC2_30_5+80_M+L_TID3_#1



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NTNV_DC_5A_n77A-3700-3980_PC2_30_5+90_M+L_TID2_#1	NTNV_DC_5A_n77A-3700-3980_PC2_30_5+90_M+L_TID3_#1