Appendix for 71A_N78A

Product Name: 5G Indoor CPE
Model No: TITAN 5000

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power（dBm）	Power Class	Verdict
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	21.93	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	22.81	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.91	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	21.90	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	20.94	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	21.92	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.86	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.83	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	22.33	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	23.34	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	22.26	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	22.33	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	21.32	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	22.21	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	21.31	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	21.27	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	22.21	PC2	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	23.26	PC2	PASS
DC_71A_n78A-3	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	22.31	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	22.30	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	21.27	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	22.20	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	21.32	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	21.22	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	21.96	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	22.89	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	21.99	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	22.10	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	20.97	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	21.92	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	21.06	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	21.19	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	22.32	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	23.34	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	22.35	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	22.41	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	21.33	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	22.41	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	21.28	PC2	PASS
DC_71A_n78A-3	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	21.42	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	22.26	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	23.25	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	22.28	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	22.32	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	21.28	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	22.28	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	21.46	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	21.35	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	21.79	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	22.70	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.86	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.07	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	20.89	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	21.90	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	21.03	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.34	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	22.31	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	23.35	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	22.20	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.29	PC2	PASS
DC_71A_n78A-3	30	5+20	DFT-16QAM	M+M	Outer_Full	21.28	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	20.89	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.41	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	21.46	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	22.16	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	23.23	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.20	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.33	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	21.19	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	22.22	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.21	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	21.32	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	22.00	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Inner_Full	23.06	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	21.92	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	22.27	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	20.99	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Inner_Full	22.04	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	20.95	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	21.36	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	22.41	PC2	PASS
DC_71A_n78A-3	30	5+30	DFT-QPSK	M+M	Inner_Full	23.34	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	22.40	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	22.44	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	21.39	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Inner_Full	22.34	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	21.47	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	21.50	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	22.34	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Inner_Full	23.19	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	22.32	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	22.22	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	21.20	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Inner_Full	22.24	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	21.32	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	21.20	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	22.15	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	23.16	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.01	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	22.21	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	21.17	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	22.19	PC2	PASS
DC_71A_n78A-3	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	21.29	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	21.33	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	22.38	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	23.31	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	22.56	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	22.40	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	21.46	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	22.37	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	21.63	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	21.50	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	22.25	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	23.18	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.60	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	22.35	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	21.26	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	22.23	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	21.78	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	21.62	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	21.81	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	23.05	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	21.65	PC2	PASS
DC_71A_n78A-3	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	21.90	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	20.96	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	22.09	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	20.78	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	20.92	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	22.11	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	23.15	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	22.18	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	21.93	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	21.11	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	22.15	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	21.14	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	21.03	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	22.01	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	23.03	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.09	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	22.04	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	21.05	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	22.14	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	21.19	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	21.10	PC2	PASS
DC_71A_n78A-3	30	5+60	DFT-QPSK	M+L	Outer_Full	22.06	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	23.03	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	21.84	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	21.98	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	21.04	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	22.12	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	20.88	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	21.06	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	22.09	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	23.10	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	22.17	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	21.94	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	21.12	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	22.14	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	21.10	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	20.92	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	22.08	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	23.15	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.19	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.12	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	21.08	PC2	PASS
DC_71A_n78A-3	30	5+60	DFT-16QAM	M+H	Inner_Full	22.15	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	21.20	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	21.12	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	21.94	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	22.76	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	21.66	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	21.84	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	20.26	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	20.93	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	20.05	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	20.96	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	22.02	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	23.05	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	21.83	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	21.77	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	21.02	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	22.05	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	21.11	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	20.96	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	22.07	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	23.04	PC2	PASS
DC_71A_n78A-3	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	22.11	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	21.89	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	21.04	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	22.05	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	21.24	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	20.97	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	21.93	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	22.99	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	21.59	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	21.70	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	20.90	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	21.98	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	20.62	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	20.79	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	21.97	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	21.75	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	21.84	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	21.95	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	20.95	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	21.99	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	20.90	PC2	PASS
DC_71A_n78A-3	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	21.07	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	22.00	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	22.98	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	21.97	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	21.98	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	20.33	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	21.94	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	21.04	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	20.36	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	21.89	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	23.02	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	21.63	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	21.96	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	20.93	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	22.06	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	20.61	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	20.99	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	21.95	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	23.08	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	21.64	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	22.06	PC2	PASS
DC_71A_n78A-3	30	5+90	DFT-16QAM	M+M	Outer_Full	20.99	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	22.11	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	20.56	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	21.00	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	22.01	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	23.00	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	21.68	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.05	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	21.00	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	21.98	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	20.61	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	21.02	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	21.94	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	16.39	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	21.43	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	21.61	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	20.96	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	22.03	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	20.69	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	21.03	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	21.97	PC2	PASS
DC_71A_n78A-3	30	5+100	DFT-QPSK	M+M	Inner_Full	23.08	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	21.37	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	21.83	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	21.02	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	22.09	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	20.67	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.09	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	22.00	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	23.06	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	21.36	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	21.77	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	21.02	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	22.12	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	20.69	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	21.10	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.28	≤13	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.27	≤13	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.27	≤13	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.61	≤13	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.46	≤13	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.72	≤13	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.57	≤13	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.72	≤13	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.69	≤13	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.92	≤13	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.92	≤13	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.99	≤13	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.98	≤13	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.04	≤13	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.76	≤13	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.18	≤13	PASS

DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.12	≤13	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.16	≤13	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.16	≤13	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.92	≤13	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.68	≤13	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.19	≤13	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.22	≤13	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.23	≤13	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.69	≤13	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.32	≤13	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.09	≤13	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.97	≤13	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.90	≤13	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.27	≤13	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.15	≤13	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.14	≤13	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.05	≤13	PASS

Test Graphs

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+M_TID2_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+H_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+L_TID2_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+M_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+H_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+L_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+M_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+H_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+L_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+M_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+H_TID2_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+L_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+M_TID2_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+H_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50_M+L_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50_M+M_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50_M+H_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+L_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+M_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+H_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+70_M+L_TID2_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+70_M+M_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+70_M+H_TID2_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+80_M+L_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+80_M+M_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+80_M+H_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+90_M+L_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+90_M+M_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+90_M+H_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+100_M+L_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+100_M+M_TID2_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+100_M+H_TID2_#1	

Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

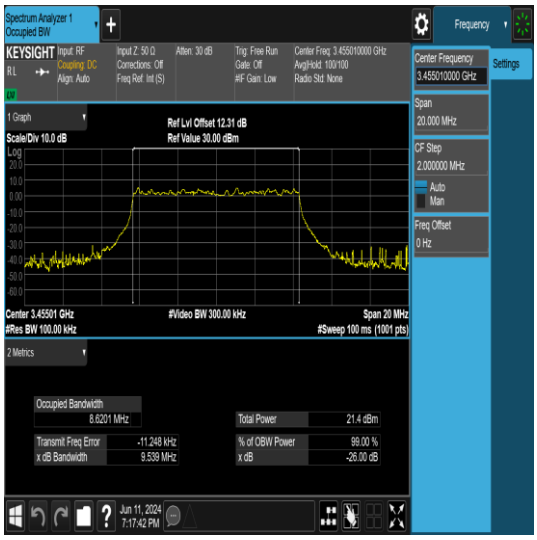
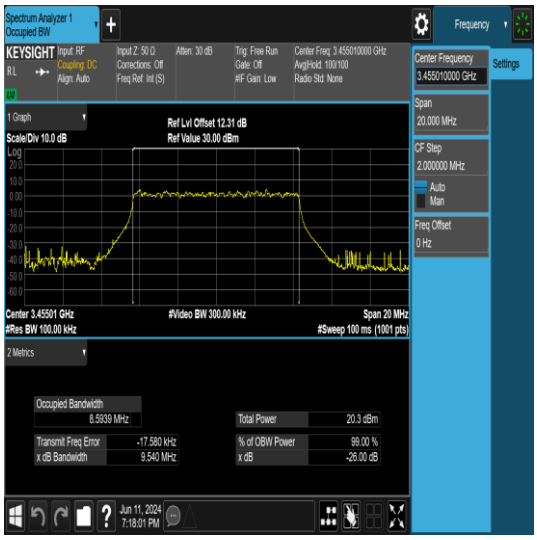
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	8.6201	9.539	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	8.5939	9.540	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	8.6191	9.750	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	8.6225	9.725	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	8.5928	9.439	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	8.5972	9.384	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	12.910	14.23	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	12.870	14.16	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	12.874	13.93	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	12.847	14.13	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	12.942	14.39	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	12.869	14.15	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	17.843	19.35	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	17.809	19.44	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	17.891	19.27	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	17.844	19.39	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	17.925	19.52	PASS

DC_71A_n78A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	17.898	19.38	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	26.805	28.57	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	26.691	28.44	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	26.745	28.61	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	26.851	28.72	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	26.798	28.58	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	26.783	28.44	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	35.622	37.73	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	35.589	37.57	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	35.743	37.71	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	35.691	37.78	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	35.689	37.95	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	35.682	37.70	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	45.616	47.81	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	45.655	48.01	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	45.629	47.85	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	45.670	48.05	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	45.604	47.77	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	45.586	47.84	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	57.877	60.49	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	57.739	60.35	PASS

DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	57.765	60.52	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	57.810	60.56	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	57.734	60.52	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	57.803	60.11	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	64.328	67.11	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	64.254	66.76	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	64.475	66.93	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	64.180	67.12	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	64.112	66.92	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	64.271	67.02	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	77.039	80.00	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	76.965	80.24	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	77.222	80.33	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	77.021	80.11	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	77.237	80.30	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	77.063	80.14	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	86.650	89.90	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	86.775	89.99	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	86.646	90.05	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	86.629	90.18	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	86.727	90.09	PASS

DC_71A_n78A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	86.603	90.11	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	96.508	99.89	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	96.265	99.78	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	96.474	99.94	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	96.258	99.81	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	96.498	99.90	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	96.252	99.73	PASS

Test Graphs

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID1_Ant1#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID3_Ant1#1
	
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	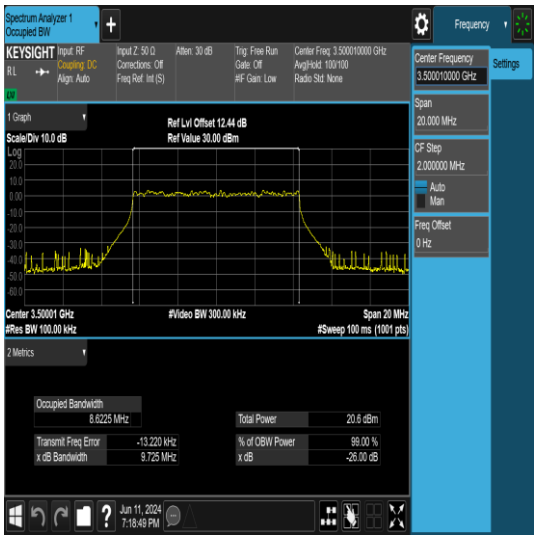
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NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+L_TID1_Ant1#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+L_TID3_Ant1#1

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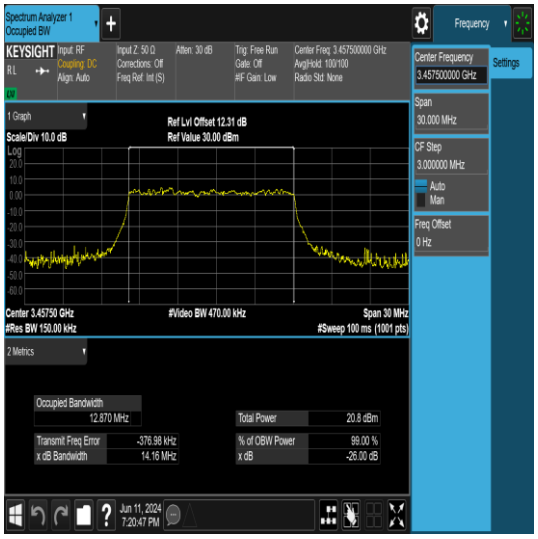

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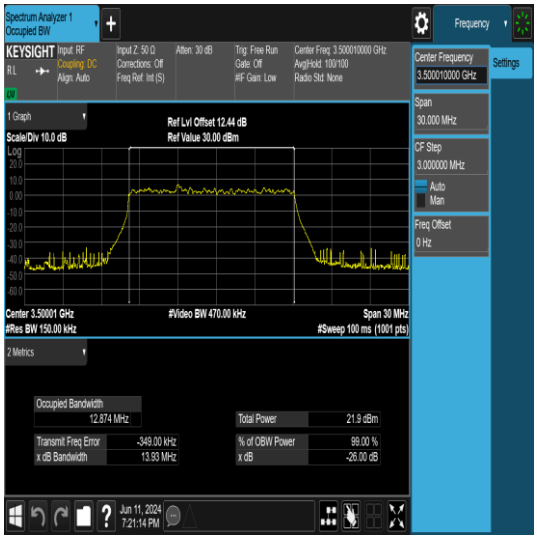

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	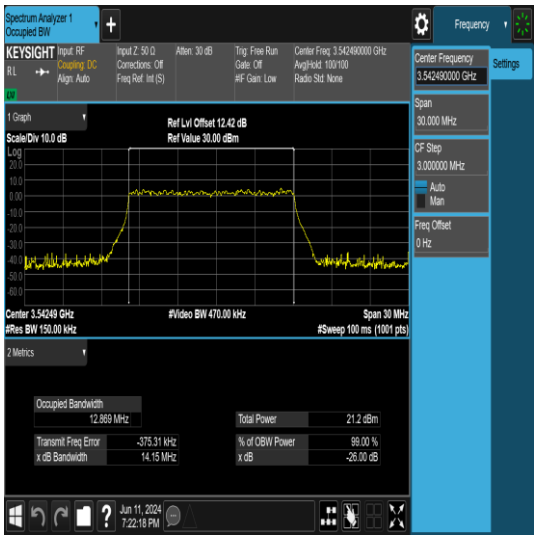
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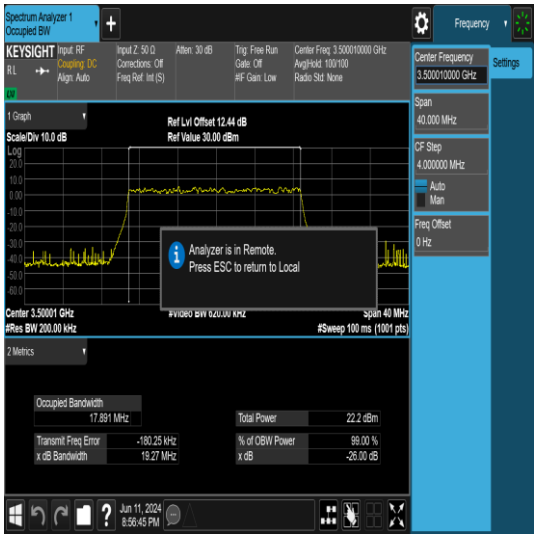
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+M_TID1_Ant1#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+M_TID3_Ant1#1



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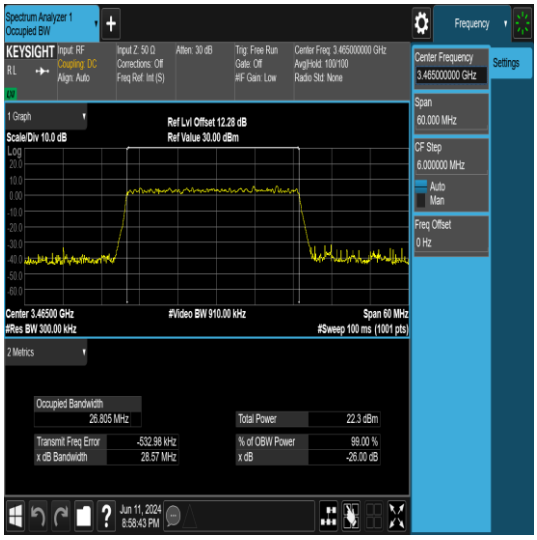
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+H_TID3_Ant1#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+L_TID1_Ant1#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+L_TID3_Ant1#1



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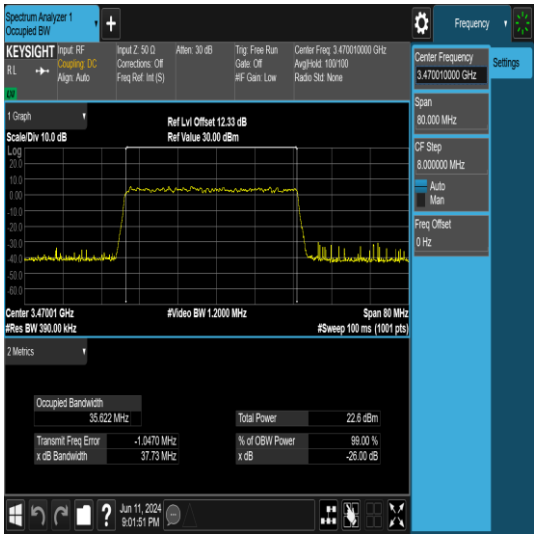
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+H_TID1_Ant1#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+H_TID3_Ant1#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+L_TID1_Ant1#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+L_TID3_Ant1#1



NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+M_TID1_Ant1#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+M_TID3_Ant1#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+H_TID1_Ant1#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+H_TID3_Ant1#1