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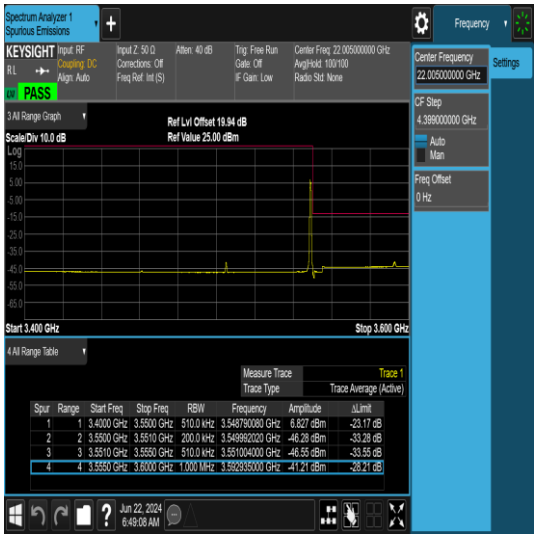

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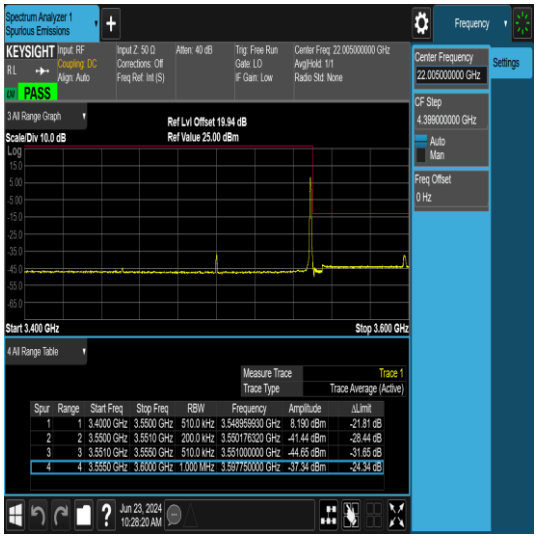


Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-61.80	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-70.76	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-52.43	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-37.63	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	3000	6000	-30.49	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-30.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-34.32	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-61.56	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.10	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-52.41	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.67	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-30.54	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-34.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.56	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.28	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-51.09	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.72	-13	PASS

DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-30.35	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.74	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-34.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-62.86	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-71.87	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-52.50	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	1000	3000	-37.73	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	3000	6000	-30.51	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	6000	26000	-30.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	26000	40000	-34.34	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.14	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.24	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-52.19	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.48	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-30.60	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-30.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-34.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.39	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.38	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-52.19	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.67	-13	PASS

DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-30.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-30.62	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-34.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.17	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.34	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-52.17	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-37.67	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	3000	6000	-30.55	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-30.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-34.44	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.69	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.71	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.32	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.58	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-30.45	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-34.37	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.28	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.35	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-52.38	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.66	-13	PASS

DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-30.50	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.77	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-34.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-62.43	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-73.39	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-52.16	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	1000	3000	-37.65	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	3000	6000	-30.39	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	6000	26000	-30.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	26000	40000	-34.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.48	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.25	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-52.32	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.72	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-30.51	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-30.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-34.40	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.98	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.06	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-44.50	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.60	-13	PASS

DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-30.49	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-30.76	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-34.41	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-62.07	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.15	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-45.86	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-37.68	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-59.69	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-70.78	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-50.61	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	3000	6000	-29.58	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-30.36	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.70	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	3000	6000	-30.59	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-30.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-34.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.01	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.82	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-52.55	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.59	-13	PASS

DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-30.44	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.71	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-34.35	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.11	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.72	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-52.19	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.73	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-30.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.65	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-34.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-62.39	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-73.01	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-52.06	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	1000	3000	-37.60	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	3000	6000	-30.54	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	6000	26000	-30.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	26000	40000	-34.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-63.31	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-71.44	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-52.57	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.69	-13	PASS

DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-30.42	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-30.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-34.36	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-72.30	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-84.04	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-62.37	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-47.44	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-40.53	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-40.76	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-44.46	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.38	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.36	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-55.01	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.13	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.46	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.79	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.08	-13	PASS

DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.77	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.19	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.48	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.81	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.29	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.86	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	6000	26000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.18	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.21	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.59	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.93	-13	PASS

DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-33.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.22	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.26	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.39	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.13	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-54.78	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-38.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-37.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.36	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.72	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.55	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.46	-13	PASS

DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.47	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.79	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.12	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.95	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-54.96	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.52	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.95	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-38.03	-13	PASS

DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-63.88	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.25	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.55	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.75	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.47	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.18	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-54.98	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.56	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.09	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.58	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.12	-13	PASS

DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.54	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.05	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.45	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.03	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-54.68	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.48	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.25	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-55.06	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.03	-13	PASS

DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.67	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.46	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.96	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.02	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.44	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-54.97	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.57	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.58	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.08	-13	PASS

DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.76	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.01	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.01	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-26.01	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.74	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.00	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.27	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-54.66	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	1000	3000	-37.52	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.68	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.30	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.98	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.10	-13	PASS

DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.29	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.14	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.53	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.15	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.91	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-54.98	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-37.48	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.24	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.87	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.79	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.09	-13	PASS

DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.69	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.70	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.66	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.65	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.09	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-54.73	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.13	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.97	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.96	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-36.63	-13	PASS

DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.52	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.38	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.28	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-55.00	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.44	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.30	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-54.97	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.77	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.83	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.51	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.85	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.10	-13	PASS

DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.27	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.75	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.07	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-36.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.18	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-73.67	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-54.82	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.56	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.44	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.96	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.69	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.14	-13	PASS

DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.58	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.18	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.79	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-37.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.59	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.87	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-54.85	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.37	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.46	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.32	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-35.74	-13	PASS

DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-37.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.16	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.11	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.82	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.76	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.20	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.80	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	30	1000	-54.67	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.54	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.84	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.08	-13	PASS

DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.71	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.99	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.39	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.91	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.36	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.88	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-54.48	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	3000	-37.55	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.48	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.81	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.62	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.05	-13	PASS

DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.03	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.86	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.03	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.39	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.51	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.69	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.66	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.72	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.47	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.13	-13	PASS

DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.70	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.70	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-52.60	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-37.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.34	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.07	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-54.57	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.65	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.75	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.14	-13	PASS

DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.32	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.09	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.88	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-35.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.79	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.03	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.34	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	30	1000	-54.96	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.41	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.97	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.05	-13	PASS

DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.68	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-73.47	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.48	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.64	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.21	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.38	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.54	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.91	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.08	-13	PASS

DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.48	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.78	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.86	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.77	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.06	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.71	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.30	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.94	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-38.63	-13	PASS

DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.64	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.05	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.86	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.77	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-54.81	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.18	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.55	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.62	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.14	-13	PASS

DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.46	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.69	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.07	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.02	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.52	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	30	1000	-54.85	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	1000	3000	-37.49	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.51	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.80	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.16	-13	PASS

DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.30	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.94	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.77	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.52	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.63	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.87	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.35	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.35	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.89	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.91	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.14	-13	PASS

DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.90	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.99	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.72	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.52	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.66	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	30	1000	-54.92	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.30	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.71	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.67	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.03	-13	PASS

DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.22	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.72	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.95	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.48	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.13	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.45	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.06	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.12	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.86	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.17	-13	PASS

DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.04	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.62	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.64	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-35.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.91	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.23	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	30	1000	-54.45	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.14	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.63	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.45	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.10	-13	PASS

DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.58	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.16	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.84	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.40	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.97	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-54.65	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.78	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.42	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.69	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.64	-13	PASS

DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.72	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.33	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.98	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.03	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.43	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	30	1000	-55.08	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	1000	3000	-38.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.30	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.72	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.98	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.16	-13	PASS

DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.53	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.67	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.81	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.41	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.95	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-37.43	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.37	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.77	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.87	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.23	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.44	-13	PASS

DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.79	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.80	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.97	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.28	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	0.15	30	-73.12	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	30	1000	-55.00	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.20	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.88	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.09	-13	PASS

DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.78	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.60	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.39	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.70	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.17	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.51	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-54.77	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.58	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.34	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.31	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.06	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.73	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.12	-13	PASS

DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.19	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.53	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.01	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.74	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.16	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.99	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	30	1000	-54.99	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.70	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.99	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.99	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.76	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.07	-13	PASS

DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.50	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.25	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.67	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.32	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.12	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.86	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.32	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.89	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.76	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.82	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.10	-13	PASS

DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.31	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.85	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.70	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.75	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.89	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.55	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	30	1000	-54.71	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.31	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.11	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.82	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.43	-13	PASS

DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.81	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.88	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.33	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.68	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.34	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.20	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.20	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-54.92	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.37	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.64	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.19	-13	PASS

DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.55	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.05	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-37.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-63.74	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.36	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	30	1000	-54.74	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.68	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.34	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.08	-13	PASS

DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.79	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.85	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.62	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.98	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.26	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-54.79	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	26000	40000	-37.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.65	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.86	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.00	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.11	-13	PASS

DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.79	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-37.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.79	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.97	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.64	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-63.78	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.39	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	30	1000	-54.82	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.77	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.93	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.39	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.72	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.14	-13	PASS

DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.61	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.56	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.66	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.92	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.01	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-54.88	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.75	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.99	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.15	-13	PASS

DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.94	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.60	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.02	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-66.05	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.39	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.36	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.38	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.29	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.46	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.09	-13	PASS

DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.77	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-63.85	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.51	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.64	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.39	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.75	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.23	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.47	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.37	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.28	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.82	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.07	-13	PASS

DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.71	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.69	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.02	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.95	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.46	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.65	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	30	1000	-54.55	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.68	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.46	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-27.08	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.22	-13	PASS

DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-33.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.12	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-73.16	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.79	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-33.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.95	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.31	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-54.84	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-38.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.37	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.59	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.08	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.79	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.16	-13	PASS

DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.71	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.09	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.75	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.32	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.39	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.76	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	3000	6000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.42	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.09	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.02	-13	PASS

DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.94	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.65	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.66	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.75	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.09	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.73	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-54.48	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-38.65	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.81	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.08	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.12	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.82	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.17	-13	PASS

DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.58	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.35	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.87	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.56	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	30	1000	-54.88	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.40	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.54	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.98	-13	PASS

DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.37	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-66.17	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.90	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.72	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-33.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.33	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.98	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-54.85	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-37.79	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.33	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.15	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.64	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.08	-13	PASS

DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.50	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.48	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.78	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.65	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.58	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.72	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	30	1000	-54.78	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.73	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.73	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.50	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.89	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.63	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.15	-13	PASS

DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.32	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.93	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.03	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.38	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.18	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-54.82	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.18	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.95	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.57	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-38.48	-13	PASS

DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.70	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.07	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.75	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.50	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.66	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.00	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	1000	3000	-37.69	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.23	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.10	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-55.07	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.52	-13	PASS

DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.08	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.50	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.73	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.37	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.98	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.76	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-54.88	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-37.48	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.09	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.43	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.33	-13	PASS

DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.35	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.56	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.31	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.63	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.76	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.58	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.02	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	30	1000	-54.63	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	1000	3000	-38.46	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.69	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.31	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.36	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.53	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.09	-13	PASS

DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-63.92	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.67	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.69	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.52	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.09	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-54.78	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.65	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.20	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.36	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.01	-13	PASS

DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.08	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.30	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-46.75	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.67	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	0.15	30	-73.30	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	30	1000	-55.14	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	1000	3000	-37.56	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.91	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.54	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.91	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.09	-13	PASS

DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.72	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.04	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.60	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.19	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.94	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.83	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.67	-13	PASS

DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.63	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.76	-43	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.45	-33	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.57	-23	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.77	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.47	-43	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.16	-33	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	30	1000	-54.58	-23	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	1000	3000	-37.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.32	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.39	-43	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.94	-33	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.09	-13	PASS

DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.70	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.68	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.71	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.61	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.40	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.54	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.44	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.42	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.40	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.75	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.12	-13	PASS

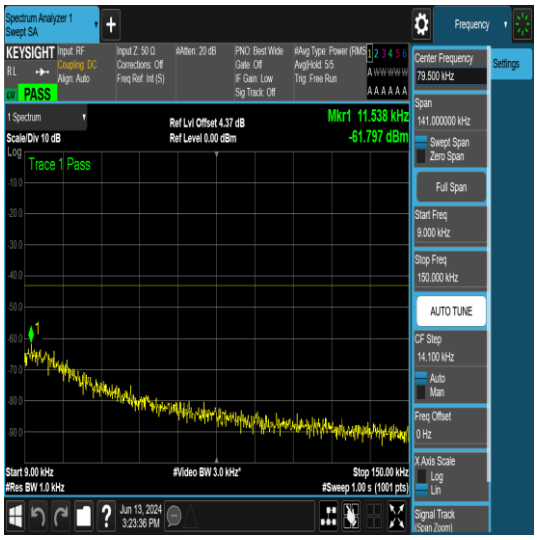
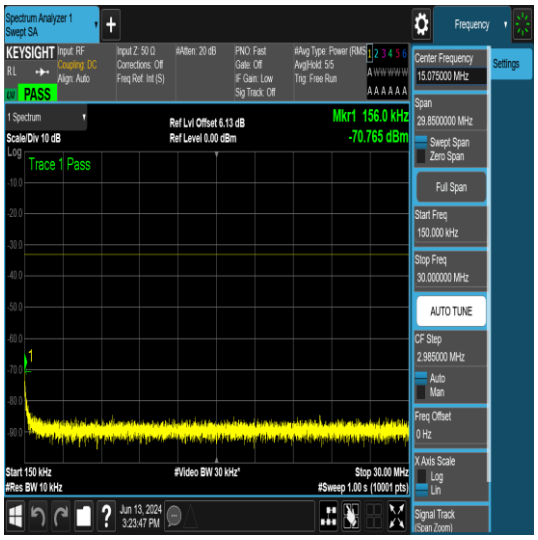
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.44	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.76	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.77	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.99	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.82	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.60	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.15	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	1000	3000	-37.48	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.55	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.11	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.74	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.75	-13	PASS

DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.42	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.71	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.71	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.48	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.06	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.27	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.00	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.84	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.06	-13	PASS

DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.79	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.12	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.56	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.67	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.02	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.15	30	-73.58	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	30	1000	-54.76	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	1000	3000	-38.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.13	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.44	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.14	-13	PASS

DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.57	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.53	-43	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.20	-33	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.95	-23	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-38.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.92	-13	PASS

Test Graphs

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID6_NS_01_0.009_0.15_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID6_NS_01_0.15_30_#1
	
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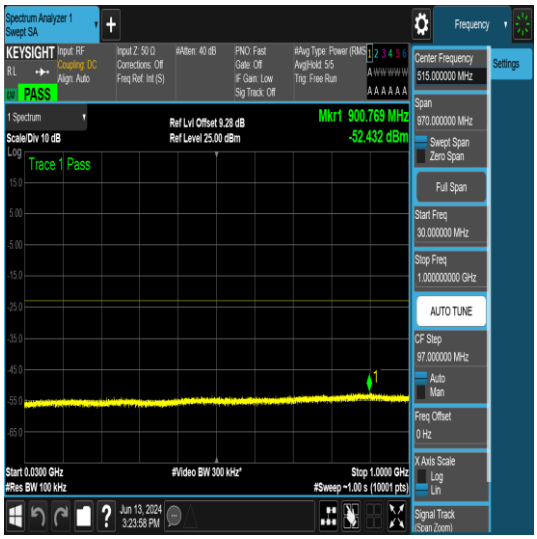	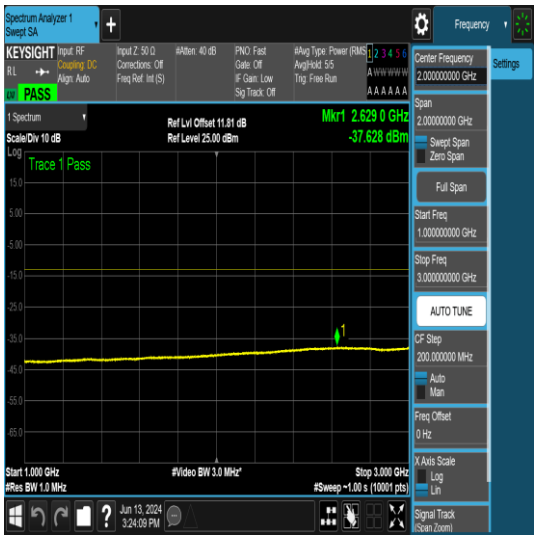
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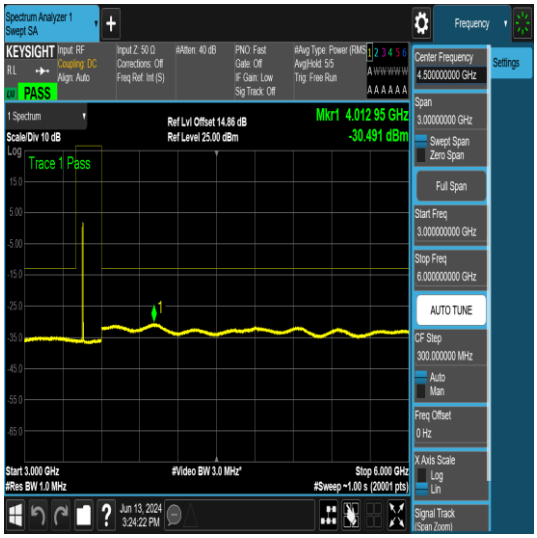

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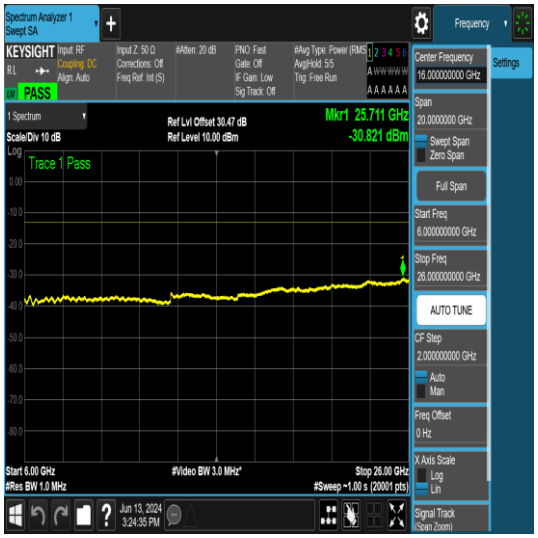

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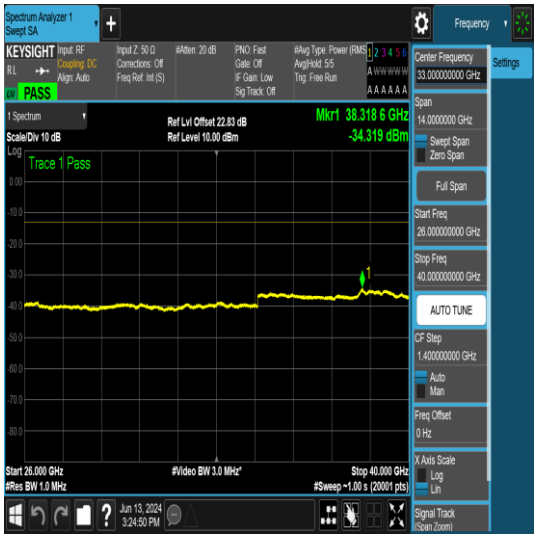

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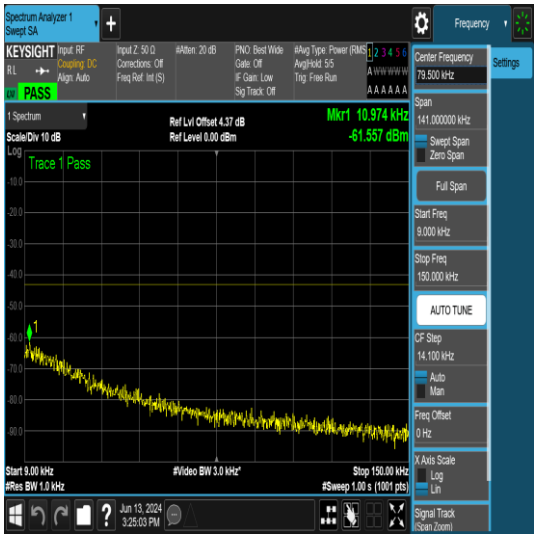

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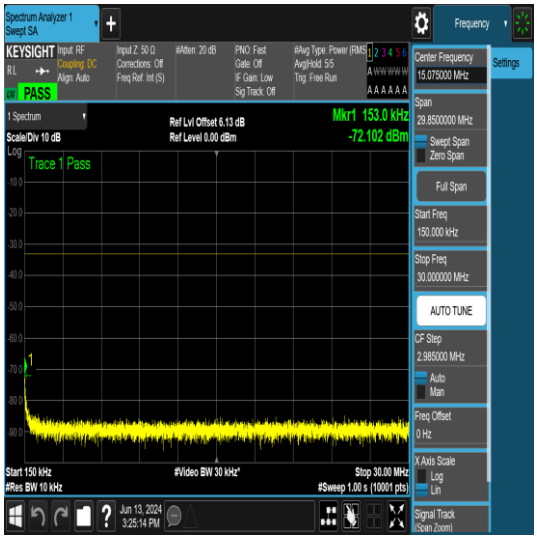

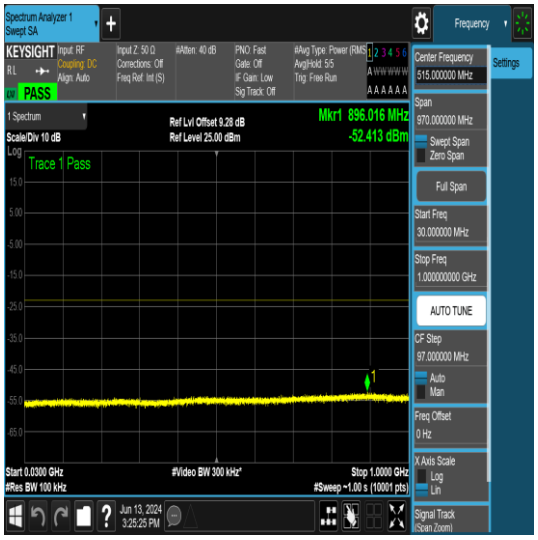

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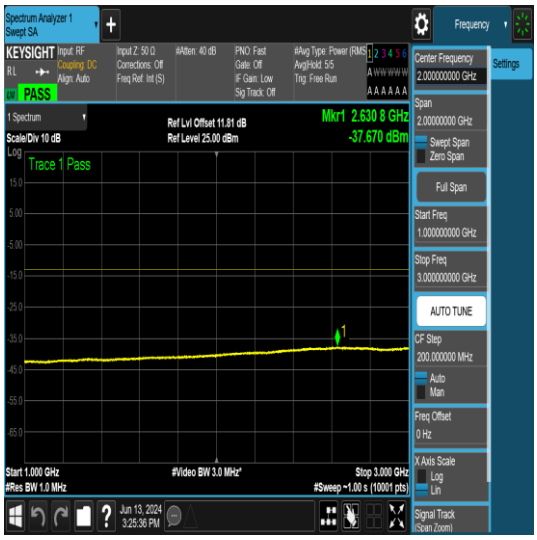	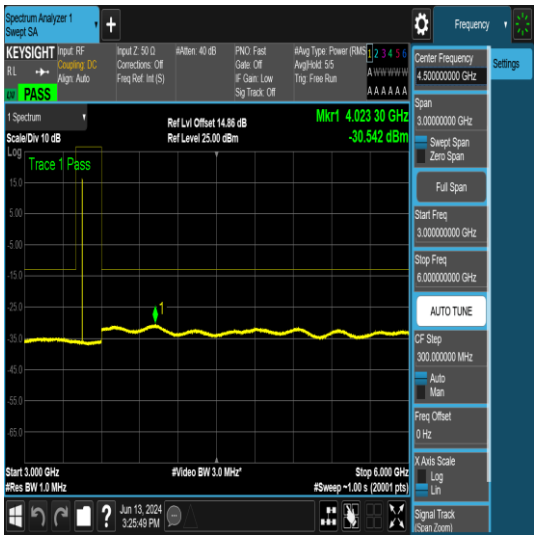
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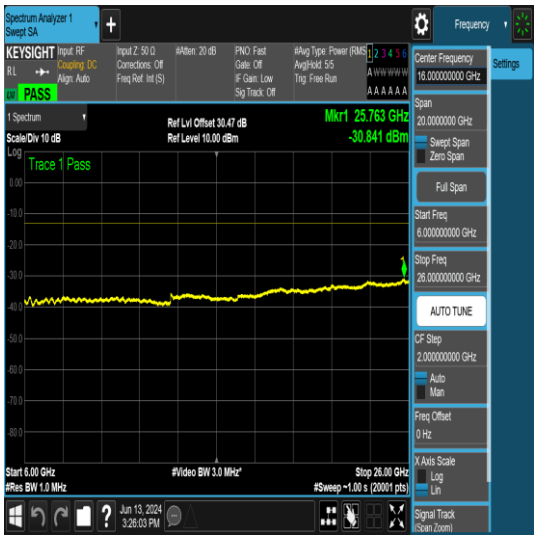	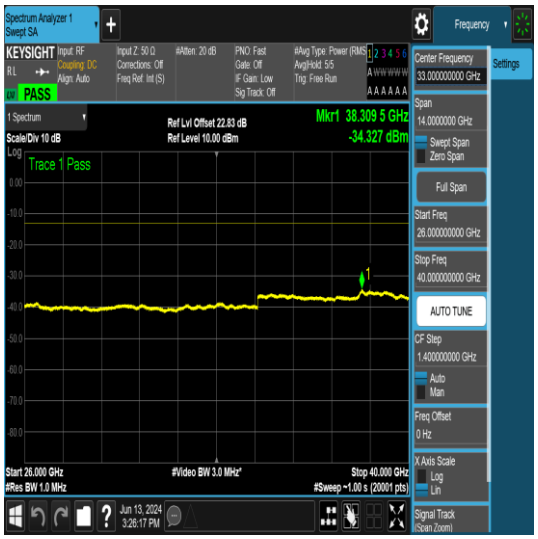
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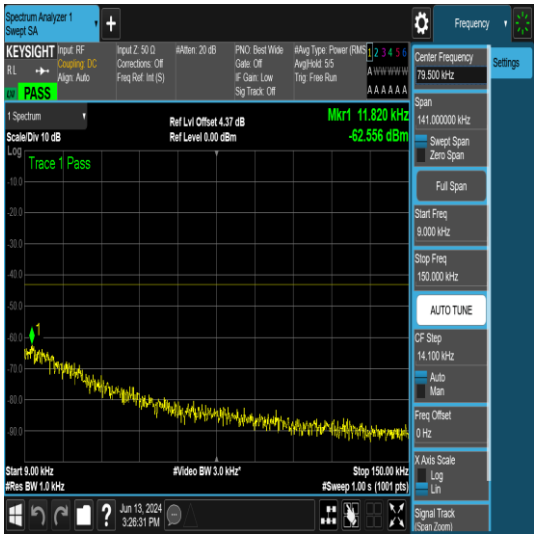

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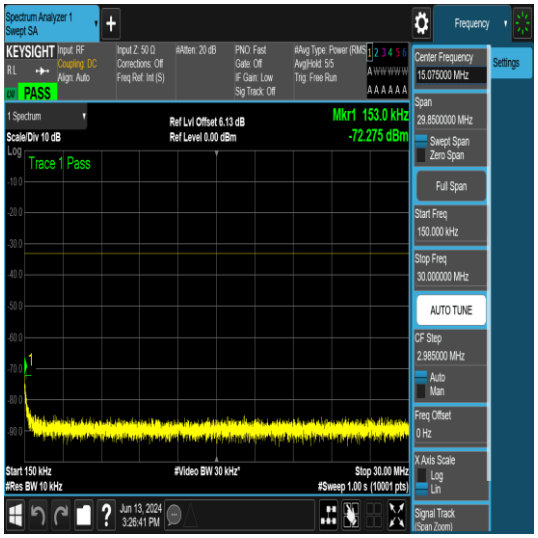

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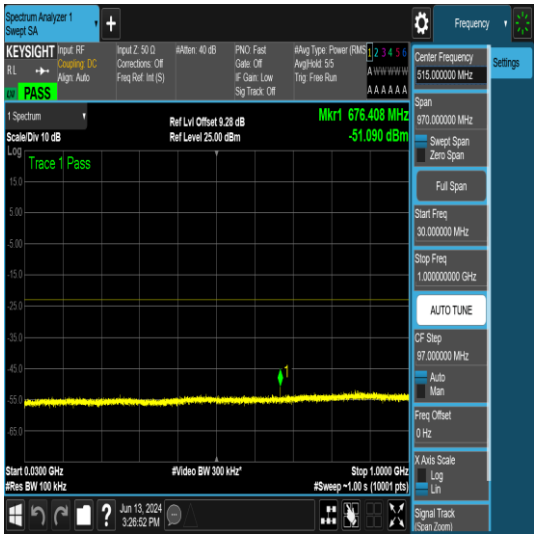

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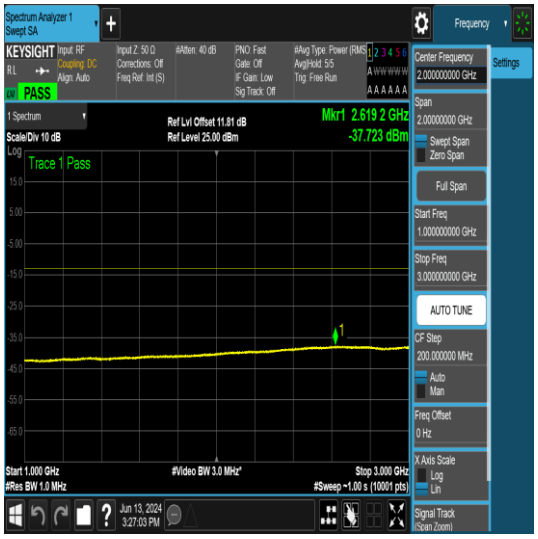

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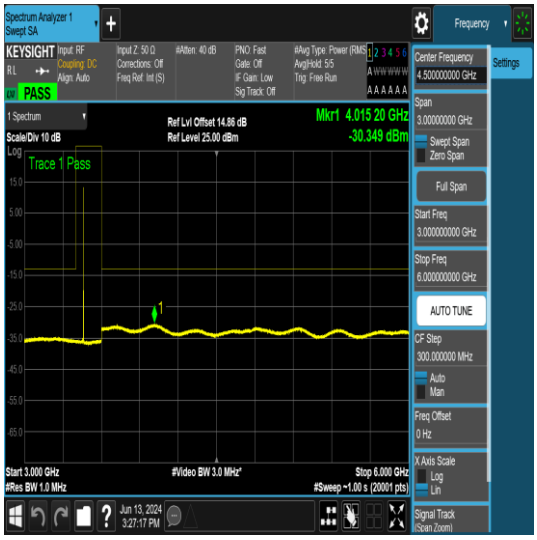

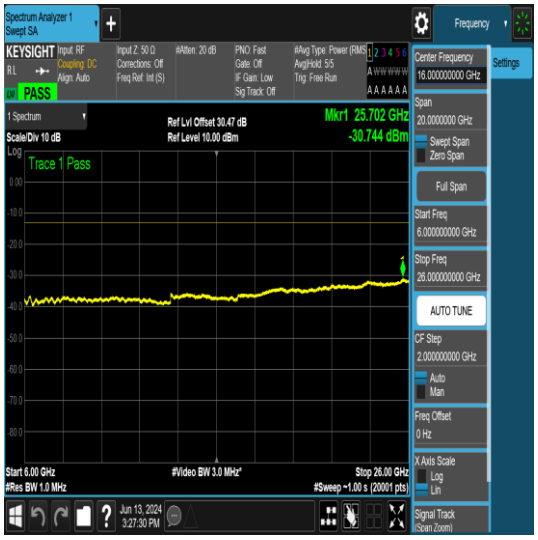

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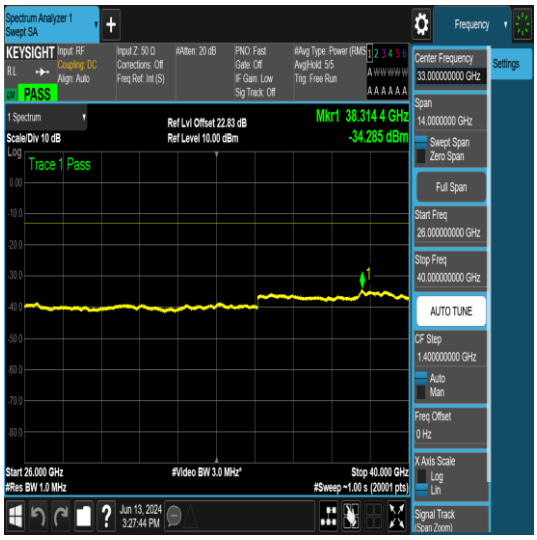	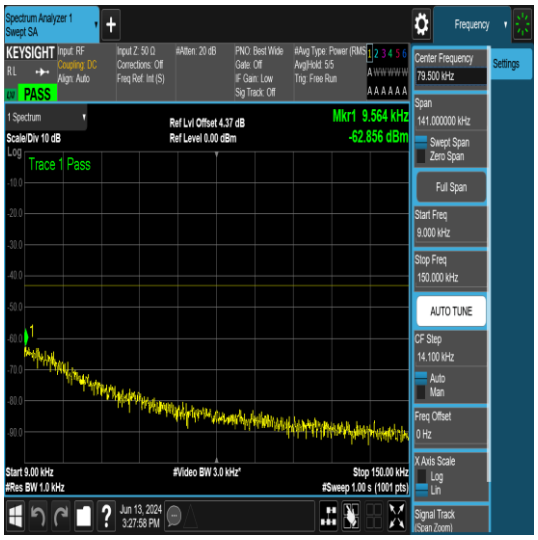
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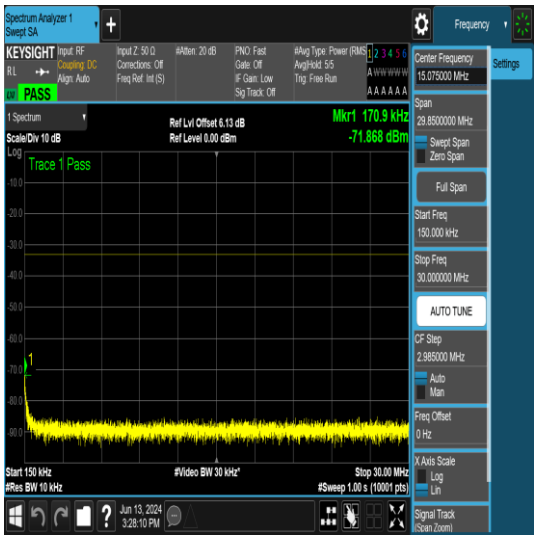	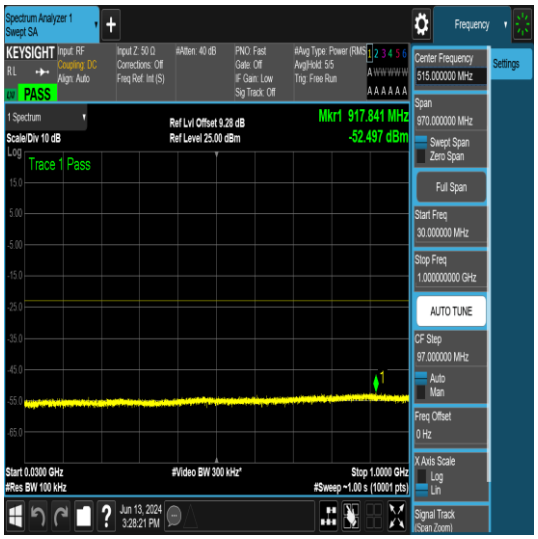
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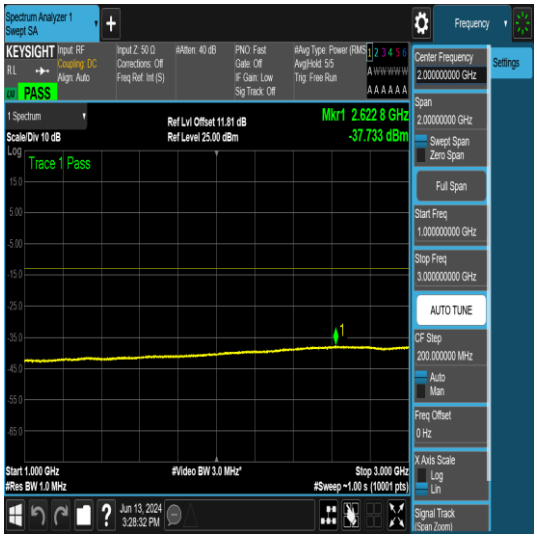

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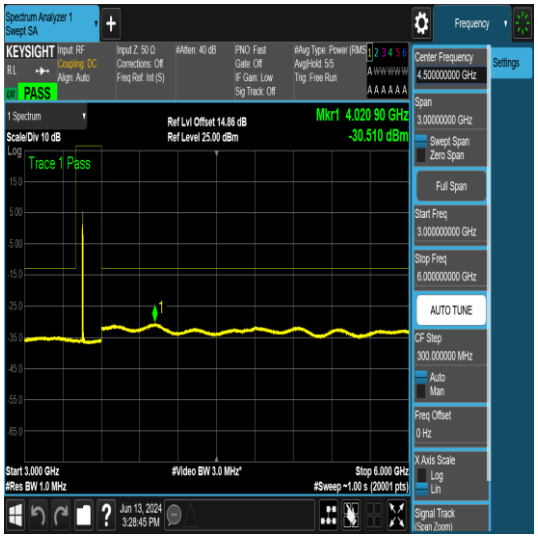

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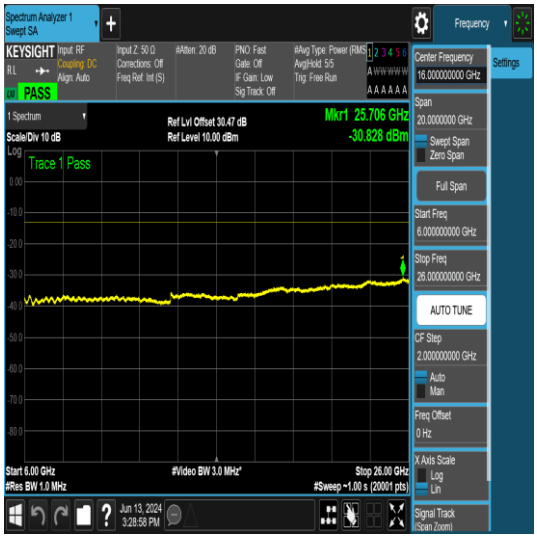

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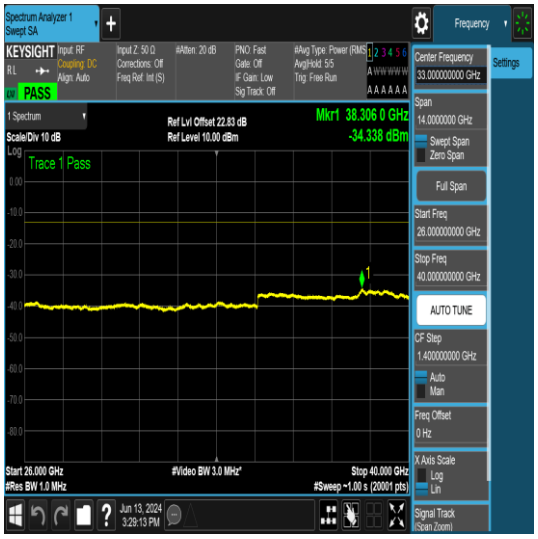

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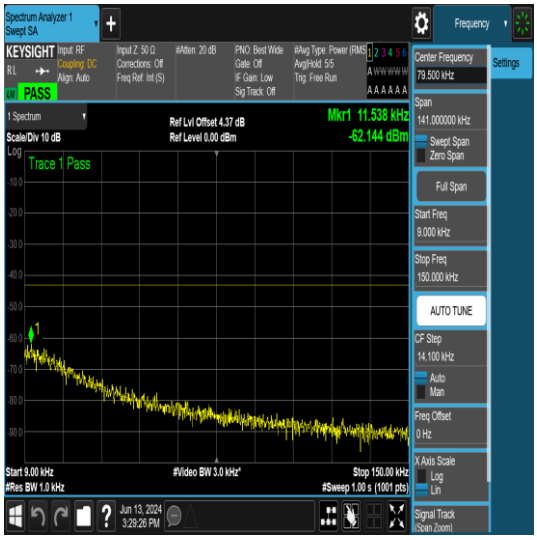

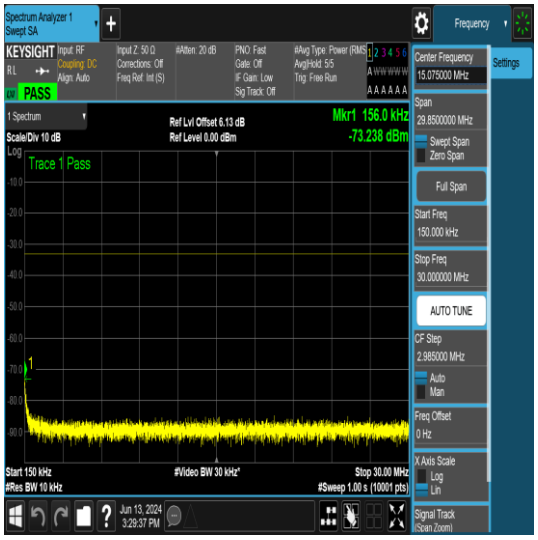

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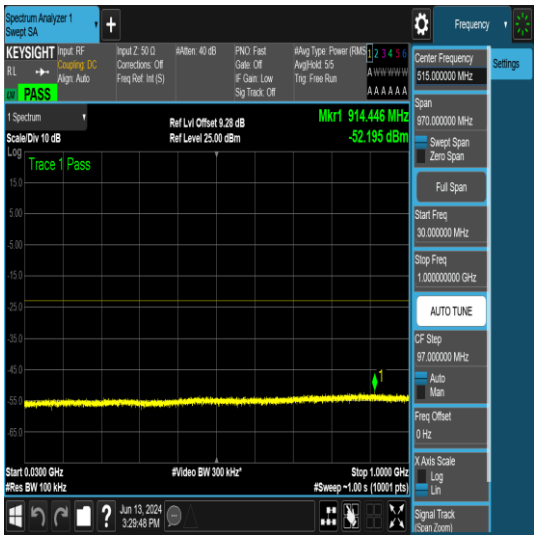	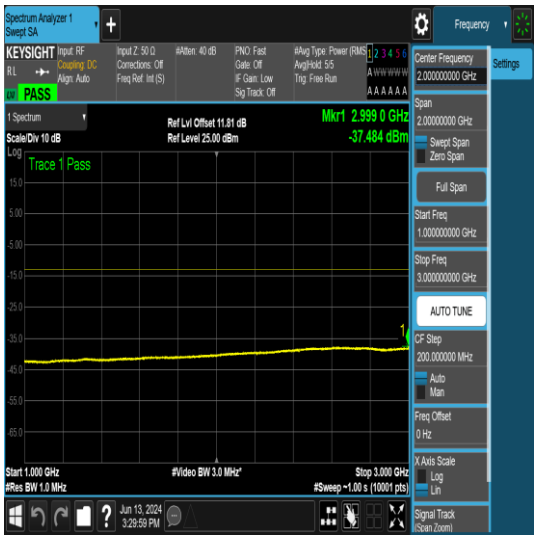
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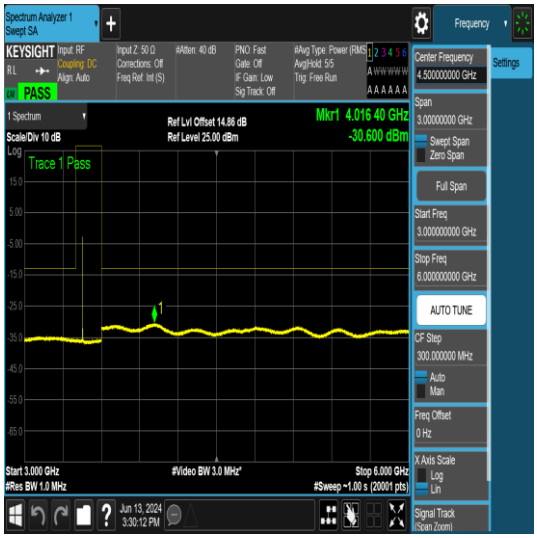	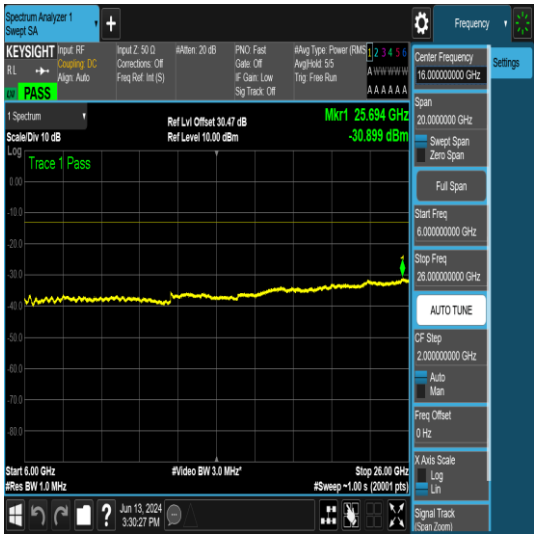
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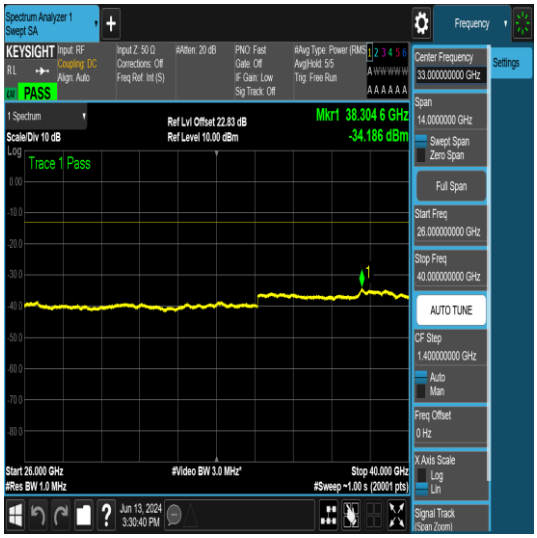

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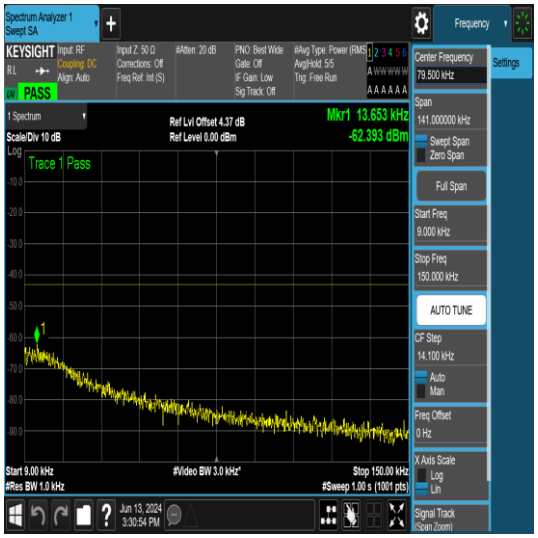

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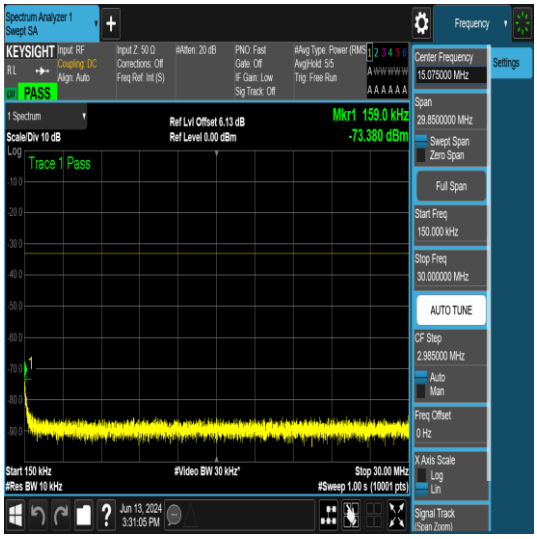

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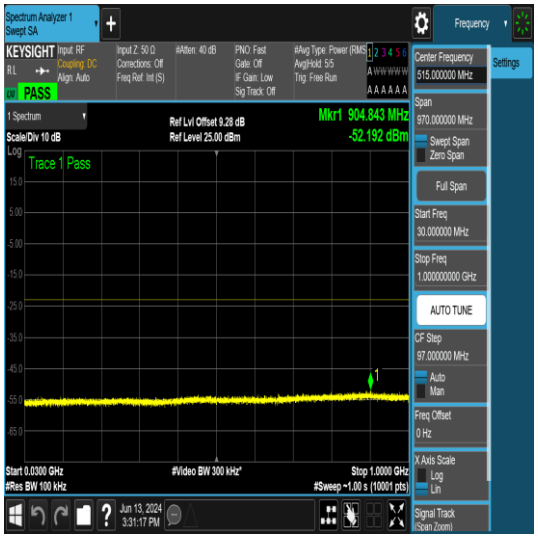

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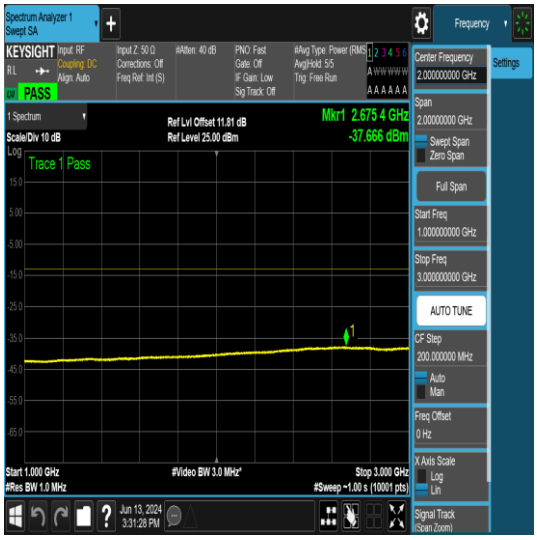

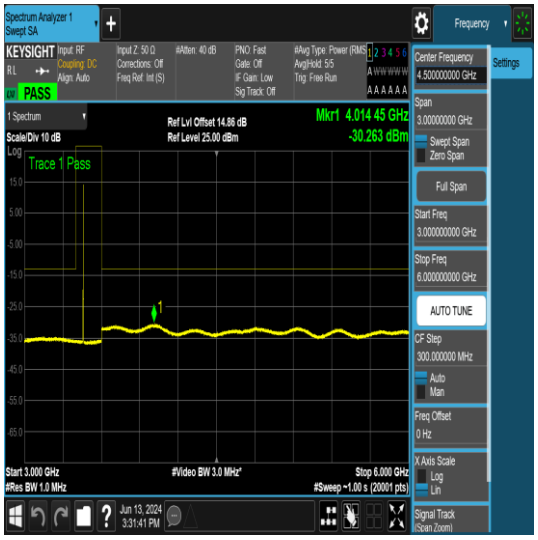

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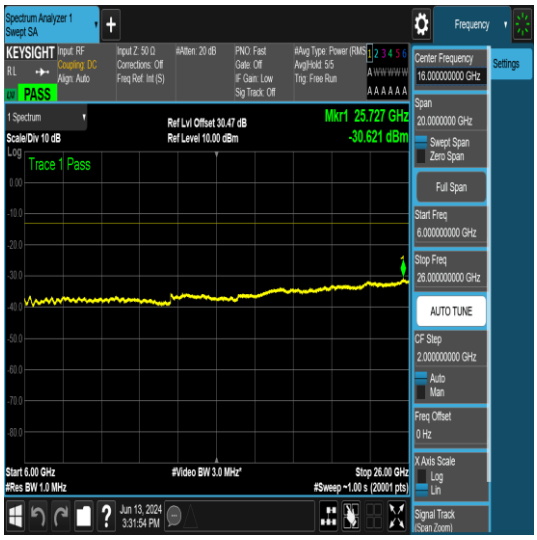	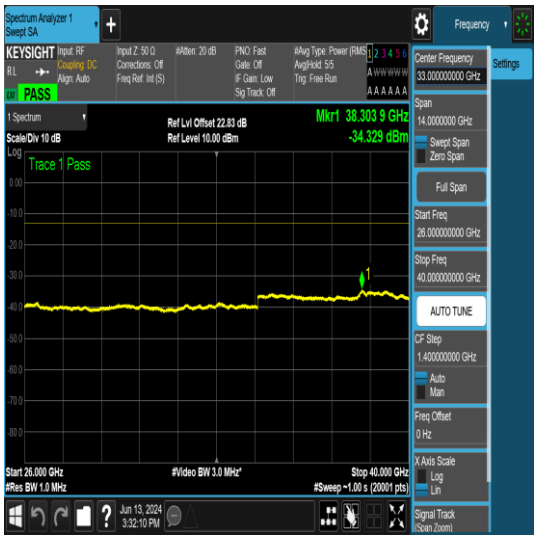
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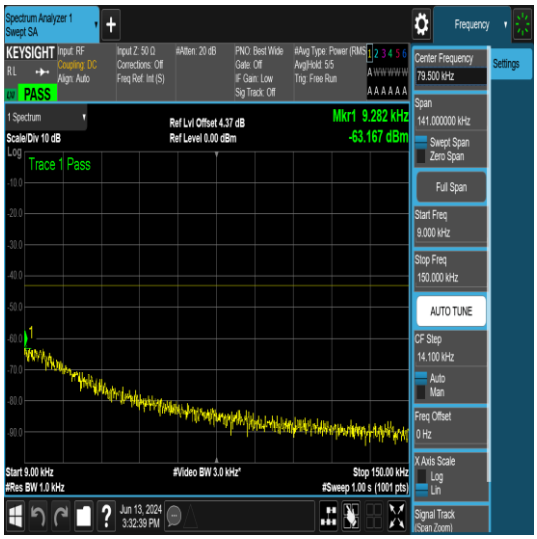	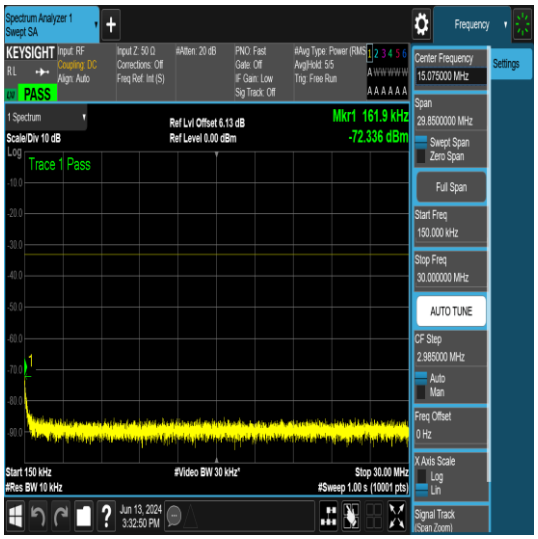
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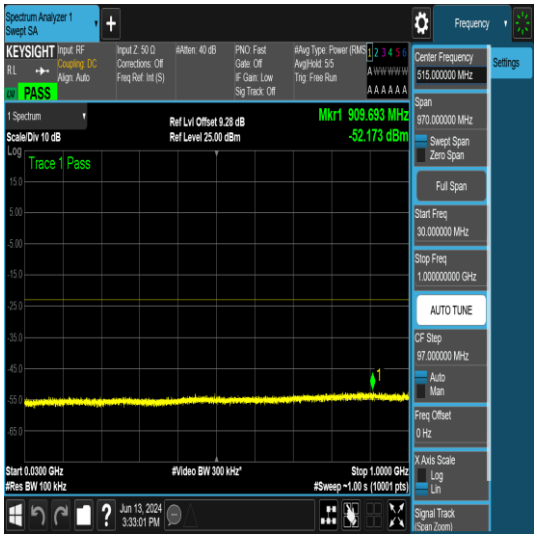

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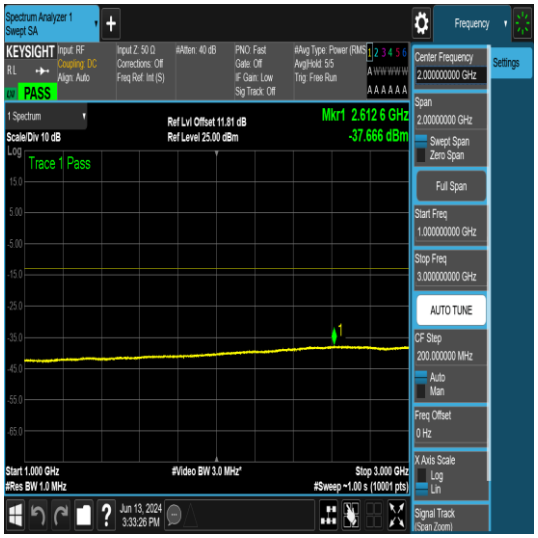

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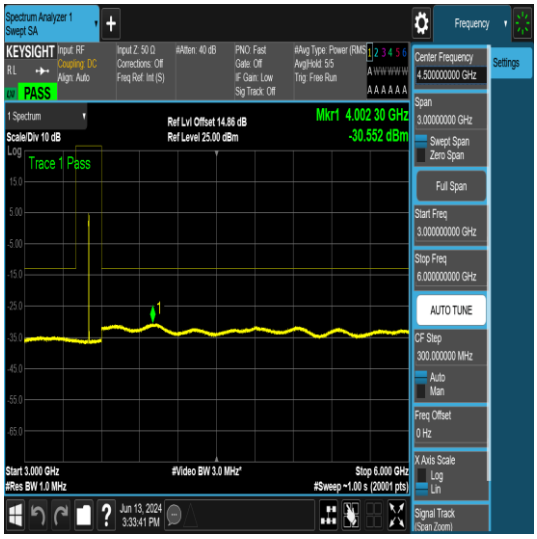

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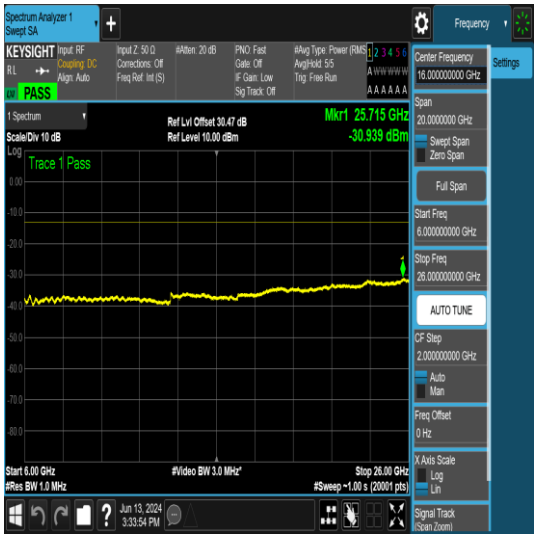
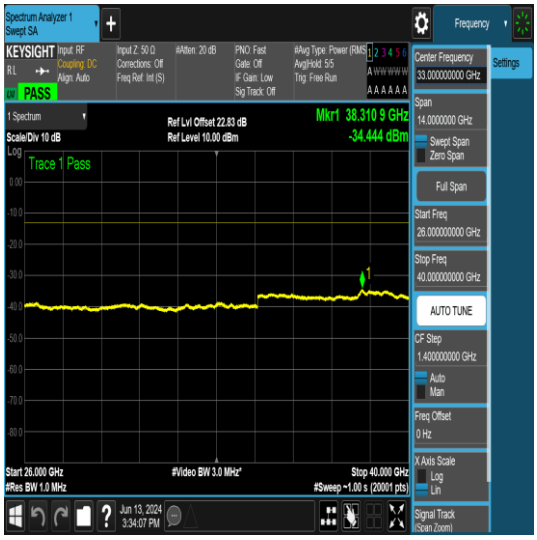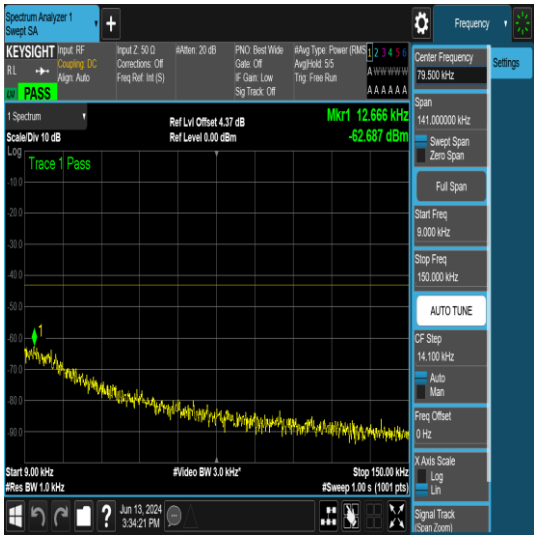
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