Appendix for 71A_N78A

Product Name: 5G Indoor CPE
Model No: TITAN 5000

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power（dBm）	Power Class	Verdict
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	23.73	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	24.75	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.71	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.67	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	22.78	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	23.65	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.75	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.57	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	24.29	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	25.26	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	24.01	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	24.19	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	23.34	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	24.39	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	23.16	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	23.49	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	23.75	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Inner_Full	24.76	PC2	PASS
DC_71A_n78A-3	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	23.75	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	23.46	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	22.76	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	23.62	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	22.72	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	22.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	23.88	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	24.77	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.77	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	23.76	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	22.78	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	23.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	23.07	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.96	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	24.35	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	25.41	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	24.25	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	24.50	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	23.34	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	24.41	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	23.41	PC2	PASS
DC_71A_n78A-3	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	23.19	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	23.91	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	24.83	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	24.04	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.78	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	22.90	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	23.79	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	23.32	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	22.90	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	23.85	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	24.83	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.50	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.86	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	22.77	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	23.88	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.72	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	23.06	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	24.36	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	25.38	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.18	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	24.09	PC2	PASS
DC_71A_n78A-3	30	5+20	DFT-16QAM	M+M	Outer_Full	23.32	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	24.32	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.46	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	23.33	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	23.87	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	24.88	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	23.80	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.86	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	22.91	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	23.83	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	23.06	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	23.19	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	23.88	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+L	Inner_Full	24.90	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.74	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	23.96	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	22.96	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+L	Inner_Full	23.88	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	23.00	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	23.10	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	24.47	PC2	PASS
DC_71A_n78A-3	30	5+30	DFT-QPSK	M+M	Inner_Full	25.37	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	24.51	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	24.30	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	23.49	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+M	Inner_Full	24.41	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	23.28	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	23.34	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	23.88	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+H	Inner_Full	24.84	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	23.97	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	23.86	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	22.96	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+H	Inner_Full	23.89	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	23.04	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	22.93	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	23.87	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	24.87	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.84	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	24.00	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	22.89	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	23.87	PC2	PASS
DC_71A_n78A-3	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.75	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.93	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	24.39	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	25.46	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.71	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	24.69	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	23.38	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	24.41	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.44	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.65	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	24.08	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	25.06	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	24.23	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.88	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	23.04	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	24.03	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	23.13	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	23.01	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	23.68	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	24.81	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.66	PC2	PASS
DC_71A_n78A-3	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	23.71	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	22.73	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	23.82	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.25	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	22.78	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	24.11	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	25.06	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	24.25	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	24.01	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	23.14	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	24.21	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.00	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.34	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	23.80	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	24.84	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.93	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.69	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	22.71	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	23.74	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	23.24	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.86	PC2	PASS
DC_71A_n78A-3	30	5+60	DFT-QPSK	M+L	Outer_Full	23.76	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	24.77	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.69	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.65	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	22.75	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	23.77	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.48	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.61	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	24.17	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	25.21	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.94	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	23.20	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	24.20	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	22.98	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	22.98	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	23.82	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	24.85	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.96	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.65	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	22.86	PC2	PASS
DC_71A_n78A-3	30	5+60	DFT-16QAM	M+H	Inner_Full	23.94	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	23.03	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.52	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	23.64	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	24.76	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.64	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	23.58	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	22.64	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	23.69	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.47	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.83	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	24.04	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	25.05	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	24.03	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	23.84	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	23.04	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	24.19	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.06	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	22.79	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	23.75	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	24.77	PC2	PASS
DC_71A_n78A-3	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.87	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	23.52	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	22.76	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	23.73	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	23.02	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.49	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	24.00	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	25.10	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.97	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.76	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	23.00	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	24.12	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.62	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.05	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	24.07	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	25.18	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.93	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	23.87	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	23.12	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	24.15	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.01	PC2	PASS
DC_71A_n78A-3	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	22.53	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	24.04	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	25.11	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	24.01	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.77	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	23.04	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	24.12	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.99	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	22.61	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	24.06	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	25.08	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.85	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	23.92	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	22.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	24.03	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	22.86	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	24.01	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	25.16	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	24.00	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	23.82	PC2	PASS
DC_71A_n78A-3	30	5+90	DFT-16QAM	M+M	Outer_Full	23.02	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	24.11	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	22.82	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	22.69	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	24.05	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	25.09	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.99	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	23.87	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	23.04	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	24.12	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	23.18	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	22.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	24.05	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	25.12	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.77	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.63	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	23.01	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	24.08	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.70	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.67	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	24.02	PC2	PASS
DC_71A_n78A-3	30	5+100	DFT-QPSK	M+M	Inner_Full	25.13	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.79	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	23.76	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	23.10	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	24.16	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.74	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	22.63	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	24.05	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	25.13	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.77	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	23.71	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	23.03	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	24.11	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.67	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.98	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.19	≤13	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.92	≤13	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.67	≤13	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.54	≤13	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.42	≤13	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.41	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.36	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.90	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.45	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.20	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.42	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.42	≤13	PASS
DC_71A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.54	≤13	PASS
DC_71A_n78A-3700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.80	≤13	PASS
DC_71A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.70	≤13	PASS
DC_71A_n78A-3700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.26	≤13	PASS
DC_71A_n78A-	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.70	≤13	PASS

3700-3800										
DC_71A_n78A-3700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.19	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.87	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.80	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.86	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.18	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.02	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.12	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.81	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.18	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.09	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.73	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.01	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.76	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.41	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.98	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.25	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.13	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.51	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.99	≤13	PASS
DC_71A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.88	≤13	PASS
DC_71A_n78A-	30	5+60	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.91	≤13	PASS

3700-3800										
DC_71A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.60	≤13	PASS
DC_71A_n78A-3700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.00	≤13	PASS
DC_71A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.19	≤13	PASS
DC_71A_n78A-3700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.63	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.65	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.97	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.07	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.78	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.11	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.97	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.31	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.20	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.05	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.78	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.84	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.55	≤13	PASS
DC_71A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.74	≤13	PASS
DC_71A_n78A-3700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.14	≤13	PASS
DC_71A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.40	≤13	PASS
DC_71A_n78A-3700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.12	≤13	PASS
DC_71A_n78A-	30	5+90	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.76	≤13	PASS

3700-3800										
DC_71A_n78A-3700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.82	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.94	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.18	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.92	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.04	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.67	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.73	≤13	PASS

Test Graphs

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+10_M+L_TID2_#1	NTNV_DC_71A_n78A-3700-3800_PC2_30_5+10_M+L_TID3_#1
NTNV_DC_71A_n78A-3700-3800_PC2_30_5+10_M+M_TID2_#1	NTNV_DC_71A_n78A-3700-3800_PC2_30_5+10_M+M_TID3_#1
NTNV_DC_71A_n78A-3700-3800_PC2_30_5+10_M+H_TID2_#1	NTNV_DC_71A_n78A-3700-3800_PC2_30_5+10_M+H_TID3_#1

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+15_M+L_TID2_#1

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+15_M+L_TID3_#1

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+15_M+M_TID2_#1

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+15_M+M_TID3_#1

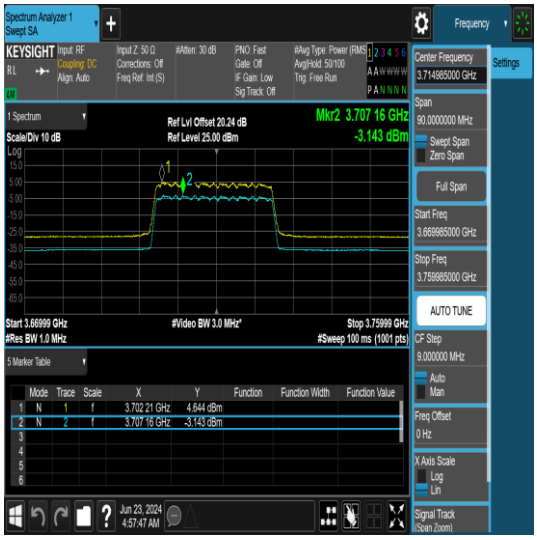
NTNV_DC_71A_n78A-3700-3800_PC2_30_5+15_M+H_TID2_#1	NTNV_DC_71A_n78A-3700-3800_PC2_30_5+15_M+H_TID3_#1
NTNV_DC_71A_n78A-3700-3800_PC2_30_5+20_M+L_TID2_#1	NTNV_DC_71A_n78A-3700-3800_PC2_30_5+20_M+L_TID3_#1
NTNV_DC_71A_n78A-3700-3800_PC2_30_5+20_M+M_TID2_#1	NTNV_DC_71A_n78A-3700-3800_PC2_30_5+20_M+M_TID3_#1

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+20_M+H_TID2_#1

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+20_M+H_TID3_#1

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+30_M+L_TID2_#1

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+30_M+L_TID3_#1



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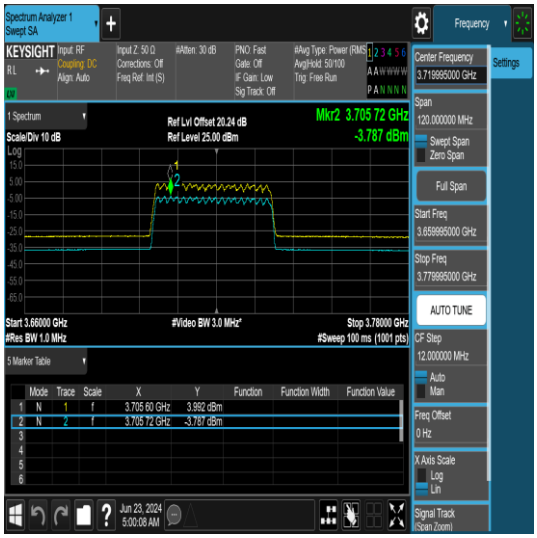
NTNV_DC_71A_n78A-3700-3800_PC2_30_5+30_M+H_TID2_#1	NTNV_DC_71A_n78A-3700-3800_PC2_30_5+30_M+H_TID3_#1
NTNV_DC_71A_n78A-3700-3800_PC2_30_5+40_M+L_TID2_#1	NTNV_DC_71A_n78A-3700-3800_PC2_30_5+40_M+L_TID3_#1



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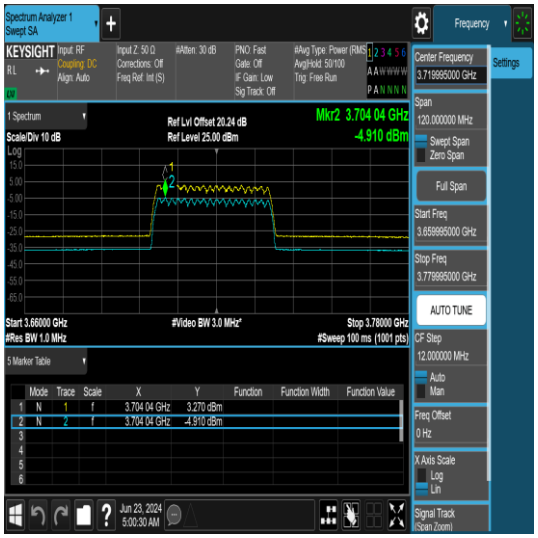

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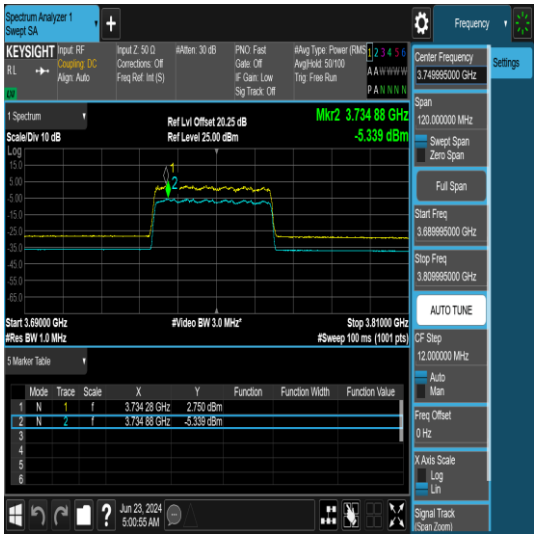

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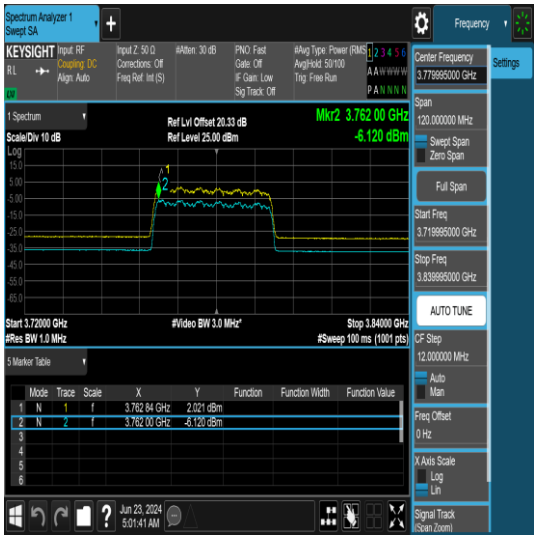

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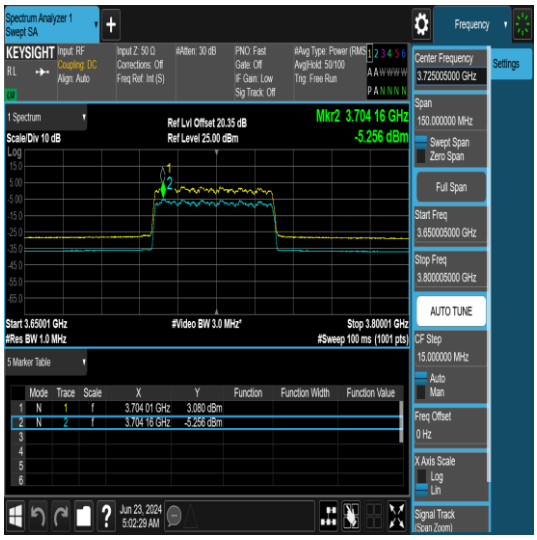	
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	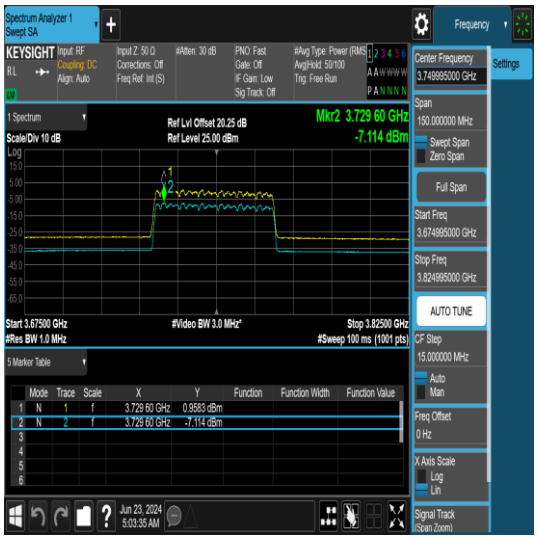
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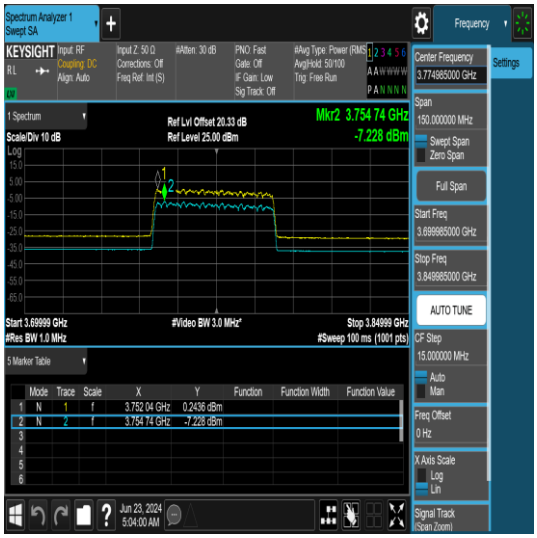

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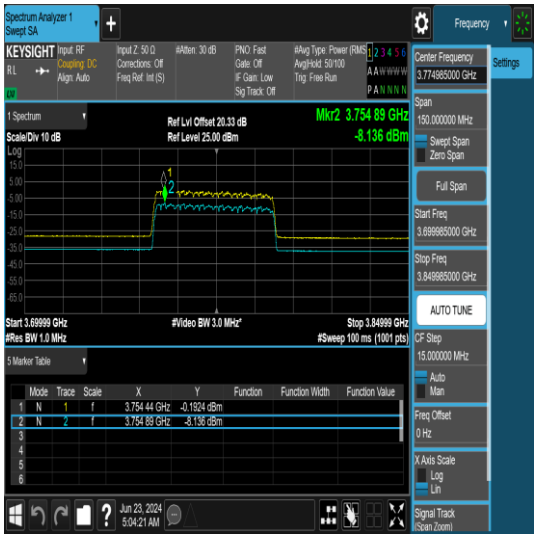

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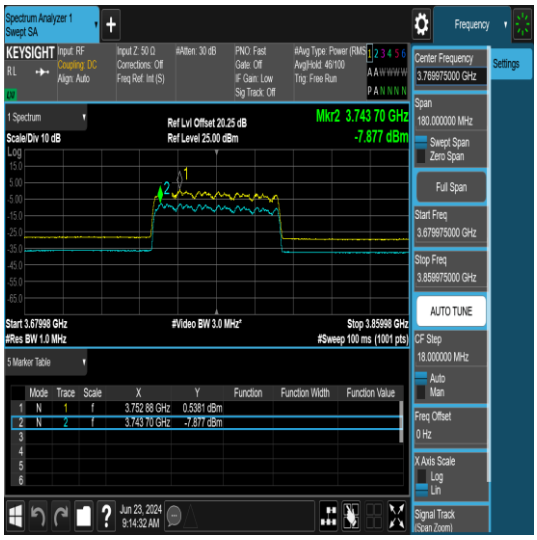
NTNV_DC_71A_n78A-3700-3800_PC2_30_5+60_M+M_TID3_#1

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+60_M+H_TID2_#1	NTNV_DC_71A_n78A-3700-3800_PC2_30_5+60_M+H_TID3_#1
	
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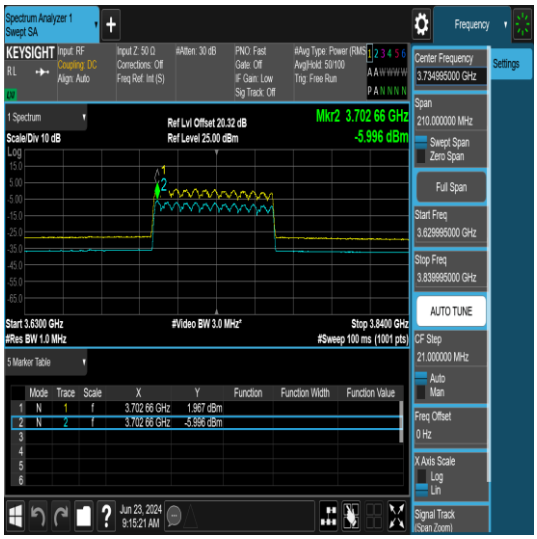	
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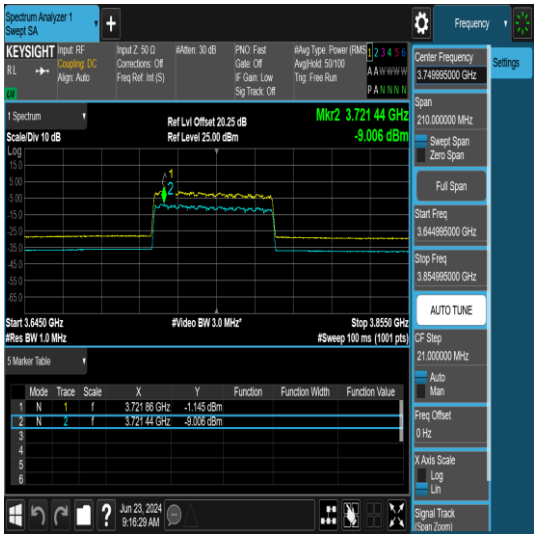

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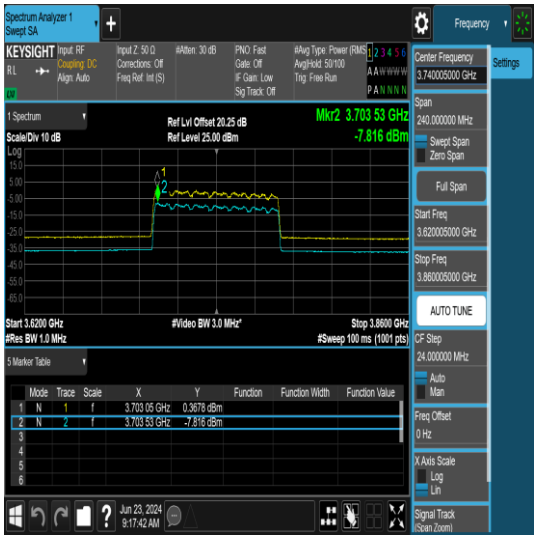

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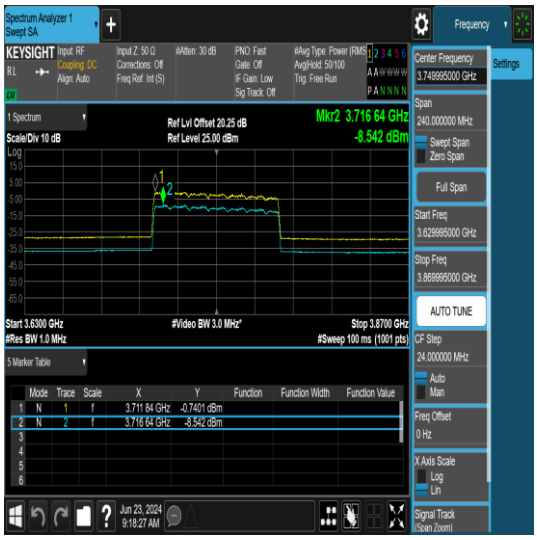	
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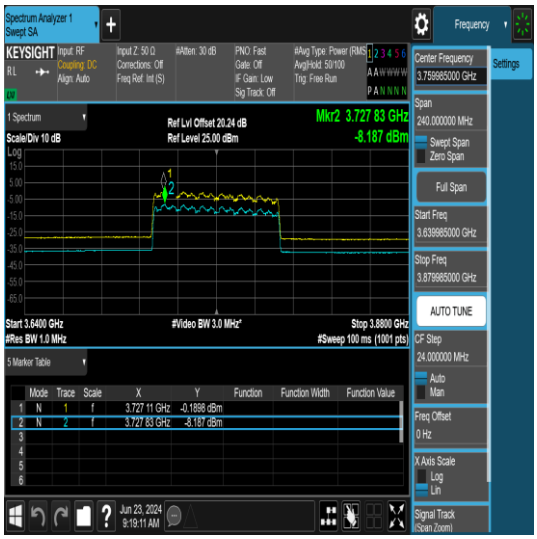	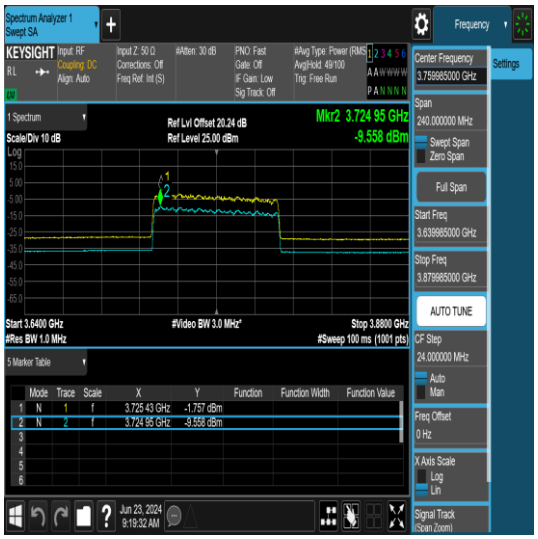
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