

Appendix for 5A_N77A(3450-3550MHz)

Product Name: 5G Window CPE

Model No: TITAN 5100

Appendix A: Average Power Output Data

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	23.64	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	24.63	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.44	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.58	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	22.64	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	23.62	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.44	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.56	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	23.79	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	24.74	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	23.91	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	23.66	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	22.84	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	23.77	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	22.69	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	22.52	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	23.40	PC2	PASS
DC_5A_n77A-34	30	5+10	DFT-QPSK	M+H	Inner_Full	24.45	PC2	PASS

50-3550								
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	23.33	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	23.31	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	22.41	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	23.38	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	22.22	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	22.54	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	23.79	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	24.81	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.56	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	23.69	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	22.82	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	23.84	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.48	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.52	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	23.96	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	24.85	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.72	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	23.49	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	22.85	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	23.80	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	22.67	PC2	PASS

DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	22.46	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	23.49	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	24.46	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	23.47	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.31	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	22.43	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	23.56	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	22.40	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	22.69	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	23.85	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	24.88	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.95	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	24.13	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	22.81	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	23.82	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.88	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	23.14	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	23.97	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	24.99	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.01	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.84	PC2	PASS
DC_5A_n77A-34	30	5+20	DFT-16QAM	M+M	Outer_Full	22.95	PC2	PASS

50-3550								
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	23.93	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.09	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	22.68	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	23.50	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	24.56	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	23.64	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.51	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	22.52	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	23.54	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.52	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.08	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	23.88	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+L	Inner_Full	24.82	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	24.06	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	24.14	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	22.90	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+L	Inner_Full	23.84	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	22.65	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	23.11	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	24.09	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+M	Inner_Full	24.99	PC2	PASS

DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	24.10	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	23.82	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	23.10	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+M	Inner_Full	24.00	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	23.15	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	22.71	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	23.57	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+H	Inner_Full	24.50	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	23.70	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	23.33	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	22.55	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+H	Inner_Full	23.53	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	23.11	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	22.46	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	23.92	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	24.97	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.90	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	24.27	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	22.97	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	24.01	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.72	PC2	PASS
DC_5A_n77A-34	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.15	PC2	PASS

50-3550								
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	24.06	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	25.07	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.27	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	24.03	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	23.10	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	23.98	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.36	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.18	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	23.69	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	24.71	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	24.04	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.72	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	22.68	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	23.68	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	23.11	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.81	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	23.77	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	24.83	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.55	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	23.62	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	22.73	PC2	PASS

DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	23.79	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.59	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	22.72	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	23.88	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	24.82	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	24.02	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.53	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	22.91	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	23.86	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.20	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	22.72	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	23.48	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	24.48	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.74	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.23	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	22.45	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	23.48	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.71	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.17	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	23.91	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	24.98	PC2	PASS
DC_5A_n77A-34	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.62	PC2	PASS

50-3550								
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.75	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	22.91	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	24.04	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.29	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.45	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	23.84	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	24.89	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.97	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.46	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	22.94	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	23.92	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.09	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	22.59	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	23.64	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	24.66	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.86	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.27	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	22.73	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	23.73	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.94	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.26	PC2	PASS

DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	23.76	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	24.79	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.43	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	23.31	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	22.67	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	23.88	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.34	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.33	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	23.71	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	24.77	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.67	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	23.12	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	22.70	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	23.74	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	22.74	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	22.05	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	23.53	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	24.56	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.80	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	23.05	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	22.47	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	23.56	PC2	PASS

50-3550								
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	22.73	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.14	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	23.70	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	24.79	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.45	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.42	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	22.65	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	23.74	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.48	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	22.19	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	23.58	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	24.76	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.66	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	23.21	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	22.57	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	23.68	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	22.60	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	22.18	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	23.54	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	24.52	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.68	PC2	PASS

DC_5A_n77A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.10	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	22.59	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	23.58	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.77	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	22.22	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	23.59	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	24.78	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.44	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	23.17	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	22.60	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	23.72	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.38	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	22.28	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	23.64	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	24.74	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.50	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	23.07	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	22.67	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	23.75	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	22.33	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	22.31	PC2	PASS
DC_5A_n77A-34	30	5+90	DFT-QPSK	M+H	Outer_Full	23.58	PC2	PASS

50-3550								
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	24.67	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.47	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	23.02	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	22.76	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	23.72	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.75	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	22.17	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	23.63	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	24.72	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.50	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.23	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	22.62	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	23.70	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.11	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.15	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	23.60	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	24.71	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.32	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	23.08	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	22.63	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	23.75	PC2	PASS

DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.12	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	22.31	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	23.65	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	24.71	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.33	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	23.07	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	22.59	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	23.71	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.46	PC2	PASS
DC_5A_n77A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.03	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_5A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.32	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.57	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.31	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.81	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.55	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.66	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.87	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.76	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.00	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.76	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.47	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.84	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.76	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.05	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.70	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.99	≤13	PASS

DC_5A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.70	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.71	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.94	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.25	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.00	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.12	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.28	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.83	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.07	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.24	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.93	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.29	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.00	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.13	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.16	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.28	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.99	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.18	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.07	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.11	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.89	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.04	≤13	PASS

450-3550										
DC_5A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.32	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.85	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.26	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.31	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.56	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.08	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.08	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.13	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.17	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.57	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.19	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.88	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.74	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.36	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.96	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.07	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.32	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.93	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.28	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.36	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.52	≤13	PASS

DC_5A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.08	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.98	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.24	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.93	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.36	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.29	≤13	PASS
DC_5A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.98	≤13	PASS

Test Graphs























