



Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.71	-43	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-63.26	-33	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-49.18	-23	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.96	-13	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.78	-13	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.17	-13	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.71	-13	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.21	-43	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.36	-33	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-47.98	-23	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.98	-13	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.72	-13	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.67	-13	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.56	-43	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-63.89	-33	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-50.10	-23	PASS
DC_5A_n77A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.99	-13	PASS

DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.88	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-63.41	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-52.75	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.87	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.40	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-50.11	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-63.46	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-65.61	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-49.88	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.67	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.14	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.50	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.92	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-50.37	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-34.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.67	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-32.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.99	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.44	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-48.94	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.72	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.99	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-64.47	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-47.40	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.70	-13	PASS

DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.97	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-64.49	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-50.02	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	1000	3000	-39.99	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.14	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.56	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-63.85	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-49.77	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.16	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-64.91	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-47.96	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-39.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-32.81	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.84	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-63.99	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-48.54	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-32.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.58	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.07	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-63.76	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-50.94	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-34.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-32.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.12	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.16	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-50.14	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.14	-13	PASS

DC_5A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-63.92	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-63.81	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-47.95	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-63.77	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-64.38	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-50.57	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-32.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.30	-43	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-63.30	-33	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-52.28	-23	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-34.47	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.67	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.96	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-64.73	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-50.07	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.72	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.24	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.12	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-49.46	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.80	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-64.16	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-52.27	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.99	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-32.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.78	-13	PASS

DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-63.71	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-65.81	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-51.44	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-63.73	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-63.66	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-50.73	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.11	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.25	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-64.17	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-48.42	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.73	-13	PASS
DC_5A_n77A-	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.81	-43	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.25	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-51.81	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.26	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.11	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-49.00	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.20	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.11	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.61	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.84	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-64.24	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-50.04	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.74	-43	PASS

DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.17	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-52.50	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	1000	3000	-34.46	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.48	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.37	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-49.29	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.01	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-64.07	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-51.39	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-34.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.89	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-64.99	-33	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-49.75	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-34.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.58	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.33	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.63	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-49.29	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-32.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.01	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-63.79	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-48.19	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-32.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.42	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-64.83	-33	PASS

DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-49.99	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.98	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.59	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-63.66	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-49.31	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-32.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.47	-43	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-65.24	-33	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-52.45	-23	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.16	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-64.17	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-49.92	-23	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.36	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-63.86	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-49.80	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-34.50	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.08	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-64.44	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-48.96	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-36.18	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.69	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.91	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-64.15	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-53.02	-23	PASS

DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.73	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-64.60	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-51.17	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-34.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.63	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.49	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-65.61	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-48.39	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-34.46	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-32.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.60	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.73	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.04	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-53.03	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-35.62	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.80	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.13	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-49.27	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.17	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.91	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-49.10	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.02	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-32.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.53	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.47	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.09	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-50.40	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	1000	3000	-39.93	-13	PASS

DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.63	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.69	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.07	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-64.33	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-49.65	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.65	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.76	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-49.63	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-34.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.61	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-32.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.72	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.87	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-64.18	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-49.72	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.77	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.71	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.30	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-50.19	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.99	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-32.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.18	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.46	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-52.72	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-34.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.26	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-65.35	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-49.92	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	1000	3000	-36.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.75	-13	PASS

DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.20	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.18	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-51.44	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-34.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.54	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.44	-43	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.73	-33	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-48.07	-23	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.69	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.15	-13	PASS
DC_5A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.54	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-64.11	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-52.20	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.98	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	26000	-32.94	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.59	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.95	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-52.92	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.11	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-66.08	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.03	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-36.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-32.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.25	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	0.15	30	-64.15	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	30	1000	-51.67	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.04	-13	PASS

DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.59	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-63.53	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-63.97	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-50.31	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.81	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-66.07	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-50.08	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.56	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.10	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.70	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-50.33	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	3000	-34.62	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.81	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.05	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-64.74	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-50.28	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.45	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-63.74	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-47.74	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.02	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-32.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.61	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-63.91	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	0.15	30	-66.01	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	30	1000	-48.76	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	1000	3000	-39.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.70	-13	PASS

DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.84	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-64.06	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-50.62	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.10	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-64.96	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-49.63	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-39.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.67	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-64.25	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-50.44	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.05	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.66	-43	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-64.96	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-51.98	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-32.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.64	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.09	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.15	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-48.65	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-32.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.63	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-63.67	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	0.15	30	-65.99	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	30	1000	-50.55	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.98	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.11	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.89	-43	PASS

DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-63.32	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-52.35	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-35.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.78	-43	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-65.80	-33	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-49.39	-23	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.22	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-62.93	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-49.96	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-32.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.61	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.08	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-63.91	-33	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-50.83	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.65	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-63.87	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-49.80	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.14	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	0.15	30	-65.98	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	30	1000	-50.39	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.18	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.25	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-64.72	-33	PASS

DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-52.50	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-34.67	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-63.84	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-64.12	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-52.68	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-32.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.41	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.47	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-50.01	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-32.98	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.94	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-63.50	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-26.31	-23	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-32.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.30	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.40	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-50.49	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.48	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.68	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	30	1000	-51.32	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	1000	3000	-39.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.26	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-64.92	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-50.53	-23	PASS

DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-34.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.62	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-32.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.28	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-66.22	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-50.84	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-39.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-32.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.97	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-63.90	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-47.70	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-37.27	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.60	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.86	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.40	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-49.89	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.97	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.53	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.42	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-50.70	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.28	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.59	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.05	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.76	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	0.15	30	-65.97	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	30	1000	-48.46	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.48	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.18	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-49.60	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-34.70	-13	PASS

DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.32	-43	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.16	-33	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-50.25	-23	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.69	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.62	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.44	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-64.20	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-50.21	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-37.67	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.14	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.65	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-50.67	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.78	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.53	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.66	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-63.48	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-50.69	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.88	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	0.15	30	-64.46	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	30	1000	-49.92	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.70	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-64.82	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-50.12	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.89	-13	PASS

DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.60	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-64.85	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-48.56	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.14	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.52	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.60	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.53	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-52.94	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-32.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.25	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-64.61	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-49.97	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.29	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-32.95	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.77	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.76	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-52.68	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-32.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-63.66	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	0.15	30	-64.43	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	30	1000	-48.89	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	6000	26000	-32.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-63.95	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.94	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-48.66	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-35.02	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.10	-13	PASS

DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.14	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.44	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-52.70	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-38.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.69	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.24	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-63.19	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-49.72	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.72	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.55	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.12	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-63.45	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-49.77	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.78	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.61	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.68	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-49.80	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.63	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.87	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	0.15	30	-64.00	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	30	1000	-49.48	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.33	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-64.81	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-51.54	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-39.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.72	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-32.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.73	-13	PASS

DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.56	-43	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.20	-33	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-50.06	-23	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-34.51	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-32.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.84	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.20	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-50.09	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.60	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-32.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.49	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.69	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-49.59	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.99	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.72	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-32.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.52	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.62	-43	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-64.05	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-50.82	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.29	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	0.15	30	-65.37	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	30	1000	-50.70	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.69	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.27	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-64.54	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-49.95	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.10	-43	PASS

DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-64.81	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-50.22	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.62	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-32.98	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.13	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.84	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-50.04	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-38.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.70	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-64.20	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-50.53	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-32.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.76	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-64.88	-33	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-50.35	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-34.50	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.43	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.68	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	30	1000	-50.91	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	1000	3000	-39.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	6000	26000	-32.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.28	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-63.65	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-49.42	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.55	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.28	-33	PASS

DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-50.04	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-39.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-32.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.90	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-64.65	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-51.24	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-32.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.56	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.92	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-64.36	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-52.79	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.87	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-63.75	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-50.28	-23	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.98	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-32.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.52	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-63.50	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	0.15	30	-64.95	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	30	1000	-49.74	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.73	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.26	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-51.77	-23	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.05	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.16	-43	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-65.88	-33	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-49.68	-23	PASS

DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.62	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.02	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-50.04	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	3000	-38.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	26000	-32.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.71	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.40	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-49.86	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-34.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.73	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.69	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-49.72	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.86	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-32.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.76	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	0.15	30	-64.65	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	30	1000	-51.18	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	1000	3000	-32.23	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	6000	26000	-32.98	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-63.80	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-62.73	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-49.68	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.72	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.21	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-66.17	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-49.22	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.93	-13	PASS

DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.69	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.87	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.33	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-48.55	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	26000	-32.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.13	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-63.52	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.20	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.15	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.55	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.28	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-26.28	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.76	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.67	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-63.88	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	0.15	30	-65.14	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	30	1000	-50.31	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	1000	3000	-35.49	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.32	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.26	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-49.15	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-35.55	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.94	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.14	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-49.46	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-34.53	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.68	-13	PASS

DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-32.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.16	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-63.65	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-47.85	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	3000	-36.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.16	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.14	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-50.88	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.95	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-32.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.47	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.99	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-50.52	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.64	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.09	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-63.83	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	0.15	30	-64.63	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	30	1000	-49.81	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-63.82	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-64.93	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-49.63	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-39.98	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.54	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-32.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-63.51	-43	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.89	-33	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-48.21	-23	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.06	-13	PASS

DC_5A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.67	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.98	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.18	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-48.33	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.62	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-32.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.29	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.30	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-52.57	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-32.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.63	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.85	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.62	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-51.32	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.72	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.11	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.67	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.21	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	0.15	30	-63.35	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	30	1000	-49.84	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.62	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.19	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-64.96	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-51.78	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-34.51	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.65	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-64.06	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-49.70	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.56	-13	PASS

DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.80	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.36	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-52.60	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.26	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-63.99	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-51.12	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.53	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-64.54	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-51.20	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.76	-13	PASS
DC_5A_n77A-	30	5+80	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.72	-43	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	0.15	30	-64.61	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	30	1000	-50.44	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	1000	3000	-35.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-63.90	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.88	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-49.18	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.69	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.21	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-64.13	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-52.36	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-39.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-32.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.63	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.41	-43	PASS

DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-63.65	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-50.77	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-36.05	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-32.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.55	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.67	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.21	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-49.42	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.53	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-32.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.83	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.42	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-50.34	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.66	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	0.15	30	-64.15	-33	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	30	1000	-51.56	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.59	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.84	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-64.34	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-49.93	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-34.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-63.98	-43	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.87	-33	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-51.14	-23	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.05	-13	PASS
DC_5A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.19	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.55	-33	PASS

DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-50.94	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.89	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.80	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-50.23	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.87	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.65	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.05	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-34.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.09	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	0.15	30	-63.76	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	30	1000	-49.53	-23	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.63	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	6000	26000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.30	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-64.21	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-49.94	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.58	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-32.88	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.67	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-64.10	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-51.35	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.93	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.75	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-66.32	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-51.12	-23	PASS

DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.05	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.68	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.35	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.28	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.86	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.05	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.93	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-63.36	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-51.24	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.37	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	0.15	30	-64.46	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	30	1000	-50.57	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	1000	3000	-39.93	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.18	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.17	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-64.76	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-52.70	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-34.50	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.65	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-63.18	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-64.58	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-52.12	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-34.55	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.67	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.31	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-64.65	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-52.30	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.93	-13	PASS

DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.75	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.02	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-50.22	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-35.22	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.75	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.12	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-62.65	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-50.78	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.72	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.63	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	0.15	30	-65.46	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	30	1000	-53.06	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.80	-13	PASS

3450-3550										
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.04	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-64.20	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-51.23	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-39.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-32.76	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-63.42	-43	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.96	-33	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-52.58	-23	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.60	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-64.43	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-52.55	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.76	-13	PASS

DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.54	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.46	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.04	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-49.15	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-34.62	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.90	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-32.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.26	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-64.84	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-50.57	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.92	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.47	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	0.15	30	-64.90	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	30	1000	-50.98	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.15	-13	PASS

3450-3550		0								
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.50	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.17	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.08	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-49.48	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.70	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.29	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-65.47	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-52.99	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.89	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.84	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.66	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.13	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.96	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-50.96	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-34.58	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-32.67	-13	PASS

DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.80	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-63.31	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.31	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.88	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.11	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-48.58	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-63.95	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.15	30	-64.86	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	30	1000	-49.97	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.77	-13	PASS

3450-3550		0								
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.89	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-63.16	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-50.87	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-32.91	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.56	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.89	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.91	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-49.95	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-32.33	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.87	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.70	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-62.15	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-50.73	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.86	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.71	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-32.80	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.85	-13	PASS

DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.49	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.75	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-50.40	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.79	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-32.98	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.57	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.52	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.69	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-51.03	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.54	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-32.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.09	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.15	30	-63.53	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	30	1000	-51.35	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	1000	3000	-37.44	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.81	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.58	-43	PASS

3450-3550		0								
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.54	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-50.43	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-38.10	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.82	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-32.94	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.64	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.91	-43	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-65.47	-33	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-49.46	-23	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.96	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.73	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-32.97	-13	PASS
DC_5A_n77A-3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.61	-13	PASS

Test Graphs





















