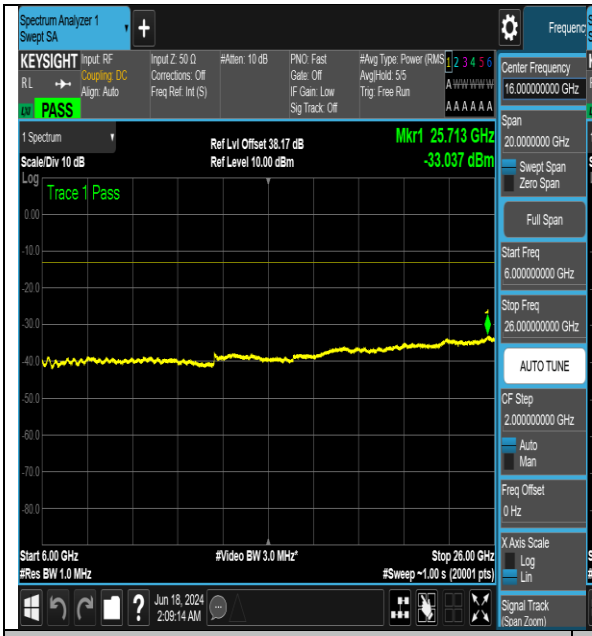
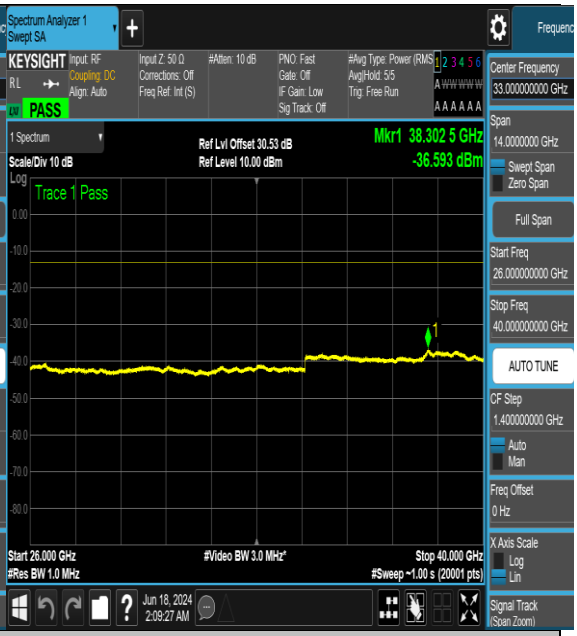
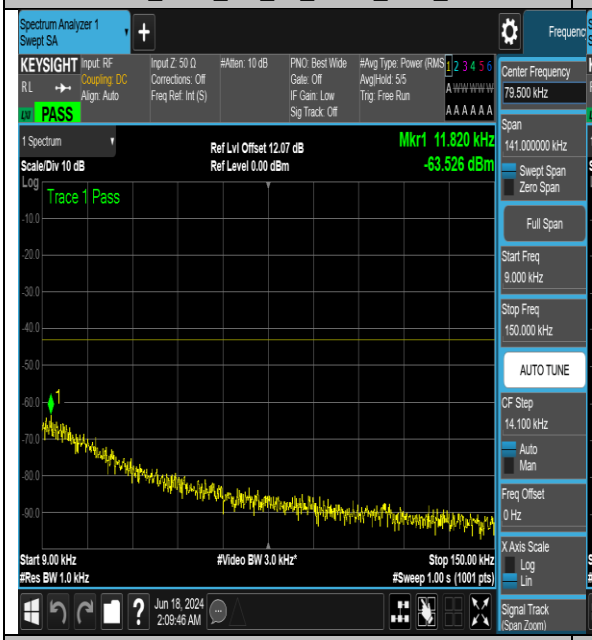


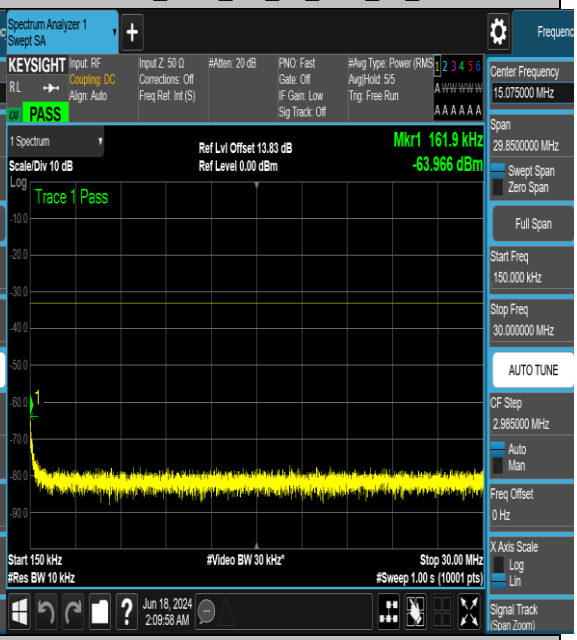
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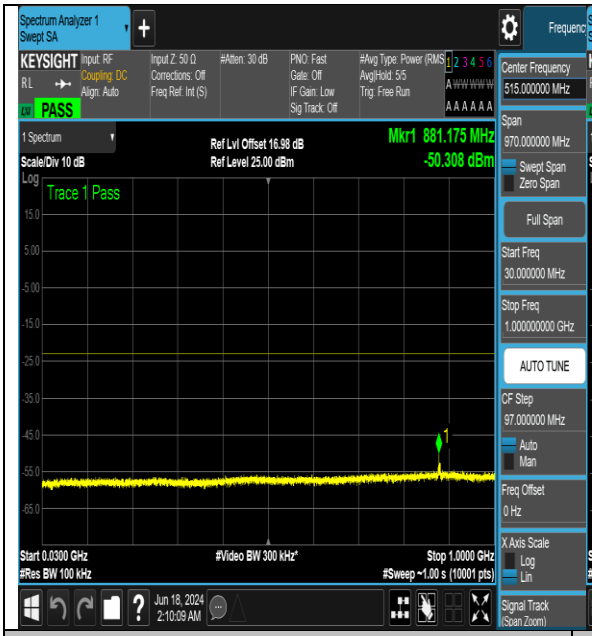
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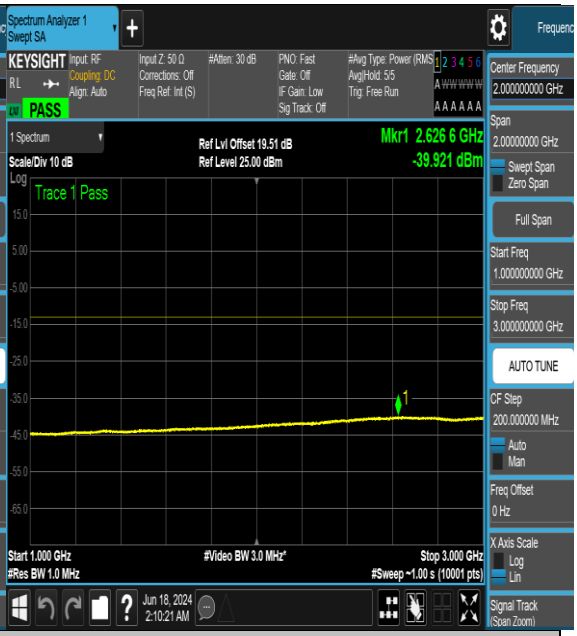
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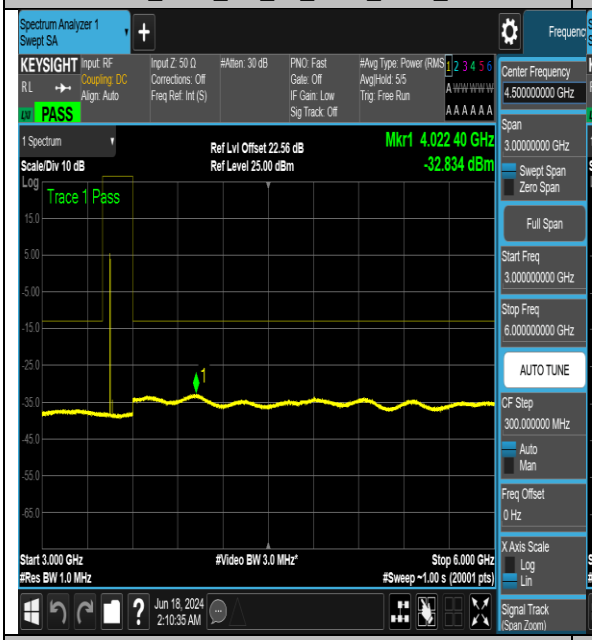
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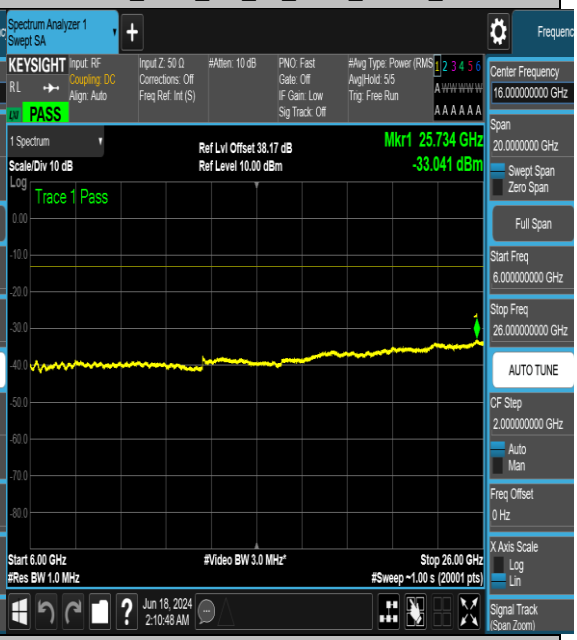
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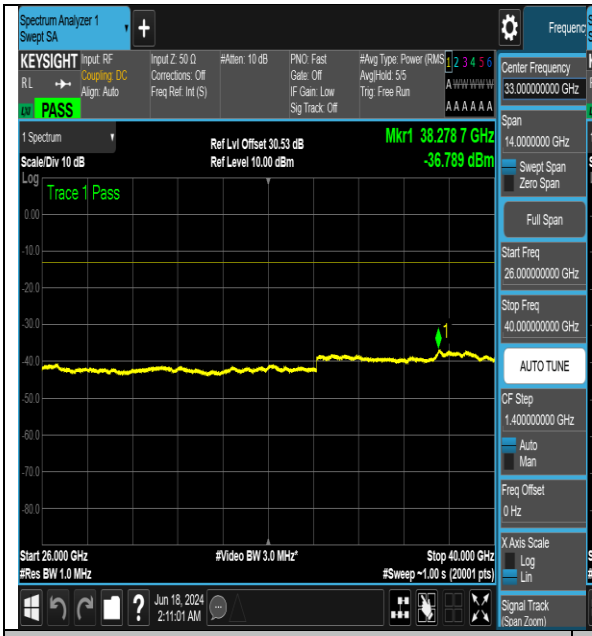
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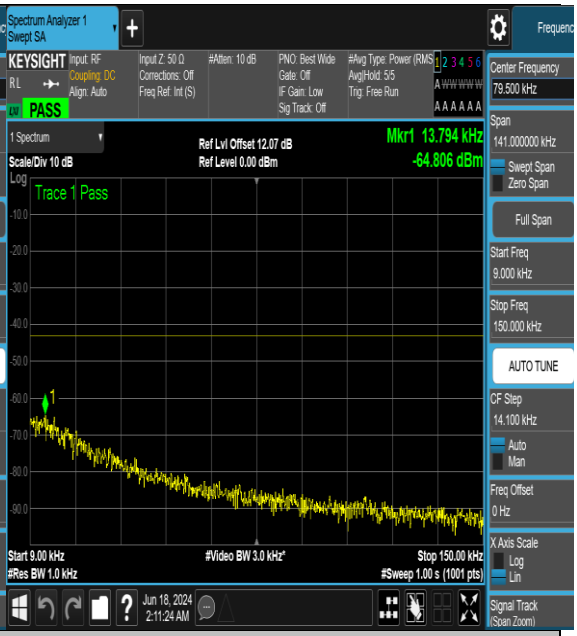
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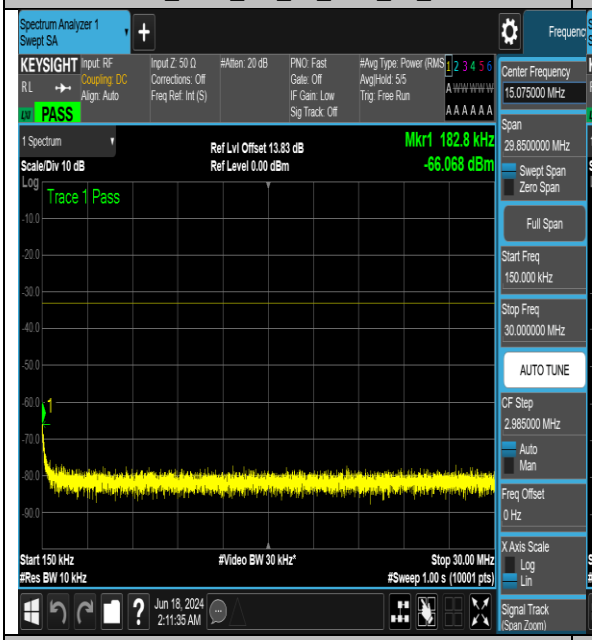
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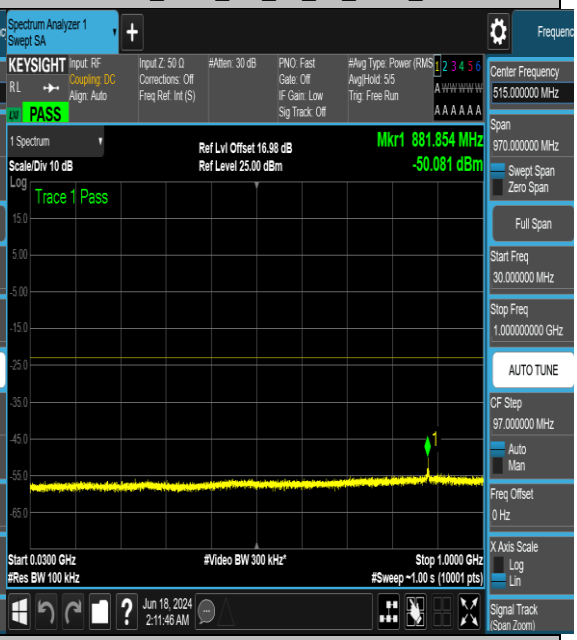
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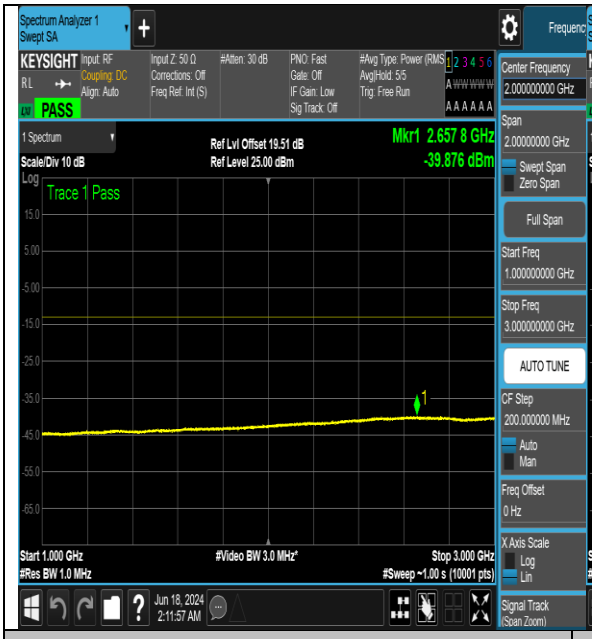
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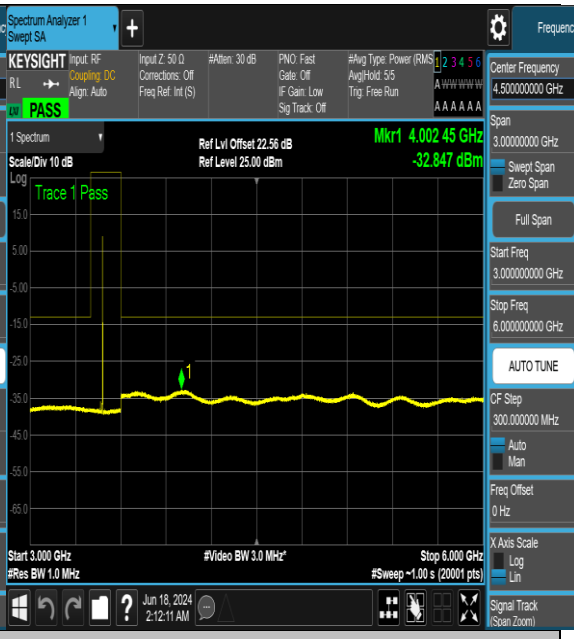
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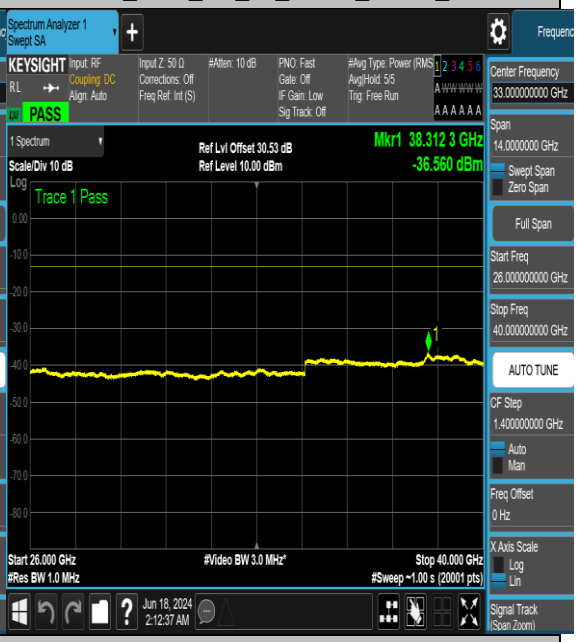
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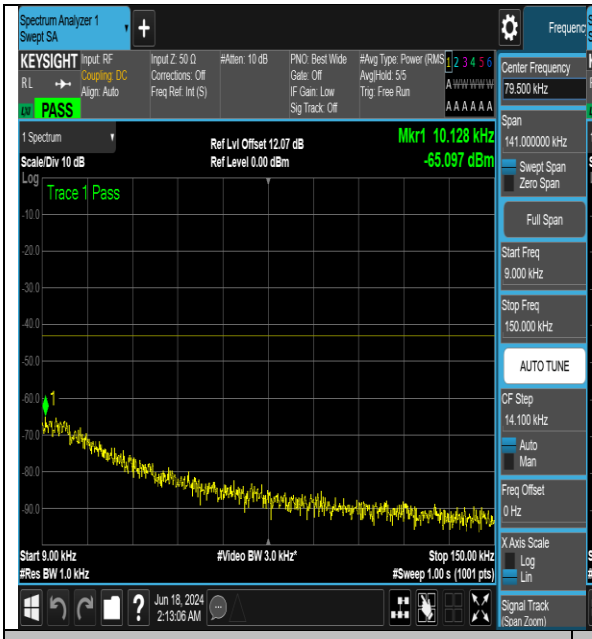
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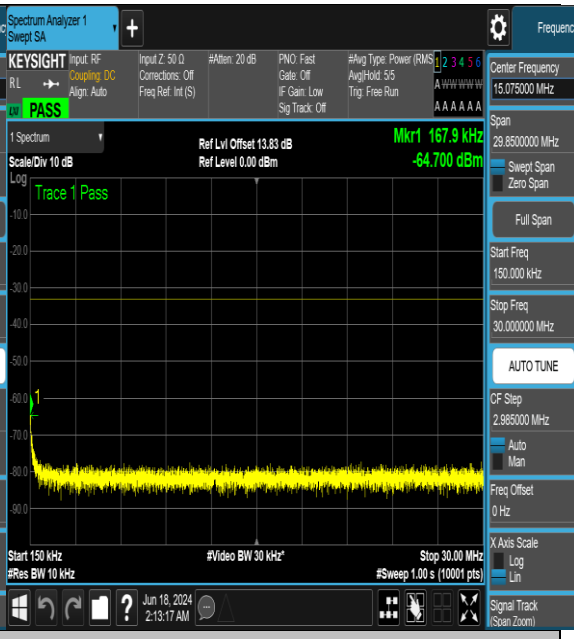
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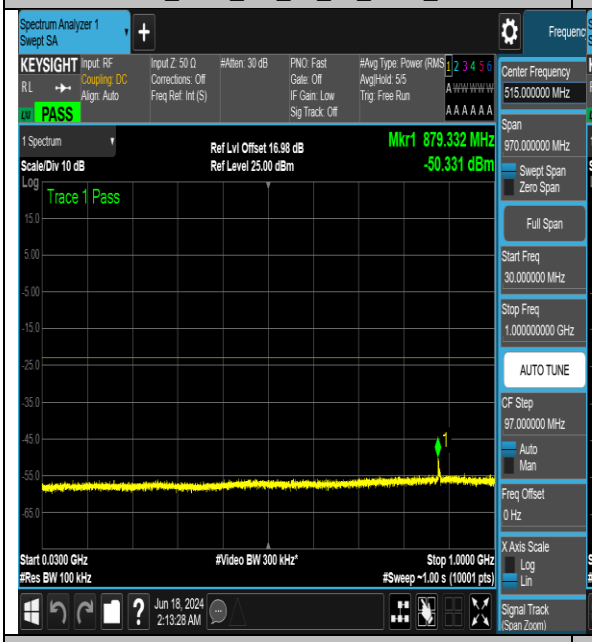
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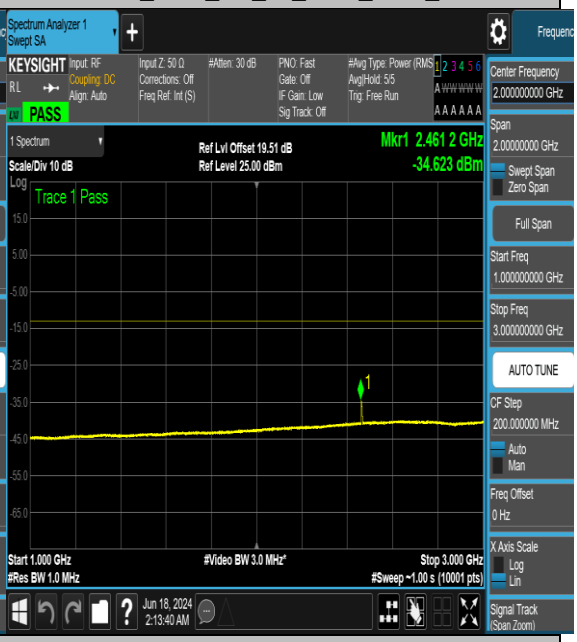
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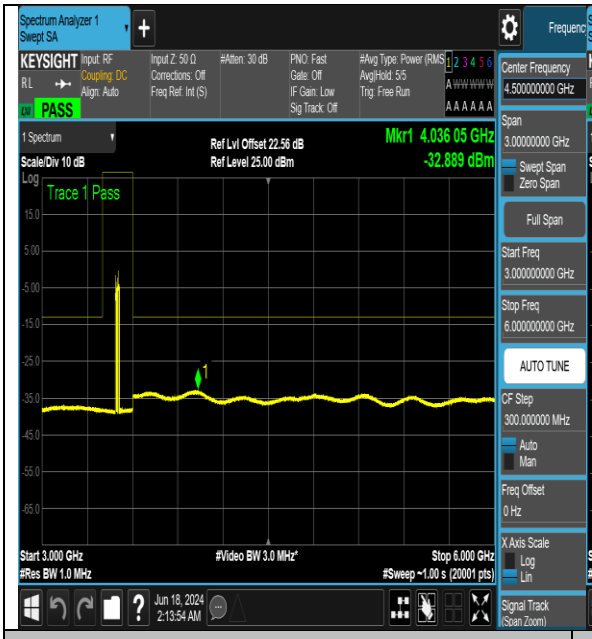
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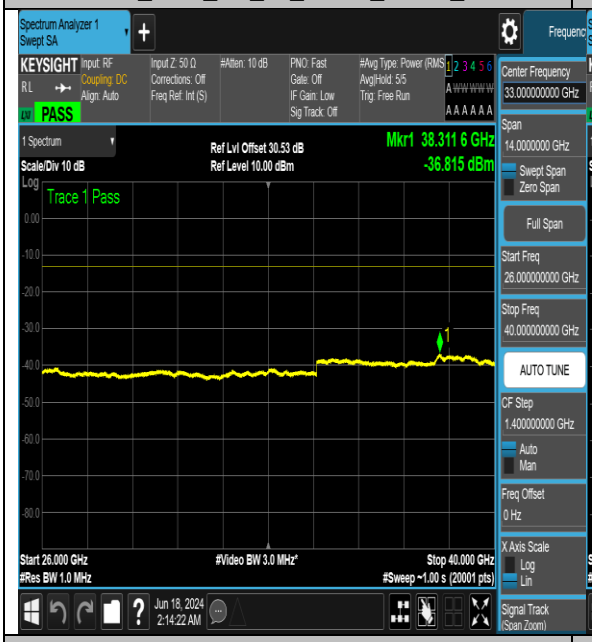
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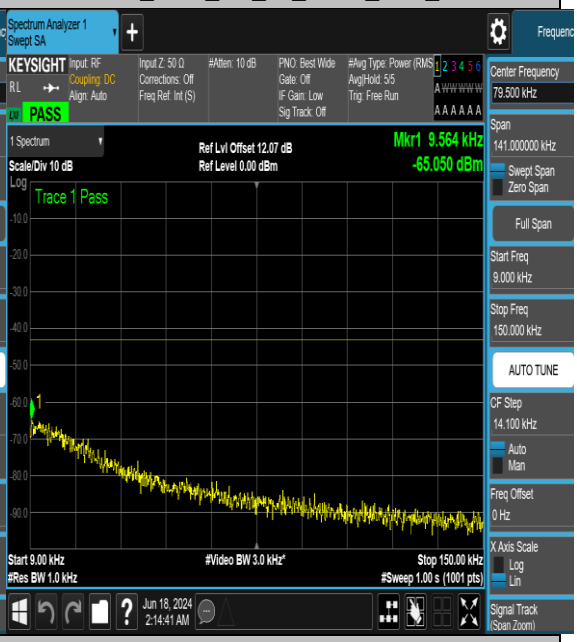
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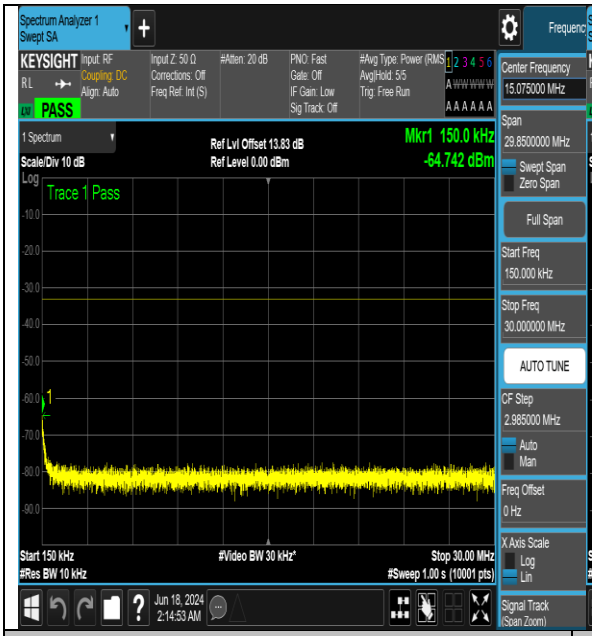
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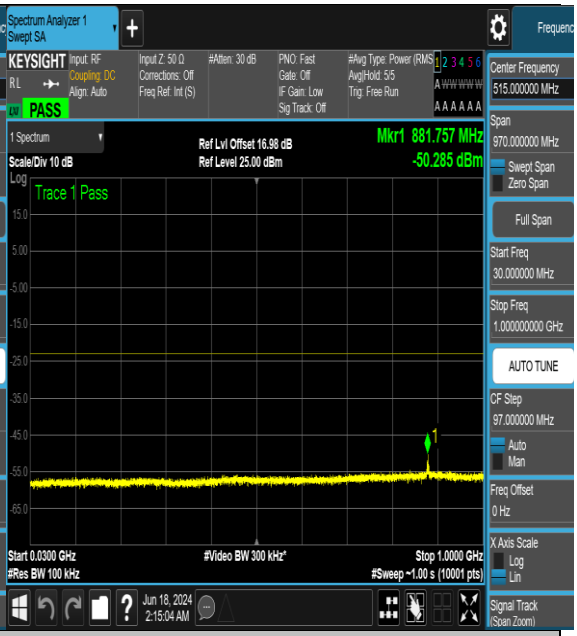
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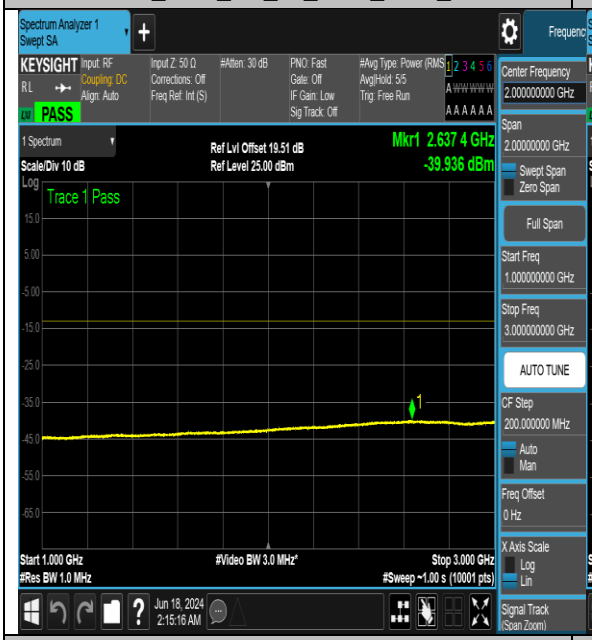
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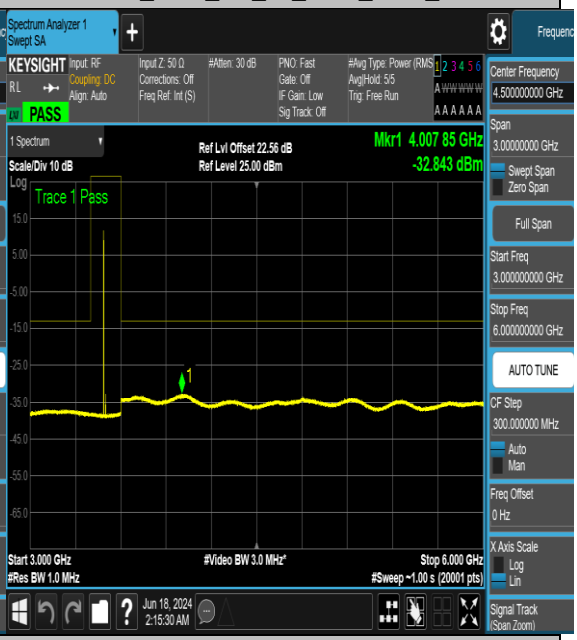
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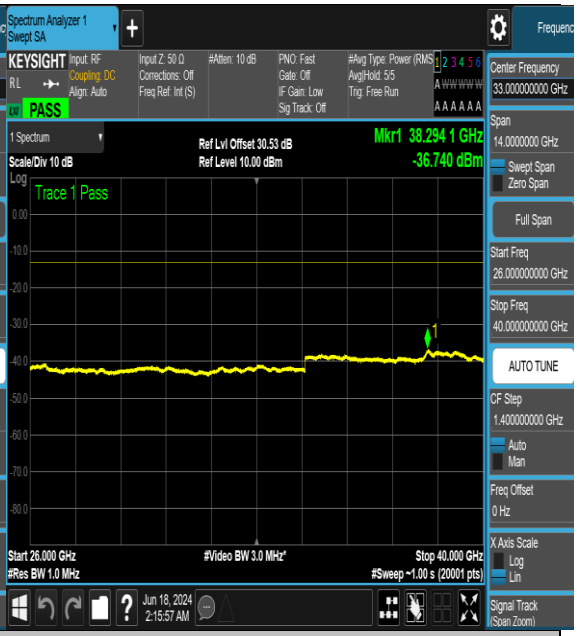
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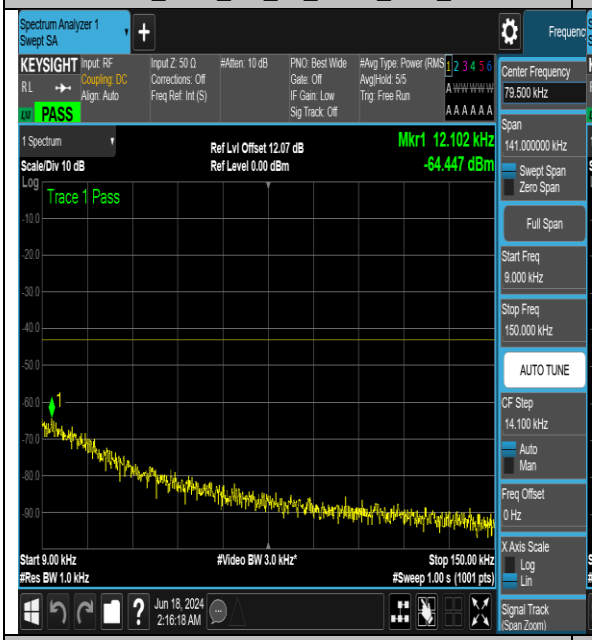
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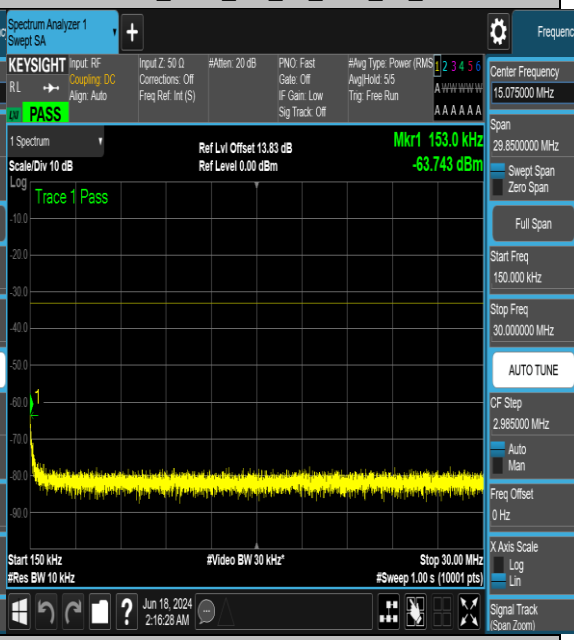
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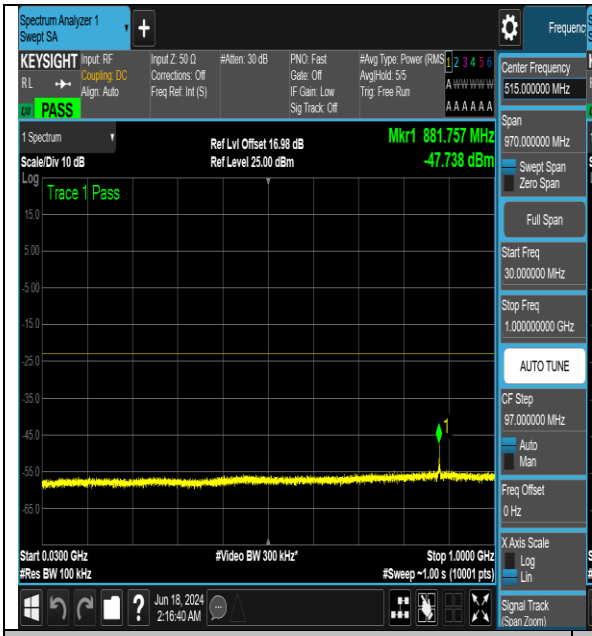
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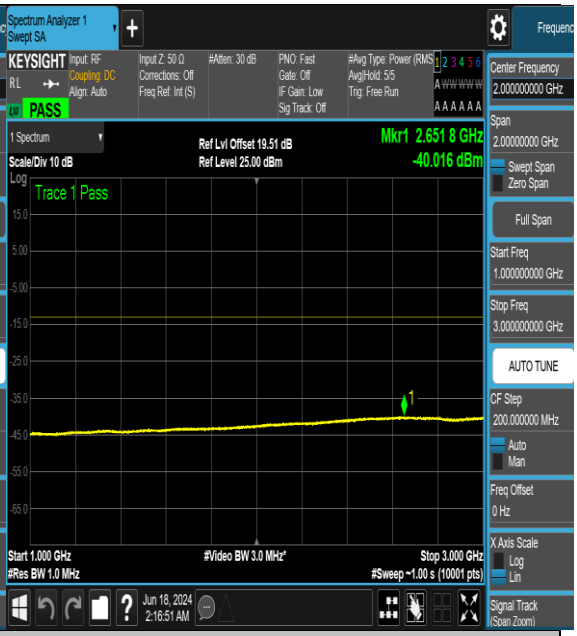
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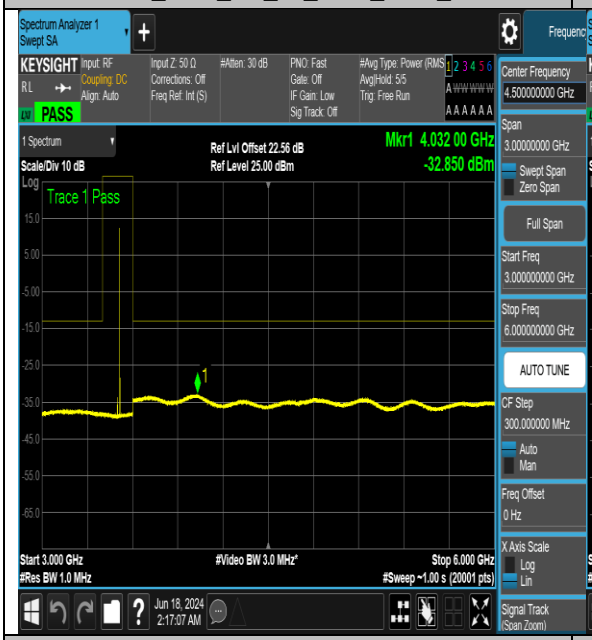
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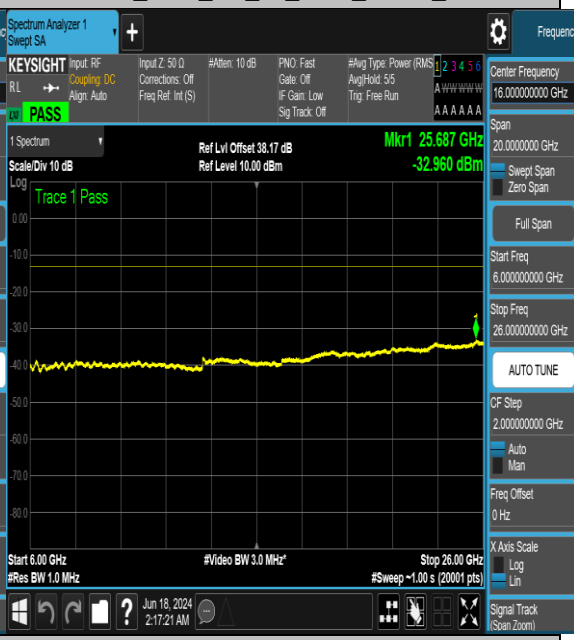
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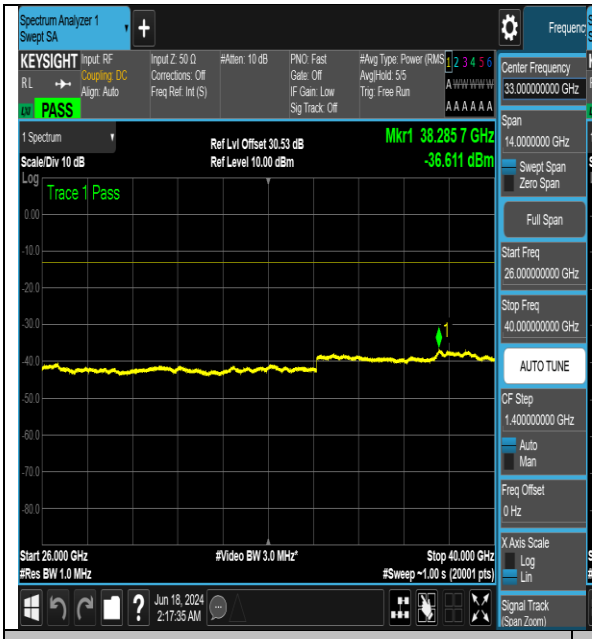
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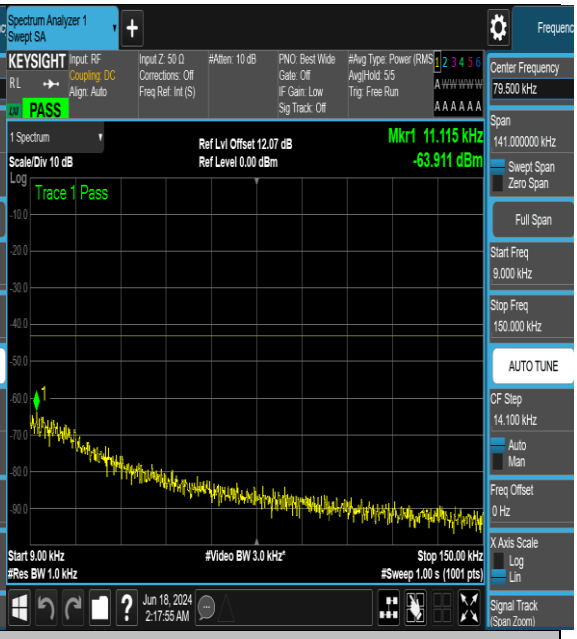
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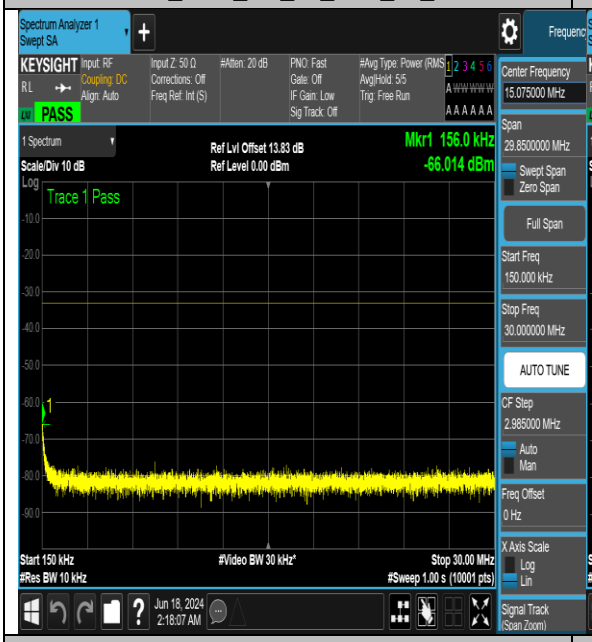
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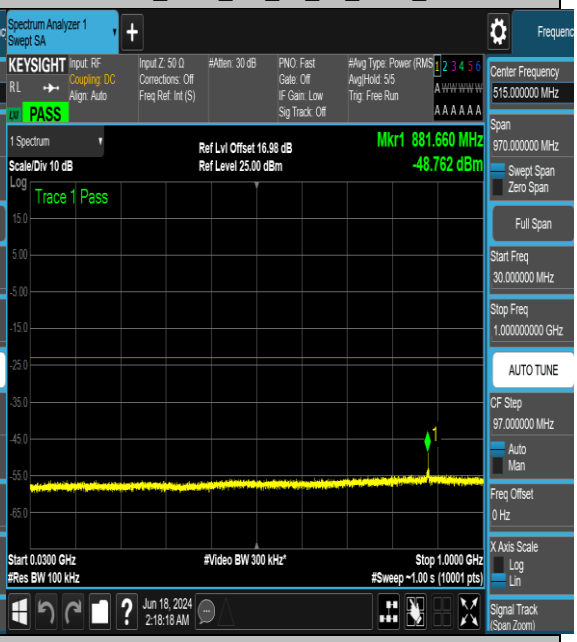
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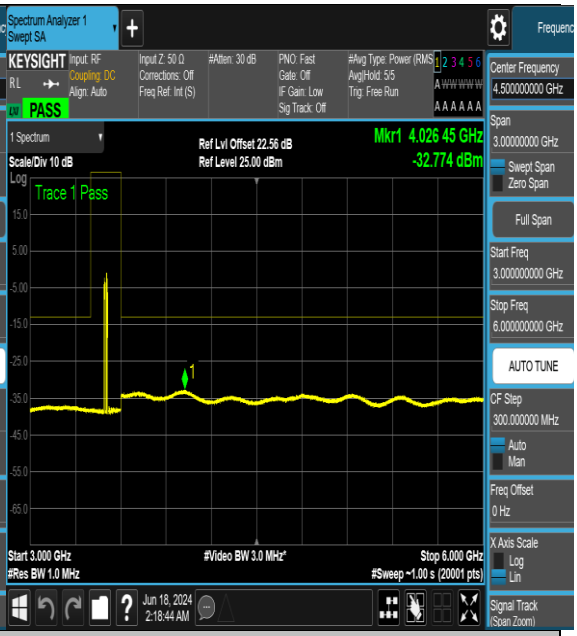
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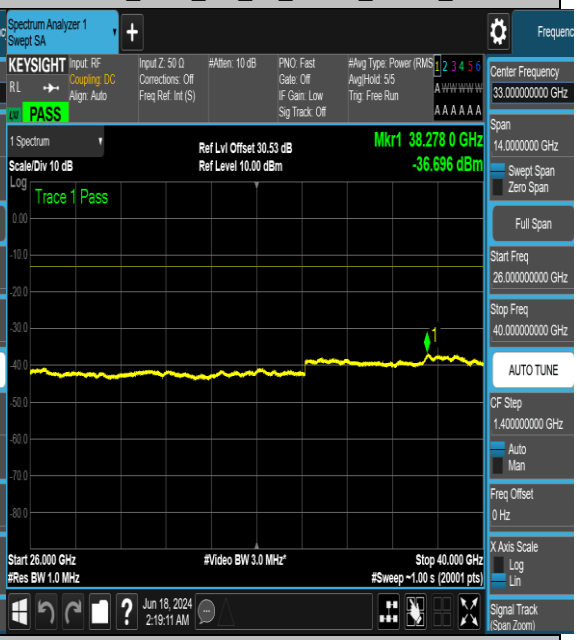
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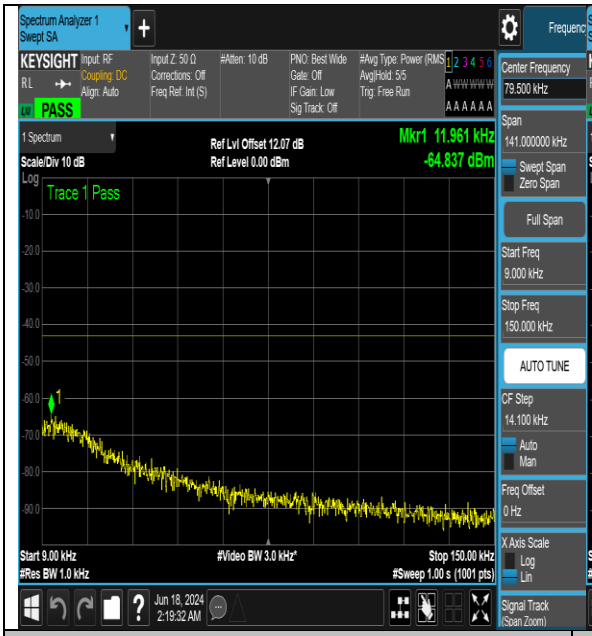
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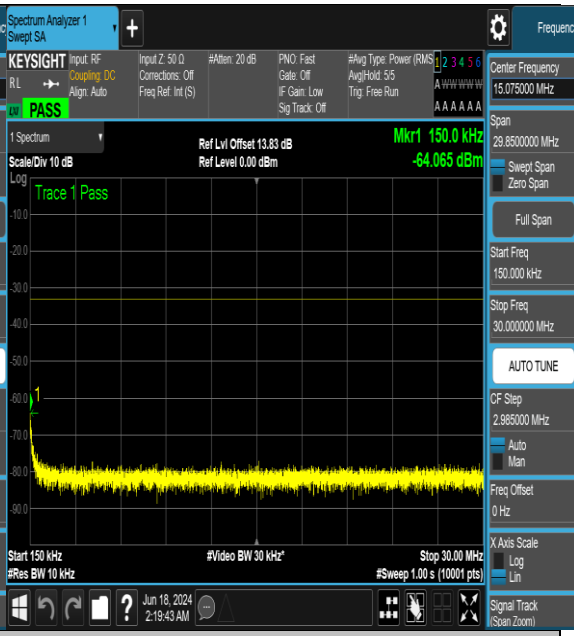
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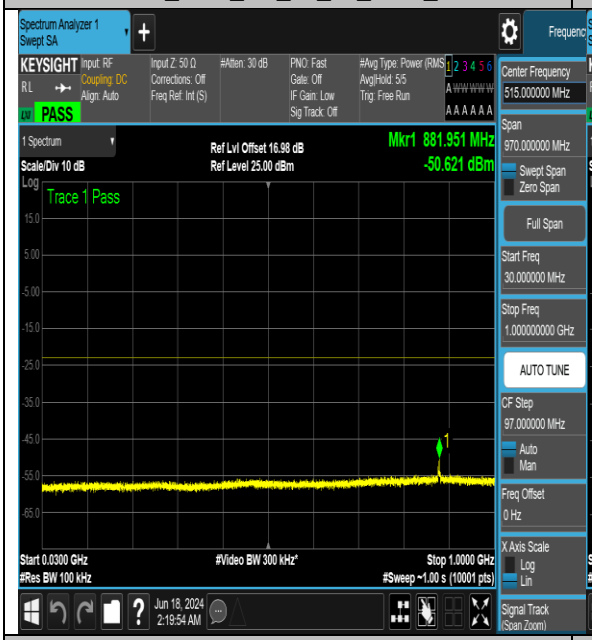
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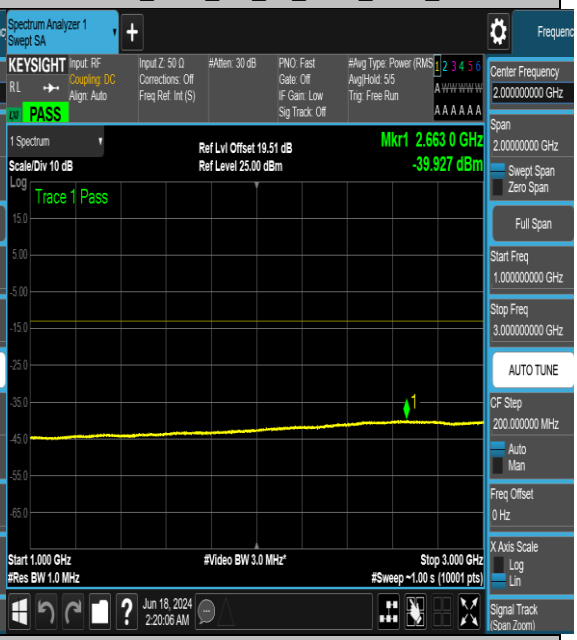
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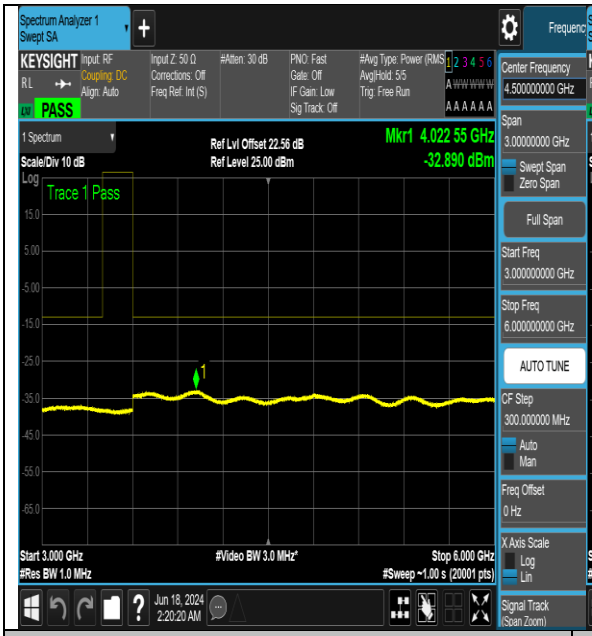
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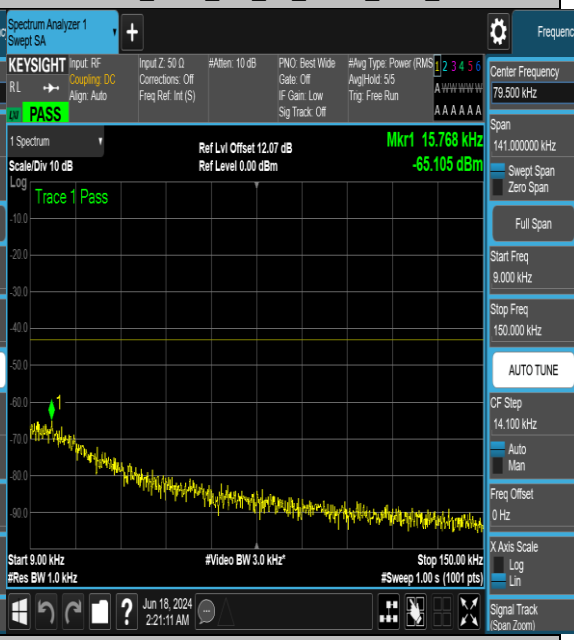
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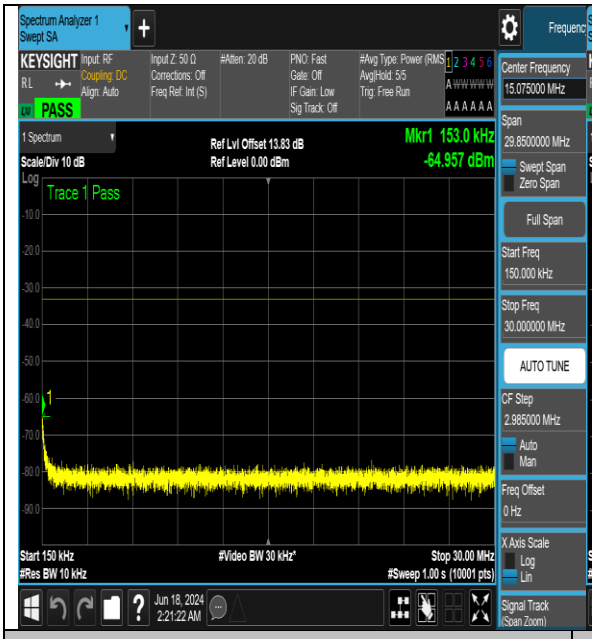
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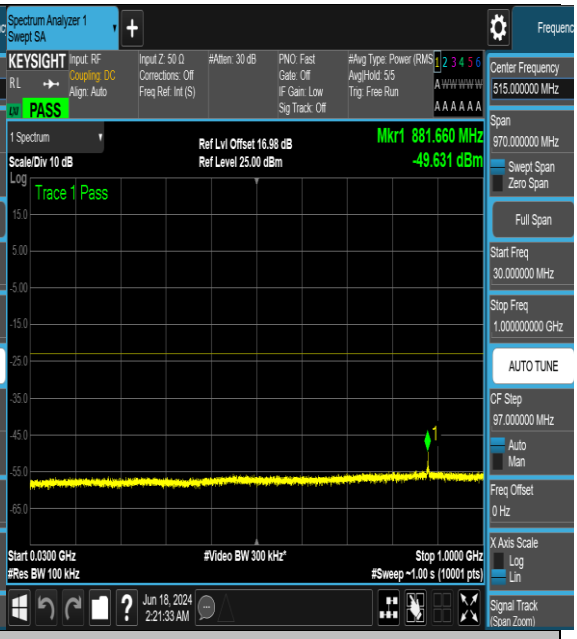
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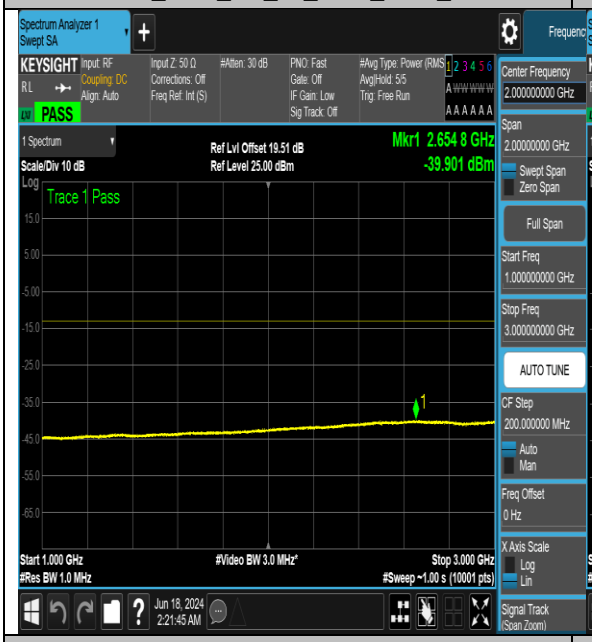
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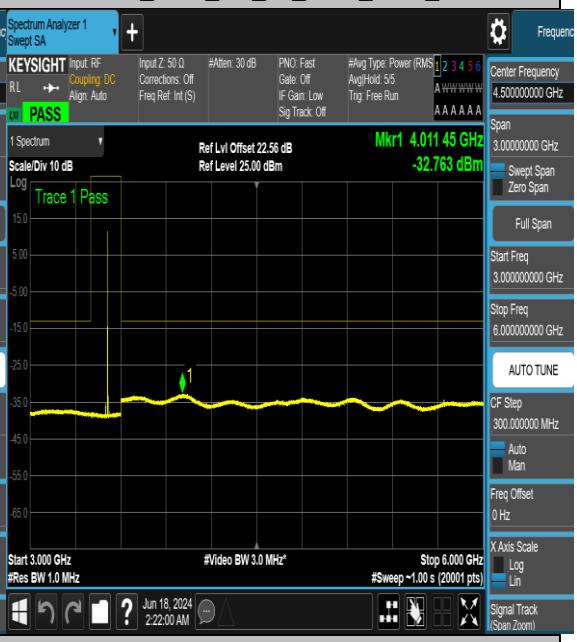
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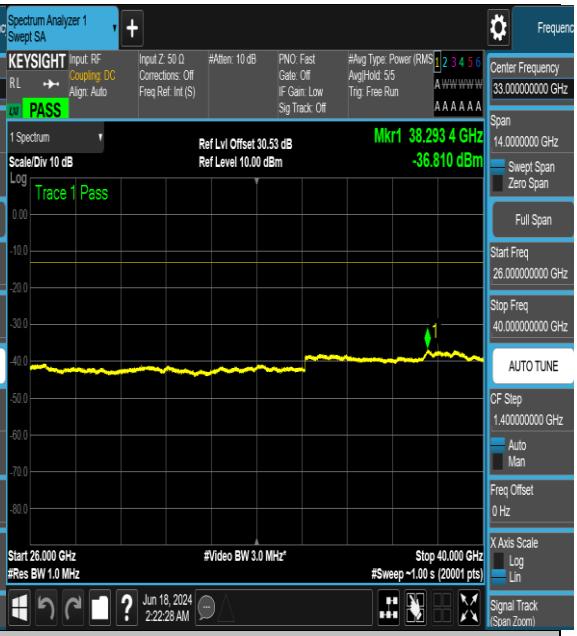
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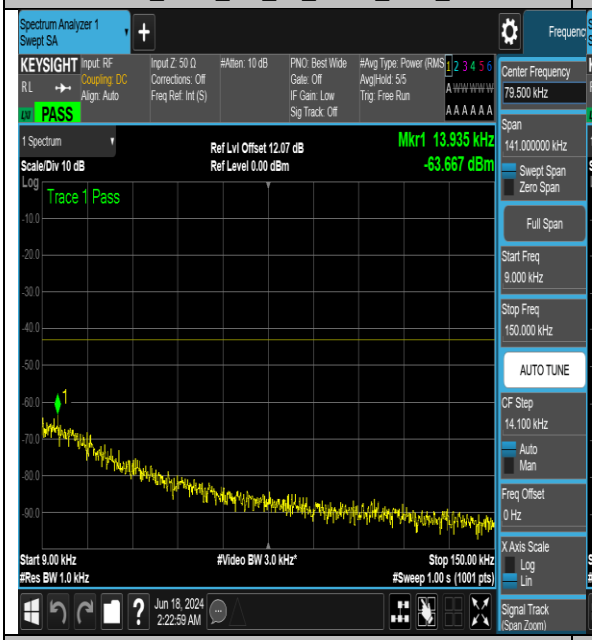
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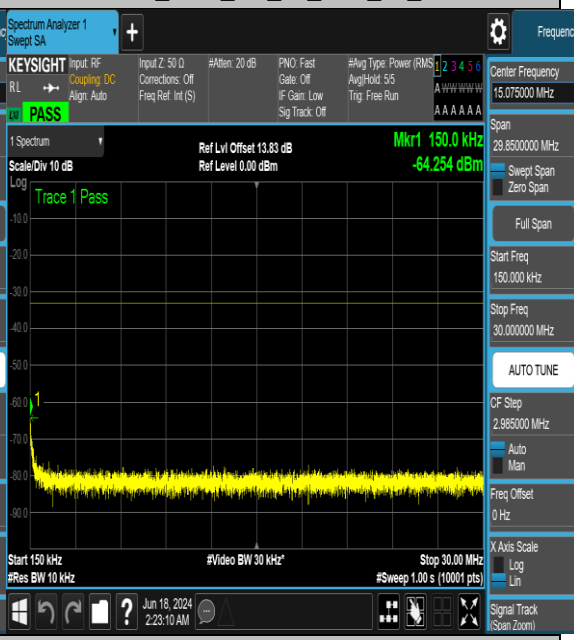
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NTNV_DC_5A_n77A-3450-3550_PC2_30_5+30_M+H_TID6_NS_01_30_1000_#1



NTNV_DC_5A_n77A-3450-3550_PC2_30_5+30_M+H_TID6_NS_01_1000_3000_#1