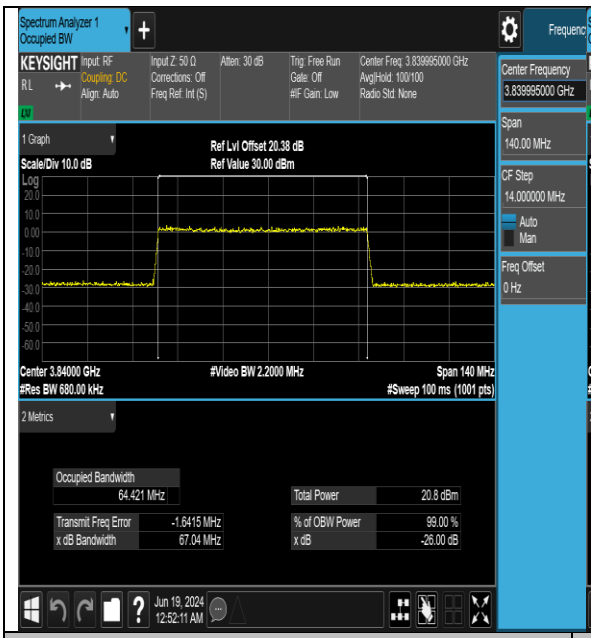

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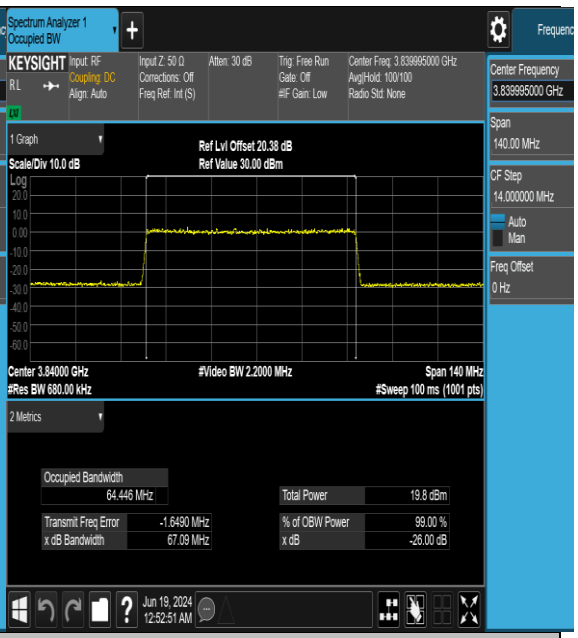

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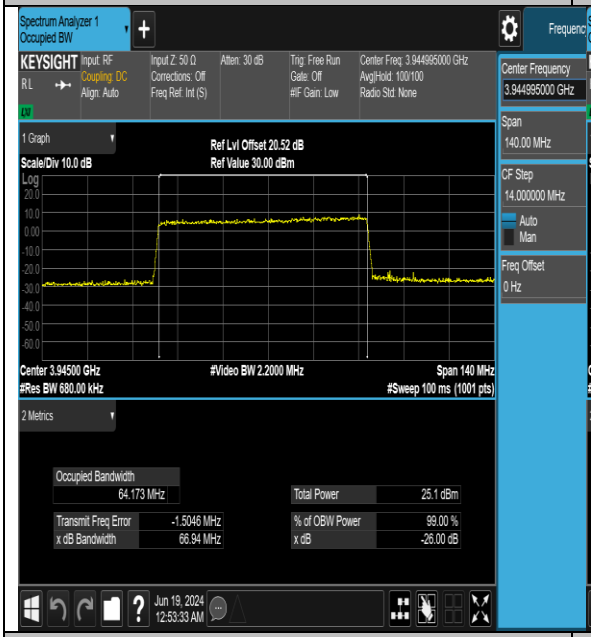

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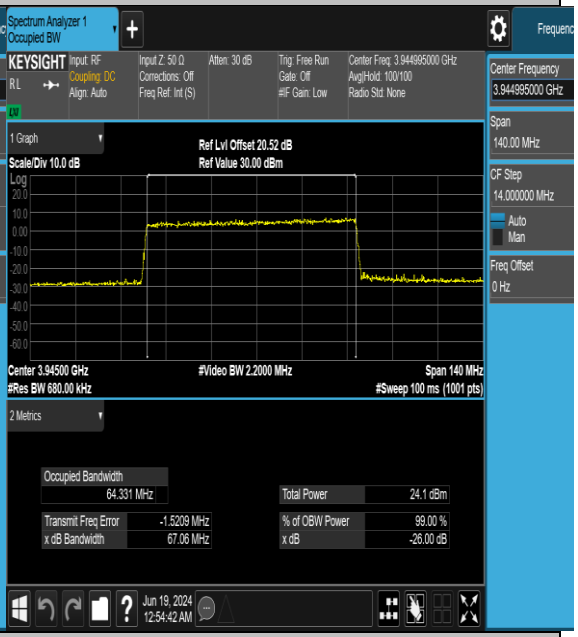

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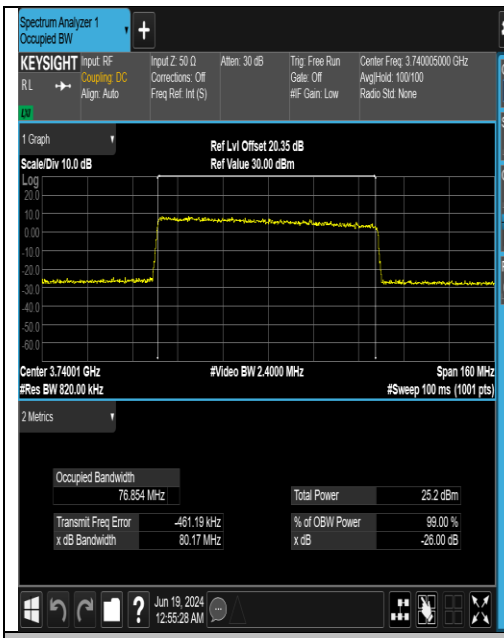

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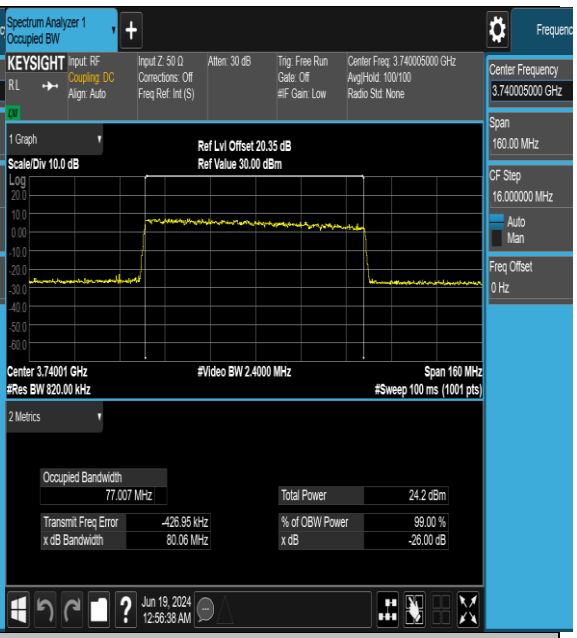

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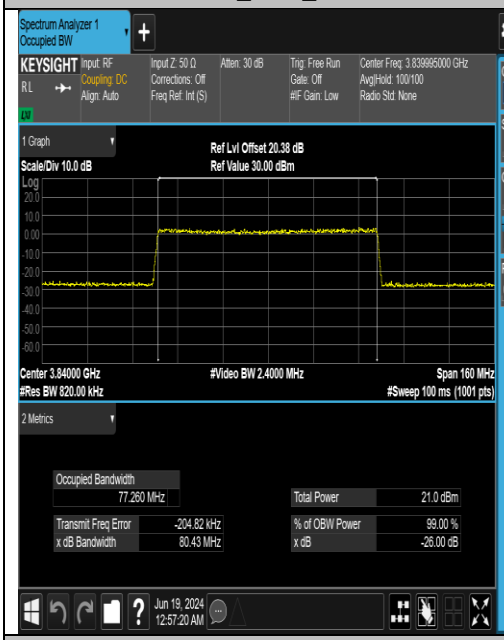

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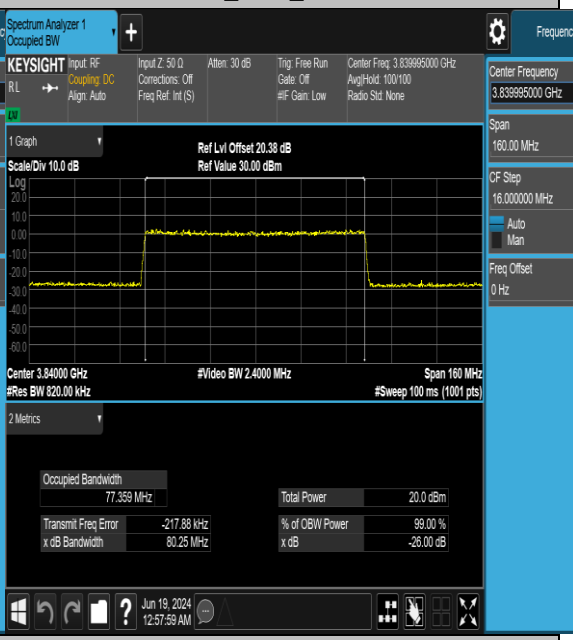

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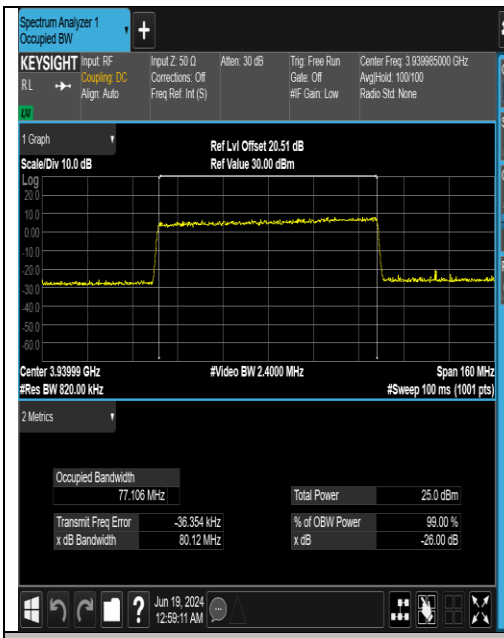

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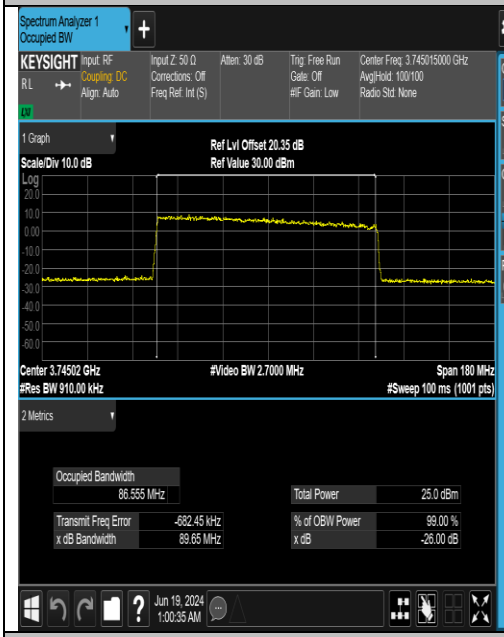

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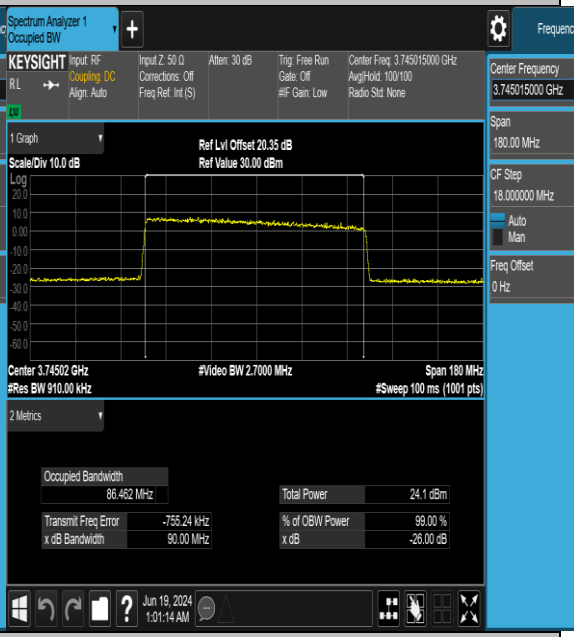

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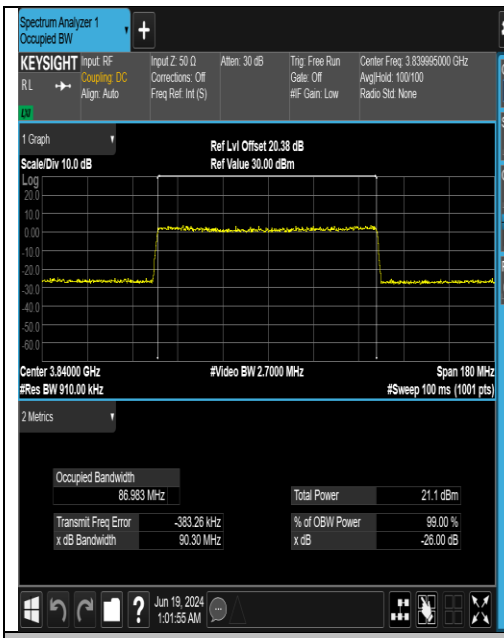

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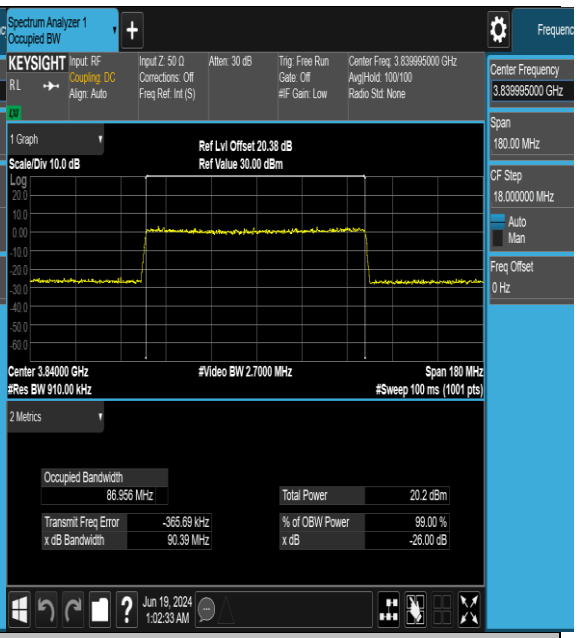

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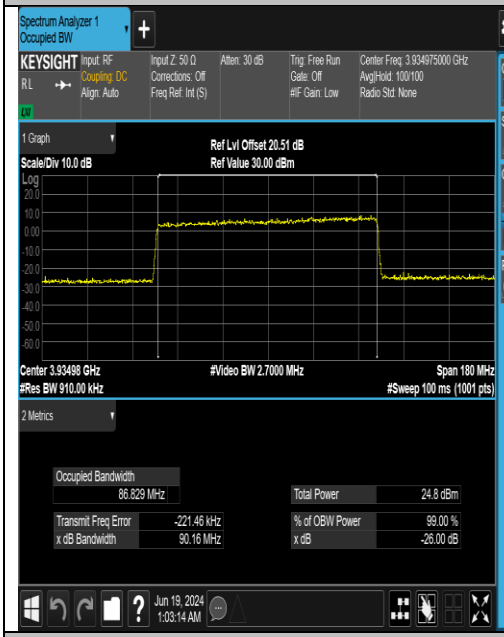

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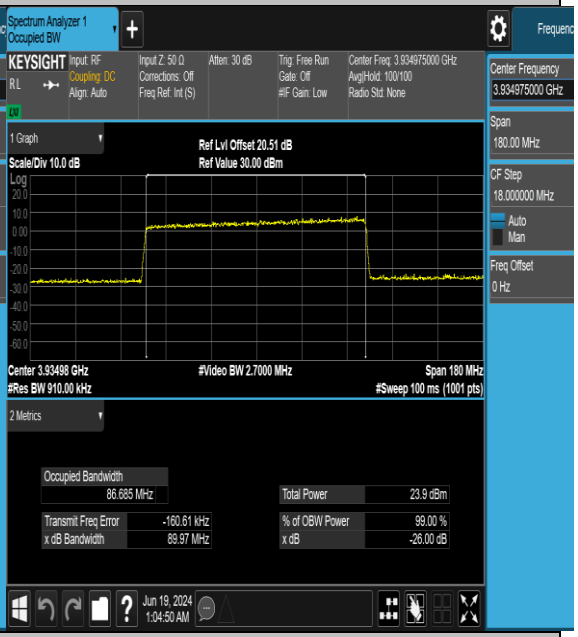

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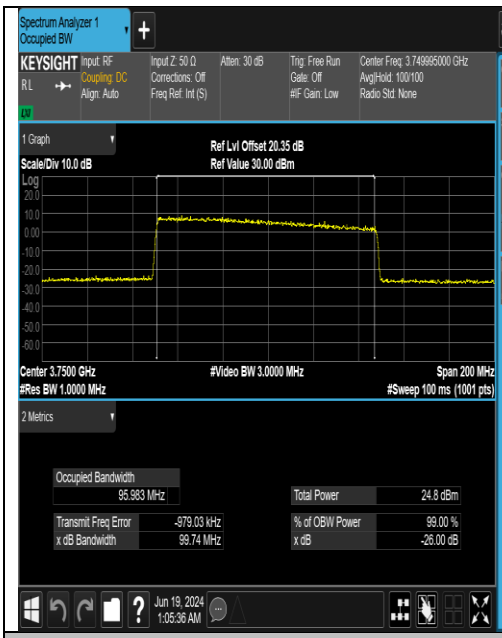

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+100_M+M_TID1_Ant1#1

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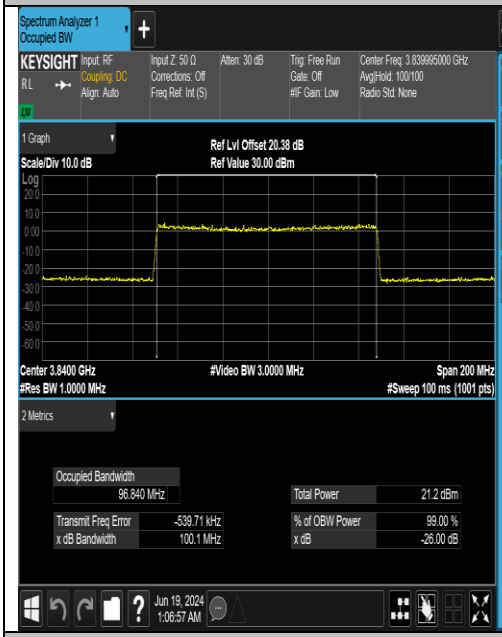

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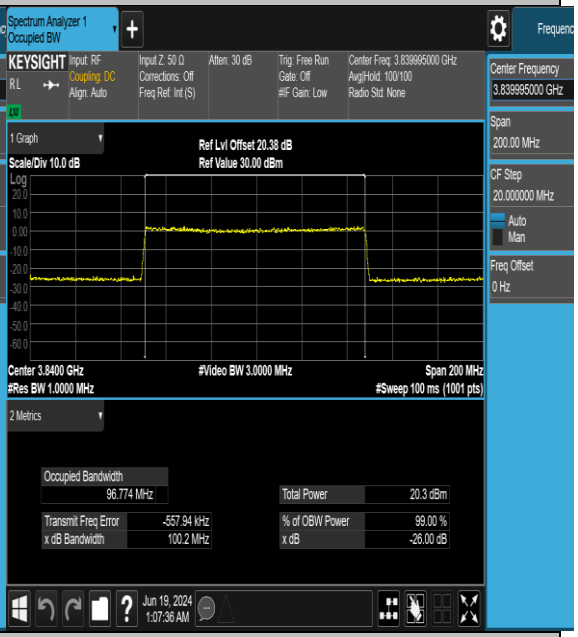


NTNV_DC_5A_n77A-3700-3980_PC2_30_5+100_M+H_TID3_Ant1#1

Spectrum Analyzer 1
Occupied BW
KEYSIGHT
Input: RF
Coupling: DC
Align: Auto
Input Z: 50 Ω
Corrections: Off
Freq Ref: Int (S)
Atten: 30 dB
Trig: Free Run
Gate: Off
#IF Gain: Low
Center Freq: 3.929995000 GHz
Avg|Hold: 100/100
Radio Std: None
Center Frequency
3.929995000 GHz
Span
200.00 MHz
CF Step
20.000000 MHz
Auto
Man
Freq Offset
0 Hz
1 Graph
Ref Lvl Offset 20.53 dB
Ref Value 30.00 dBm
Scale/Div 10.0 dB
Log
Center 3.9300 GHz
#Res BW 1.0000 MHz
#Video BW 3.0000 MHz
Span 200 MHz
#Sweep 100 ms (1001 pts)
2 Metrics
Occupied Bandwidth
96.241 MHz
Total Power
24.7 dBm
Transmit Freq Error
-262.07 kHz
% of OBW Power
99.00 %
x dB Bandwidth
99.53 MHz
x dB
-26.00 dB
Jun 19, 2024
1:08:18 AM
Spectrum Analyzer 1
Occupied BW
KEYSIGHT
Input: RF
Coupling: DC
Align: Auto
Input Z: 50 Ω
Corrections: Off
Freq Ref: Int (S)
Atten: 30 dB
Trig: Free Run
Gate: Off
#IF Gain: Low
Center Freq: 3.929995000 GHz
Avg|Hold: 100/100
Radio Std: None
Center Frequency
3.929995000 GHz
Span
200.00 MHz
CF Step
20.000000 MHz
Auto
Man
Freq Offset
0 Hz
1 Graph
Ref Lvl Offset 20.53 dB
Ref Value 30.00 dBm
Scale/Div 10.0 dB
Log
Center 3.9300 GHz
#Res BW 1.0000 MHz
#Video BW 3.0000 MHz
Span 200 MHz
#Sweep 100 ms (1001 pts)
2 Metrics
Occupied Bandwidth
96.220 MHz
Total Power
23.8 dBm
Transmit Freq Error
-204.24 kHz
% of OBW Power
99.00 %
x dB Bandwidth
99.87 MHz
x dB
-26.00 dB
Jun 19, 2024
1:08:57 AM

Appendix D: Band Edge for NSA

Test Result

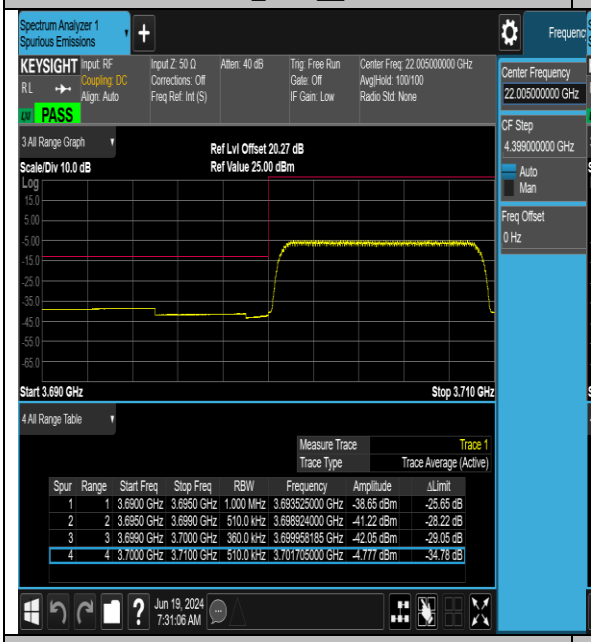
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	PASS

DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n77A-3700-39	30	5+100	DFT-QPSK	M+L	Outer_Full	PASS

80						
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs

NTNV_DC_5A_n77A-3700-3980_PC2_30_5+10_M+L_TID9__#1



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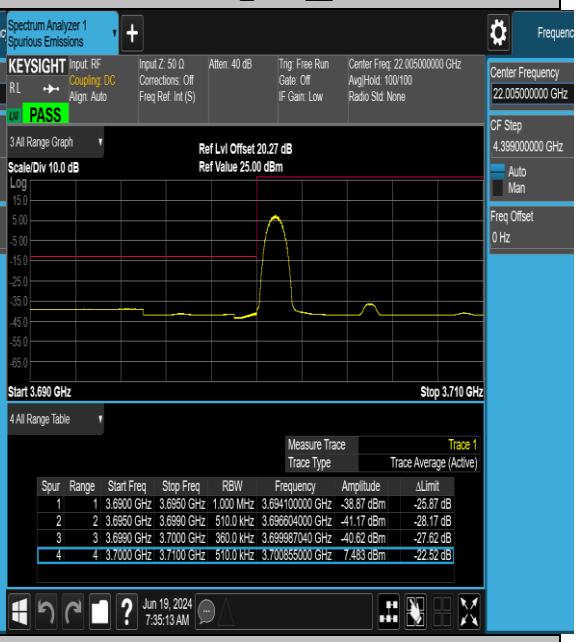

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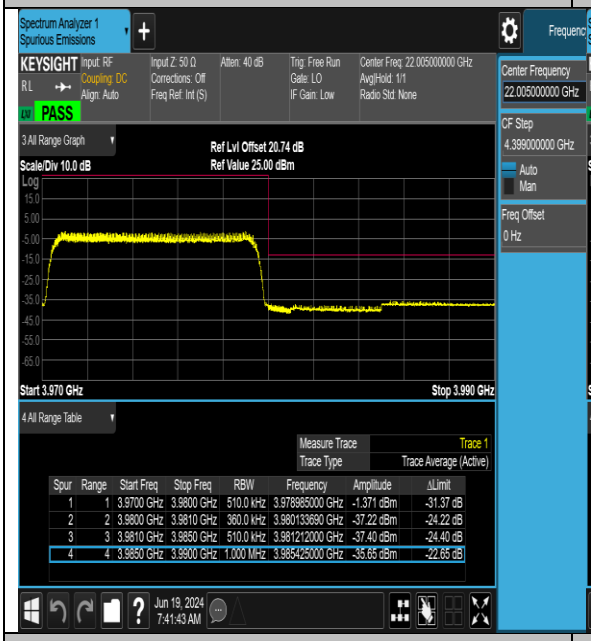

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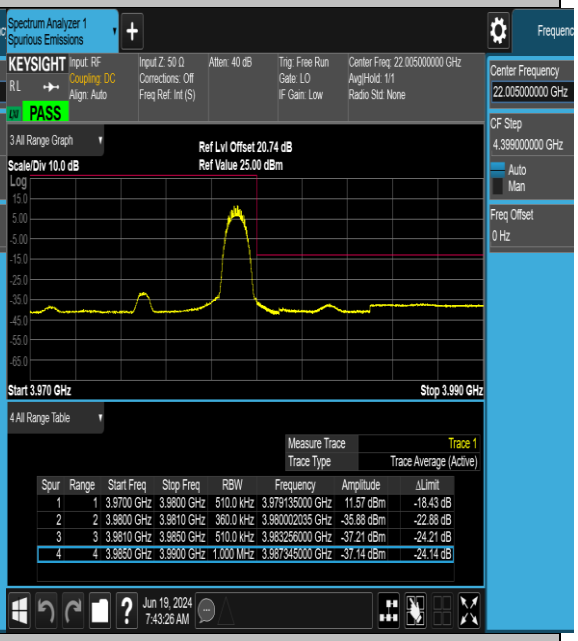

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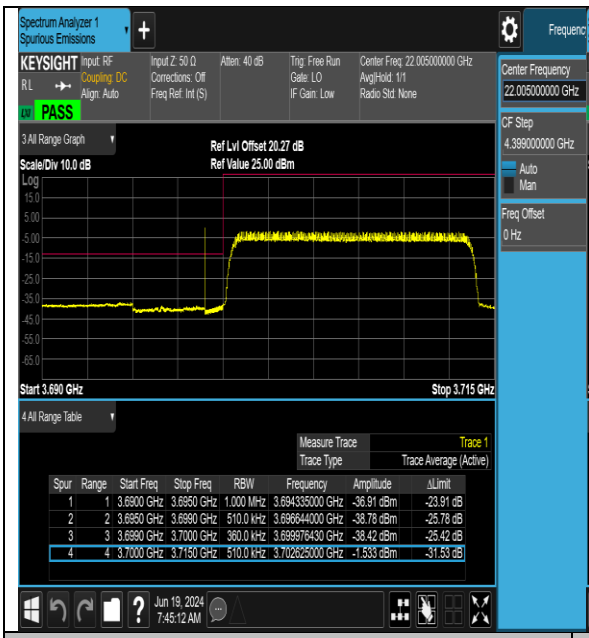

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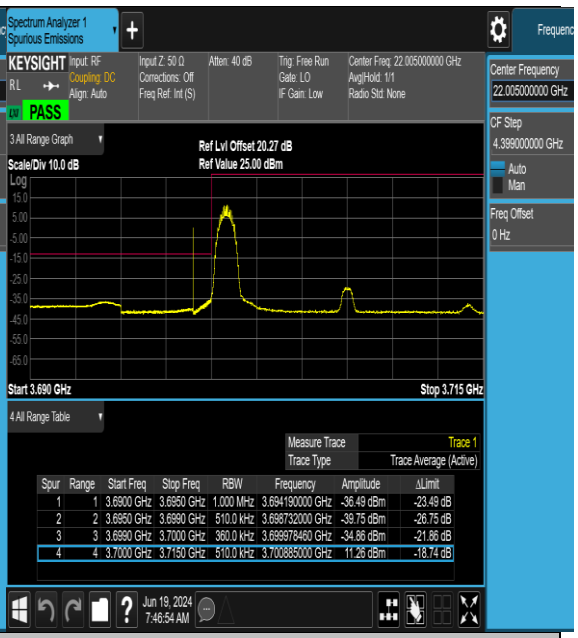

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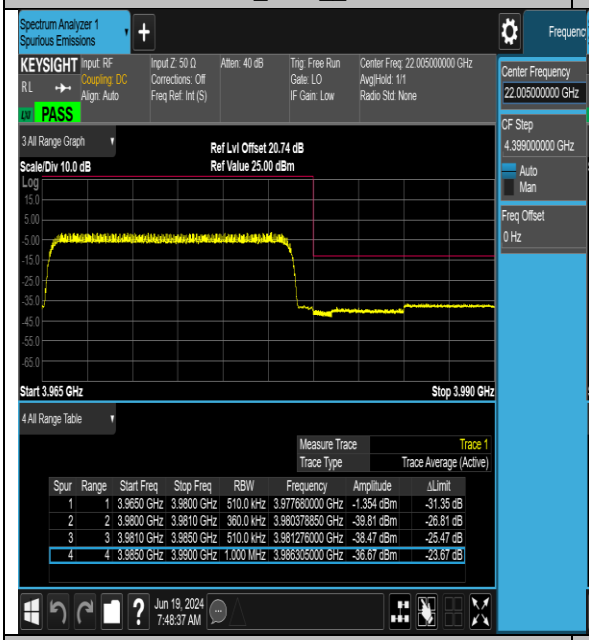

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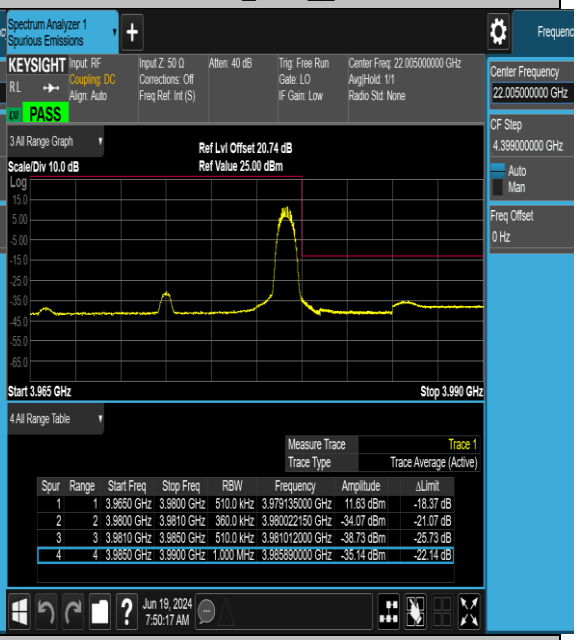

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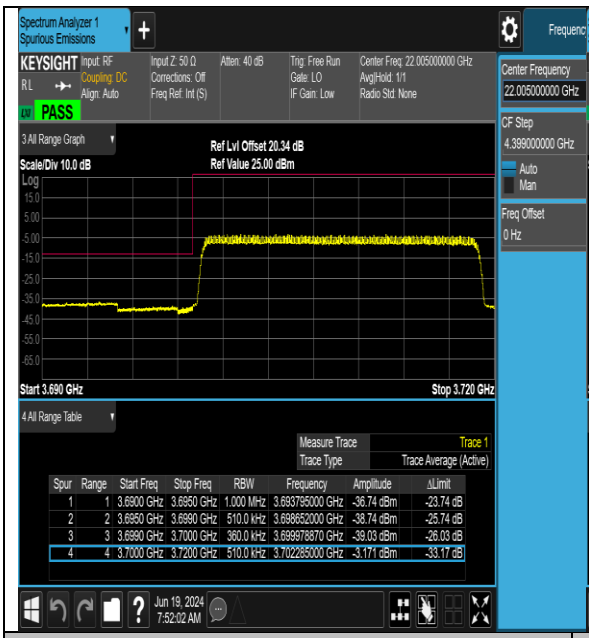

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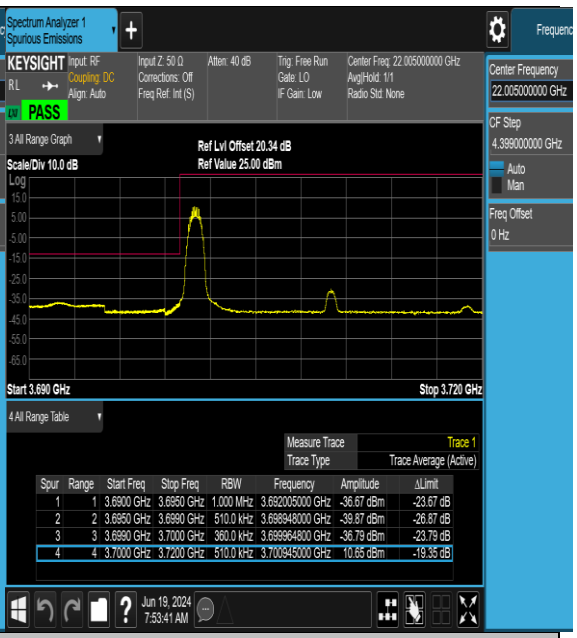

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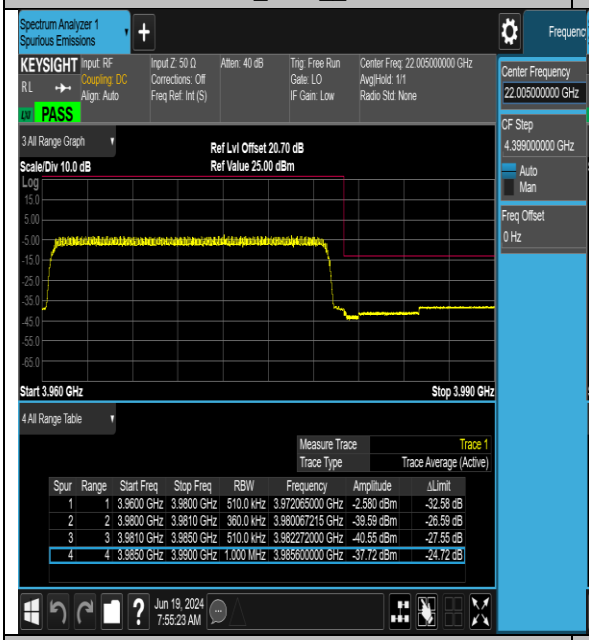

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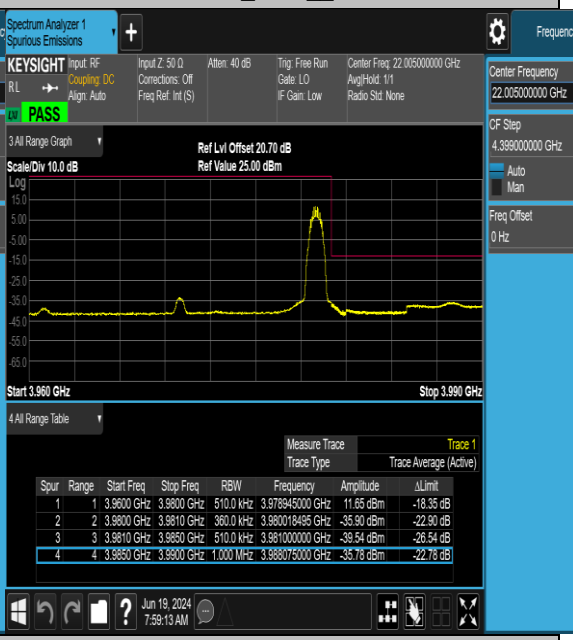
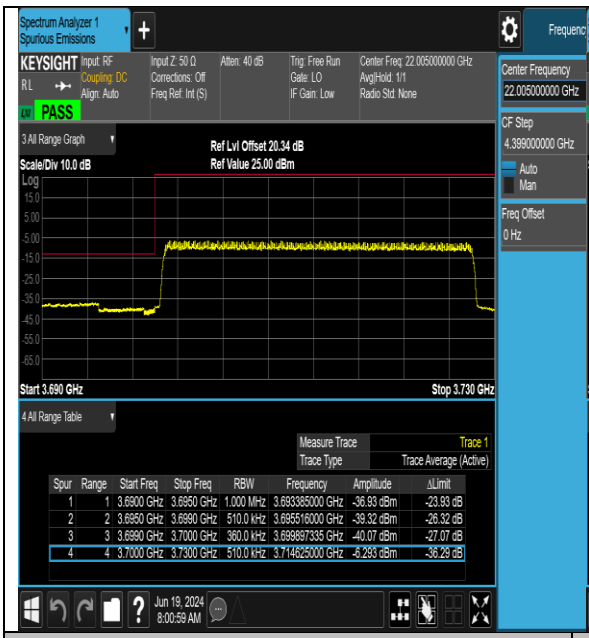


NTNV_DC_5A_n77A-3700-3980_PC2_30_5+30_M+L_TID7__#1



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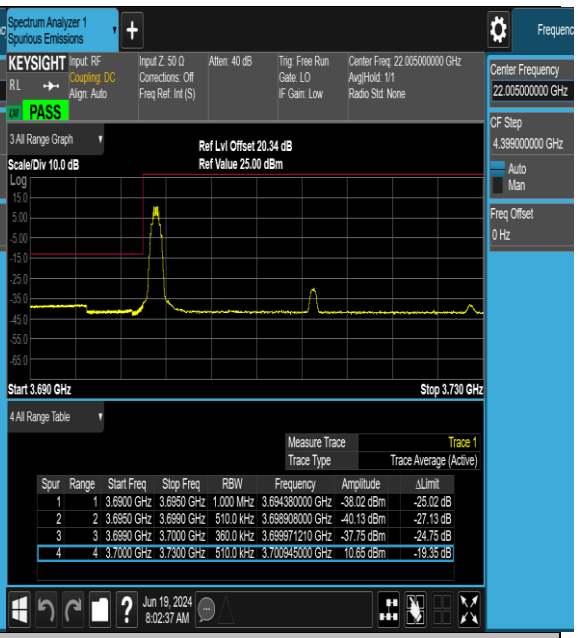

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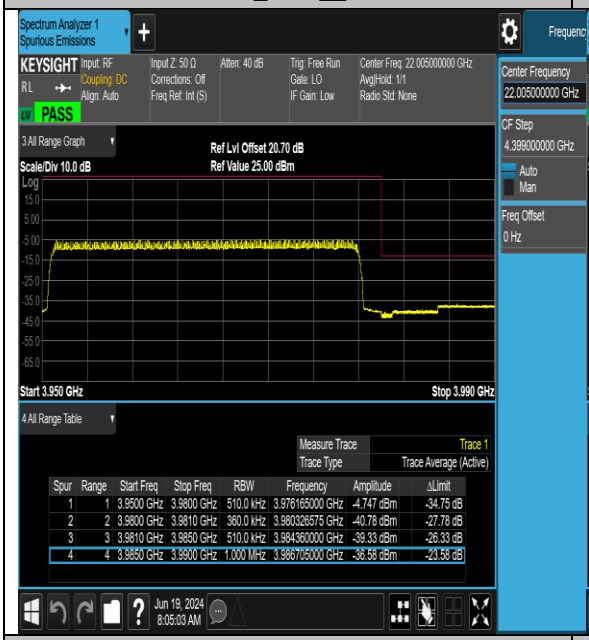

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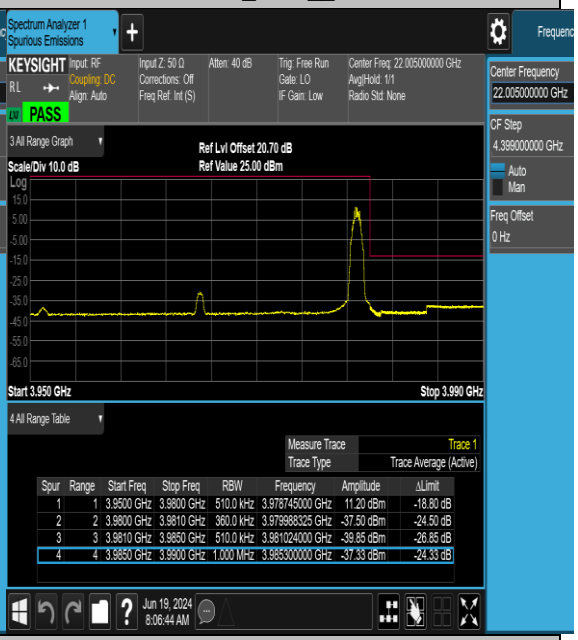

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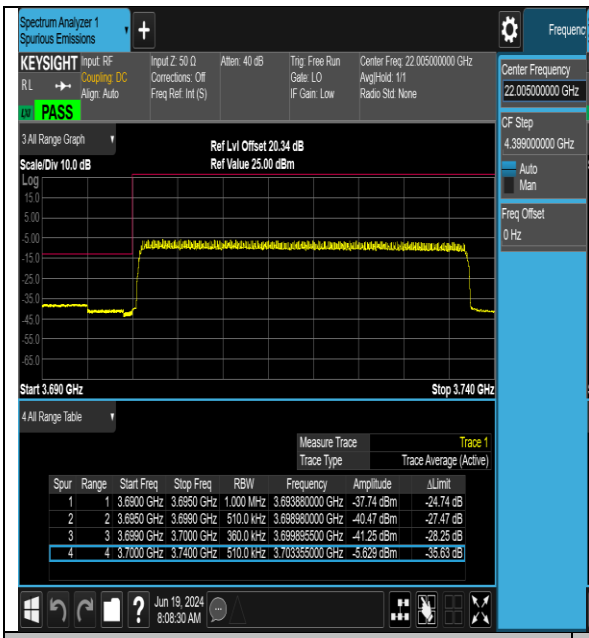

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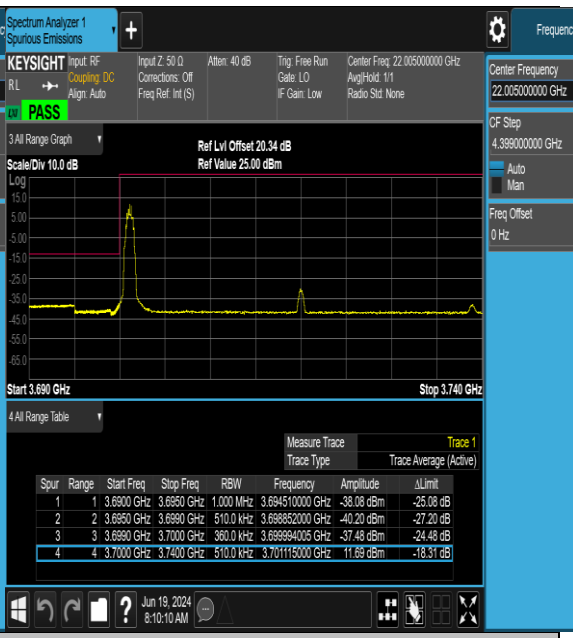

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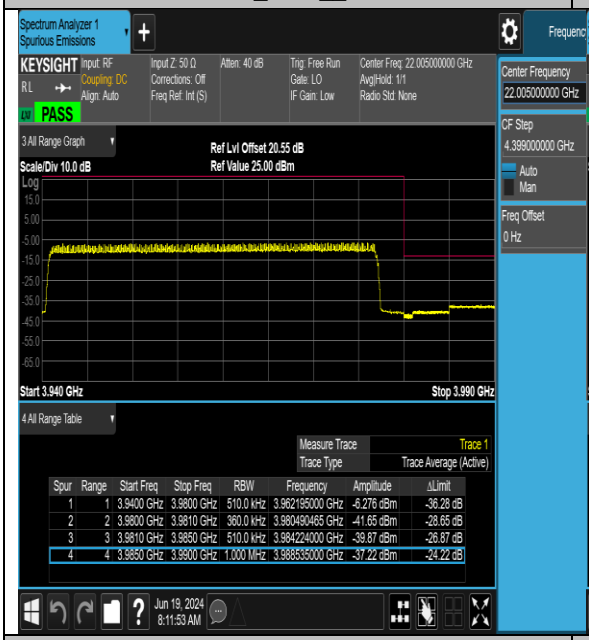

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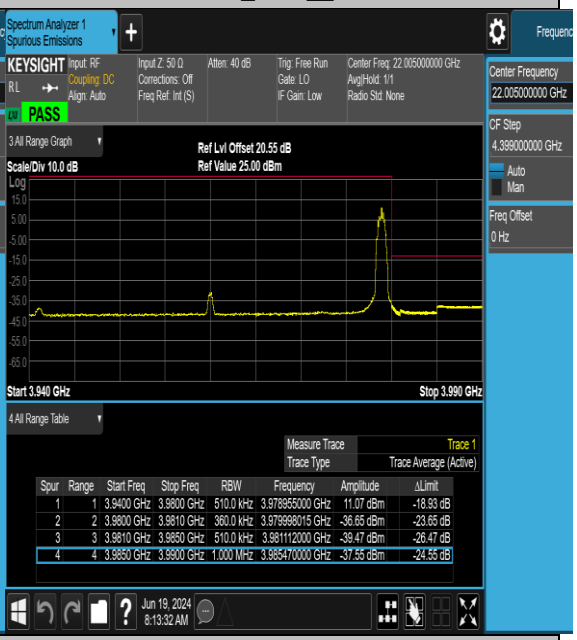

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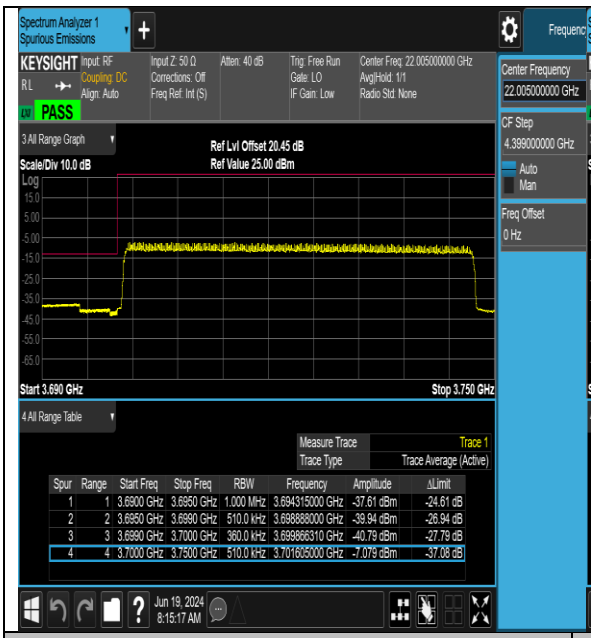

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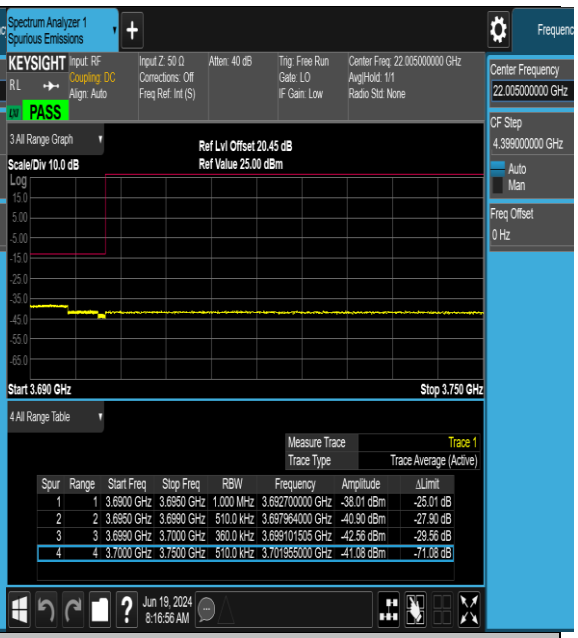

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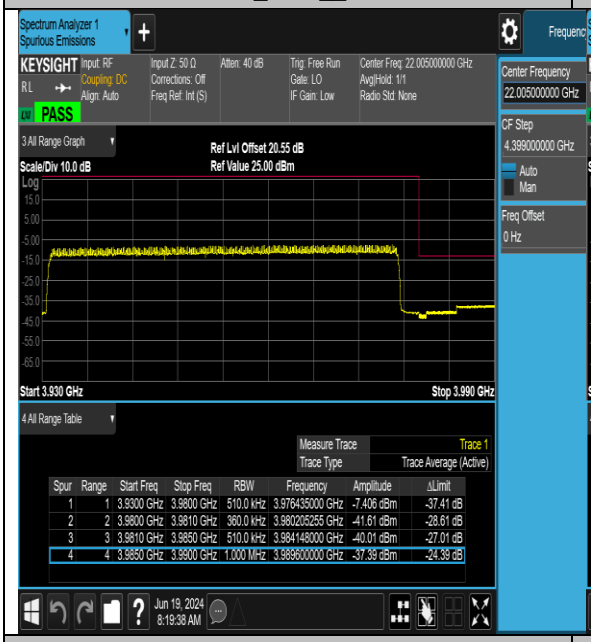

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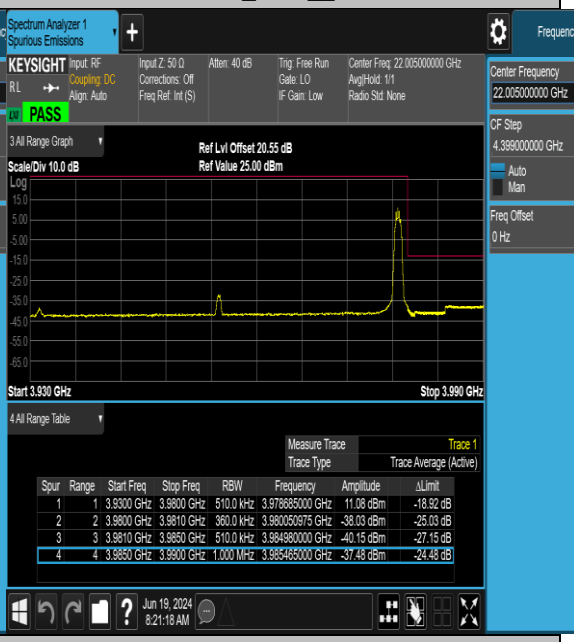

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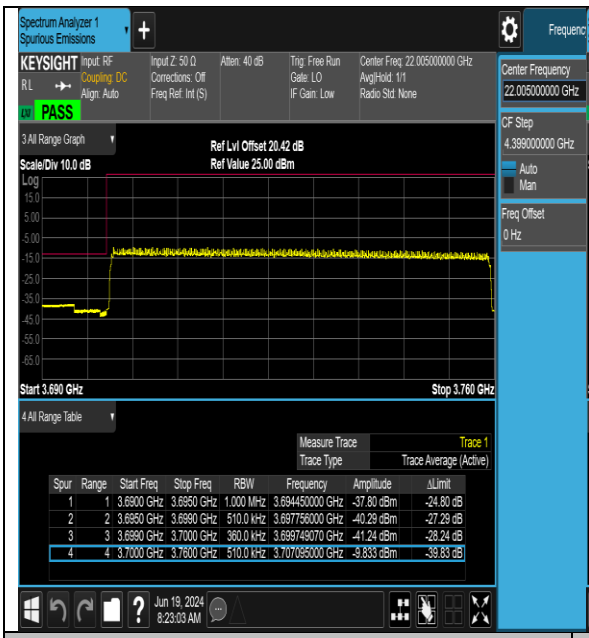

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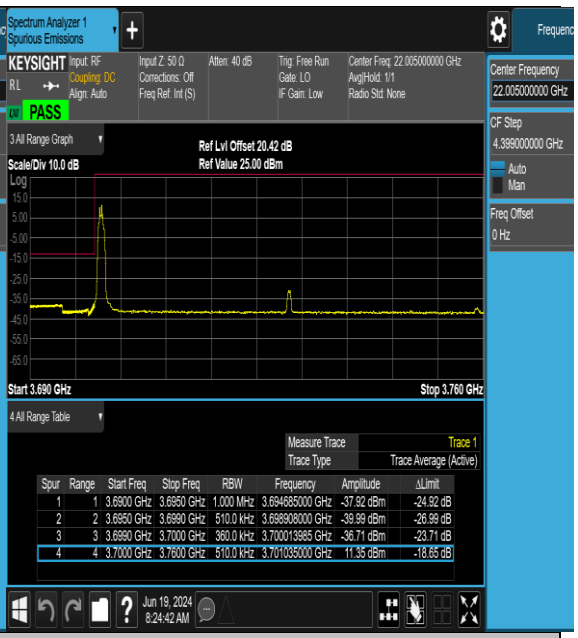

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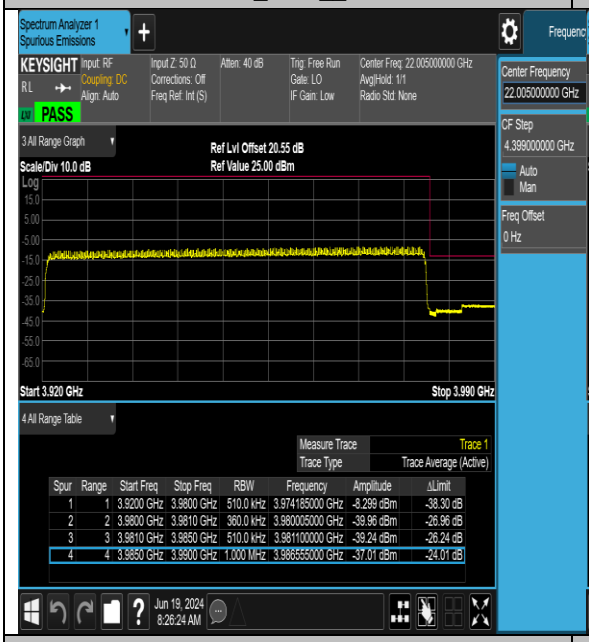

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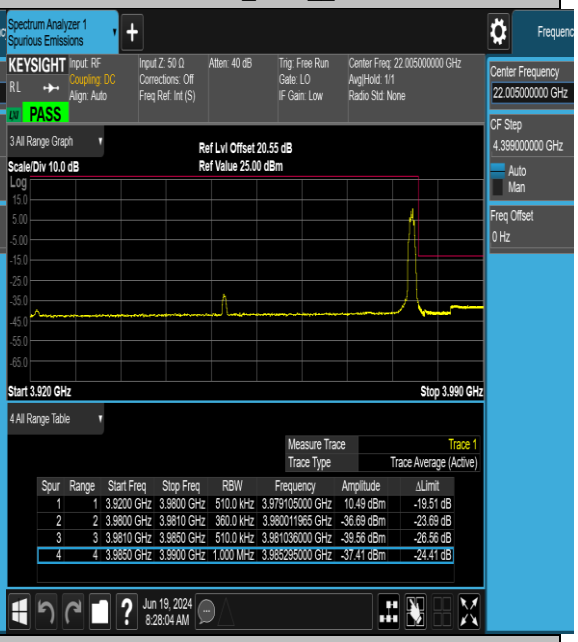

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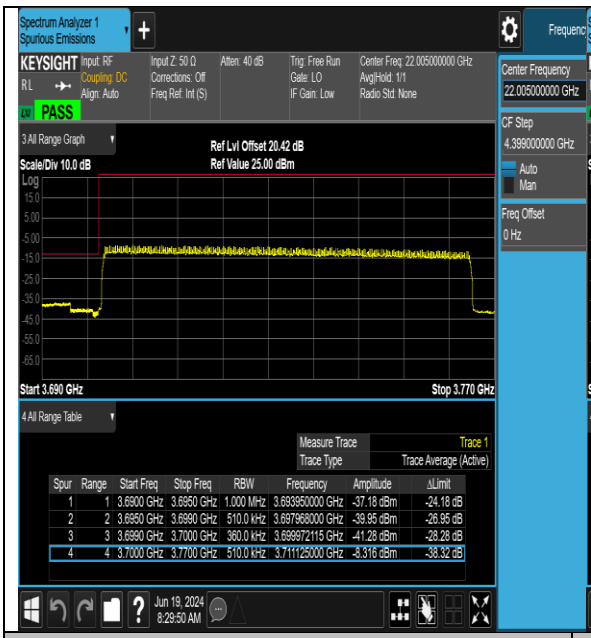

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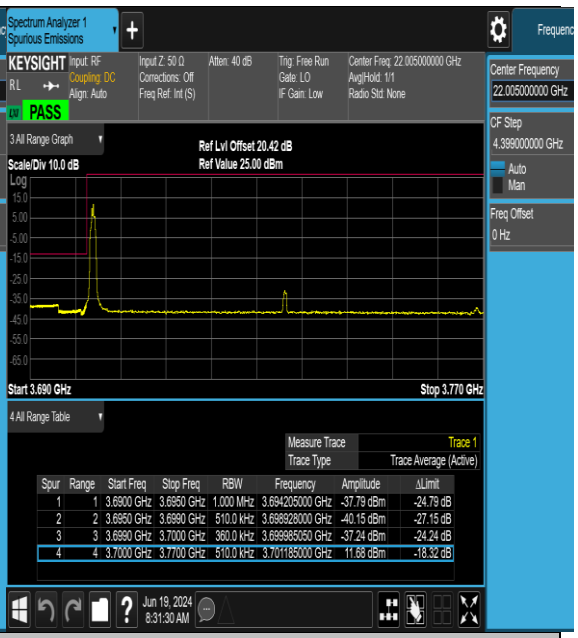

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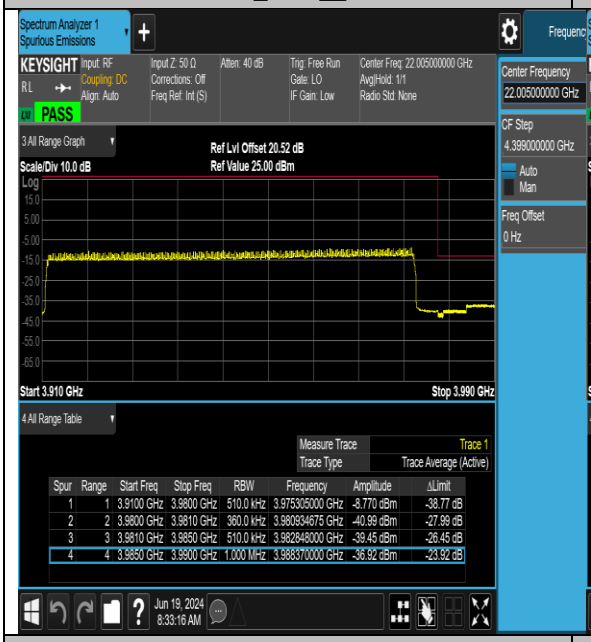

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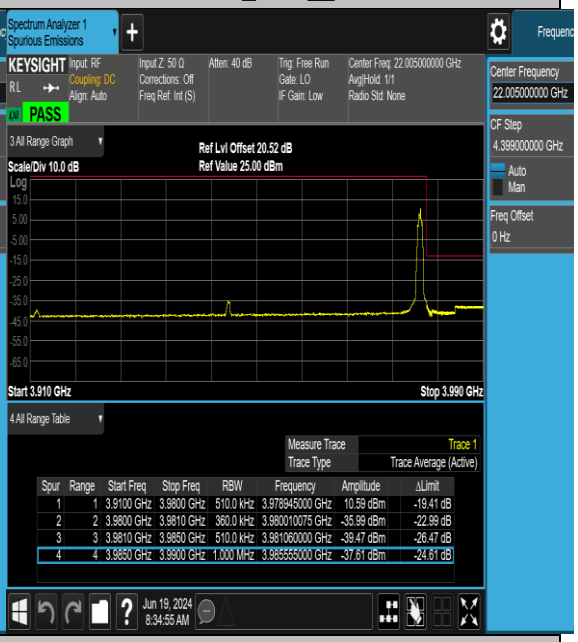

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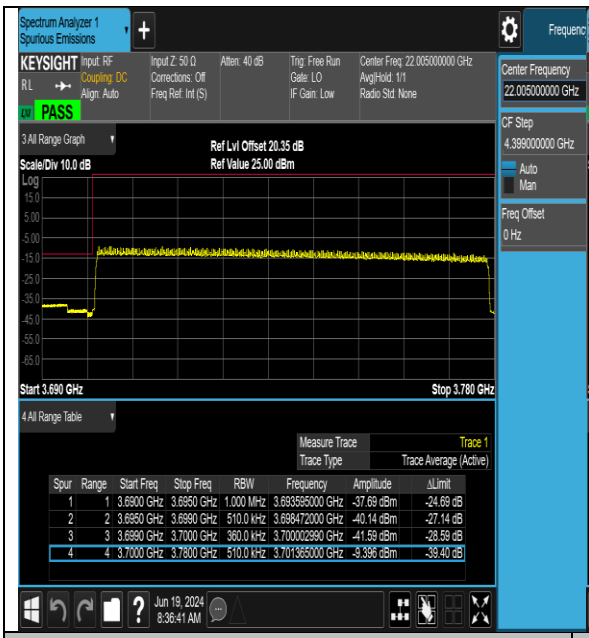

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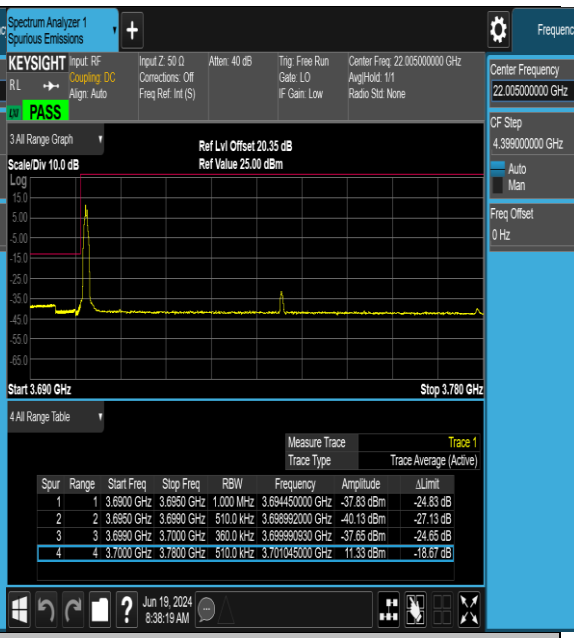

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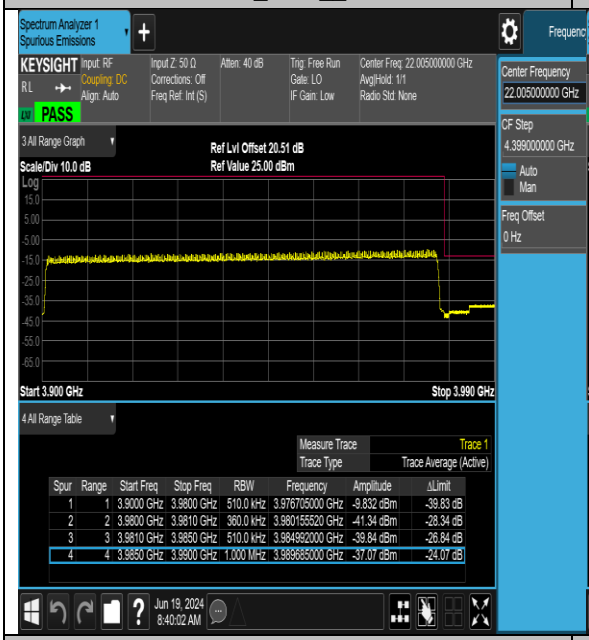

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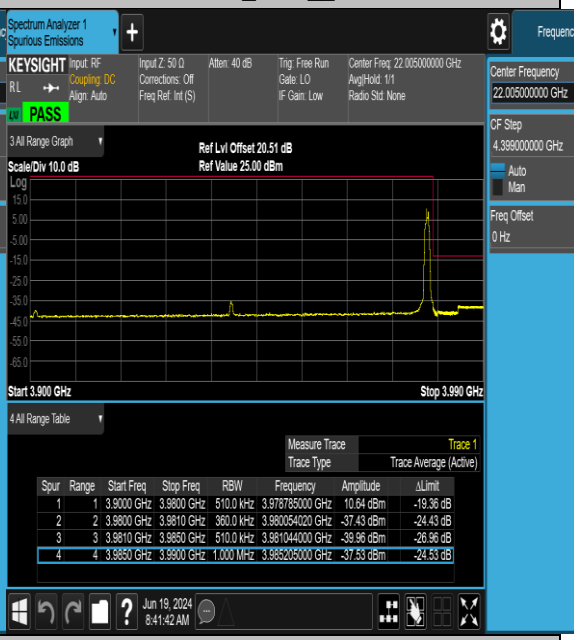

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