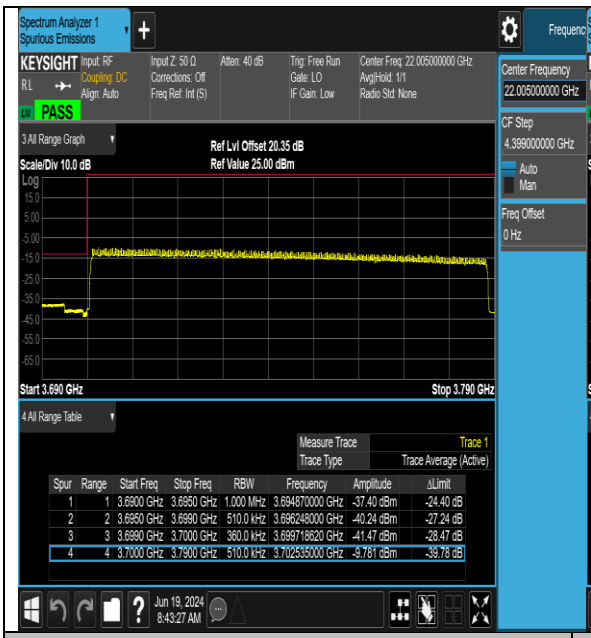

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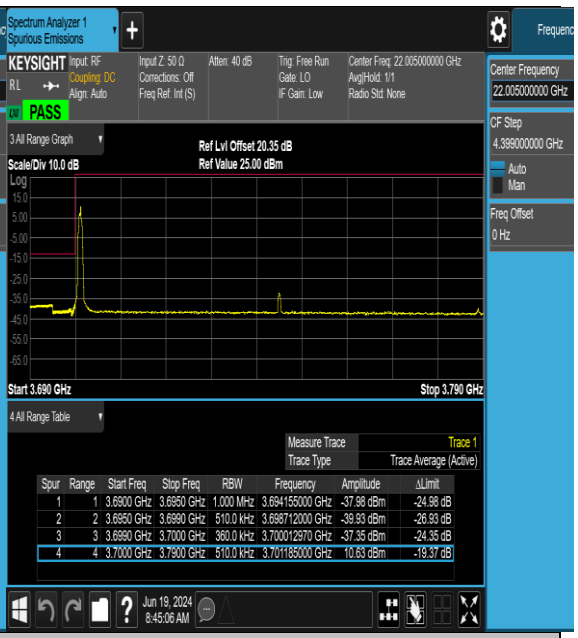

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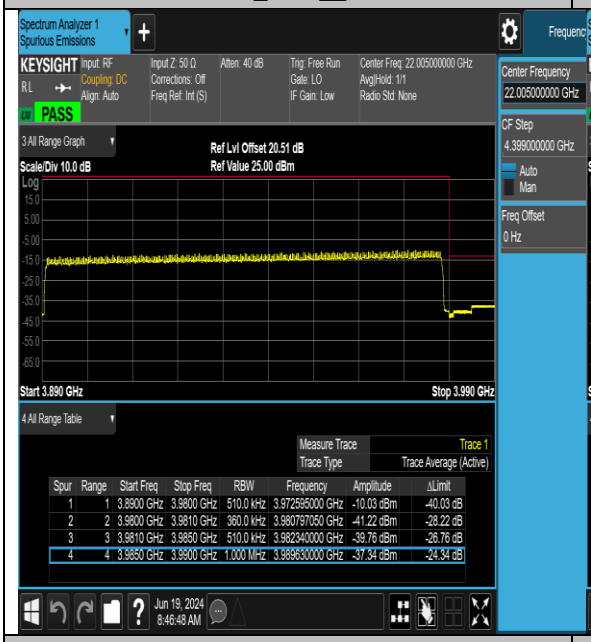

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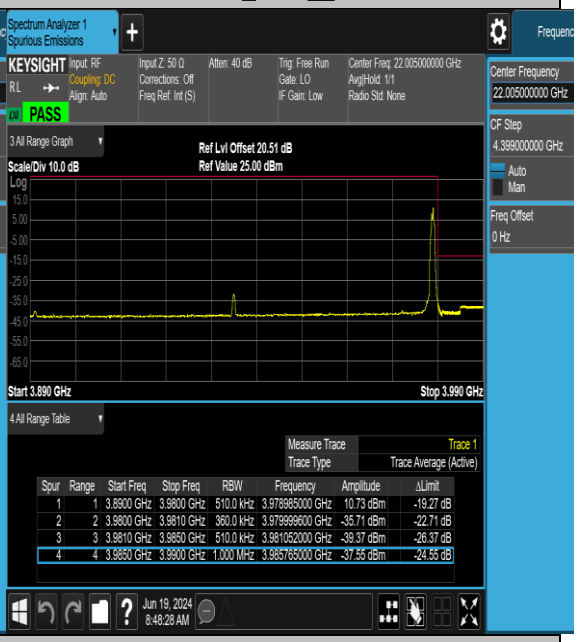

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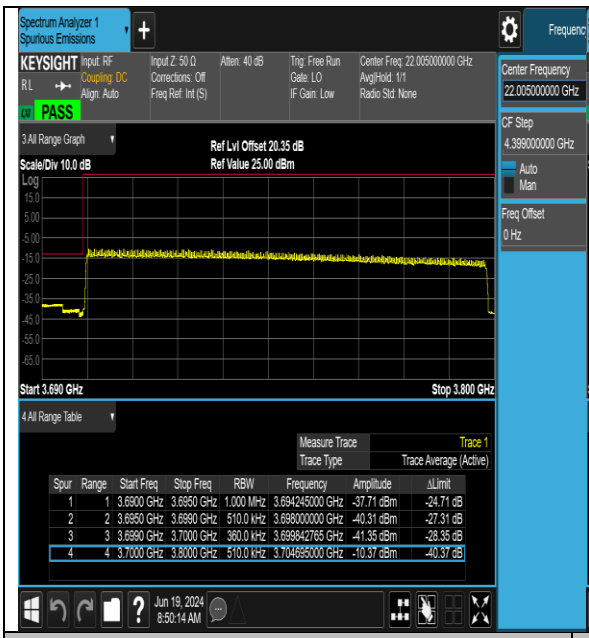

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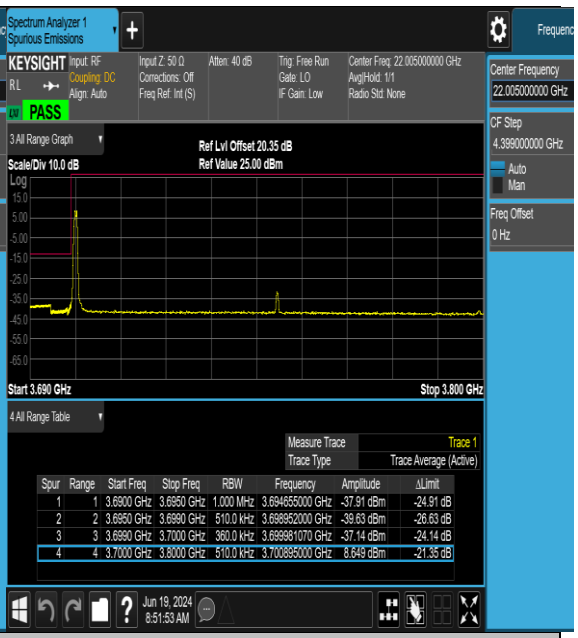

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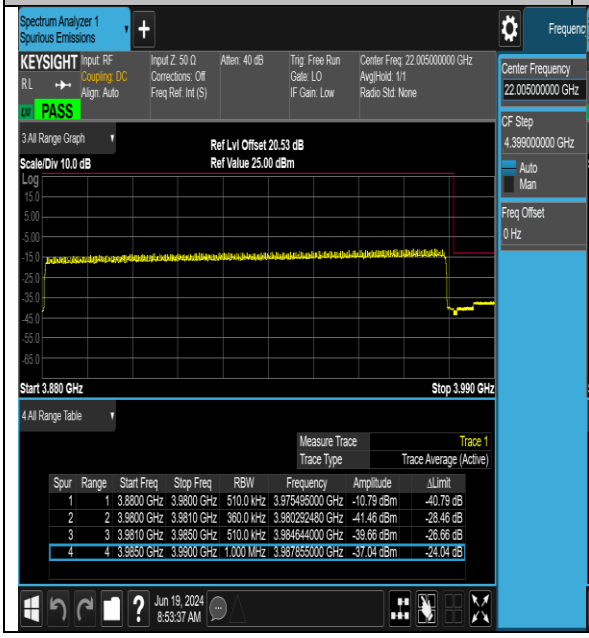

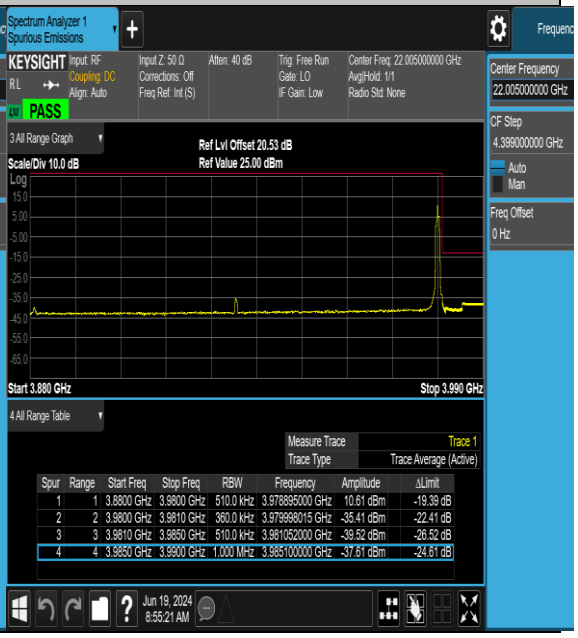

Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.12	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.90	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-53.37	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.17	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.81	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.12	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-32.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.96	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.81	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-52.31	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.05	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.84	-13	PASS

DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.47	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.45	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-53.65	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.23	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.98	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-50.92	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.31	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.52	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-53.28	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.14	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.96	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-51.66	-23	PASS
DC_5A_n77A-	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.15	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.13	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.17	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.10	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.68	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.65	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.29	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.75	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.44	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.88	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-53.35	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-63.69	-43	PASS

DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.41	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-53.59	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-32.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.69	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.67	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.73	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-53.26	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-32.97	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.40	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.19	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-51.98	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.97	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.25	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.08	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-52.23	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.17	-13	PASS
DC_5A_n77A-	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.03	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.53	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.35	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-51.06	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.37	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.47	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-52.15	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	1000	6000	-32.45	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.65	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.91	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.24	-33	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-53.48	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.79	-43	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-73.64	-33	PASS

DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-53.10	-23	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-32.89	-13	PASS
DC_5A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.64	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.90	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-52.51	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.69	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.55	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.67	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.19	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.69	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.18	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.00	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.14	-13	PASS
DC_5A_n77A-	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.80	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.29	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.99	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-52.34	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.49	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-63.33	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.86	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-52.54	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.76	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.68	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-52.75	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.09	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.64	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-52.12	-23	PASS

DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.88	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.73	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.38	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.82	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.55	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.24	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-52.54	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.06	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.62	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-50.69	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.87	-13	PASS
DC_5A_n77A-	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.49	-43	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.48	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-52.99	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.67	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.11	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.10	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-52.38	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.05	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.95	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.01	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-51.81	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.79	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.88	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.35	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.14	-13	PASS

DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.82	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.79	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.21	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-50.56	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.40	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.61	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-53.39	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-63.78	-43	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.44	-33	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-52.11	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.02	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.68	-43	PASS
DC_5A_n77A-	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.72	-33	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-52.44	-23	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-32.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-32.89	-13	PASS
DC_5A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.89	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.96	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.08	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-53.23	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	6000	-32.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.22	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.15	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.11	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.33	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.87	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-52.05	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.09	-13	PASS

DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-63.07	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-73.53	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-53.09	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.17	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.05	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-52.75	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.82	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.00	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.32	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-53.89	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.03	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.27	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.04	-33	PASS
DC_5A_n77A-	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-54.28	-23	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.77	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.86	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.31	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-32.78	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.18	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.45	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-52.97	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.65	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.94	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-53.24	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.81	-13	PASS

DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.73	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.14	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-50.65	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.05	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.57	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.45	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-49.72	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-32.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-32.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.95	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.71	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-52.71	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.30	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.73	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-26.82	-23	PASS
DC_5A_n77A-	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.12	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-32.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.72	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.46	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-52.90	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.69	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.55	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-52.43	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.59	-43	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.70	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.35	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.11	-43	PASS

DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.30	-33	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.01	-23	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.93	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.51	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-52.82	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.81	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.22	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.69	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.39	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.17	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-50.47	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.11	-13	PASS
DC_5A_n77A-	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-32.95	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.04	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	0.15	30	-72.15	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	30	1000	-51.88	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.25	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.20	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-52.77	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.05	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.68	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.22	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-53.61	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.68	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.53	-33	PASS

DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-52.76	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.76	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.75	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.79	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.13	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.98	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.23	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.42	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.73	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.41	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.53	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	30	1000	-51.60	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.00	-13	PASS
DC_5A_n77A-	30	5+30	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.78	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.97	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.13	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-53.17	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.05	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.64	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.10	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-52.63	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.80	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.18	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-53.89	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.64	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	26000	-32.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.53	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.22	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.61	-23	PASS

DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.05	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.86	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.31	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.17	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.32	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.56	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	30	1000	-52.80	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-63.91	-43	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.89	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-52.25	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.76	-13	PASS
DC_5A_n77A-	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.44	-43	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.74	-33	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-53.16	-23	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.53	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.77	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-53.82	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.00	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.89	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-52.89	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.90	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.17	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-52.38	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.10	-13	PASS

DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.65	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.68	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.46	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	30	1000	-53.56	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.91	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.23	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-52.65	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-32.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.69	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.70	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.33	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-53.60	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.27	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.77	-43	PASS
DC_5A_n77A-	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.60	-33	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-51.73	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.94	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.41	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.45	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.38	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.15	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.52	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.06	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	0.15	30	-73.70	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	30	1000	-52.30	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	1000	6000	-32.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.07	-13	PASS

DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.33	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.49	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-52.59	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-32.97	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.29	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.26	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-53.07	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.39	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.33	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-49.45	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.96	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.72	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.30	-33	PASS
DC_5A_n77A-	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.58	-23	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.72	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.63	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.02	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.75	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.42	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.42	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	30	1000	-51.03	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.70	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.19	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.74	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-53.40	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-32.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.78	-13	PASS

DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.04	-43	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.85	-33	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-51.70	-23	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.39	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.89	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-53.90	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.64	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.87	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.42	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-52.42	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.56	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.98	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.26	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-52.92	-23	PASS
DC_5A_n77A-	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.97	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.05	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.18	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	30	1000	-52.76	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.87	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.13	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-52.08	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.21	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.64	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-53.39	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.16	-43	PASS

DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.42	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-52.78	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.26	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.06	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.41	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.25	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.30	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.32	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-52.93	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.23	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.04	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.42	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	30	1000	-50.51	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.15	-13	PASS
DC_5A_n77A-	30	5+50	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.14	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.15	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.77	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-51.21	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.65	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.80	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.64	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-53.26	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.36	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.75	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-52.83	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.36	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.31	-33	PASS

DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-51.45	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.57	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.75	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-52.66	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.02	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-63.28	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.82	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	30	1000	-51.82	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	1000	6000	-32.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.79	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.44	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-51.66	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.07	-13	PASS
DC_5A_n77A-	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.85	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.80	-43	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.88	-33	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-53.21	-23	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-31.71	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.46	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.75	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-53.83	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.82	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.82	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.55	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.01	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.33	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.09	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-51.94	-23	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-32.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.46	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	0.15	30	-76.74	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	30	1000	-53.87	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.28	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.64	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-53.52	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-63.57	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.07	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-52.41	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.91	-13	PASS
DC_5A_n77A-	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.90	-43	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.81	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-51.70	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.05	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.14	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.64	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.84	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.02	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.31	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.85	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.88	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.97	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.40	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.42	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	30	1000	-53.57	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.12	-13	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.99	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.20	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-52.07	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.64	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.46	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.25	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-53.02	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-32.80	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.24	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.56	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-53.68	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.97	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-32.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.05	-43	PASS
DC_5A_n77A-	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.69	-33	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-51.68	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.05	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.44	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.98	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.12	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.57	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.75	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.46	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	30	1000	-52.46	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	1000	6000	-32.97	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.14	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-76.13	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-53.06	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.13	-13	PASS

DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.53	-43	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.42	-33	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-53.32	-23	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-32.66	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.03	-13	PASS
DC_5A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.00	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.80	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-52.47	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.68	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.62	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.18	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.31	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.27	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.55	-33	PASS
DC_5A_n77A-	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-50.54	-23	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.22	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-37.03	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-63.88	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.86	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	30	1000	-50.80	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.75	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.37	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-51.54	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.18	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.38	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-51.87	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.89	-13	PASS

DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.69	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.58	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-53.42	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.64	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.63	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.46	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-51.09	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-32.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.67	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.67	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.22	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-52.90	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.24	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.25	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.17	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	30	1000	-53.11	-23	PASS
DC_5A_n77A-	30	5+70	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.09	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.74	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.22	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.89	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-53.06	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-63.89	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.42	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-53.41	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.78	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.46	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.46	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-52.72	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.82	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	26000	-32.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.82	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.67	-43	PASS

DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.05	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-52.73	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.67	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.15	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.20	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.19	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.89	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.66	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.53	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	30	1000	-51.47	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	1000	6000	-32.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.43	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.62	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-53.36	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_5A_n77A-	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.00	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.26	-43	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.70	-33	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-53.79	-23	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-32.54	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-32.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.72	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.53	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.79	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-53.74	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-32.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.38	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.04	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-52.96	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.72	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.30	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.24	-33	PASS

DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.02	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-32.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.38	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.28	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	30	1000	-53.43	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	6000	26000	-32.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.76	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.02	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.41	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-53.38	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.25	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.79	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.29	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.30	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-51.72	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-32.97	-13	PASS
DC_5A_n77A-	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.90	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.43	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.47	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-53.44	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-32.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.38	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.32	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.01	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-32.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.35	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.81	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-52.62	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.59	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.55	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	30	1000	-52.08	-23	PASS

DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	6000	26000	-32.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.38	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.39	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-53.54	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.71	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.20	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.06	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-52.72	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.32	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.29	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-54.25	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.76	-13	PASS
DC_5A_n77A-	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.61	-43	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.10	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.38	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.64	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.41	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.98	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.49	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.37	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.96	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.47	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	30	1000	-52.83	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	1000	6000	-32.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.56	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.59	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-50.75	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.13	-13	PASS

DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.57	-43	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-73.46	-33	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-52.26	-23	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-32.89	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-32.86	-13	PASS
DC_5A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.80	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.58	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-53.00	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.94	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.78	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.55	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.39	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.00	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.76	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.30	-43	PASS
DC_5A_n77A-	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.02	-33	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-52.72	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-63.94	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	0.15	30	-73.89	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	30	1000	-53.11	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.21	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.18	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-52.91	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.19	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.64	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.91	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.94	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-52.50	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.11	-13	PASS

DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.75	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.81	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.69	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-53.45	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-32.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.73	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.95	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.55	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.68	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.60	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.89	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.32	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.99	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	0.15	30	-72.64	-33	PASS
DC_5A_n77A-	30	5+90	DFT-16QAM	M+M	Outer_Full	30	1000	-53.25	-23	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.42	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.09	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-52.39	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.57	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.01	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-51.27	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.80	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.61	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.34	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-53.85	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.53	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.02	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.94	-13	PASS

DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.45	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.66	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-51.21	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.06	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.76	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.53	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.91	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-51.89	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.70	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-37.00	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-63.67	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.33	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	30	1000	-53.07	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	1000	6000	-32.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.90	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.15	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-76.12	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-51.47	-23	PASS
DC_5A_n77A-	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.11	-13	PASS

3700-3980										
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.76	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.26	-43	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.61	-33	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-52.87	-23	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-32.51	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.92	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.47	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.24	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	30	1000	-53.71	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.09	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.14	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.34	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.15	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-27.74	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.77	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.77	-43	PASS

DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.96	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-51.25	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.17	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.00	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.93	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.32	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.83	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	30	1000	-51.51	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.08	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.07	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.72	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.82	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-51.27	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.89	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.35	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.86	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-52.75	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.17	-13	PASS
DC_5A_n77A-	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.07	-13	PASS

3700-3980		0								
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.19	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.46	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	30	1000	-52.34	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.21	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.91	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.41	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.30	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.18	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.20	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.84	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.51	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.07	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.18	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.10	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-32.88	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.99	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.06	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.86	-33	PASS

DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	30	1000	-53.87	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.01	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.16	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.44	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.15	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.16	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-32.98	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.87	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.42	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.68	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-52.94	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.05	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.55	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.19	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	30	1000	-53.45	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.64	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	6000	26000	-32.98	-13	PASS
DC_5A_n77A-	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.68	-13	PASS

3700-3980		0								
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.47	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.28	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-51.18	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.12	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.18	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.85	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.72	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.40	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.09	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.76	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.04	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.73	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.36	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	0.15	30	-73.54	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	30	1000	-52.66	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	1000	6000	-32.81	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	6000	26000	-32.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.83	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.38	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.11	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-53.47	-23	PASS

DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.13	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.76	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.38	-43	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.02	-33	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-52.48	-23	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.11	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-32.95	-13	PASS
DC_5A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.86	-13	PASS

Test Graphs

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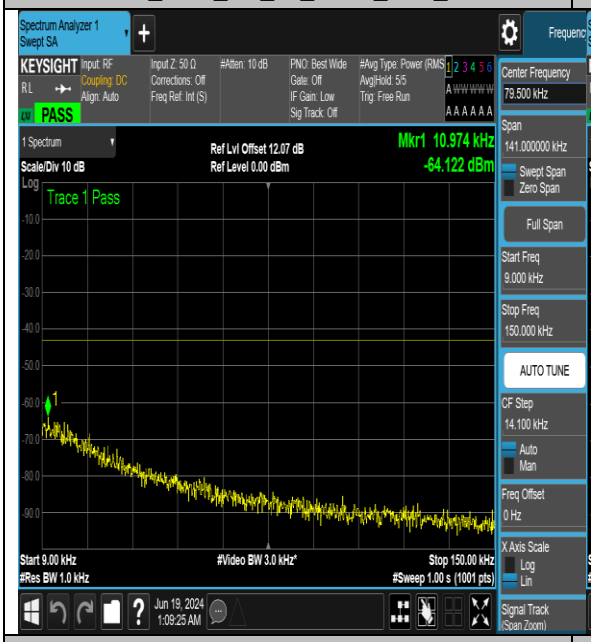

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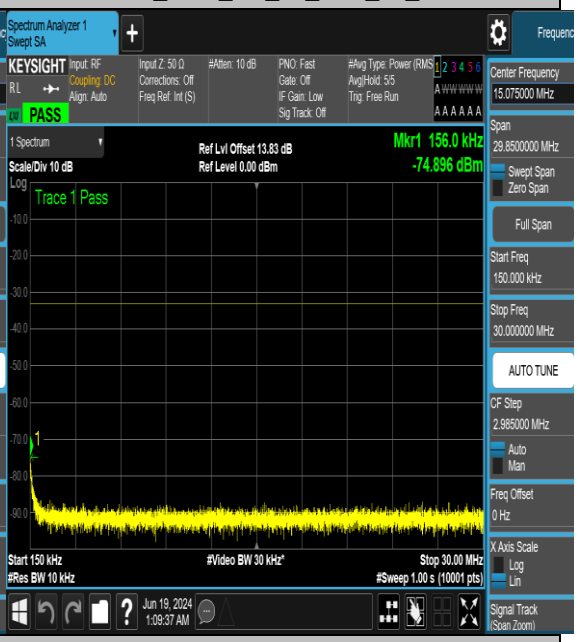

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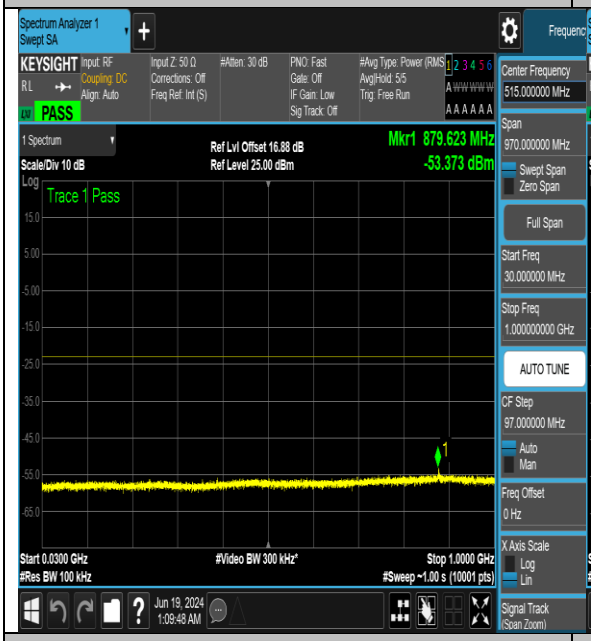

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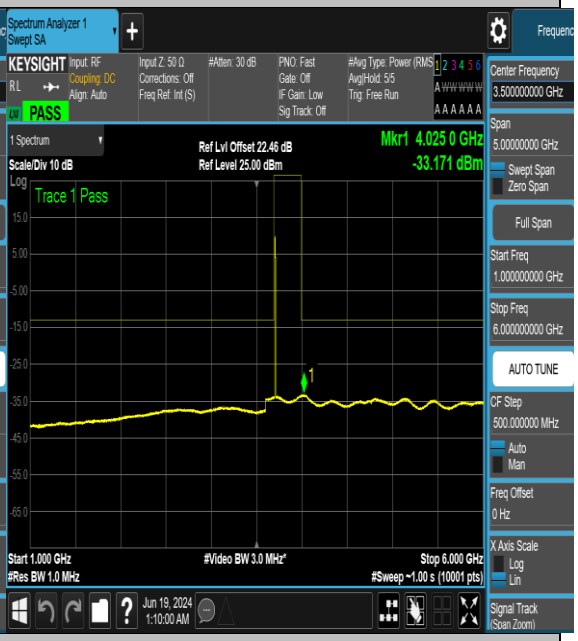

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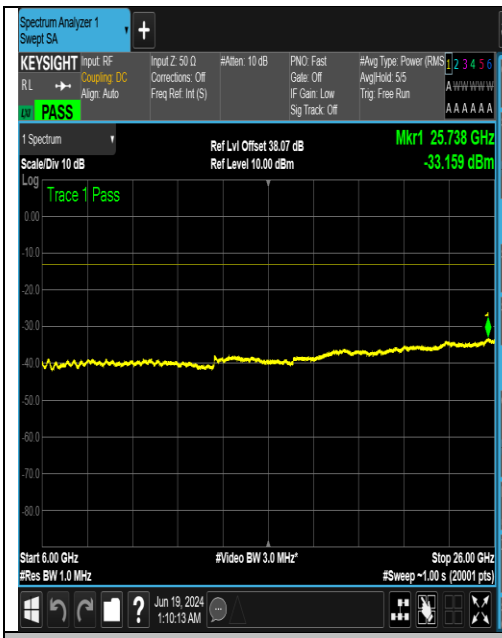

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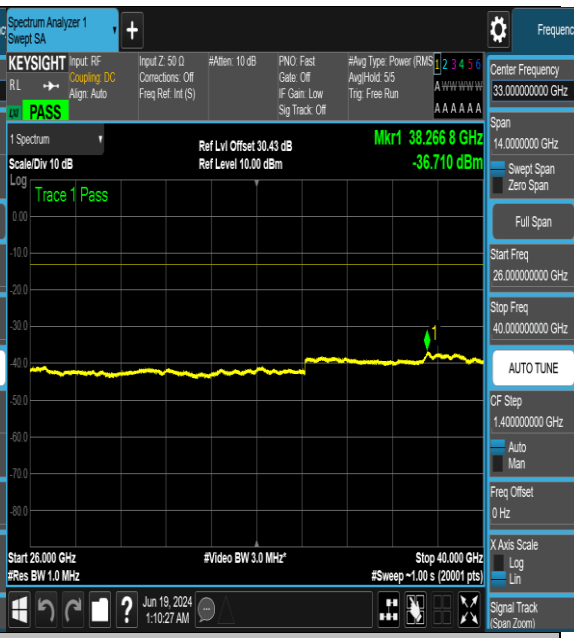

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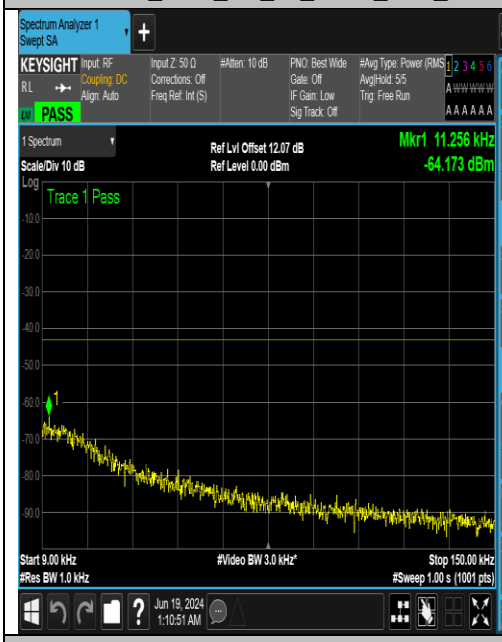

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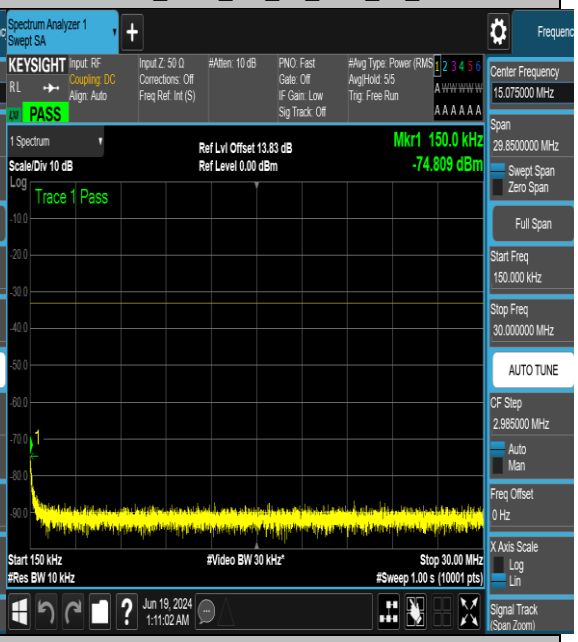

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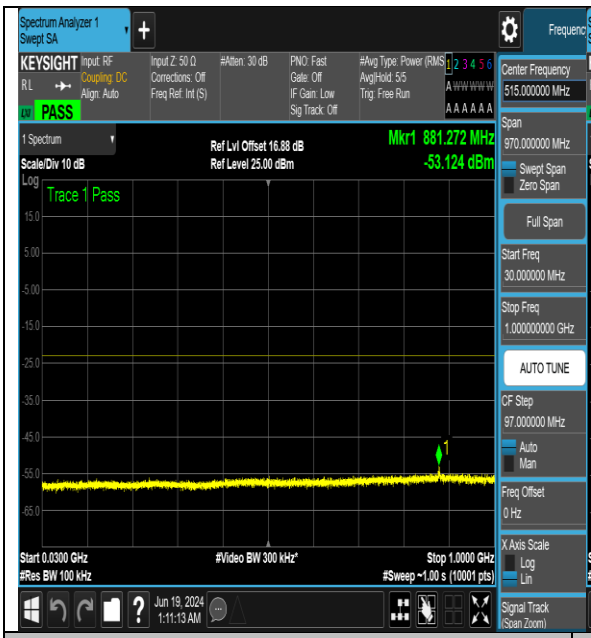

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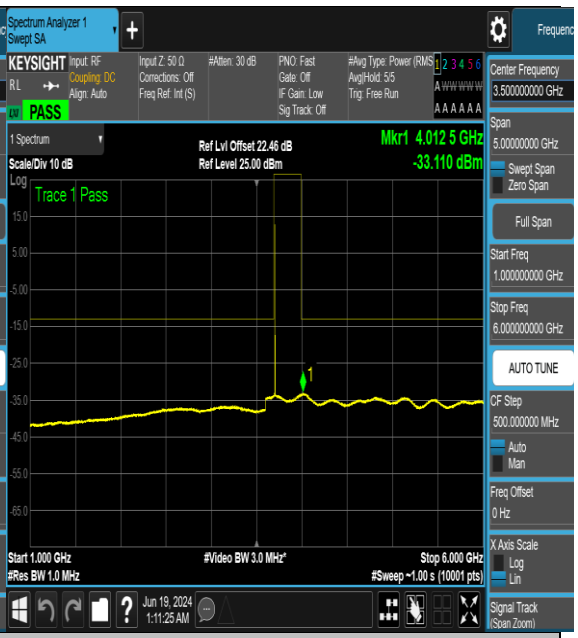

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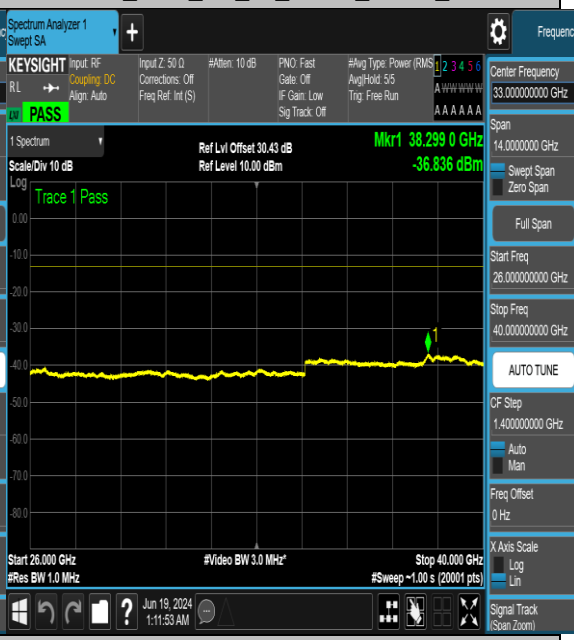

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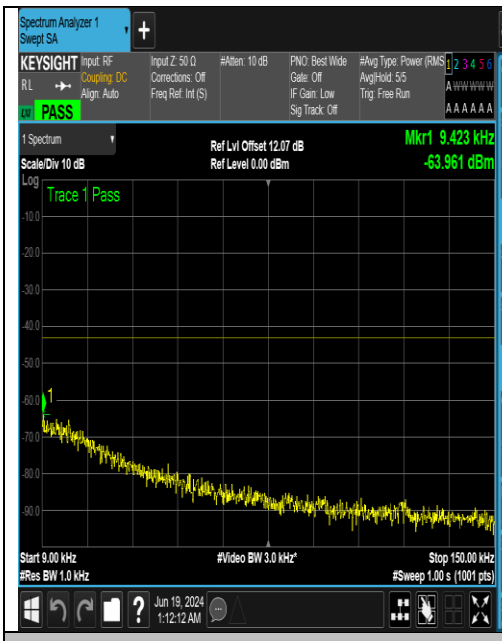

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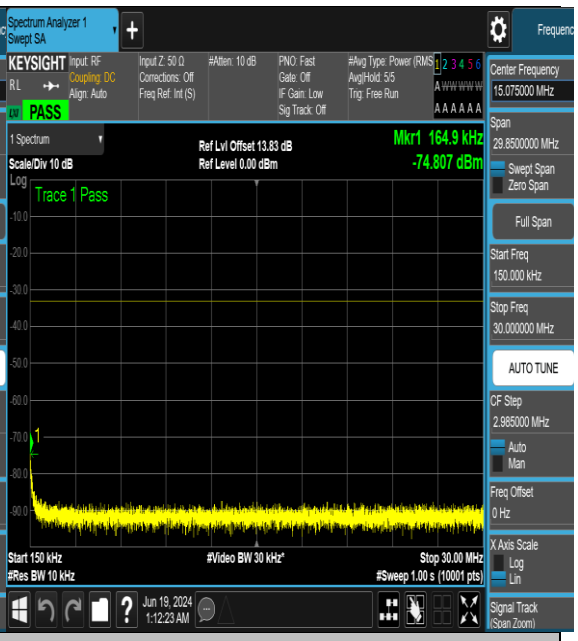

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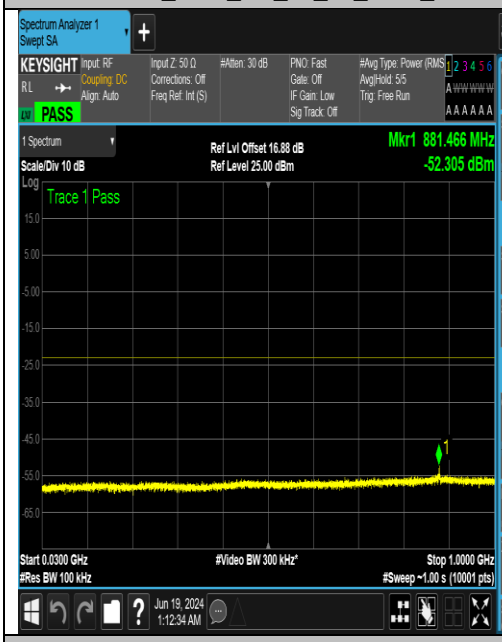

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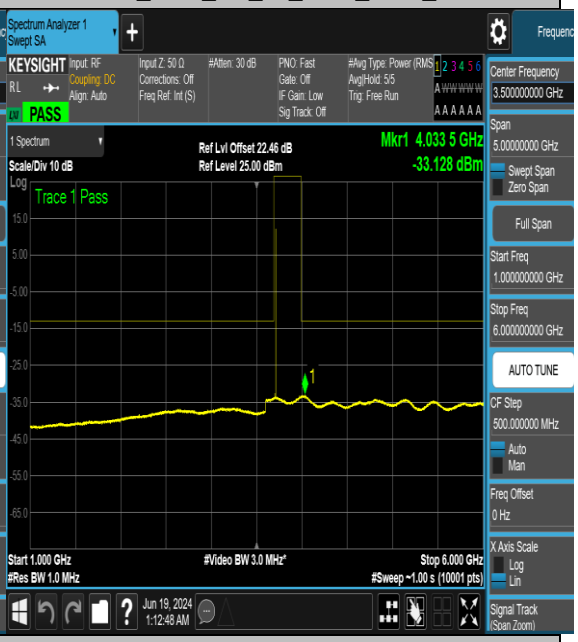

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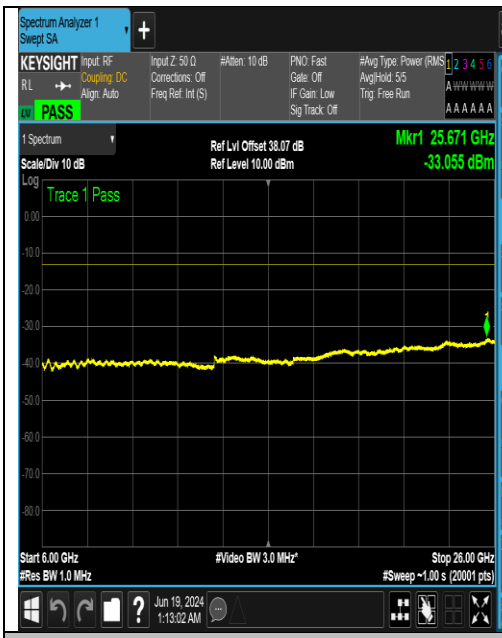

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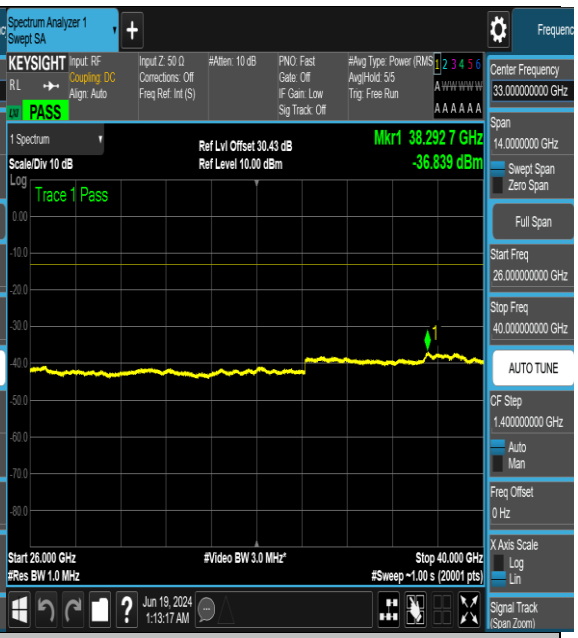

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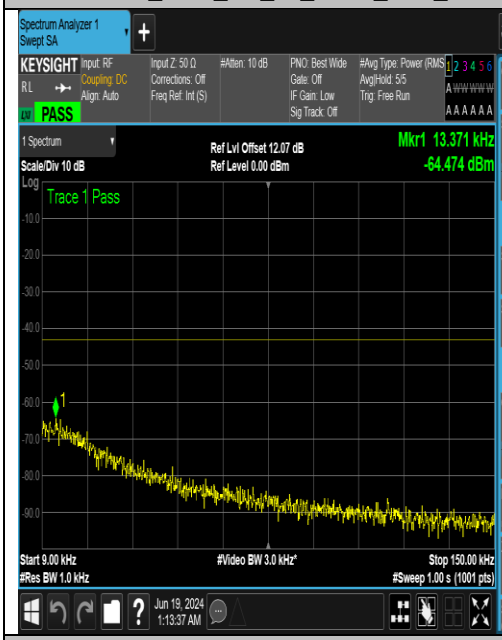

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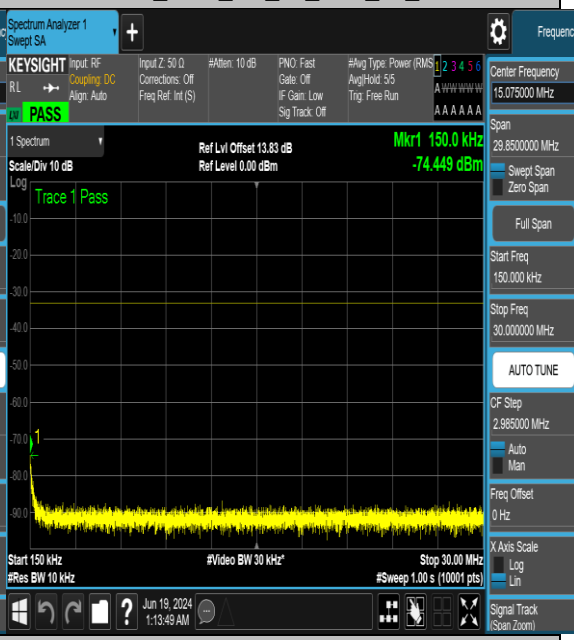

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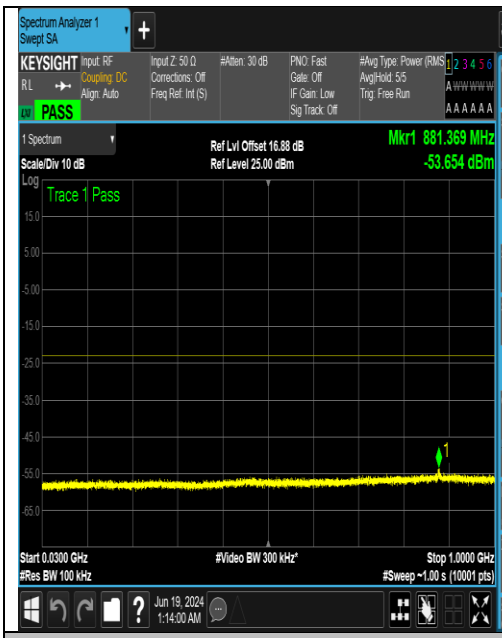

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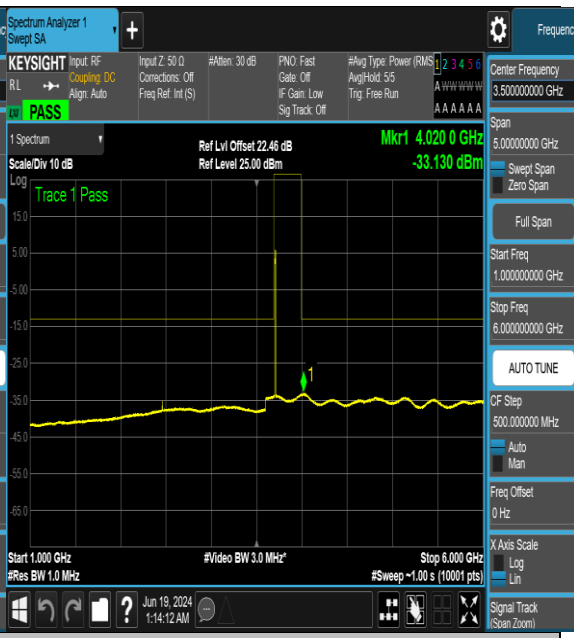

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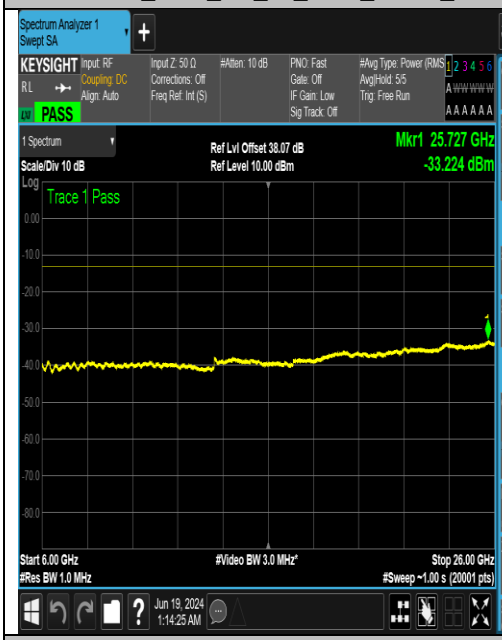

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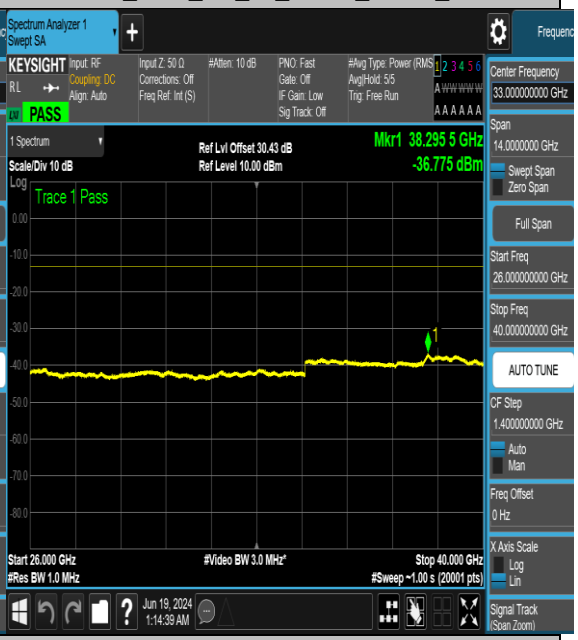

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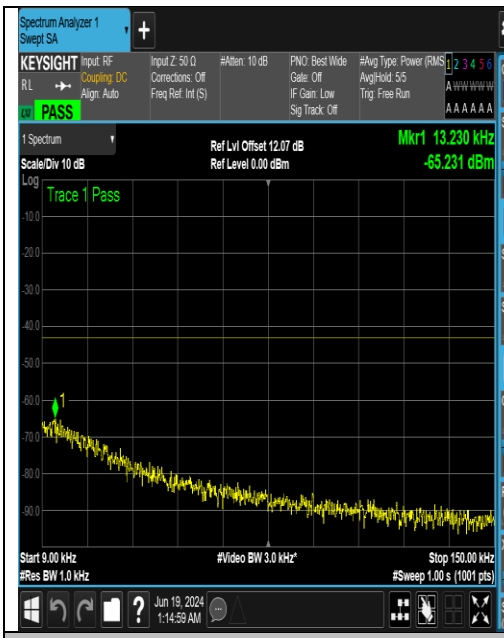

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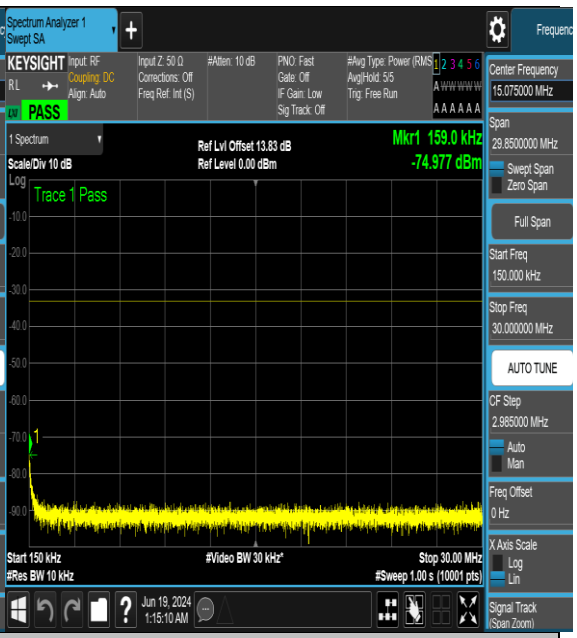

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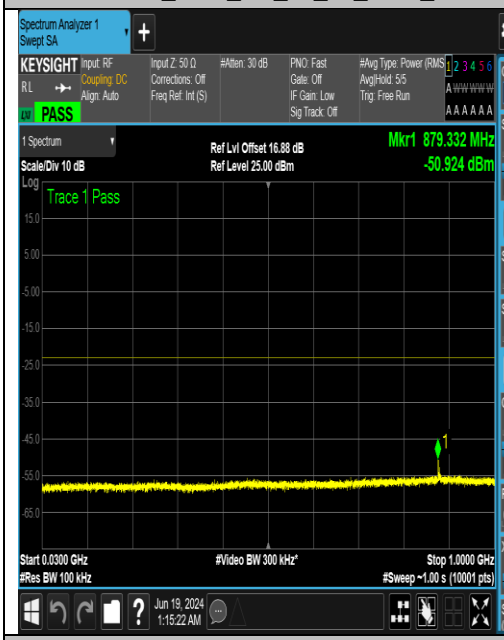

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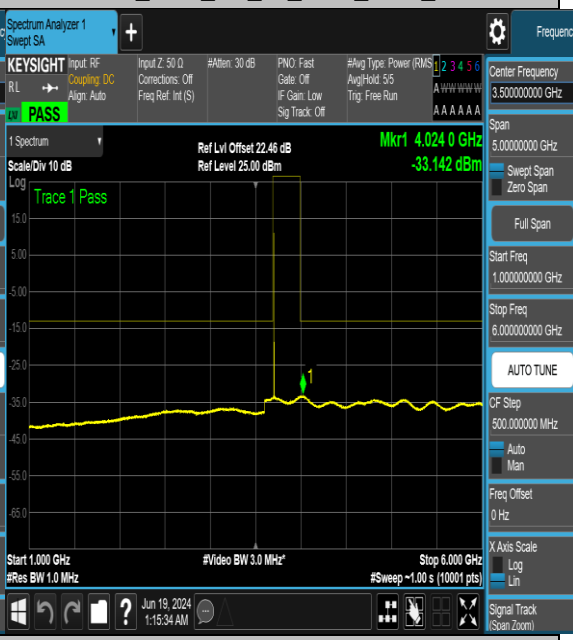

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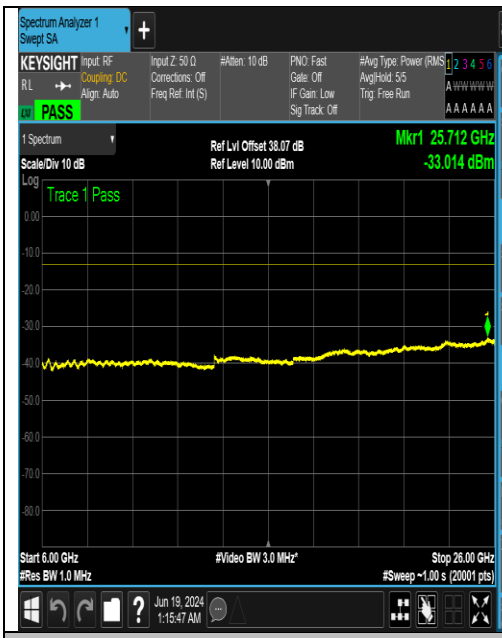

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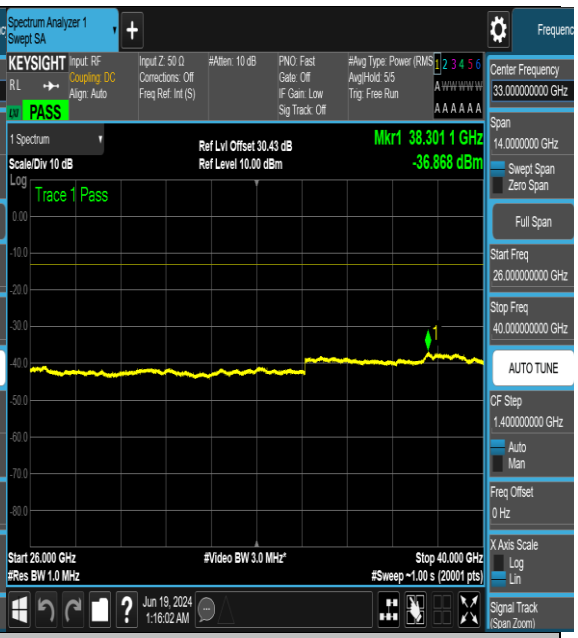

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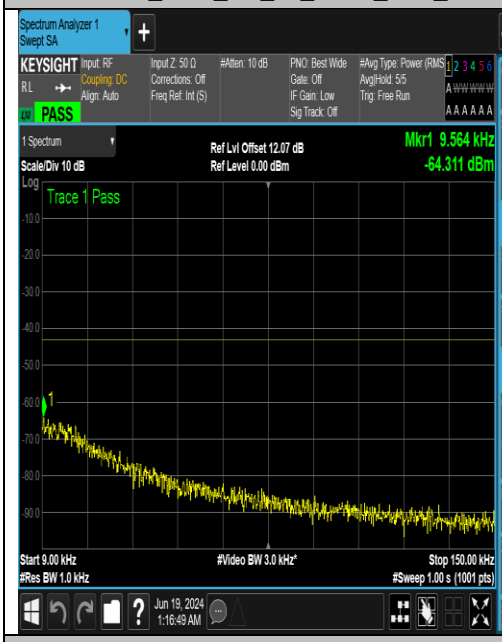

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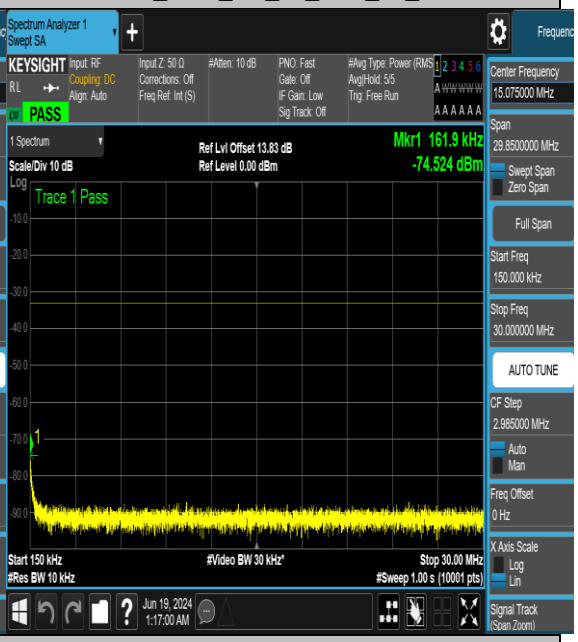

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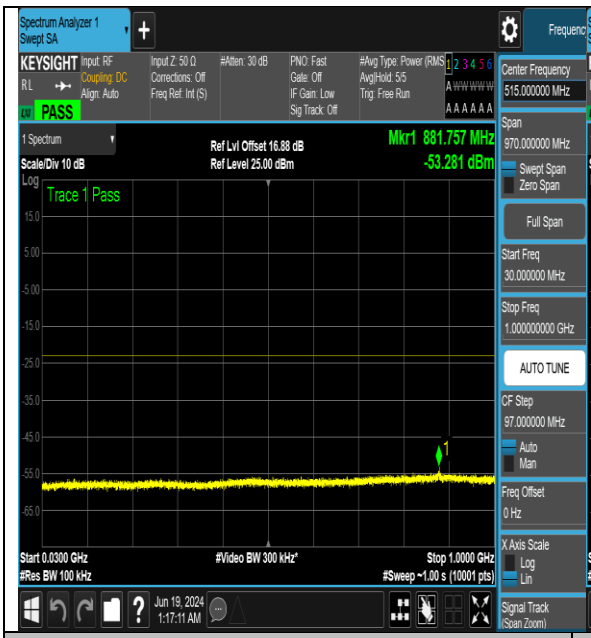

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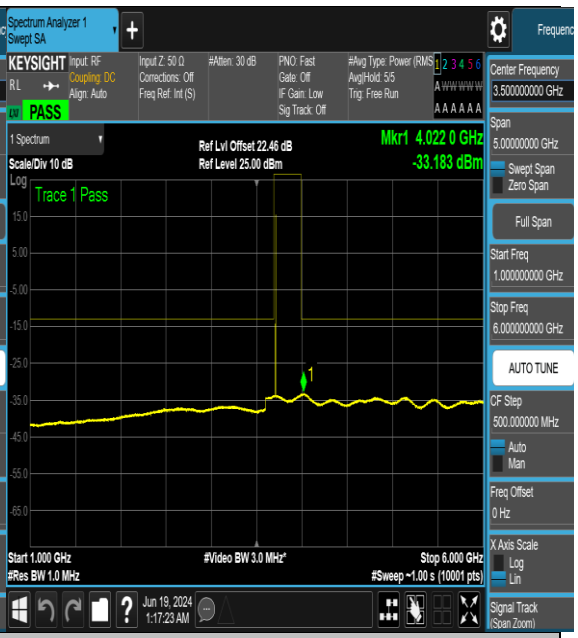

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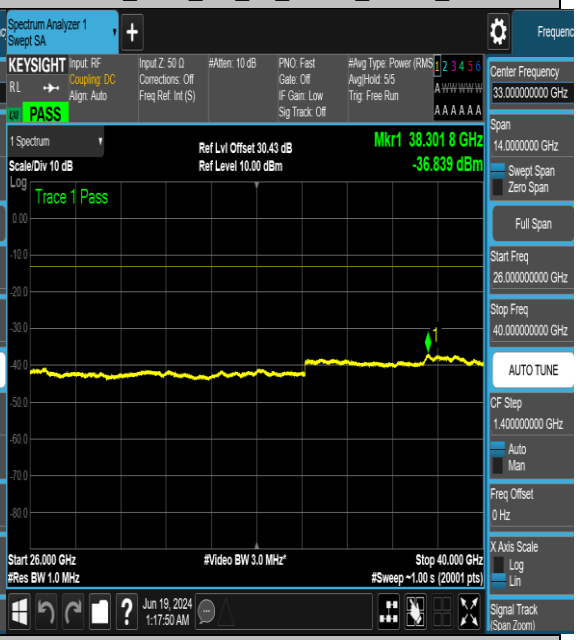

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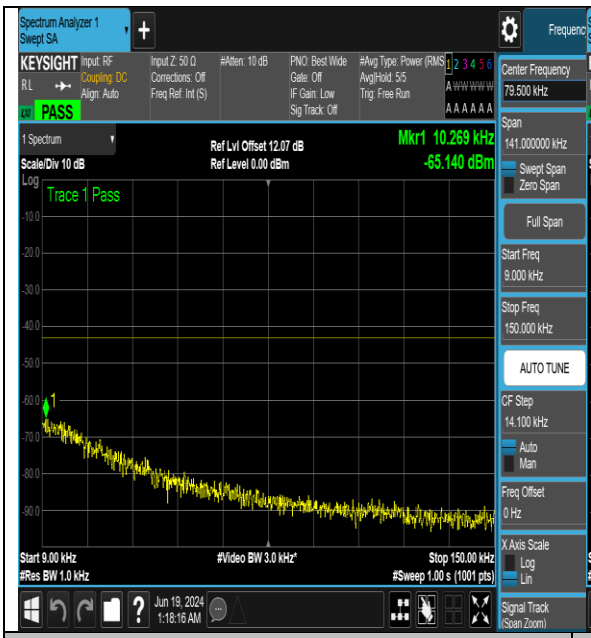

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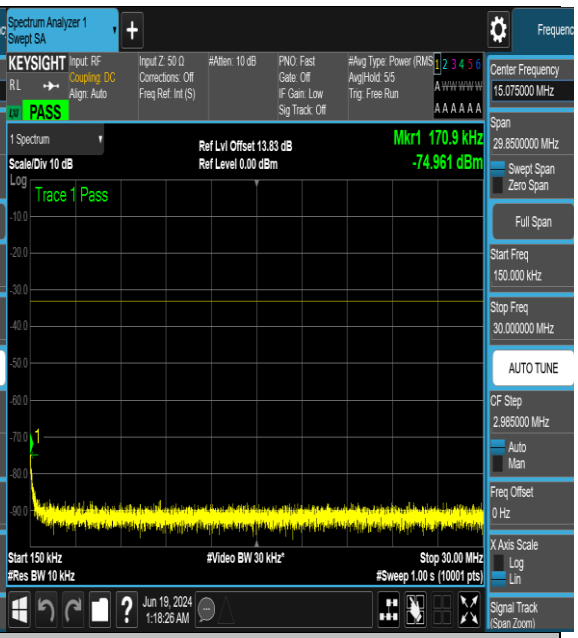

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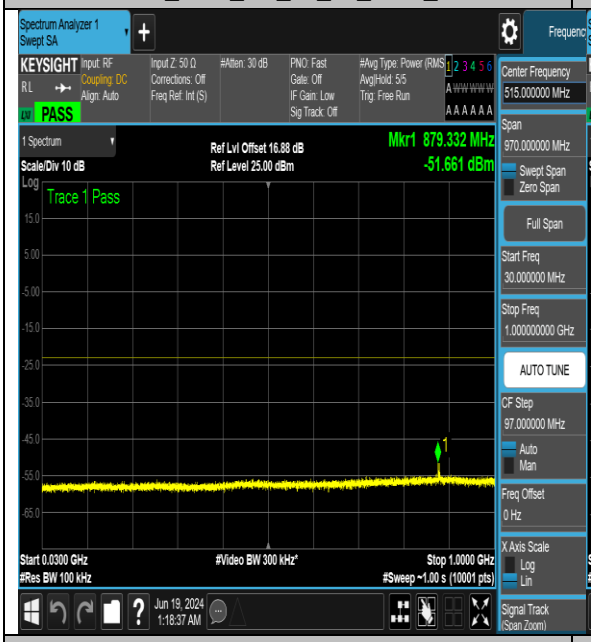

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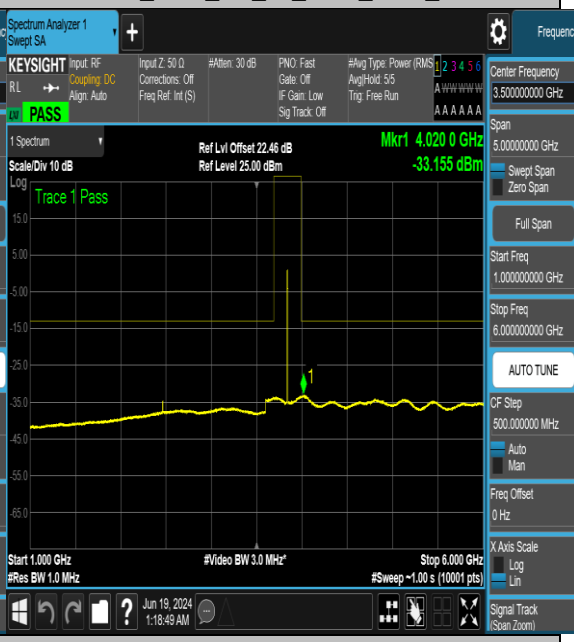

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