

Appendix for 71A_N2A

Product Name: 5G Window CPE

Model No: TITAN 5100

Appendix A: Average Power Output Data

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	20.75	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Inner_Full	21.46	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	20.77	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	20.53	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	19.90	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Inner_Full	20.72	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	20.15	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	18.80	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	20.41	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Inner_Full	21.18	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	20.43	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	20.24	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	19.33	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Inner_Full	20.44	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	20.06	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	19.57	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	18.78	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Inner_Full	19.60	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	18.78	PC3	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	18.66	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	17.70	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Inner_Full	18.84	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	17.70	PC3	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	17.56	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	20.67	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Inner_Full	21.43	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.25	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	20.80	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	19.65	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Inner_Full	20.77	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	19.85	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	19.43	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	20.38	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Inner_Full	21.13	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	20.52	PC3	PASS

DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	20.14	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	19.44	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Inner_Full	20.43	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	19.74	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	19.03	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	18.86	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Inner_Full	19.60	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	18.98	PC3	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	18.57	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	17.80	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Inner_Full	18.84	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	18.08	PC3	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	17.32	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	20.71	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Inner_Full	21.47	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	20.86	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	20.50	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	19.69	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Inner_Full	20.65	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	19.94	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	19.58	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	20.46	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Inner_Full	21.45	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	20.75	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	20.24	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	19.49	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Inner_Full	20.69	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	19.30	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	18.88	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	19.24	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Inner_Full	20.05	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.34	PC3	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	18.70	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	18.24	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Inner_Full	19.18	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	18.77	PC3	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	17.63	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	20.70	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Inner_Full	21.03	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	20.27	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	19.67	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	19.07	PC3	PASS

DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Inner_Full	20.13	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	19.25	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	18.29	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	20.50	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Inner_Full	21.26	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	20.59	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	20.16	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	19.47	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Inner_Full	20.53	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	19.33	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	19.22	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	19.41	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Inner_Full	20.17	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	20.02	PC3	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	18.90	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	18.48	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Inner_Full	19.31	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	18.81	PC3	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	17.84	PC3	PASS

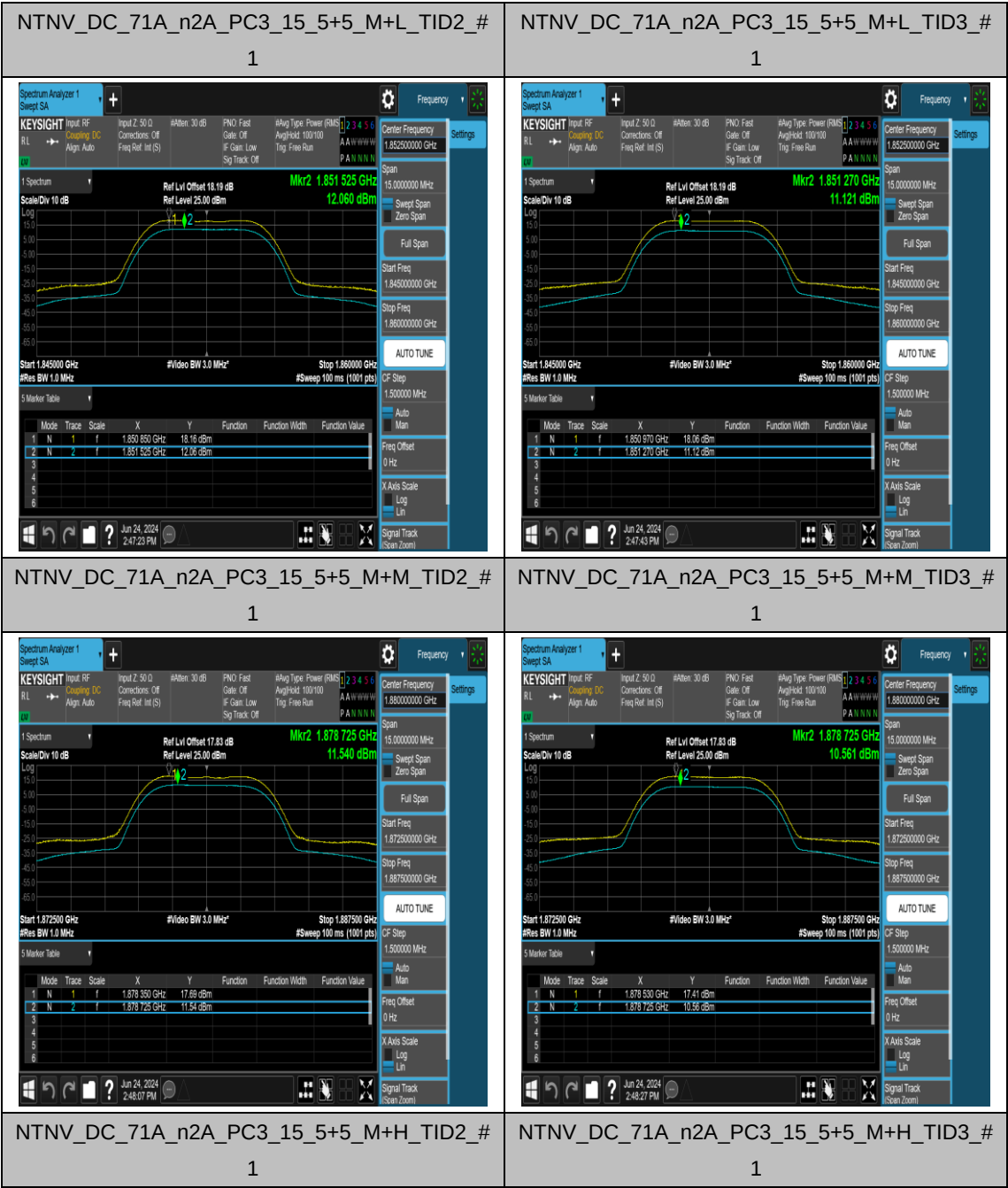
Appendix B: Peak-to-Average Ratio for NSA

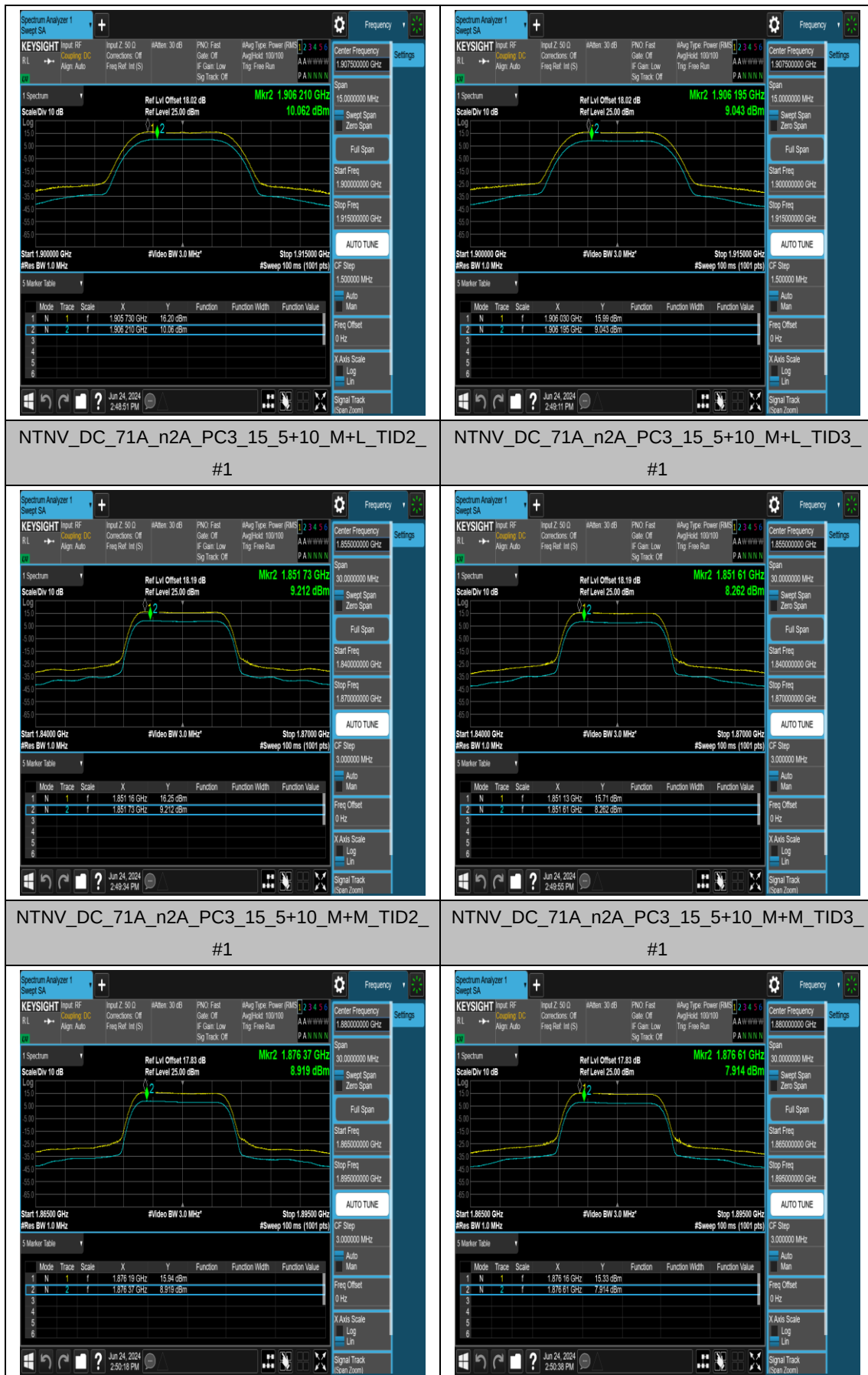
Peak-to-Average Ratio(PAPR)

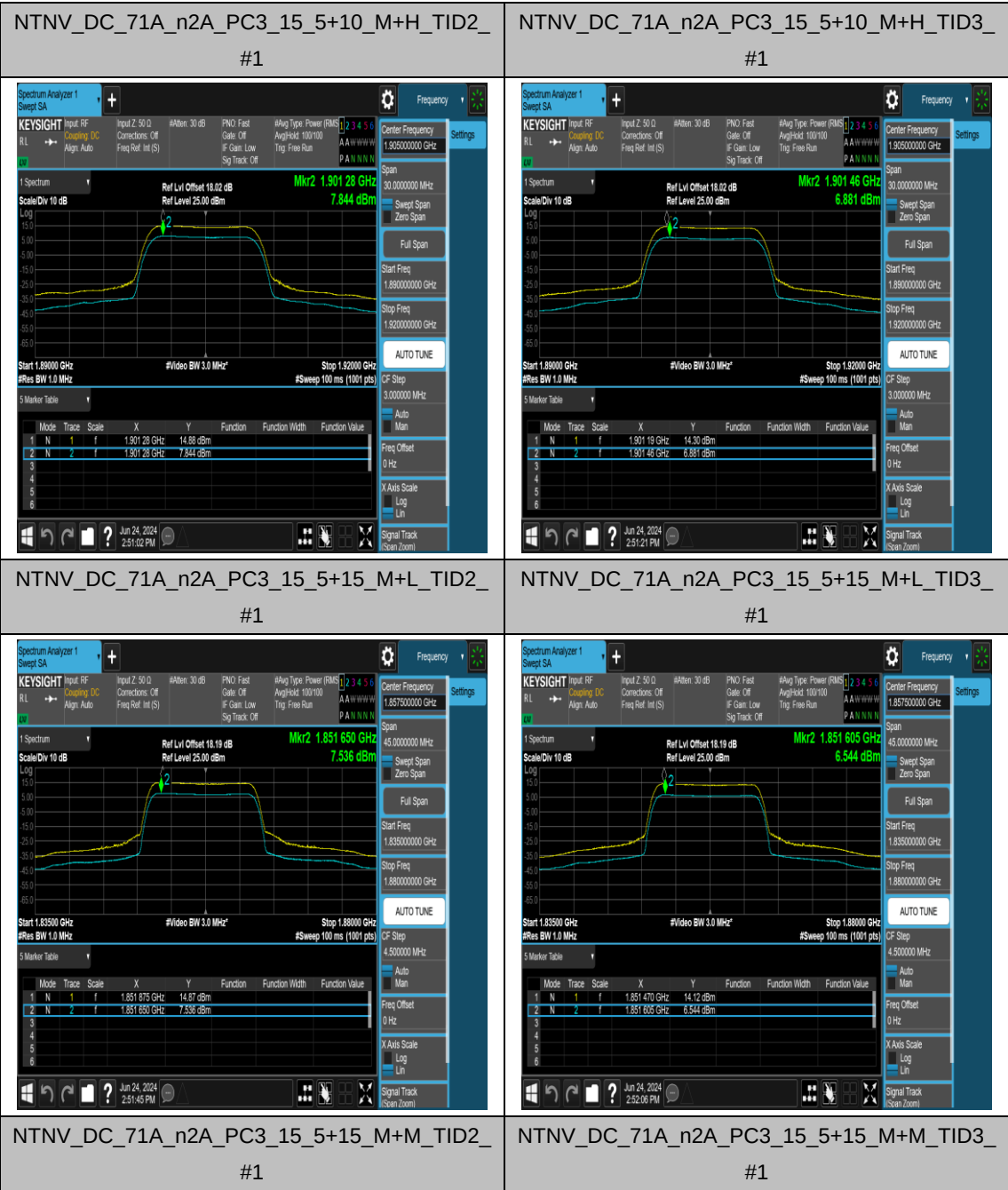
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	100%	0.00	6.10	≤13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	100%	0.00	6.94	≤13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	100%	0.00	6.15	≤13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	100%	0.00	6.85	≤13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	100%	0.00	6.14	≤13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	100%	0.00	6.95	≤13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.04	≤13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.45	≤13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.02	≤13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.42	≤13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.04	≤13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.42	≤13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.33	≤13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.58	≤13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.34	≤13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.55	≤13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.34	≤13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.61	≤13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.60	≤13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	10.01	≤13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.49	≤13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.69	≤13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.51	≤13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.64	≤13	PASS

Test Graphs









NTNV_DC_71A_n2A_PC3_15_5+15_M+H_TID2_#1



NTNV_DC_71A_n2A_PC3_15_5+15_M+H_TID3_#1

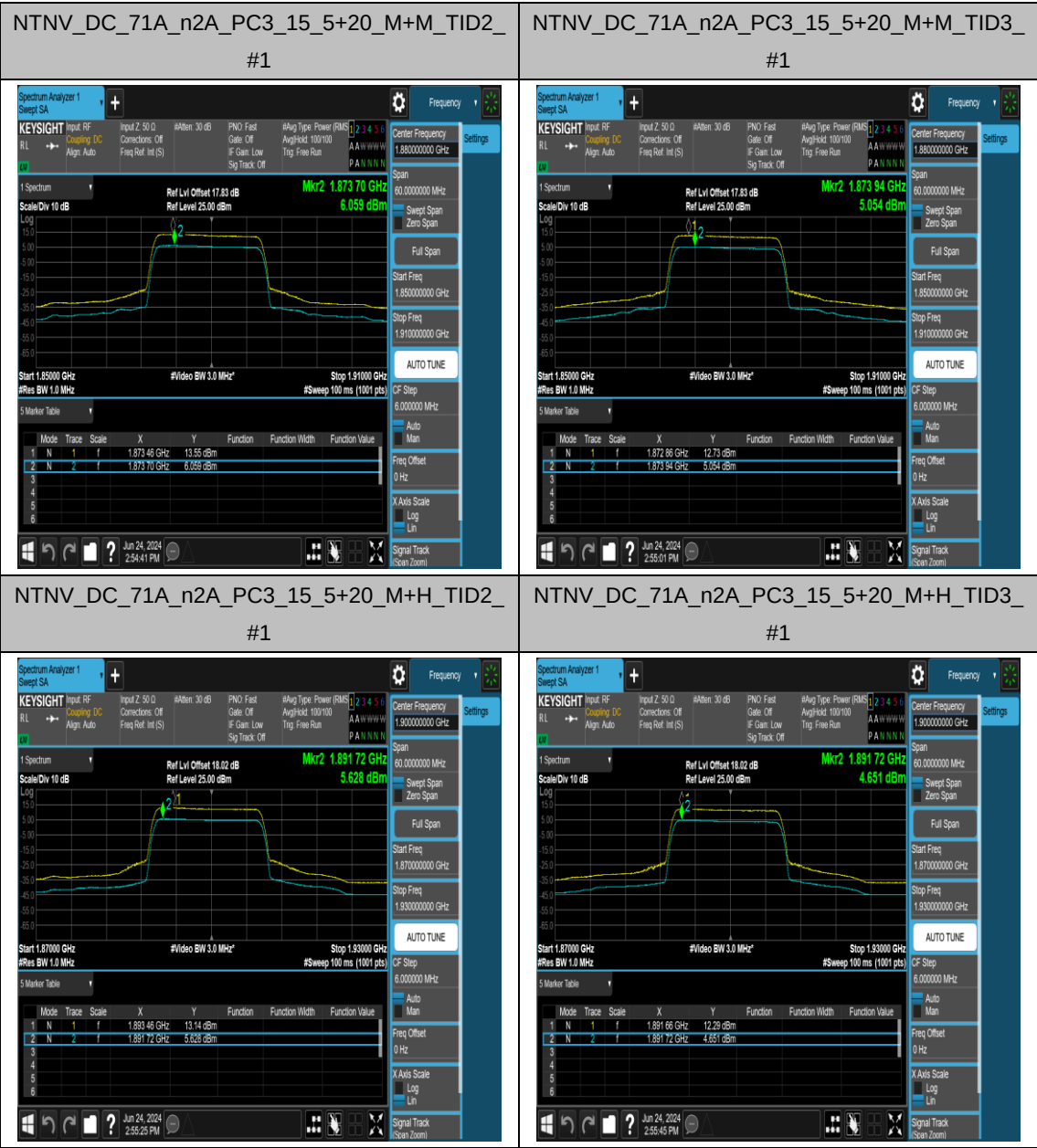


NTNV_DC_71A_n2A_PC3_15_5+20_M+L_TID2_#1



NTNV_DC_71A_n2A_PC3_15_5+20_M+L_TID3_#1

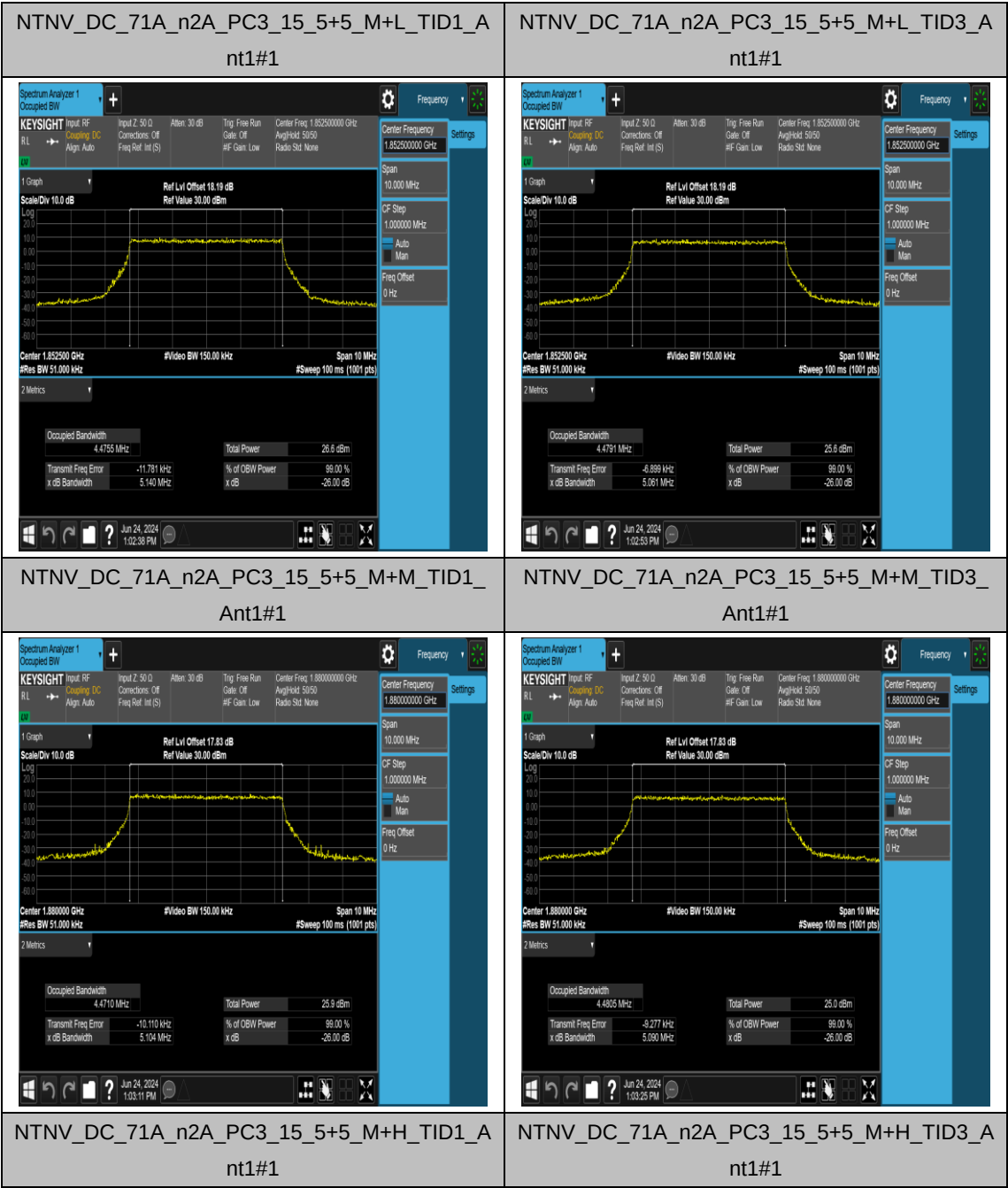


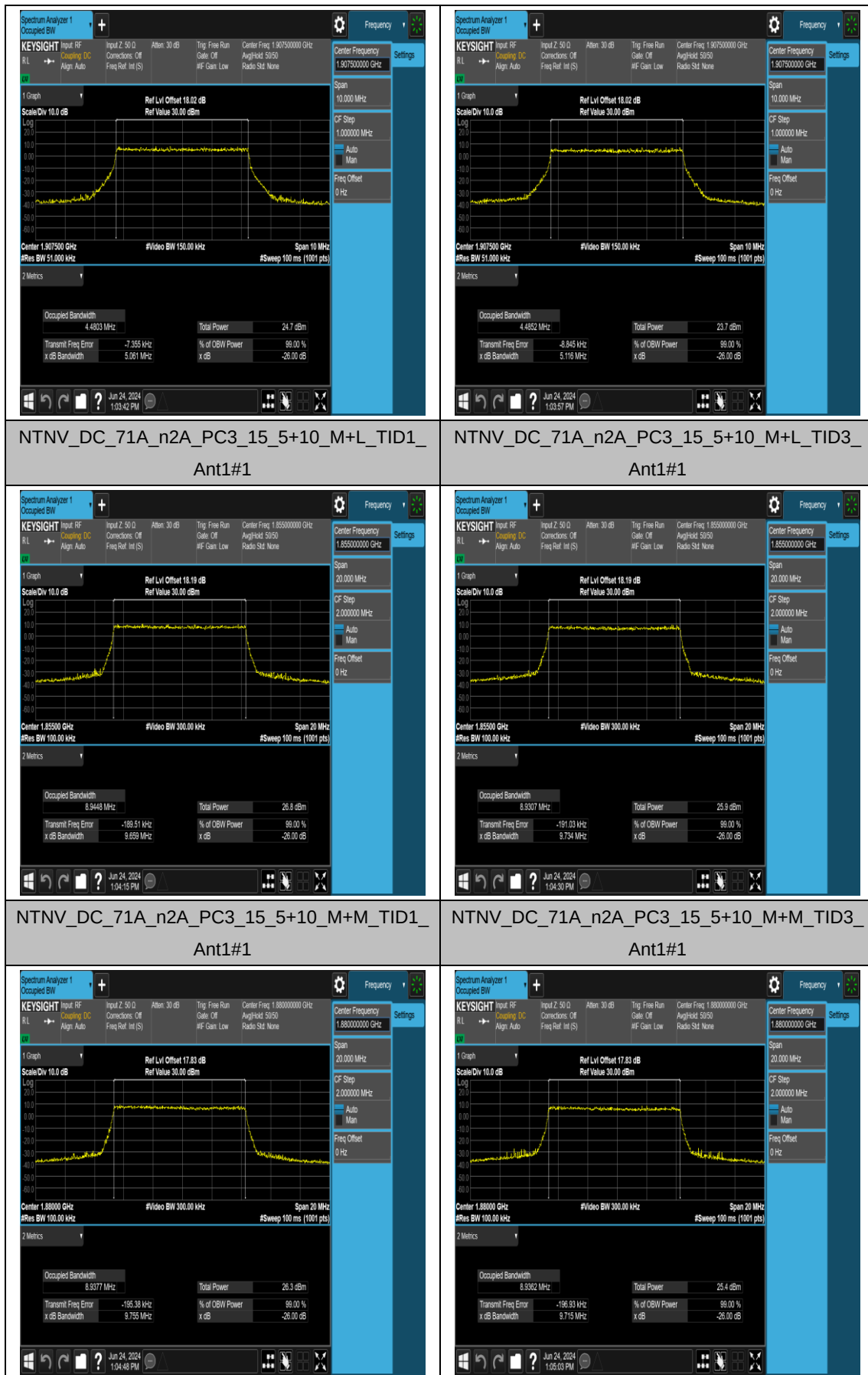


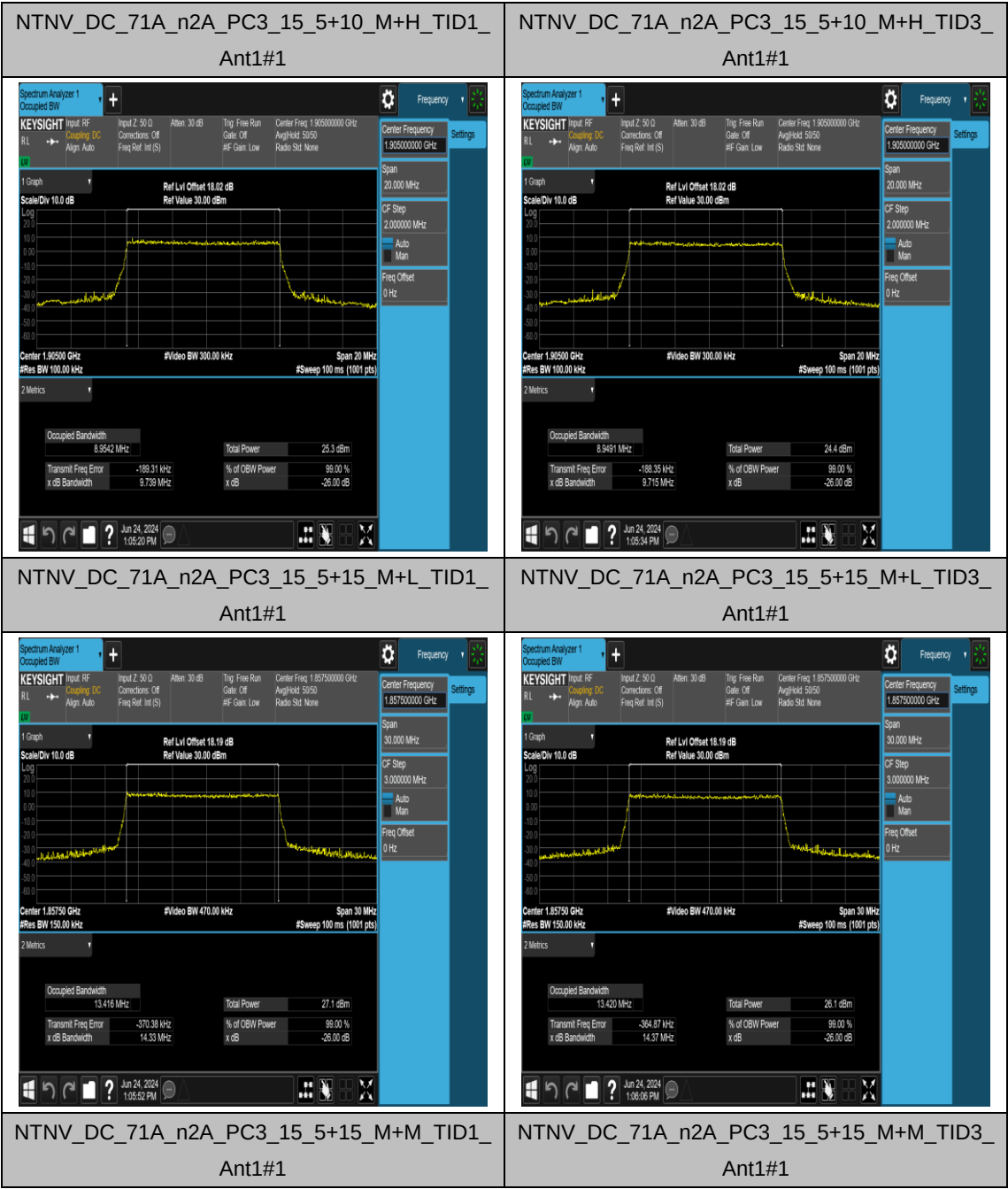
Appendix C: 26dB Bandwidth and Occupied Bandwidth for**NSA****Test Result**

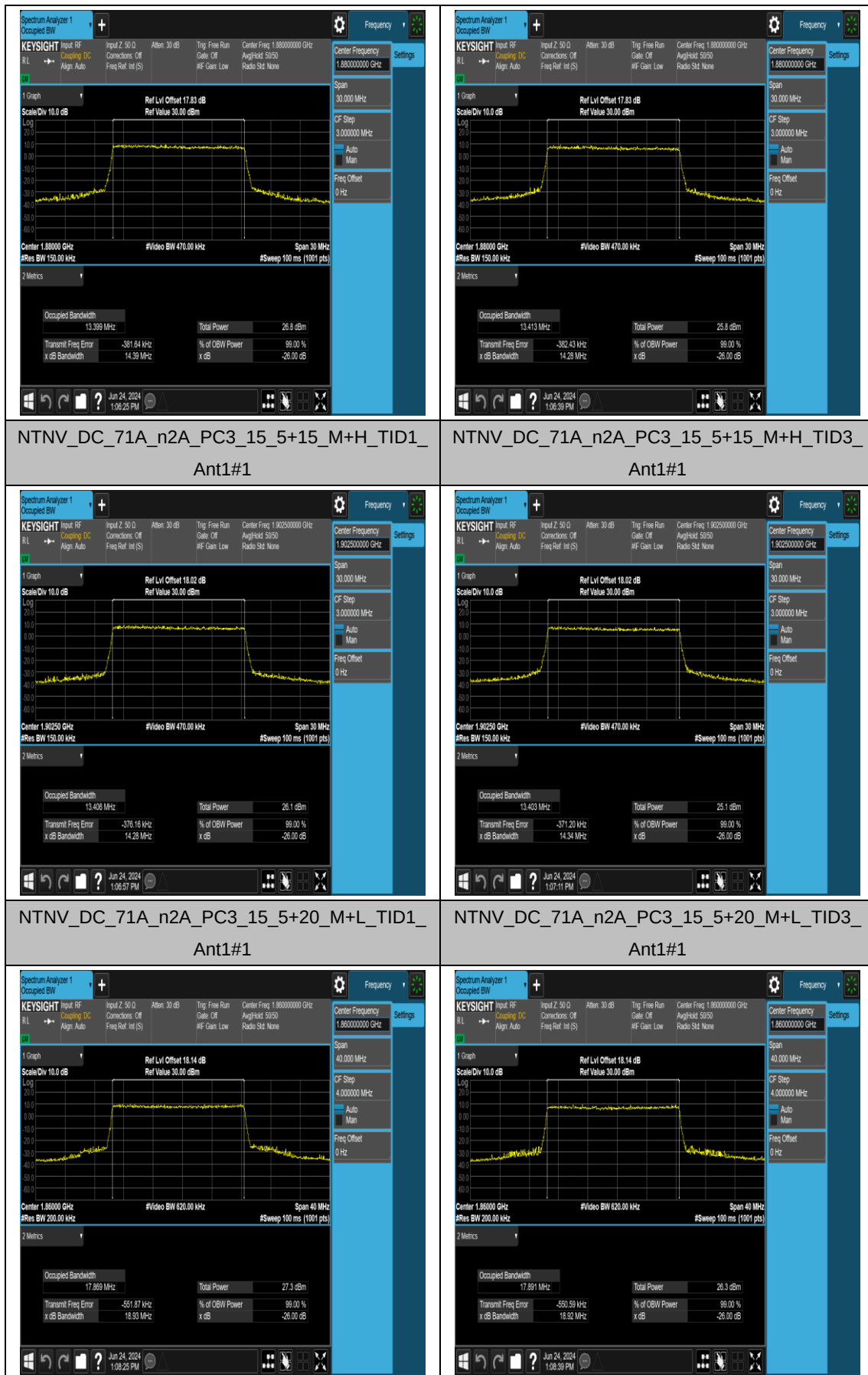
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	4.4755	5.140	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	4.4791	5.061	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	4.4710	5.104	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	4.4805	5.090	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	4.4803	5.061	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	4.4852	5.116	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	8.9448	9.659	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	8.9307	9.734	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	8.9377	9.755	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	8.9362	9.715	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	8.9542	9.739	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	8.9491	9.715	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	13.416	14.33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	13.420	14.37	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	13.399	14.39	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	13.413	14.28	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	13.406	14.28	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	13.403	14.34	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	17.869	18.93	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	17.891	18.92	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	17.844	18.86	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	17.849	18.85	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	17.858	18.89	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	17.850	18.86	PASS

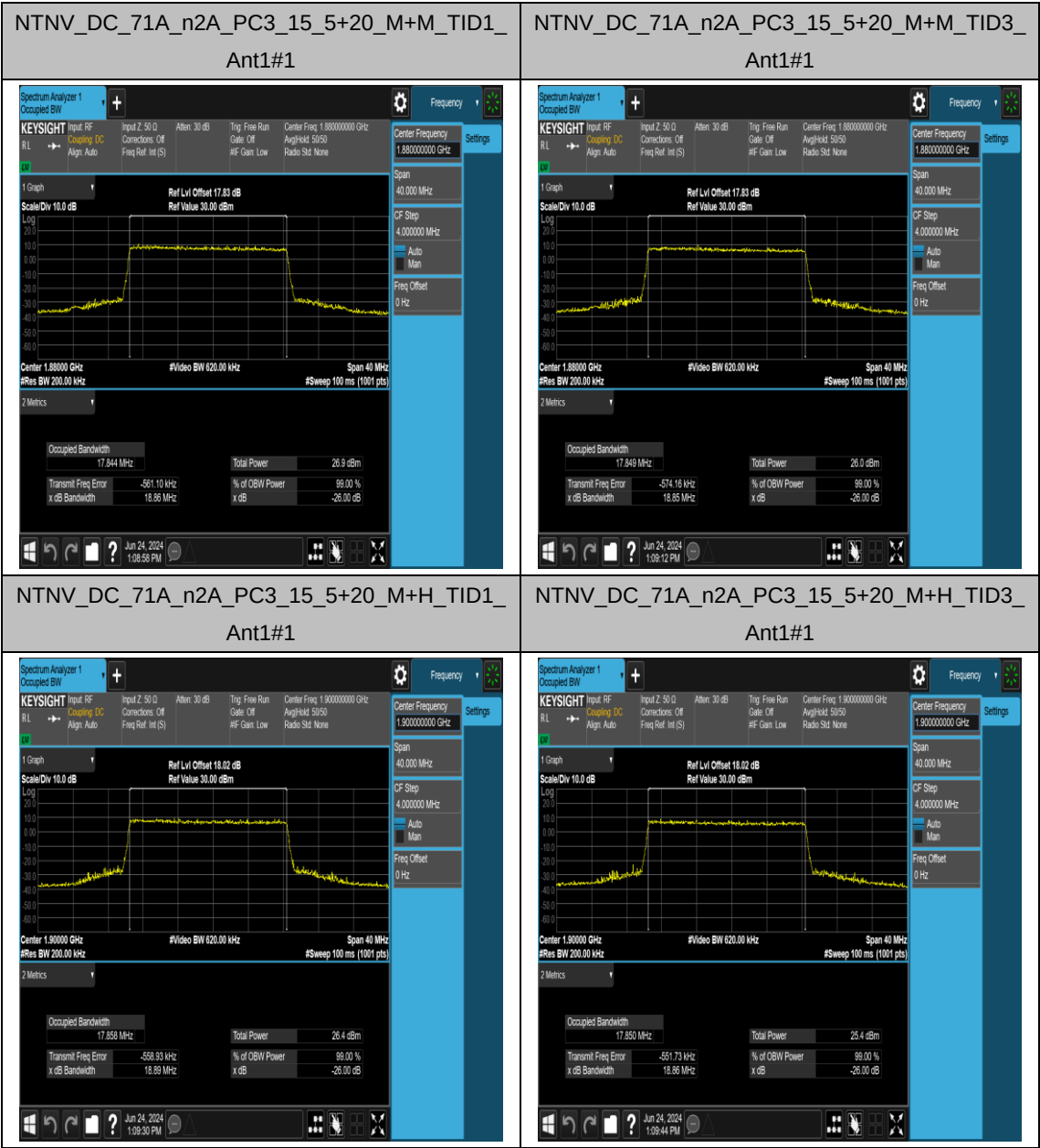
Test Graphs









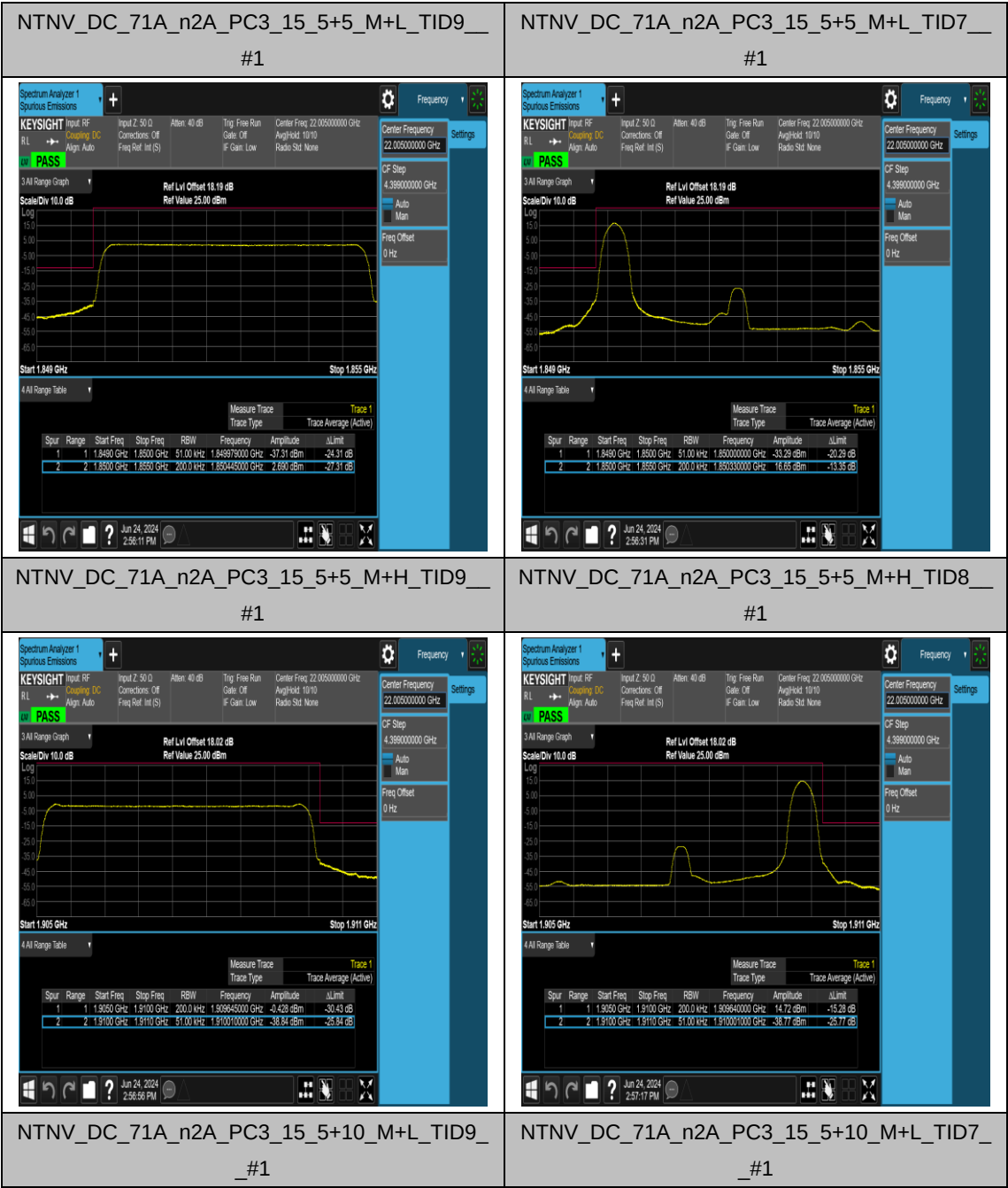


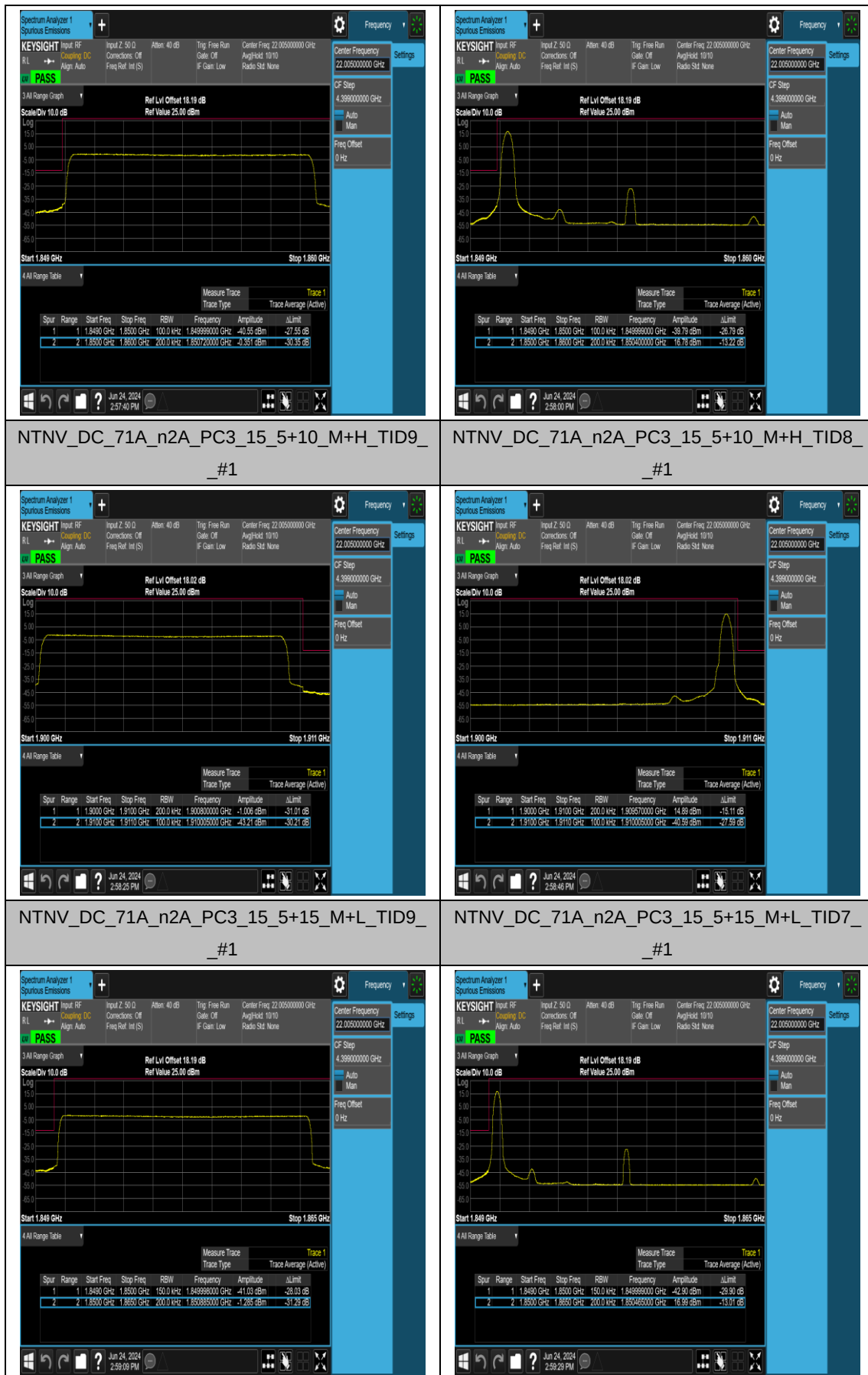
Appendix D: Band Edge for NSA

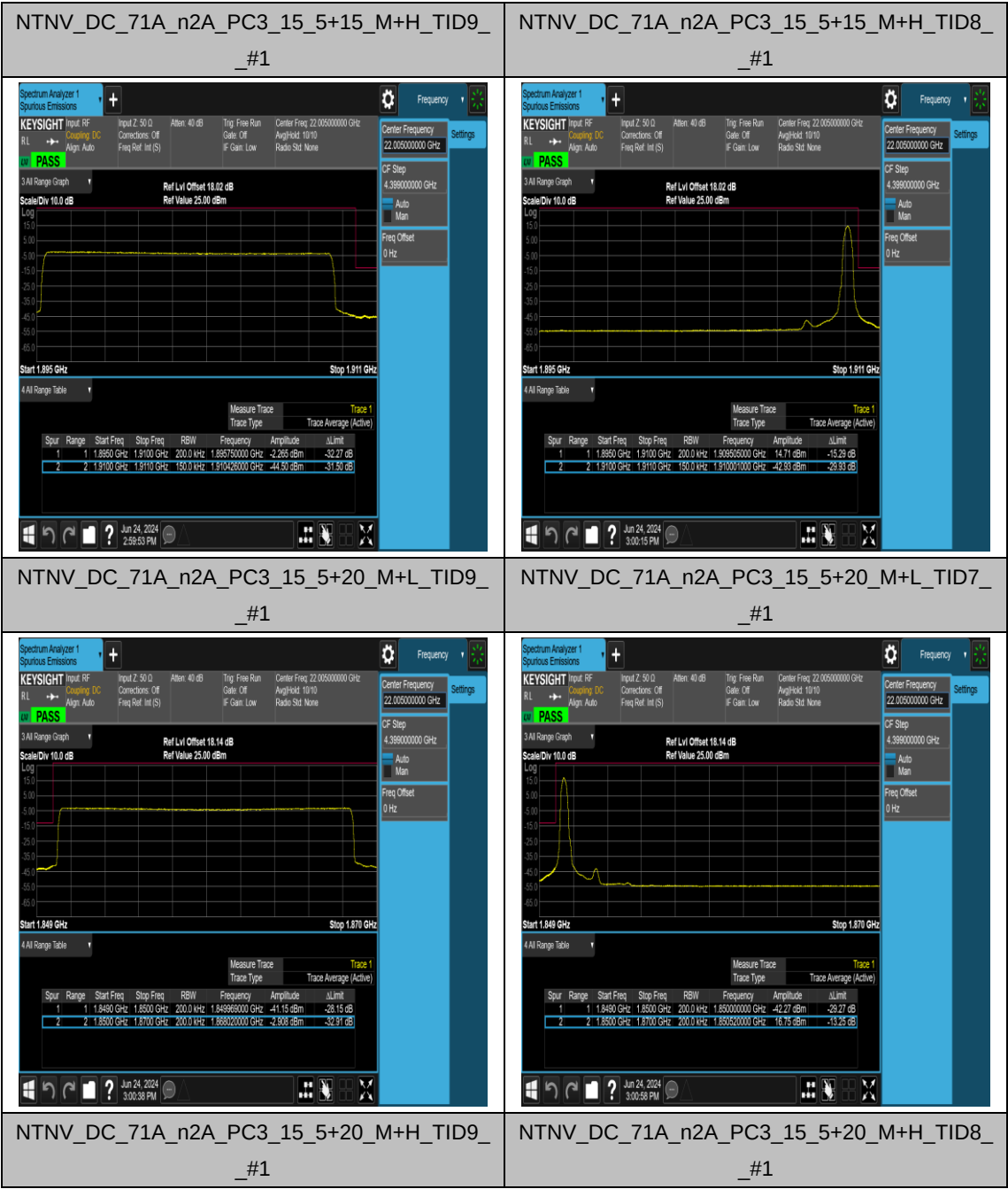
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.68	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.34	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	30	1000	-54.94	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	1000	3000	-30.83	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	3000	12000	-45.36	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	12000	20000	-47.96	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.37	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.50	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.14	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-31.90	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-46.25	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-48.12	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.29	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.33	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.13	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.35	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-46.01	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-48.03	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.45	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	0.15	30	-76.16	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	30	1000	-54.93	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	1000	3000	-30.31	-13	PASS

DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	3000	12000	-44.80	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	12000	20000	-48.01	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.55	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.40	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.93	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-33.09	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-45.35	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-48.01	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.02	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.40	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-55.18	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.25	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-46.25	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-48.05	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.82	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.58	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	30	1000	-55.36	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.39	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	3000	12000	-46.22	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	12000	20000	-48.05	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.01	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.32	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.19	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.42	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-46.22	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-48.04	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.59	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.50	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.92	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.44	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-46.11	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-48.07	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.88	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.08	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	30	1000	-54.97	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.36	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	3000	12000	-46.40	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	12000	20000	-48.12	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.27	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.42	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-41.20	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.35	-13	PASS

DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-46.34	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-47.57	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.71	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.47	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-55.10	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.39	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-46.08	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-48.04	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.55	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.13	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	30	1000	-55.19	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	1000	3000	-33.70	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	3000	12000	-46.29	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	12000	20000	-48.05	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.02	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.95	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.12	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.36	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-46.17	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-48.06	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.71	-43	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.72	-33	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.95	-23	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-34.90	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-45.16	-13	PASS
DC_71A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-48.07	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.83	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.81	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	30	1000	-55.02	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	1000	3000	-33.04	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	3000	12000	-46.25	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	12000	20000	-48.04	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.01	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.97	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-55.06	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.40	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-44.82	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-47.89	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.90	-43	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.06	-33	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.72	-23	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-35.73	-13	PASS

DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-45.91	-13	PASS
DC_71A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-47.92	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.83	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.83	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-55.06	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-31.64	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	3000	12000	-46.08	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	12000	20000	-47.95	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.40	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.21	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.26	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-35.58	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-46.23	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-48.01	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.74	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.93	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.96	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.29	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-46.28	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-47.94	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.82	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.18	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-55.18	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	1000	3000	-30.33	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	3000	12000	-46.25	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	12000	20000	-48.08	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.45	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.32	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-55.22	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-35.82	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-44.71	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-48.08	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.16	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.65	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-55.09	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.82	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-46.27	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-47.94	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.04	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.80	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-55.16	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-37.54	-13	PASS

DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	3000	12000	-46.38	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	12000	20000	-48.07	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.81	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-70.42	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.10	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.92	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-45.23	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-47.99	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.69	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.04	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.94	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.40	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-46.17	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-48.10	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.92	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-73.87	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-54.77	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.44	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	3000	12000	-46.17	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	12000	20000	-47.91	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.17	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.69	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-55.02	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.38	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-46.32	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-47.93	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.40	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.30	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.96	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.24	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-46.16	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-48.06	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.03	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.16	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-55.18	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-34.01	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	3000	12000	-46.34	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	12000	20000	-48.09	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.75	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.07	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.25	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.36	-13	PASS

DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-46.40	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-48.00	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.02	-43	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.17	-33	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.78	-23	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-36.83	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-46.37	-13	PASS
DC_71A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-48.15	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.42	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.13	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-55.05	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	1000	3000	-33.82	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	3000	12000	-46.28	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	12000	20000	-48.00	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.95	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.35	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.90	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-38.14	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-46.10	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-48.06	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.83	-43	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.22	-33	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-55.18	-23	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.37	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-46.14	-13	PASS
DC_71A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-48.02	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.04	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.86	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-55.14	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-32.65	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	3000	12000	-46.23	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	12000	20000	-48.01	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.84	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.23	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.90	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.23	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-46.18	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-47.96	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.16	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.29	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.23	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.38	-13	PASS

DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-46.30	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-47.77	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.84	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-73.77	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-55.02	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	1000	3000	-32.33	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	3000	12000	-46.36	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	12000	20000	-48.02	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.62	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.93	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-55.36	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-38.53	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-46.04	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-48.04	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.68	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-71.92	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.96	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.43	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-46.30	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-48.04	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.19	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.98	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-54.96	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.34	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	3000	12000	-46.39	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	12000	20000	-48.09	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.43	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.72	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.93	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.38	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-46.26	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-48.01	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.42	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.49	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.22	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.31	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-46.24	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-47.94	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.16	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.64	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-54.71	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.35	-13	PASS

DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	3000	12000	-46.10	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	12000	20000	-48.09	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.89	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.03	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.83	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.66	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-46.06	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-48.05	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.07	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.65	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-55.16	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.38	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-46.34	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-48.16	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.38	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.19	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-54.95	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-34.41	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	3000	12000	-46.25	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	12000	20000	-48.14	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.22	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.45	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.34	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-46.26	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-48.05	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.78	-43	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.74	-33	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.27	-23	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-36.74	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-46.25	-13	PASS
DC_71A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-48.05	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.32	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.96	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-55.08	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	1000	3000	-34.10	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	3000	12000	-46.26	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	12000	20000	-47.94	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.13	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.76	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.40	-13	PASS

DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-46.10	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-47.98	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-66.00	-43	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-76.21	-33	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-55.03	-23	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.05	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-46.32	-13	PASS
DC_71A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-48.03	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.75	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.71	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-55.25	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-32.69	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	3000	12000	-46.31	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	12000	20000	-48.03	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.69	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.41	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.96	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-35.51	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-46.24	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-47.94	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.54	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.15	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.19	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.91	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-46.36	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-48.01	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.19	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.50	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-54.67	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	1000	3000	-33.80	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	3000	12000	-46.30	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	12000	20000	-48.07	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.02	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.80	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-55.01	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.13	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-46.19	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-48.00	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.56	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.61	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-55.26	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.32	-13	PASS

DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-46.30	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-47.90	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.43	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.63	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-55.17	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.23	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	3000	12000	-46.36	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	12000	20000	-47.98	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.38	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.80	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.99	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.30	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-46.35	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-48.02	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.93	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.75	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.97	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.39	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-46.29	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-47.98	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.00	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.29	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-55.06	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	1000	3000	-38.08	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	3000	12000	-46.13	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	12000	20000	-48.11	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.15	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.22	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-30.58	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.34	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-45.78	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-48.02	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.35	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.14	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.77	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.41	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-46.34	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-48.08	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.34	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.90	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-55.08	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-34.92	-13	PASS

DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	3000	12000	-46.36	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	12000	20000	-47.86	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.52	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.88	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.91	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.61	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-46.35	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-48.06	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.98	-43	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.25	-33	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.99	-23	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.09	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-46.11	-13	PASS
DC_71A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-48.00	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.82	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-76.46	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-55.05	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	1000	3000	-35.12	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	3000	12000	-46.21	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	12000	20000	-48.15	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.62	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.57	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-55.21	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.36	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-46.12	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-47.92	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.92	-43	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-73.83	-33	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.86	-23	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.23	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-46.16	-13	PASS
DC_71A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-47.85	-13	PASS