Appendix for 71A_N78A(3450-3550MHz)

Product Name: 5G Window CPE
Model No: TITAN 5100

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	23.51	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	24.45	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.35	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.42	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	22.40	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	23.62	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.27	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.16	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	23.64	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	24.72	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	23.85	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	23.64	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	22.74	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	23.67	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	22.43	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	22.93	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	23.25	PC2	PASS
DC_71A_n78A-3	30	5+10	DFT-QPSK	M+H	Inner_Full	24.29	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	23.28	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	23.22	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	22.19	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	23.21	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	22.22	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	22.36	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	23.62	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	24.73	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.78	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	23.70	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	22.65	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	23.70	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.52	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.65	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	23.81	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	24.79	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.68	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	23.85	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	22.78	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	23.82	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	22.82	PC2	PASS

DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	23.09	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	23.20	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	24.39	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	23.27	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.19	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	22.31	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	23.14	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	22.15	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	22.47	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	23.70	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	24.69	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.64	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.91	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	22.67	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	23.61	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.98	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.48	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	23.79	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	24.81	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.91	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.65	PC2	PASS
DC_71A_n78A-3	30	5+20	DFT-16QAM	M+M	Outer_Full	22.72	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	23.76	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	22.88	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	22.85	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	23.30	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	24.29	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	23.54	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.30	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	22.32	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	23.32	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.48	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.26	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	23.79	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Inner_Full	24.70	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.67	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	24.07	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	22.71	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Inner_Full	23.68	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	22.69	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	23.01	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	23.94	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+M	Inner_Full	24.74	PC2	PASS

DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	24.03	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	23.87	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	22.82	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Inner_Full	23.92	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	22.90	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	22.66	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	23.43	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Inner_Full	24.36	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	23.60	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	23.21	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	22.43	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Inner_Full	23.43	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	22.74	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	22.47	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	23.86	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	24.79	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.54	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.77	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	22.89	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	23.79	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.75	PC2	PASS
DC_71A_n78A-3	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.08	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	23.95	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	24.82	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.20	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.76	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	22.91	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	23.88	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.36	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.01	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	23.31	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	24.47	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	23.69	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.56	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	22.51	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	23.48	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.62	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.34	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	23.56	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	24.63	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.32	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	23.68	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	22.57	PC2	PASS

DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	23.69	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.36	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	22.72	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	23.69	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	24.72	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.96	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.48	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	22.72	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	23.76	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	22.75	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	22.44	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	23.37	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	24.38	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.51	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.14	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	22.35	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	23.42	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.50	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.23	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	23.74	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	24.68	PC2	PASS
DC_71A_n78A-3	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.32	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.40	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	22.60	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	23.75	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.45	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.26	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	23.75	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	24.69	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.73	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.18	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	22.69	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	23.74	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	22.59	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	22.25	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	23.49	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	24.56	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.68	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.00	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	22.54	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	23.55	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.81	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.27	PC2	PASS

DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	23.42	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	24.53	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.28	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	23.19	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	22.48	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	23.53	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.37	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.37	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	23.58	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	24.60	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.54	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	22.97	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	22.58	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	23.60	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	22.48	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	22.04	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	23.41	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	24.42	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.74	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	23.08	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	22.34	PC2	PASS
DC_71A_n78A-3	30	5+70	DFT-16QAM	M+H	Inner_Full	23.37	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	22.94	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.14	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	23.35	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	24.54	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.25	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.10	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	22.52	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	23.46	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.23	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	21.92	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	23.48	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	24.52	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.39	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.85	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	22.51	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	23.57	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	22.19	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	21.91	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	23.39	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	24.46	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.43	PC2	PASS

DC_71A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	22.86	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	22.46	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	23.43	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.26	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	22.05	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	23.39	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	24.60	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.23	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	22.96	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	22.46	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	23.54	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.24	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	21.80	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	24.10	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	24.62	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.25	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	22.93	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	22.46	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	23.61	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	22.29	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	22.03	PC2	PASS
DC_71A_n78A-3	30	5+90	DFT-QPSK	M+H	Outer_Full	23.38	PC2	PASS

450-3550								
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	24.48	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.30	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.95	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	22.45	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	23.49	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.25	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	21.95	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	23.47	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	24.59	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.11	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	22.89	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	22.53	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	23.63	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.18	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	21.82	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	23.44	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	24.58	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.12	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.85	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	22.46	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	23.59	PC2	PASS

DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	21.85	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.84	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	23.44	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	24.59	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.07	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.78	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	22.47	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	23.60	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	21.97	PC2	PASS
DC_71A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	21.90	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.20	≤13	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.58	≤13	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	4.43	≤13	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.60	≤13	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.29	≤13	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.64	≤13	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.96	≤13	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.67	≤13	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.82	≤13	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.71	≤13	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.68	≤13	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.14	≤13	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.13	≤13	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.08	≤13	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.24	≤13	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.99	≤13	PASS

DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.15	≤13	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.10	≤13	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.86	≤13	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.07	≤13	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.90	≤13	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.35	≤13	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.70	≤13	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.13	≤13	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.00	≤13	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.08	≤13	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.26	≤13	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.07	≤13	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.40	≤13	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.02	≤13	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.35	≤13	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.17	≤13	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.21	≤13	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.13	≤13	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.99	≤13	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	100%	0.00	9.80	≤13	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.34	≤13	PASS
DC_71A_n78A-	30	5+60	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.24	≤13	PASS

3450-3550										
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.87	≤13	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.91	≤13	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.20	≤13	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.91	≤13	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.92	≤13	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.41	≤13	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.44	≤13	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.03	≤13	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.93	≤13	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.94	≤13	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.01	≤13	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.91	≤13	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.05	≤13	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.45	≤13	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.97	≤13	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.31	≤13	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	100%	0.00	0.00	≤13	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.34	≤13	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.10	≤13	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.04	≤13	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.81	≤13	PASS

DC_71A_n78A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.89	≤13	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.17	≤13	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.83	≤13	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.31	≤13	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.11	≤13	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.02	≤13	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.15	≤13	PASS

Test Graphs

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID3_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+M_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+M_TID3_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+H_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+H_TID3_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+L_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+L_TID3_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+M_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+M_TID3_#1

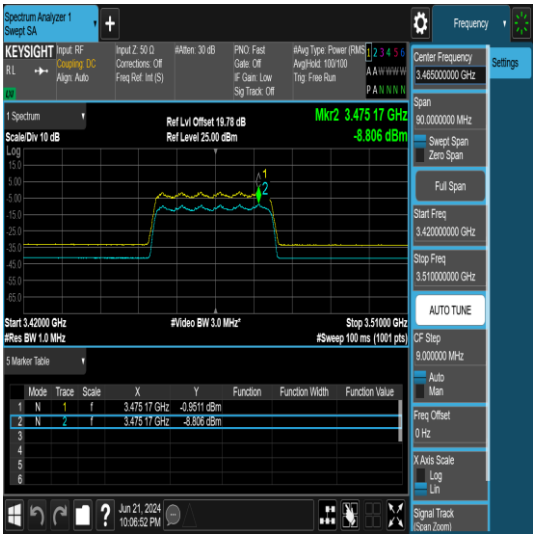
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+H_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+H_TID3_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+L_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+L_TID3_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+M_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+M_TID3_#1

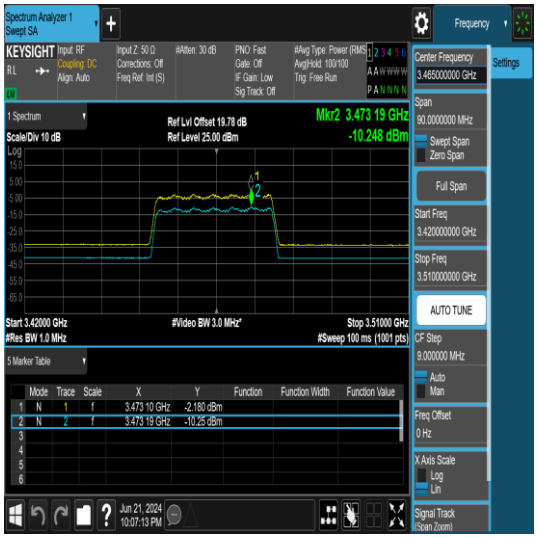
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+H_TID2_#1

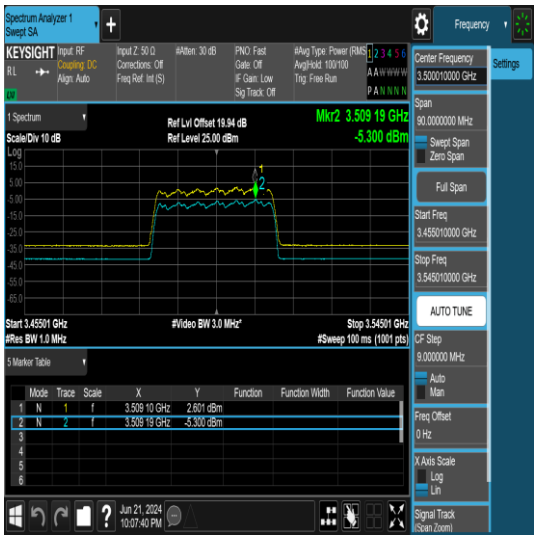
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+H_TID3_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+L_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+L_TID3_#1





NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+M_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+30_M+M_TID3_#1
	
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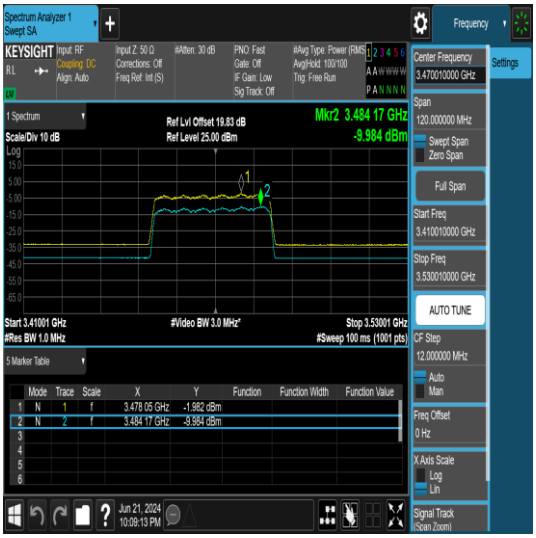
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+L_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+L_TID3_#1



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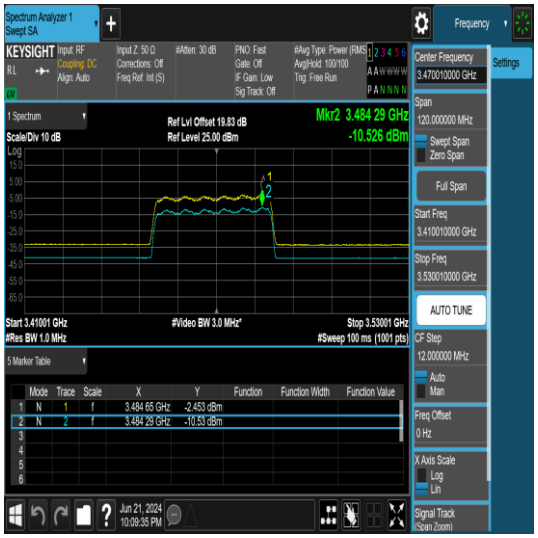

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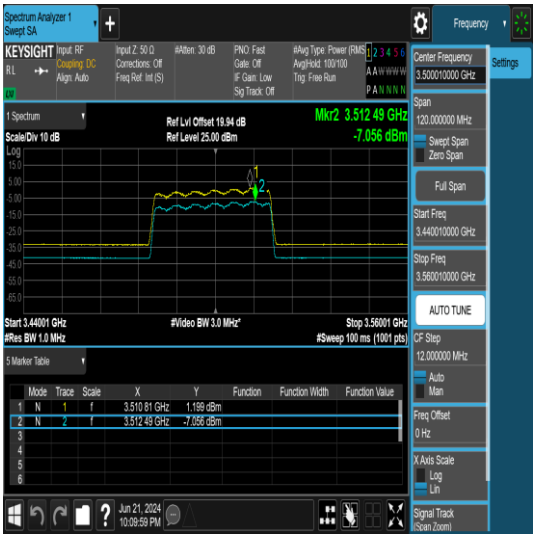

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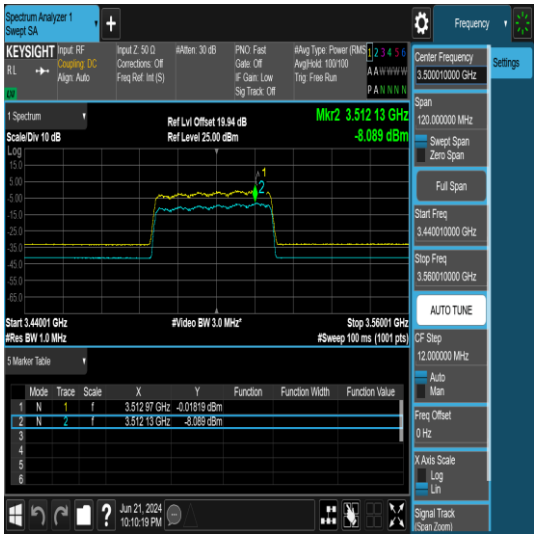
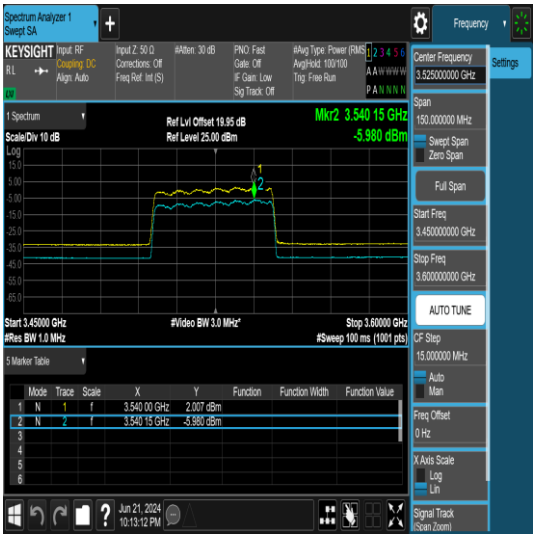


NTNV_DC_71A_n78A-3450-3550_PC2_30_5+40_M+H_TID3_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50_M+L_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50_M+L_TID3_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50_M+M_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50_M+M_TID3_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50_M+H_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+50_M+H_TID3_#1



NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+L_TID2_#1

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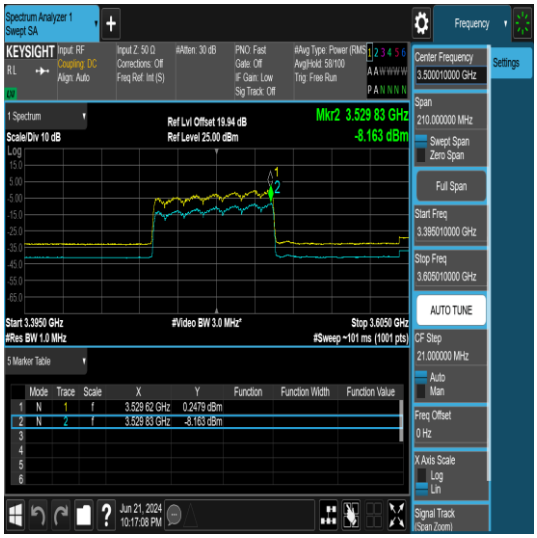
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+M_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+M_TID3_#1

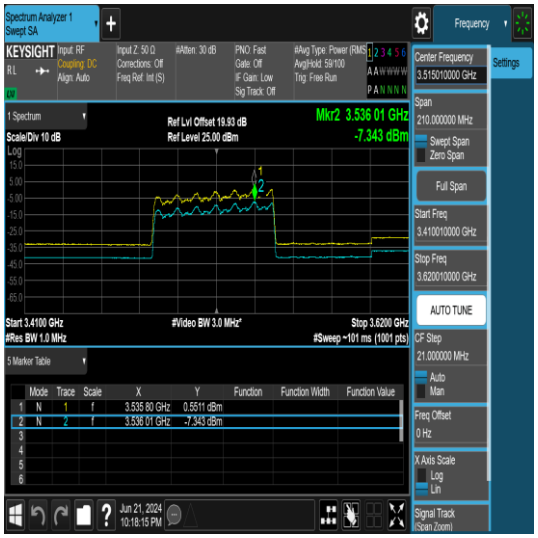
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+H_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+H_TID3_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+70_M+L_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+70_M+L_TID3_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+70_M+M_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+70_M+M_TID3_#1



NTNV_DC_71A_n78A-3450-3550_PC2_30_5+70_M+H_TID2_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+70_M+H_TID3_#1

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+80_M+L_TID2_#1



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NTNV_DC_71A_n78A-3450-3550_PC2_30_5+80_M+H_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+80_M+H_TID3_#1
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+90_M+L_TID2_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+90_M+L_TID3_#1