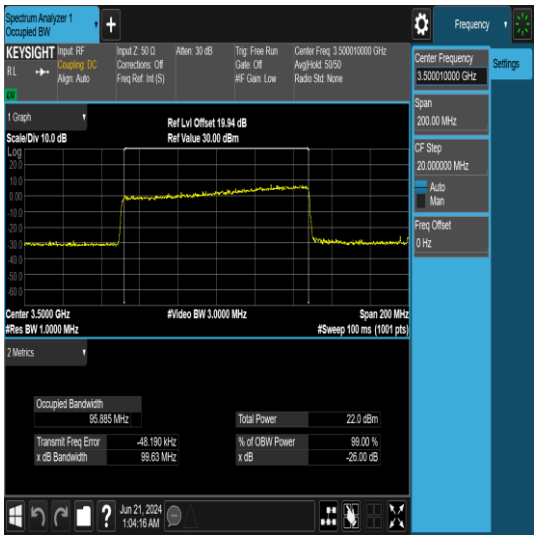
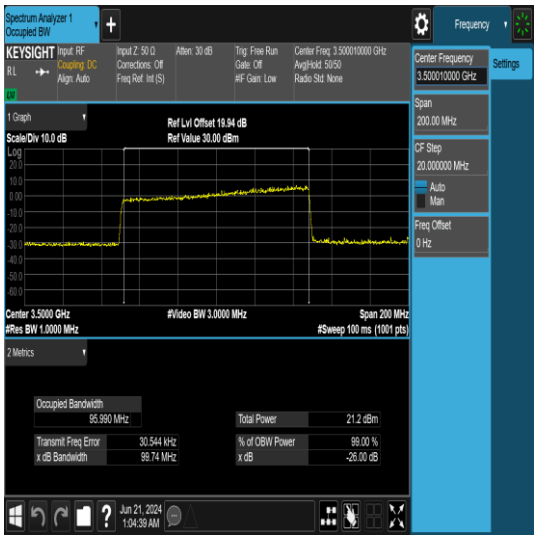
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+100_M+L_TID1_Ant1#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+100_M+L_TID3_Ant1#1
	
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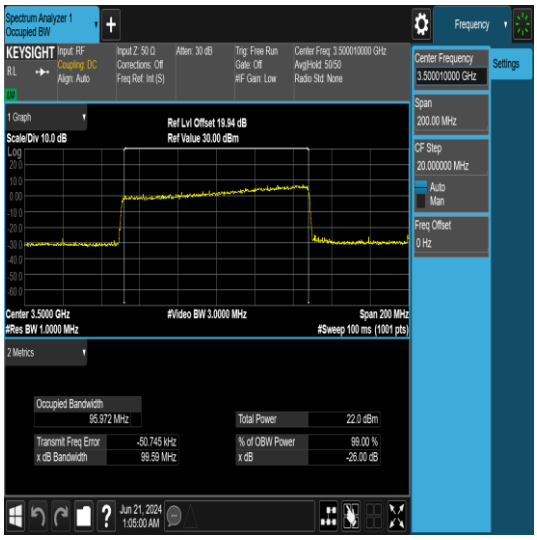
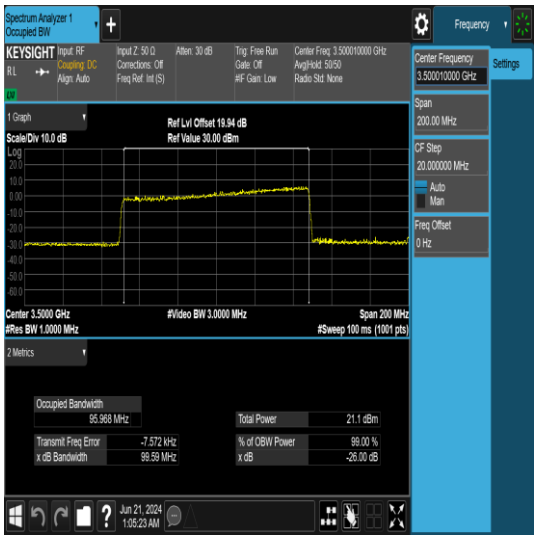
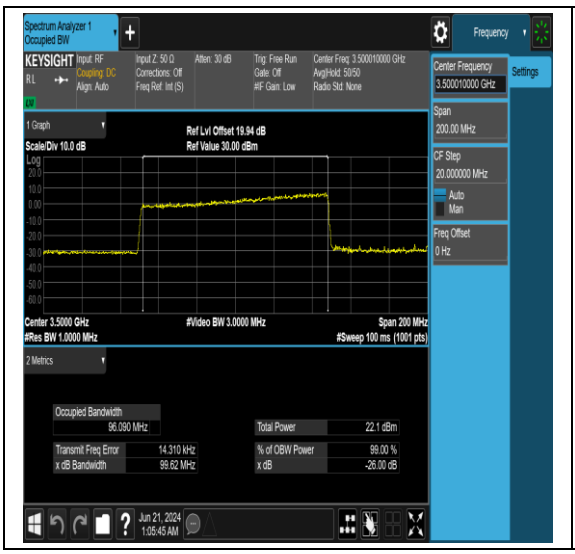
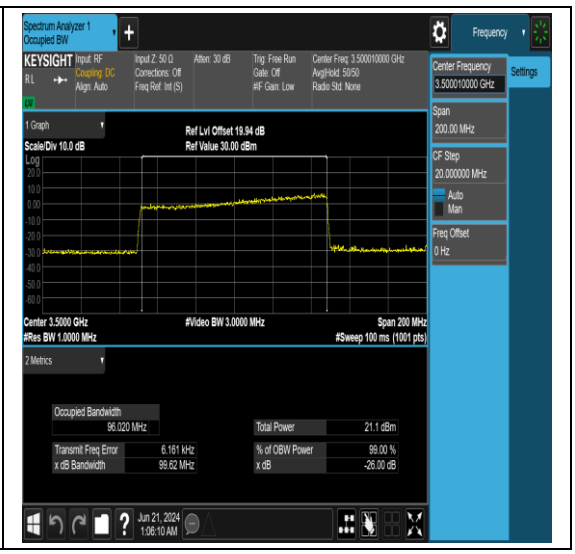
	
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+100_M+H_TID1_Ant1#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+100_M+H_TID3_Ant1#1


Spectrum Analyzer 1
Occupied BW
Frequency
KEYSIGHT
Input: RF
Coupling: DC
Align: Auto
Input Z: 50 Ω
Corrections: Off
Freq Ref: Int (S)
Atten: 30 dB
Trig: Free Run
Gate: Off
#IF Gain: Low
Center Freq: 3.500010000 GHz
AvgHold: 50/50
Radio Std: None
Center Frequency
3.500010000 GHz
Settings
Span
200.00 MHz
CF Step
20.000000 MHz
Auto
Man
Freq Offset
0 Hz
1 Graph
Ref Lvl Offset 19.94 dB
Ref Value 30.00 dBm
Scale/Div 10.0 dB
Log
Center 3.5000 GHz
#Video BW 3.0000 MHz
Span 200 MHz
#Res BW 1.0000 MHz
#Sweep 100 ms (1001 pts)
2 Metrics
Occupied Bandwidth
96.090 MHz
Total Power
22.1 dBm
Transmit Freq Error
14.310 kHz
% of OBW Power
99.00 %
x dB Bandwidth
99.62 MHz
x dB
-26.00 dB
Jun 21, 2024
1:05:45 AM


Spectrum Analyzer 1
Occupied BW
Frequency
KEYSIGHT
Input: RF
Coupling: DC
Align: Auto
Input Z: 50 Ω
Corrections: Off
Freq Ref: Int (S)
Atten: 30 dB
Trig: Free Run
Gate: Off
#IF Gain: Low
Center Freq: 3.500010000 GHz
AvgHold: 50/50
Radio Std: None
Center Frequency
3.500010000 GHz
Settings
Span
200.00 MHz
CF Step
20.000000 MHz
Auto
Man
Freq Offset
0 Hz
1 Graph
Ref Lvl Offset 19.94 dB
Ref Value 30.00 dBm
Scale/Div 10.0 dB
Log
Center 3.5000 GHz
#Video BW 3.0000 MHz
Span 200 MHz
#Res BW 1.0000 MHz
#Sweep 100 ms (1001 pts)
2 Metrics
Occupied Bandwidth
96.020 MHz
Total Power
21.1 dBm
Transmit Freq Error
6.161 kHz
% of OBW Power
99.00 %
x dB Bandwidth
99.62 MHz
x dB
-26.00 dB
Jun 21, 2024
1:06:10 AM

Appendix D: Band Edge for NSA

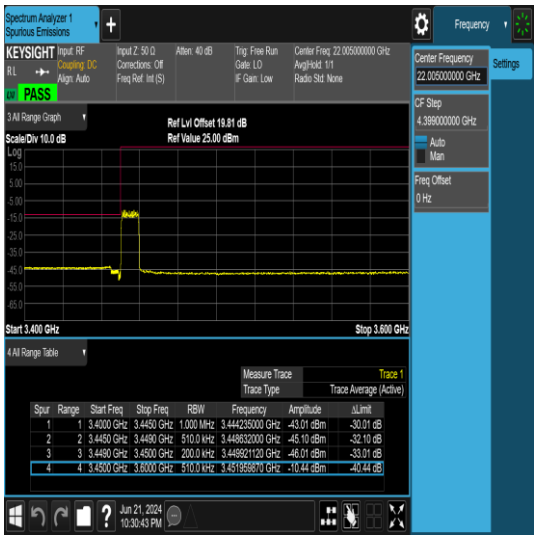
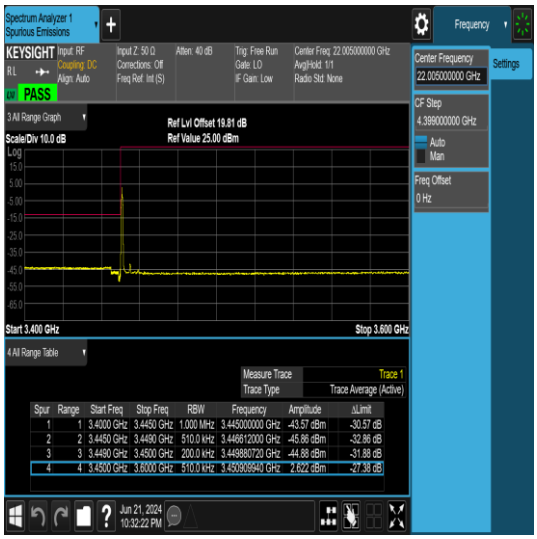
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	PASS

DC_71A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n78A-3450-3	30	5+100	DFT-QPSK	M+L	Outer_Full	PASS

550						
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	PASS
DC_71A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID9__#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID7__#1
	
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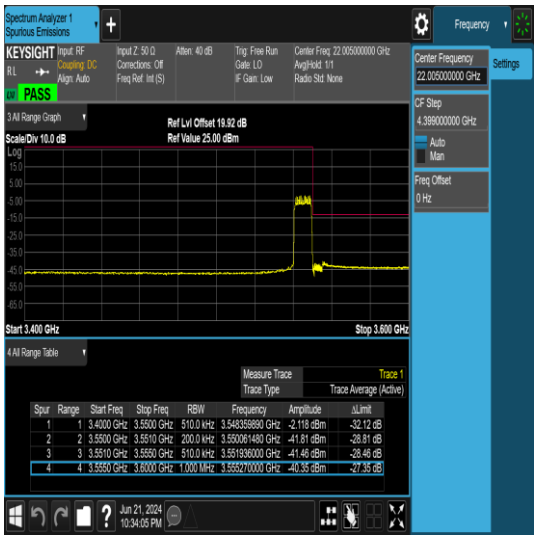
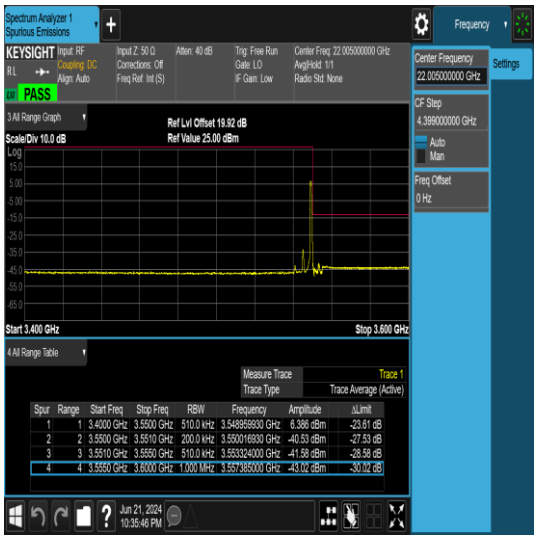
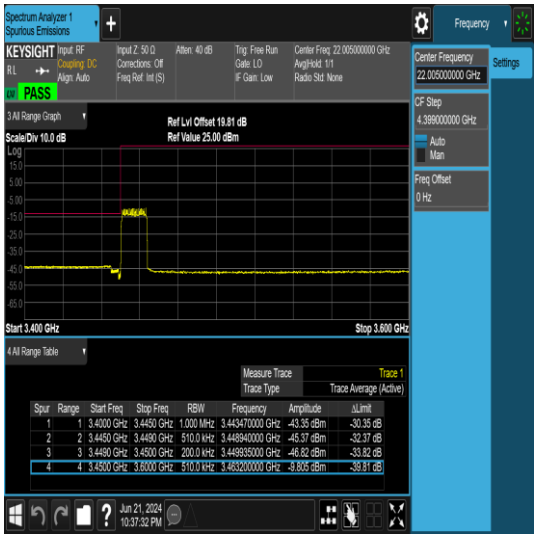
	
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+L_TID9__#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+15_M+L_TID7__#1



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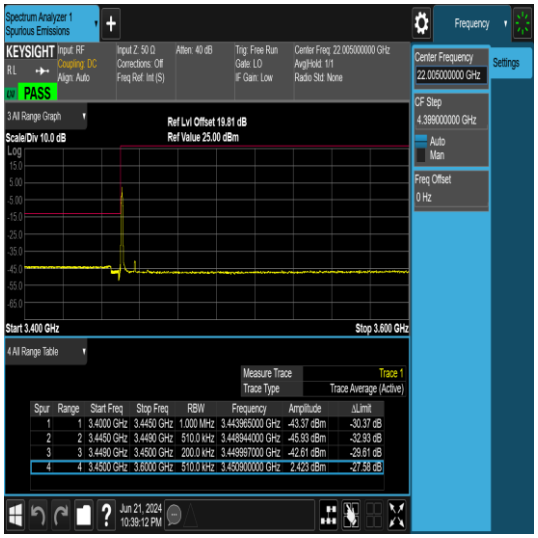

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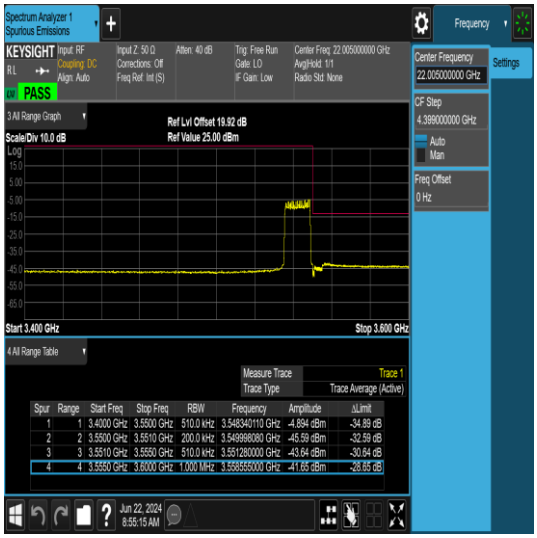

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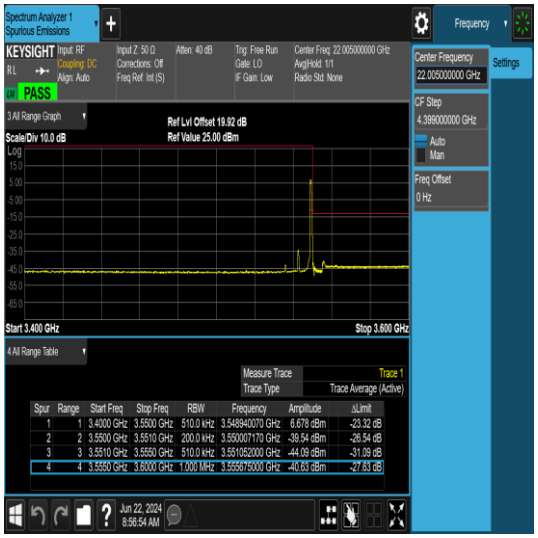

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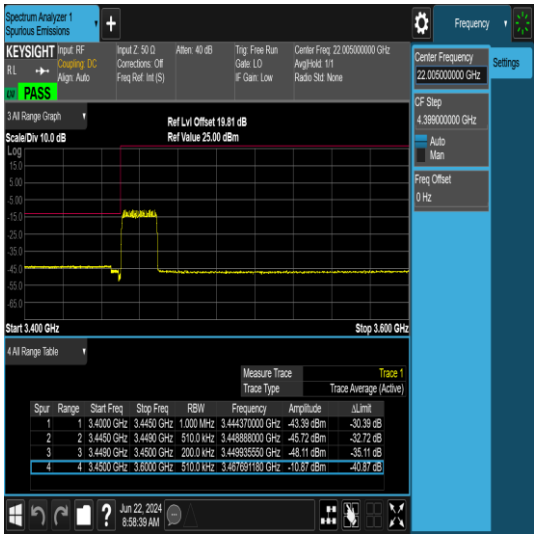
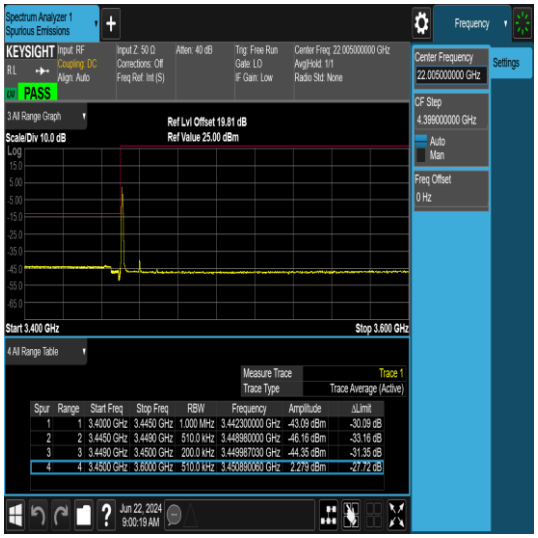
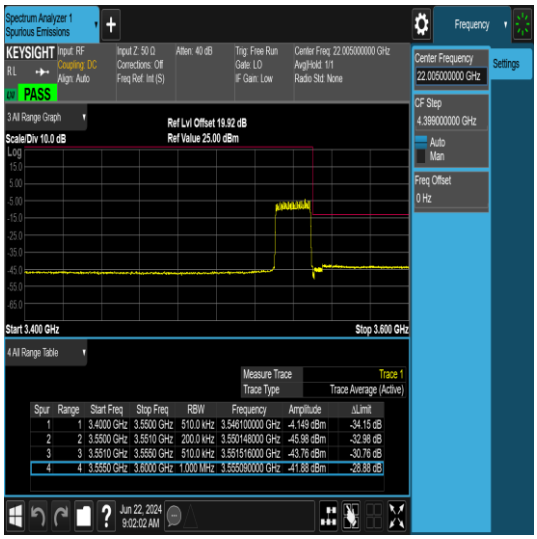
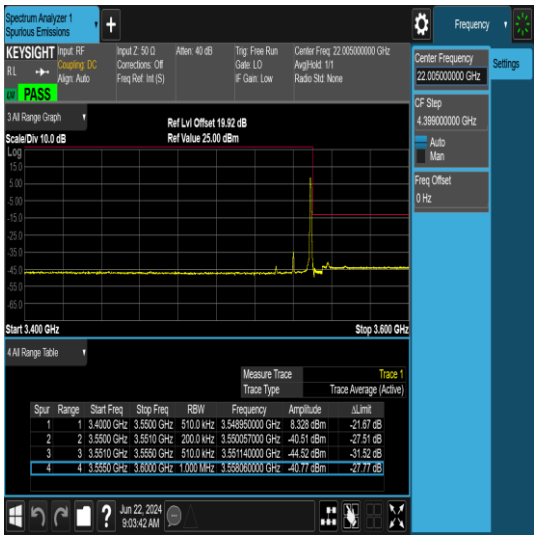




NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+H_TID9__#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+20_M+H_TID8__#1
	
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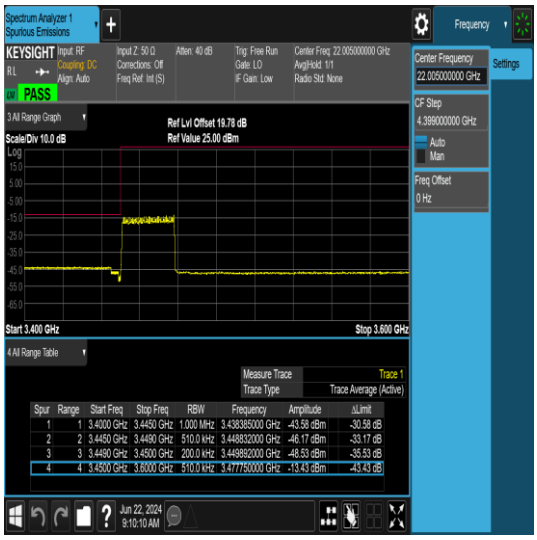	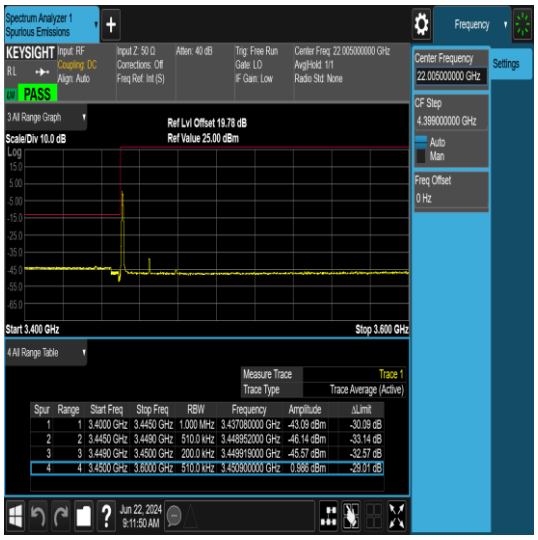
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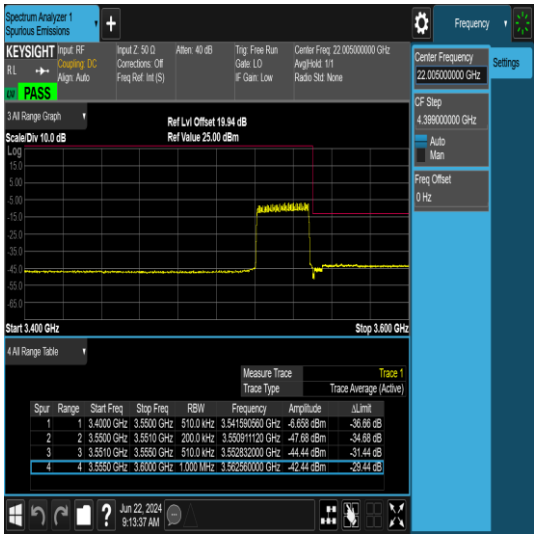

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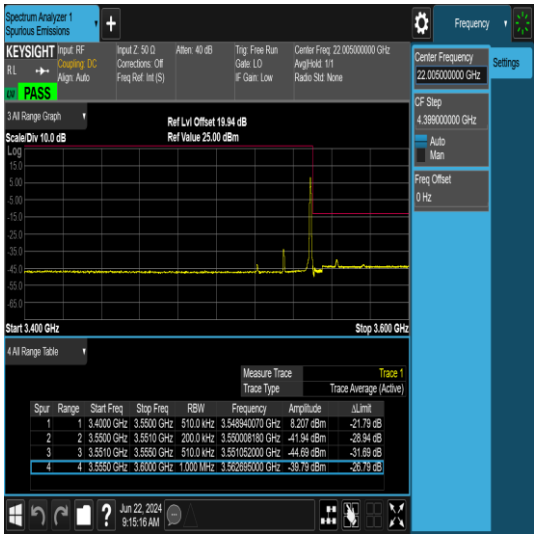

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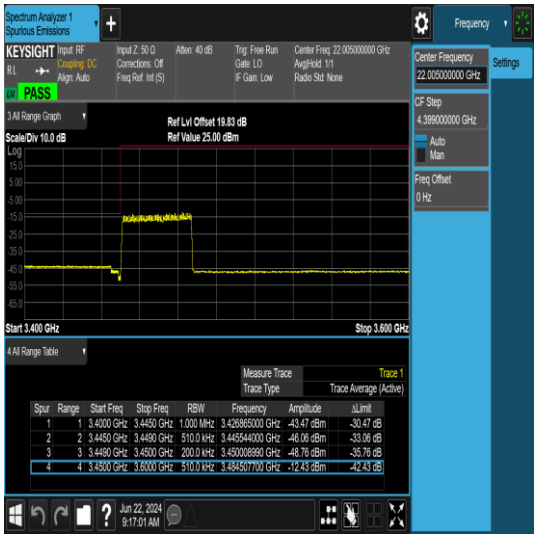

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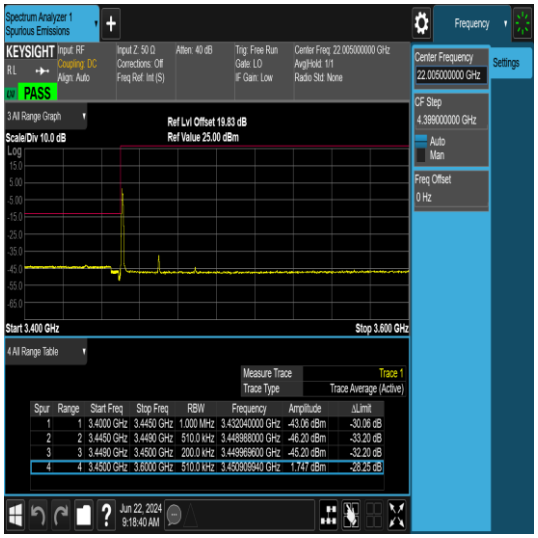

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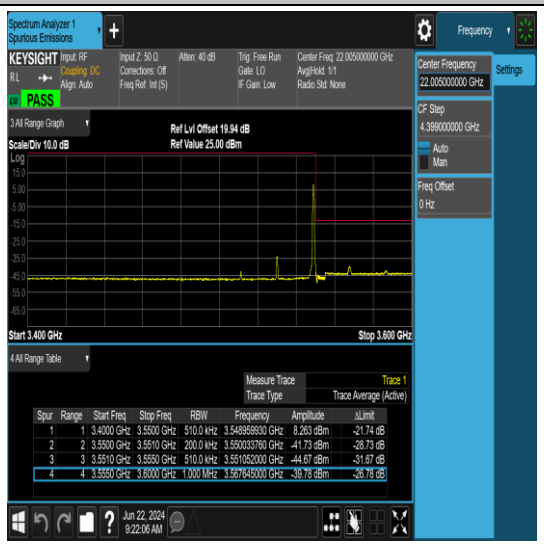

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	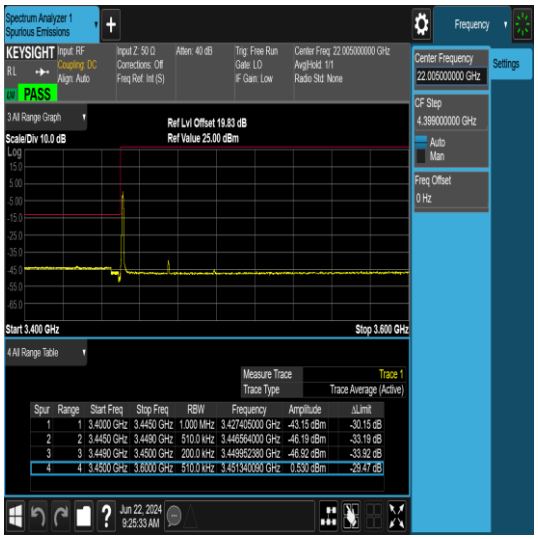
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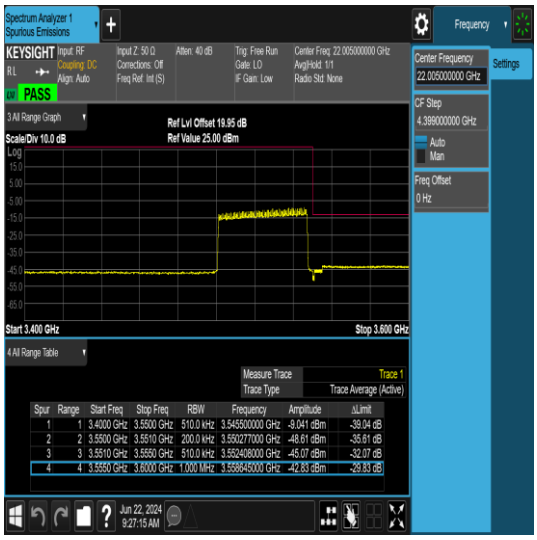
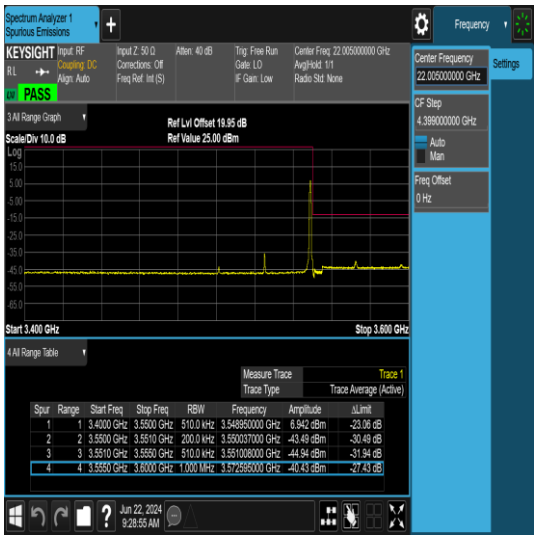
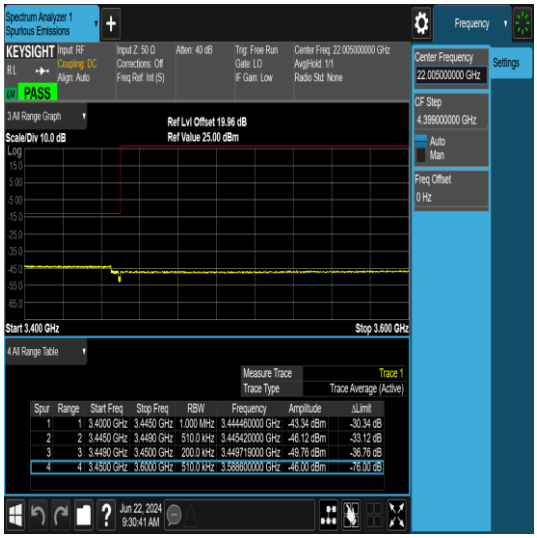
	
NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+L_TID9__#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+L_TID7__#1



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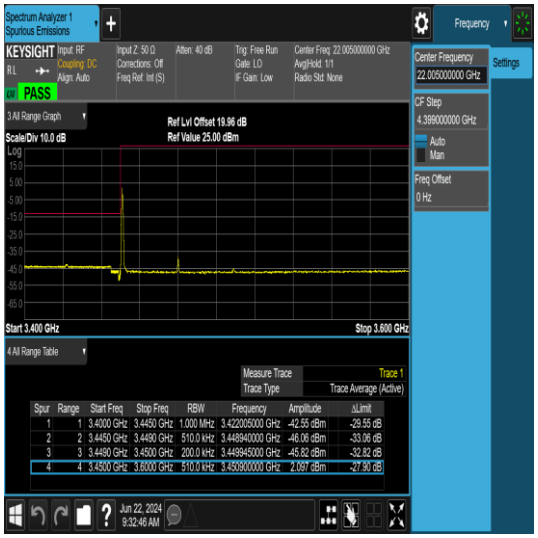
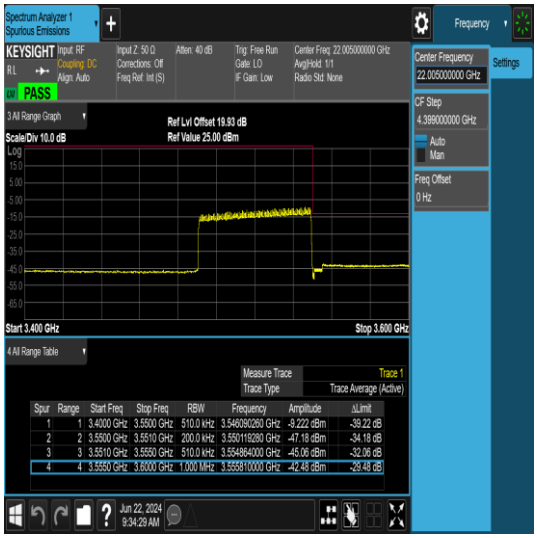


NTNV_DC_71A_n78A-3450-3550_PC2_30_5+60_M+H_TID8__#1



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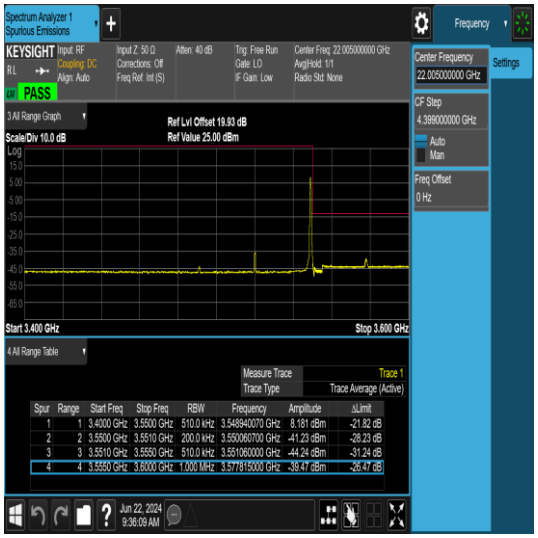

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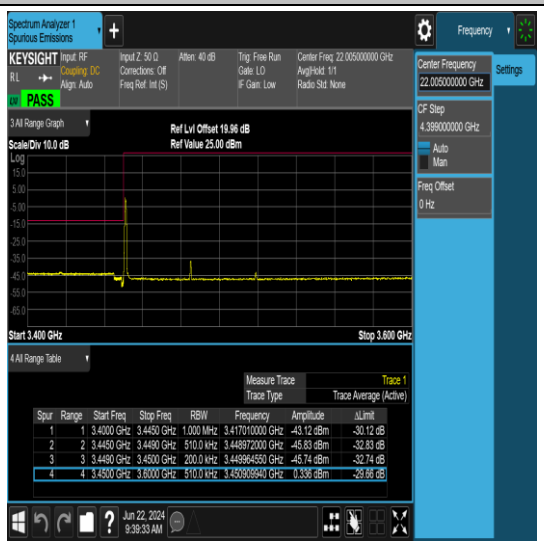

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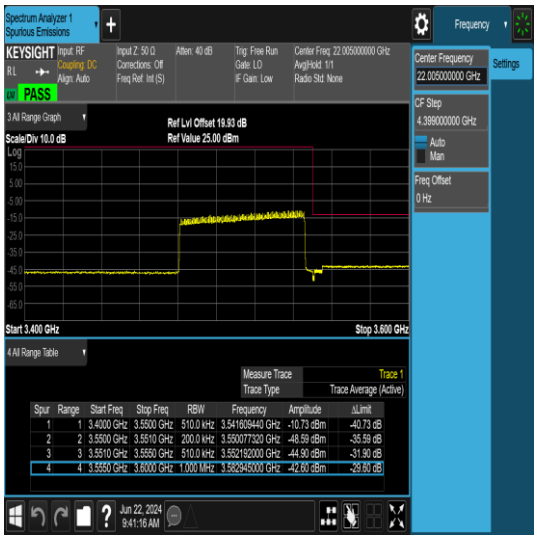	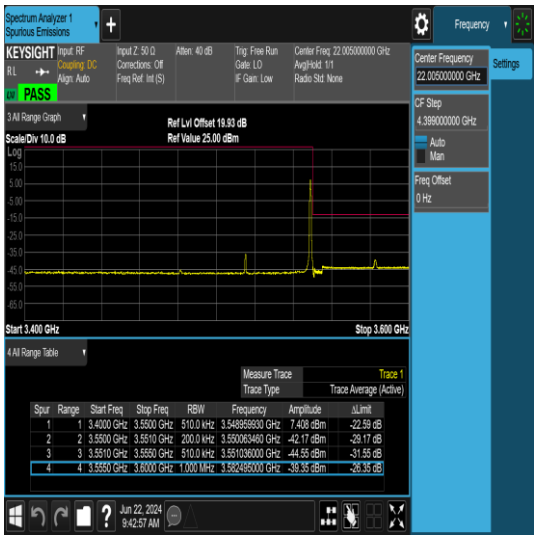
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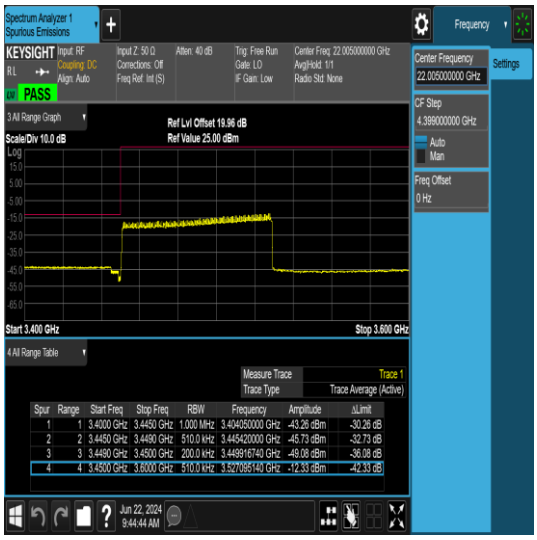	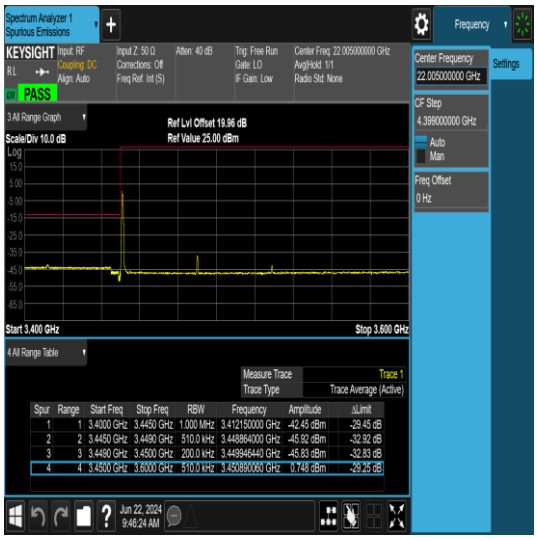
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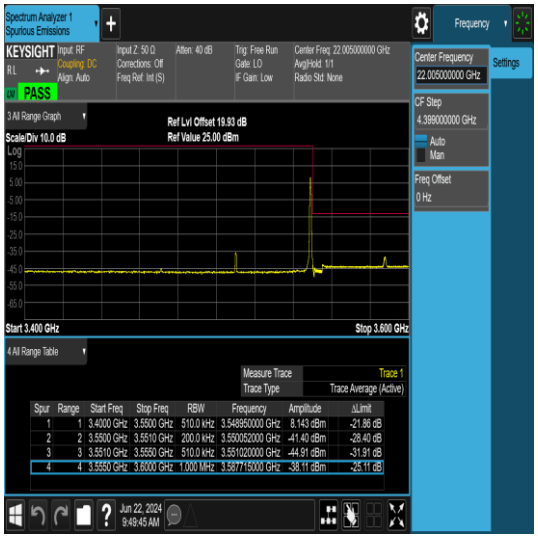

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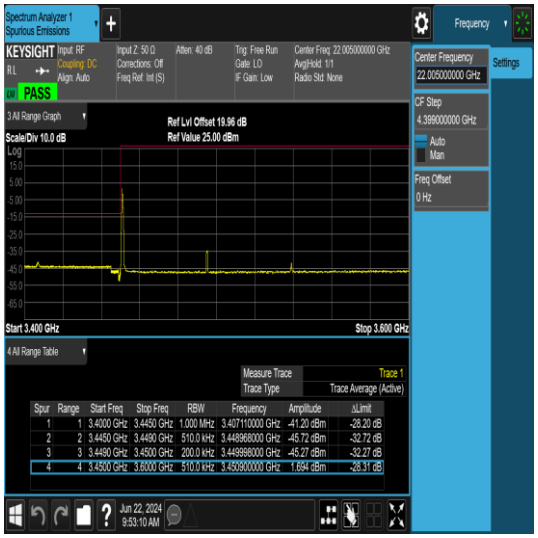

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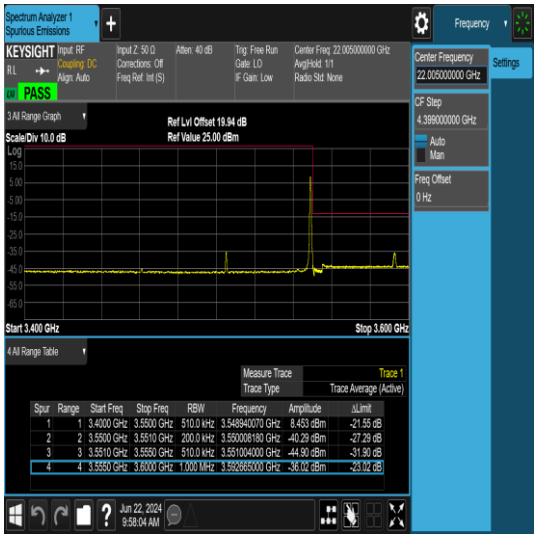

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	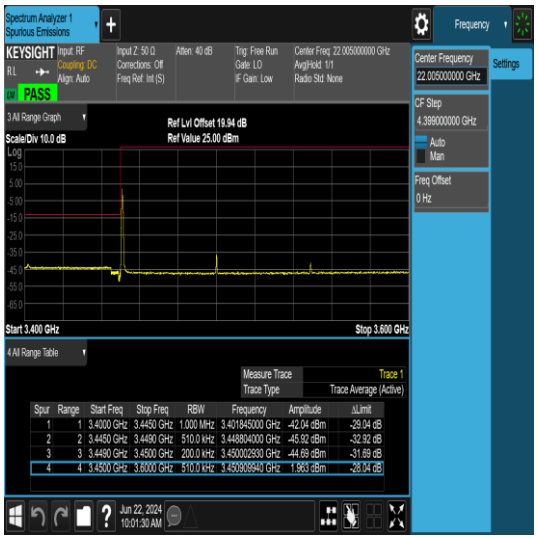
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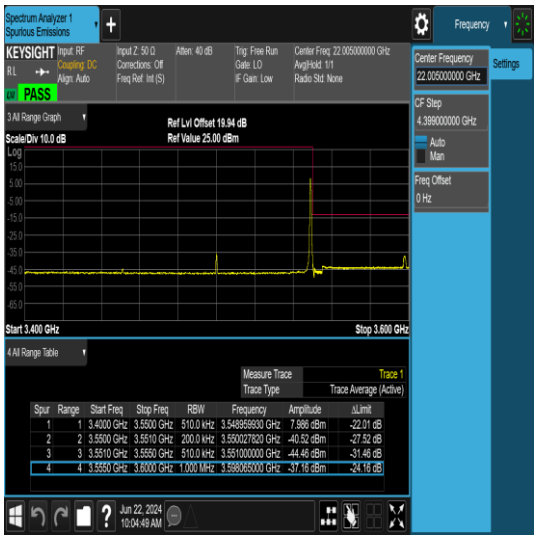
	

Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.13	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.82	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-54.76	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.28	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.07	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.78	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.56	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.48	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.01	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.20	-13	PASS

DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.71	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-73.00	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-54.81	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	3000	6000	-33.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.79	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.00	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.18	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.75	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.02	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.42	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.83	-13	PASS
DC_71A_n78A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-33.13	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.08	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.00	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-55.10	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.10	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.13	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.61	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.11	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.62	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.01	-13	PASS

DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-37.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.57	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.75	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-54.85	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	3000	6000	-33.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.62	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.45	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-55.08	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.79	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.21	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.83	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.96	-13	PASS
DC_71A_n78A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.23	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-37.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.87	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.23	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.34	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.96	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-37.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.33	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.58	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.96	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.76	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.31	-13	PASS

DC_71A_n78A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.97	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.01	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-54.98	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.37	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.64	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.51	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.73	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.25	-43	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.73	-33	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.79	-23	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.27	-13	PASS
DC_71A_n78A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-37.01	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.66	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.95	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.69	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-37.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.49	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.91	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.47	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-37.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.65	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.19	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-52.99	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-37.05	-13	PASS

DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.51	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.12	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-54.54	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	3000	6000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.15	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.83	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-55.13	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.70	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.71	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.96	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.34	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-37.02	-13	PASS
DC_71A_n78A	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.39	-43	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.52	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-54.19	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-37.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.19	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.12	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.44	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.36	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-37.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.62	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.10	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.84	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.69	-43	PASS

DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.60	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-54.64	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.39	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.34	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.48	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.53	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.16	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.73	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-37.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.05	-43	PASS
DC_71A_n78A	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.93	-33	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-37.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.17	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.06	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.91	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.41	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.87	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.17	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-28.30	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.34	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.91	-33	PASS

DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-54.81	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	3000	6000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	26000	40000	-37.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.73	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.18	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.84	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-39.60	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.36	-43	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.79	-33	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.77	-23	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.67	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.74	-33	PASS
DC_71A_n78A	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-54.85	-23	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.96	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.28	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.85	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.66	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.93	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.40	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.81	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.69	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.96	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-54.52	-23	PASS

DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	3000	6000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	26000	40000	-37.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.57	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.07	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.73	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.70	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.94	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.04	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.62	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-37.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.16	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.90	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-54.68	-23	PASS
DC_71A_n78A	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.27	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.95	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.84	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.77	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.31	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.87	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.02	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.99	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.81	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-55.04	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.11	-13	PASS

DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.66	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.01	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.78	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.96	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.91	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-53.12	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.03	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.56	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.20	-13	PASS
DC_71A_n78A	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.04	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.02	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.04	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.95	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.15	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.91	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.01	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.78	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	1000	3000	-39.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.85	-13	PASS

DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.23	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.92	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-37.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.14	-43	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.51	-33	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.35	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.40	-13	PASS
DC_71A_n78A -3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.03	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.22	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.96	-13	PASS
DC_71A_n78A	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.23	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.69	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.20	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.61	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.69	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.41	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.98	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.96	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.91	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	30	1000	-54.96	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	1000	3000	-38.74	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.04	-13	PASS

DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	26000	40000	-37.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.30	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.25	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.81	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.56	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.92	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.74	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.89	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-54.63	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.13	-13	PASS
DC_71A_n78A	30	5+30	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.86	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.68	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.48	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.08	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.88	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.37	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.58	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.48	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.35	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.33	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.13	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	30	1000	-54.74	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.95	-13	PASS

DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.01	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.75	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.10	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.04	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.52	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.26	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.98	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-54.97	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.44	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.93	-13	PASS
DC_71A_n78A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.19	-43	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.34	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-37.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.15	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.70	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.48	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.39	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.99	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.68	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	30	1000	-55.05	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	3000	6000	-33.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.43	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	26000	40000	-37.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.30	-43	PASS

DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.39	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.71	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-33.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-37.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.00	-43	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.16	-33	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.61	-23	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.65	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.34	-13	PASS
DC_71A_n78A -3450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.03	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.03	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-54.81	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.21	-43	PASS
DC_71A_n78A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.70	-33	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.38	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.41	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.13	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.50	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-37.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.69	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.07	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	30	1000	-55.09	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	3000	6000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	26000	40000	-37.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.36	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.86	-33	PASS

DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.84	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-39.54	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-37.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.72	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.84	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-39.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.30	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.43	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-54.96	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.33	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.69	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.57	-33	PASS
DC_71A_n78A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.82	-23	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.32	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.39	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.03	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.74	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.35	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-37.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.10	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.56	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	30	1000	-55.02	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	3000	6000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	26000	40000	-37.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-63.30	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.06	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.60	-23	PASS

DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-63.52	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.86	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.53	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-39.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.60	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.19	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-55.02	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.38	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-37.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.67	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.28	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.18	-23	PASS
DC_71A_n78A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.22	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.34	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.76	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.97	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.44	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.04	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.10	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	0.15	30	-73.31	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	30	1000	-54.60	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	26000	40000	-37.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.89	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.91	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-55.06	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-39.40	-13	PASS

DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.34	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-63.81	-43	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.40	-33	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-55.05	-23	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.35	-13	PASS
DC_71A_n78A -3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-37.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.23	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.56	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-54.68	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.48	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-37.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.13	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.62	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.07	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.16	-13	PASS
DC_71A_n78A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.13	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.41	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-37.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.61	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.66	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.07	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-37.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.38	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.91	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	30	1000	-54.97	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.04	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.06	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.85	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-33.16	-13	PASS

DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-37.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.19	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.50	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.00	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.42	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.83	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.20	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.30	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.44	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.90	-13	PASS
DC_71A_n78A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.27	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-37.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.51	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.64	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.84	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-37.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.16	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.38	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	30	1000	-54.05	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	1000	3000	-39.37	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	3000	6000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.49	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.52	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.99	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.15	-13	PASS

DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-37.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.39	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.70	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.95	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-39.42	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.32	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.49	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.48	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-53.58	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.35	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-37.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.69	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.21	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.74	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.25	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.14	-13	PASS
DC_71A_n78A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-37.02	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.66	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.55	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.01	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.57	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.35	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-37.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.95	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.89	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	30	1000	-54.55	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	26000	40000	-37.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-66.17	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.04	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.76	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-39.45	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-37.02	-13	PASS

DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.70	-43	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-73.99	-33	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.11	-23	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.97	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.29	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-54.77	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-37.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.38	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.32	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.35	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-36.45	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-32.36	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.68	-13	PASS
DC_71A_n78A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-61.93	-43	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.21	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.03	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.52	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.41	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-32.61	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-63.47	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	0.15	30	-71.70	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	30	1000	-53.61	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	1000	3000	-39.53	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	6000	26000	-32.75	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.47	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.96	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-74.14	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.67	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.76	-43	PASS

DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.67	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.73	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.65	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.95	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-54.97	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.34	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.01	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.91	-43	PASS
DC_71A_n78A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.67	-33	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.04	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.39	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.84	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.99	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.81	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.92	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.46	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.44	-33	PASS

DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.77	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-35.56	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.98	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.59	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-37.39	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.22	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.22	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.94	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.53	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.61	-33	PASS
DC_71A_n78A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.85	-23	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-37.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.28	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	0.15	30	-74.54	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.78	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.67	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.32	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.08	-43	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.50	-33	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.87	-23	PASS

DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-38.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.54	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.45	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	3000	-38.74	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.82	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.11	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-36.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.25	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.92	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.74	-23	PASS
DC_71A_n78A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.40	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.93	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.86	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	30	1000	-54.86	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-65.08	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.19	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.59	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.75	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.48	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.96	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.40	-13	PASS

DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.84	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.76	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-54.95	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	3000	-35.61	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.21	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.19	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.73	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.33	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.07	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.59	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.13	-13	PASS
DC_71A_n78A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.02	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.72	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.11	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.32	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	1000	3000	-39.45	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.44	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.41	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.26	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.42	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.33	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.53	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.95	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-38.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.96	-13	PASS

DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.79	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.88	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.87	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-54.99	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	26000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.50	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.23	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.88	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.95	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.85	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.70	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.07	-13	PASS
DC_71A_n78A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.19	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.11	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.20	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	30	1000	-54.91	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.28	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.75	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-37.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.75	-43	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.15	-33	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.82	-23	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-39.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.08	-13	PASS

DC_71A_n78A -3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-37.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.78	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.80	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-54.84	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.23	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.73	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.77	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.88	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.77	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.53	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.35	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-27.03	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.36	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.30	-13	PASS
DC_71A_n78A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.98	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-64.46	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	0.15	30	-72.17	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	30	1000	-55.07	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.57	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-76.13	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.61	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.63	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-74.64	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.84	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-38.03	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.90	-13	PASS

DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.20	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.70	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-54.86	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.68	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.12	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.11	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.54	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.66	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.61	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.96	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.27	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.99	-13	PASS
DC_71A_n78A	30	5+80	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.38	-43	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.21	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	30	1000	-55.05	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-64.15	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.72	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.97	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.35	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.12	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.85	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.46	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.83	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.02	-43	PASS

DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.11	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-54.80	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-37.54	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.84	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.70	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.43	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.77	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.58	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.71	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.06	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.69	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-64.16	-43	PASS
DC_71A_n78A	30	5+80	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.49	-33	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	30	1000	-54.88	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.31	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.46	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.86	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.42	-43	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.01	-33	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.79	-23	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-37.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.51	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.42	-33	PASS

DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-54.71	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-38.60	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.72	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.47	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.07	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.84	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.76	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.83	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.96	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.28	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.81	-33	PASS
DC_71A_n78A	30	5+90	DFT-16QAM	M+L	Outer_Full	30	1000	-54.28	-23	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.34	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.33	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.85	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.20	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-64.65	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.82	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.24	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.31	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.82	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.26	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.98	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-54.74	-23	PASS

DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.12	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.19	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.11	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.58	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.86	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.82	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.88	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.79	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.18	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.67	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.30	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	0.15	30	-73.46	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	30	1000	-54.95	-23	PASS
DC_71A_n78A	30	5+90	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.12	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	3000	6000	-33.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.13	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-74.08	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-38.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-33.07	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.30	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.67	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-63.71	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.47	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.43	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.80	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.34	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.46	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.08	-13	PASS

DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.78	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.25	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.54	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.73	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-37.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.29	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.23	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.71	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.09	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.05	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.78	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.73	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	30	1000	-54.83	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.18	-13	PASS
DC_71A_n78A	30	5+90	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.85	-13	PASS

-3450-3550										
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.76	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.18	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.81	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-65.33	-43	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-74.55	-33	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.87	-23	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.68	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.29	-43	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.95	-33	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	30	1000	-55.09	-23	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	1000	3000	-37.45	-13	PASS
DC_71A_n78A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.91	-13	PASS

DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-33.28	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-36.79	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.42	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.32	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.73	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.64	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.87	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.34	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-36.99	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.81	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.73	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.99	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.01	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-33.14	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-36.96	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-65.03	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	0.15	30	-74.57	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	30	1000	-54.79	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	3000	6000	-32.90	-13	PASS
DC_71A_n78A	30	5+10	DFT-16QAM	M+L	Outer_Full	6000	26000	-33.05	-13	PASS

-3450-3550		0								
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Outer_Full	26000	40000	-36.85	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-64.80	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.33	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-55.05	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-40.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	3000	6000	-32.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-33.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-36.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-65.13	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.62	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.86	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.42	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	3000	6000	-32.95	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-33.22	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-37.00	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.91	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.05	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-54.82	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-38.06	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.90	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-33.29	-13	PASS

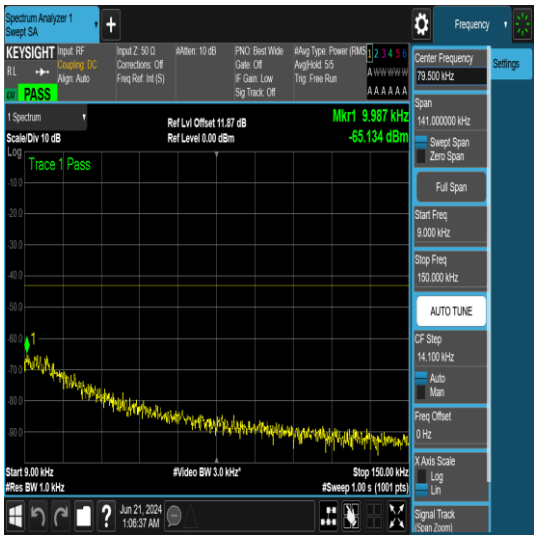
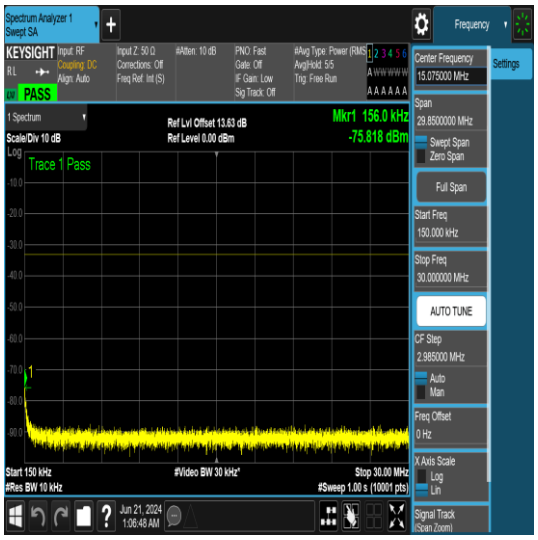
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-36.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.86	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.28	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.90	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.11	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.04	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-36.86	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.87	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.93	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.00	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.45	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-33.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-64.15	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.15	30	-74.41	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	30	1000	-54.81	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	1000	3000	-40.17	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Outer_Full	6000	26000	-33.16	-13	PASS
DC_71A_n78A	30	5+10	DFT-16QAM	M+M	Outer_Full	26000	40000	-36.80	-13	PASS

-3450-3550		0								
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-65.27	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.27	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.64	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	3000	6000	-32.89	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-33.24	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-36.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-64.85	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-74.76	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.93	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-40.16	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	3000	6000	-32.81	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-33.29	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-36.88	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.77	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.98	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-54.91	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.97	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-36.86	-13	PASS

DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.80	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.79	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.89	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.19	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.02	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.26	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-36.98	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.82	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.28	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.48	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.10	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-36.81	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.22	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.15	30	-76.09	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	30	1000	-54.62	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	1000	3000	-40.12	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	3000	6000	-32.92	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	6000	26000	-33.13	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Outer_Full	26000	40000	-36.85	-13	PASS
DC_71A_n78A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-64.81	-43	PASS

-3450-3550		0								
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.06	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.92	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.52	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	3000	6000	-32.93	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-33.21	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-36.91	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-64.25	-43	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-76.12	-33	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.91	-23	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-40.15	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	3000	6000	-32.94	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-33.08	-13	PASS
DC_71A_n78A -3450-3550	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-36.83	-13	PASS

Test Graphs

NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID6_NS_01_0.009_0.15_#1	NTNV_DC_71A_n78A-3450-3550_PC2_30_5+10_M+L_TID6_NS_01_0.15_30_#1
	
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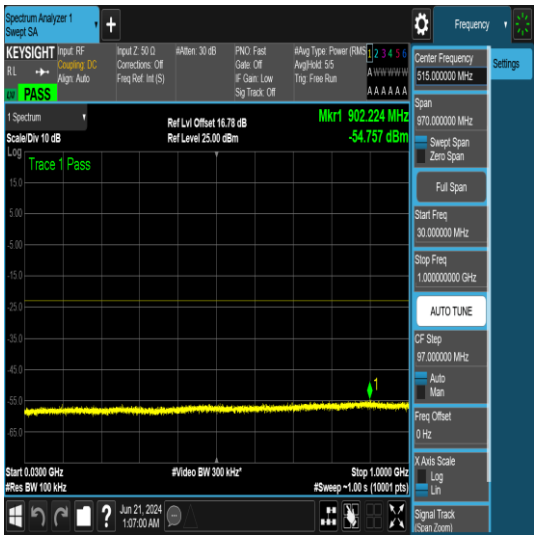	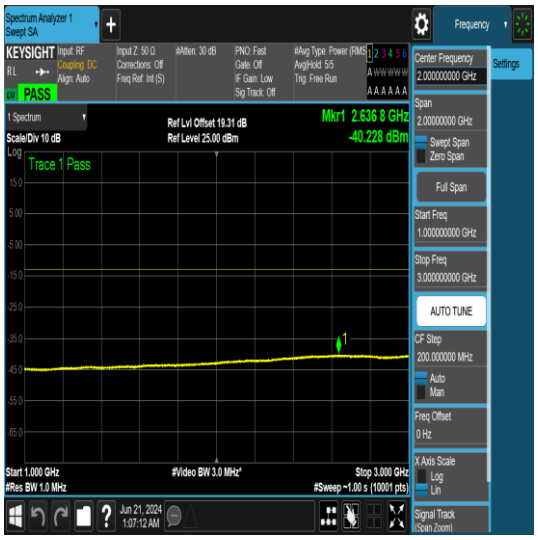
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