Appendix for 71A_N78A(3700-3800MHz)

Product Name: 5G Window CPE
Model No: TITAN 5100

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm	Power Class	Verdict
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	23.78	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	24.71	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.46	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.51	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	22.84	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	23.81	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.68	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.91	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	24.60	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	25.64	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	24.53	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	24.53	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	23.74	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	24.64	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	23.58	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	23.21	PC2	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	24.03	PC2	PASS
DC_71A_n78A-3	30	5+10	DFT-QPSK	M+H	Inner_Full	24.97	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	24.07	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	23.96	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	22.98	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	24.08	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	22.85	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	22.81	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	23.89	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	24.88	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.89	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	23.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	22.91	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	23.86	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.62	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.50	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	24.64	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	25.67	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	24.91	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	24.91	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	23.70	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	24.74	PC2	PASS
DC_71A_n78A-3	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	23.98	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	23.69	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	24.21	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	25.11	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	24.34	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	23.29	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	24.17	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	23.46	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	23.32	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	23.88	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	24.88	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.67	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.80	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	22.82	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	23.87	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.76	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.76	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	24.68	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	25.72	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.41	PC2	PASS
DC_71A_n78A-3	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	24.62	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	23.53	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	24.72	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.72	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	23.50	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	24.20	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	25.20	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	24.09	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.87	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	23.04	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	24.12	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	23.39	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	23.39	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	24.00	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+L	Inner_Full	24.91	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	24.38	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	22.96	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+L	Inner_Full	23.89	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	23.25	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	23.24	PC2	PASS
DC_71A_n78A-3	30	5+30	DFT-QPSK	M+M	Outer_Full	24.71	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+M	Inner_Full	25.74	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	24.67	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	24.70	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	23.80	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+M	Inner_Full	24.71	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	23.83	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	23.87	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	24.22	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+H	Inner_Full	25.25	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	24.50	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	23.92	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	23.22	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+H	Inner_Full	24.23	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	23.48	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	22.83	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	23.96	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	24.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.81	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	24.24	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	22.99	PC2	PASS
DC_71A_n78A-3	30	5+40	DFT-16QAM	M+L	Inner_Full	23.95	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.82	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.15	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	24.58	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	25.60	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.79	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	24.80	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	23.70	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	24.68	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	24.04	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.84	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	24.38	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	25.38	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	24.60	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	24.26	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	23.34	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	24.37	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	23.30	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	23.24	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	23.89	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	24.93	PC2	PASS
DC_71A_n78A-3	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.68	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	24.15	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	22.86	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	23.92	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.52	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	23.03	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	24.39	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	25.51	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	24.32	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	24.37	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	23.48	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	24.52	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.34	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.28	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	24.07	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	25.15	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	24.33	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.92	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	23.18	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	24.18	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	23.29	PC2	PASS
DC_71A_n78A-3	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.99	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	23.94	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	24.97	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.54	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.95	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	22.91	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	23.99	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.49	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.92	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	24.47	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	25.45	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	24.07	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	24.23	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	23.43	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	24.50	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.43	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	23.23	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	24.14	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	25.21	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	24.18	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.85	PC2	PASS
DC_71A_n78A-3	30	5+60	DFT-16QAM	M+H	Outer_Full	23.18	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	24.18	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.99	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.80	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	23.87	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	24.97	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.48	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	23.89	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	22.85	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	23.89	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.64	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.77	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	24.37	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	25.42	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	24.08	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	24.06	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	23.38	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	24.44	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.18	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.01	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	24.07	PC2	PASS
DC_71A_n78A-3	30	5+70	DFT-QPSK	M+H	Inner_Full	25.11	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	24.02	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	23.70	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	23.02	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	24.12	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	22.92	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.72	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	24.24	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	25.30	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.80	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	24.16	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	23.22	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	24.28	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.73	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.20	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	24.28	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	25.42	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.99	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	24.10	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	23.28	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	24.46	PC2	PASS
DC_71A_n78A-3	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.09	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	22.99	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	24.28	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	25.40	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	24.11	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	24.11	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	23.29	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	24.35	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.84	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	23.01	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	24.25	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	25.29	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.75	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	24.26	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	23.21	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	24.27	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.89	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	23.17	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	24.37	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	25.39	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.96	PC2	PASS
DC_71A_n78A-3	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	24.09	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	23.38	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	24.45	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	22.68	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	23.16	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	24.28	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	25.43	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	24.13	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	24.15	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	23.31	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	24.43	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	23.23	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	23.03	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	24.31	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	25.42	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.77	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.87	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	23.30	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	24.42	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.76	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.82	PC2	PASS
DC_71A_n78A-3	30	5+100	DFT-QPSK	M+M	Outer_Full	24.27	PC2	PASS

700-3800								
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	25.45	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.78	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	24.00	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	23.22	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	24.45	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.89	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	23.07	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	24.31	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	25.44	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.78	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	24.00	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	23.36	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	24.43	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.84	PC2	PASS
DC_71A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.98	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.41	≤13	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.77	≤13	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.03	≤13	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.50	≤13	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.42	≤13	PASS
DC_71A_n78A-3700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.63	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.54	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.94	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.57	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.49	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.78	≤13	PASS
DC_71A_n78A-3700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.77	≤13	PASS
DC_71A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.92	≤13	PASS
DC_71A_n78A-3700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.93	≤13	PASS
DC_71A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.66	≤13	PASS
DC_71A_n78A-3700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.86	≤13	PASS

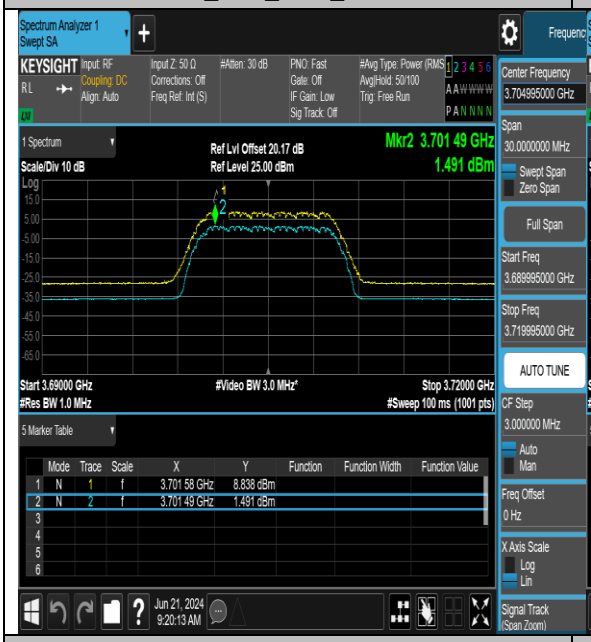
DC_71A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.87	≤13	PASS
DC_71A_n78A-3700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.43	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.15	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.02	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.68	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.05	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.84	≤13	PASS
DC_71A_n78A-3700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.19	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.00	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.91	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.24	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.37	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.70	≤13	PASS
DC_71A_n78A-3700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.07	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.11	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.89	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.96	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.07	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.07	≤13	PASS
DC_71A_n78A-3700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.76	≤13	PASS
DC_71A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.21	≤13	PASS

DC_71A_n78A-3700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.99	≤13	PASS
DC_71A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.42	≤13	PASS
DC_71A_n78A-3700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.90	≤13	PASS
DC_71A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.81	≤13	PASS
DC_71A_n78A-3700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.39	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.10	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.06	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.03	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.64	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.01	≤13	PASS
DC_71A_n78A-3700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.33	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.44	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.29	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.21	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.13	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.93	≤13	PASS
DC_71A_n78A-3700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.79	≤13	PASS
DC_71A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.84	≤13	PASS
DC_71A_n78A-3700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.07	≤13	PASS
DC_71A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.11	≤13	PASS
DC_71A_n78A-3700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.81	≤13	PASS

DC_71A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.05	≤13	PASS
DC_71A_n78A-3700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.75	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.18	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.09	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.11	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.97	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.34	≤13	PASS
DC_71A_n78A-3700-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.04	≤13	PASS

Test Graphs

NTNV_DC_71A_n78A-3700-3800_PC2_30_5+10_M+L_TID2_#1



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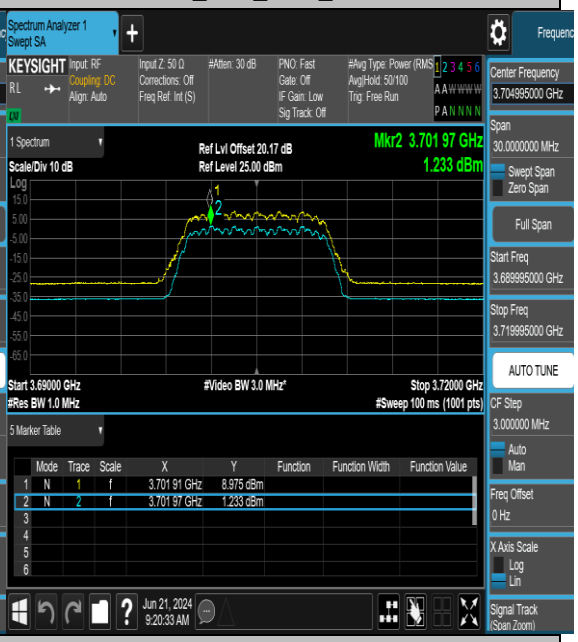

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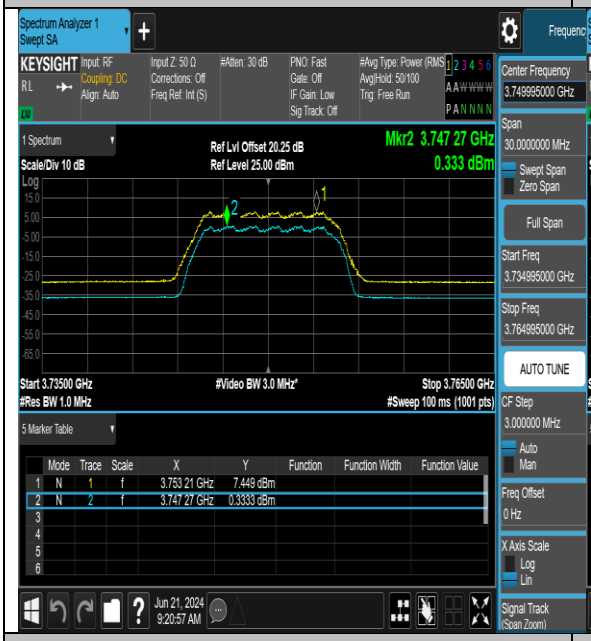

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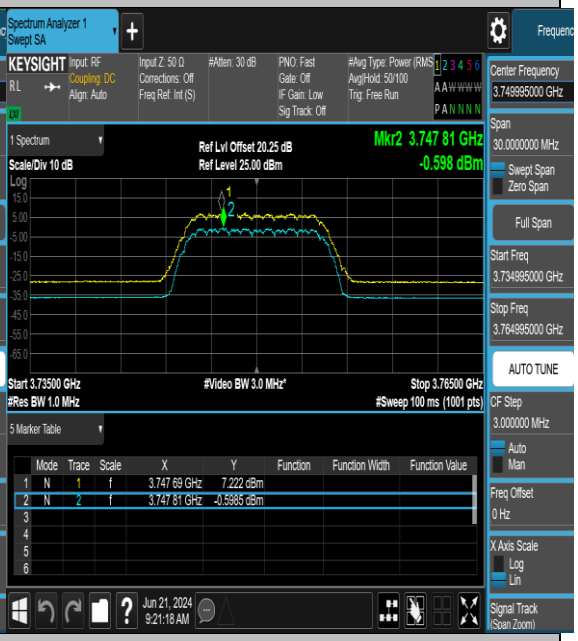

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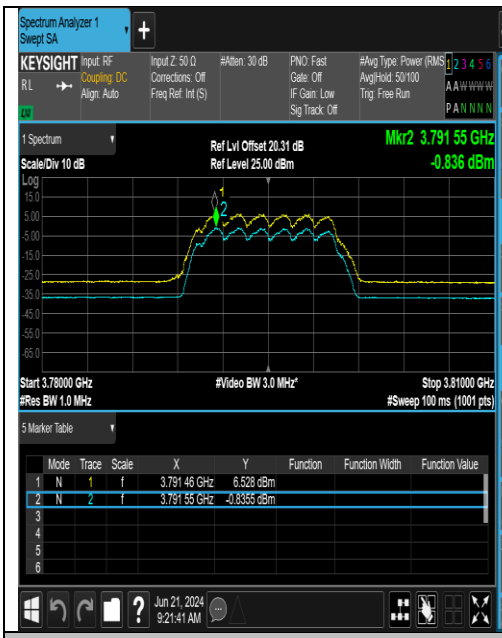

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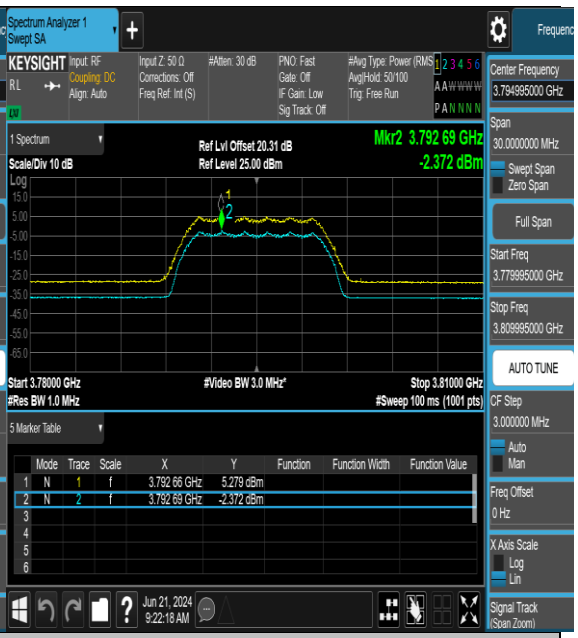

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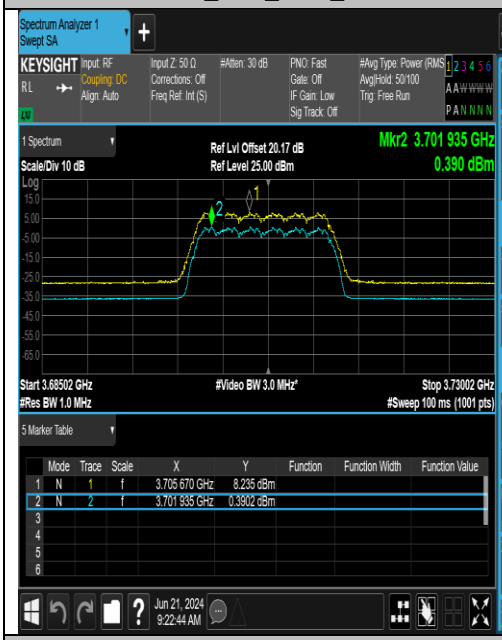

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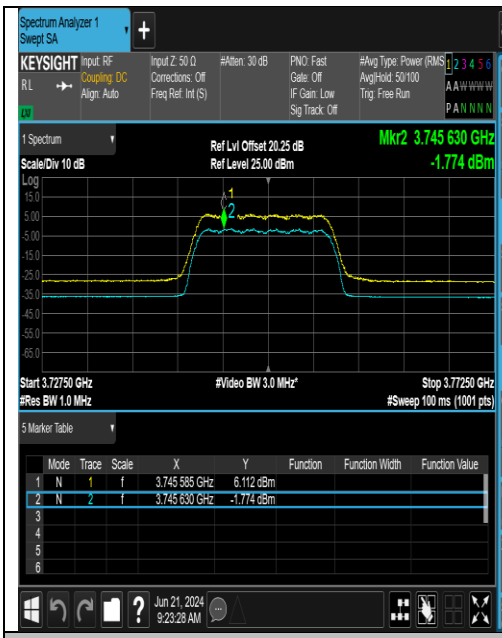

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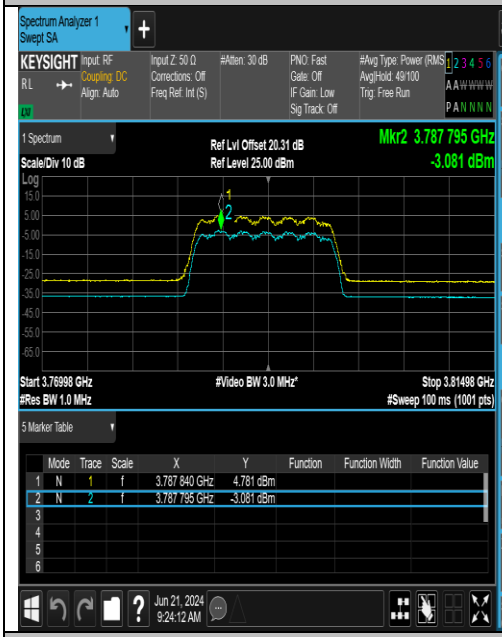

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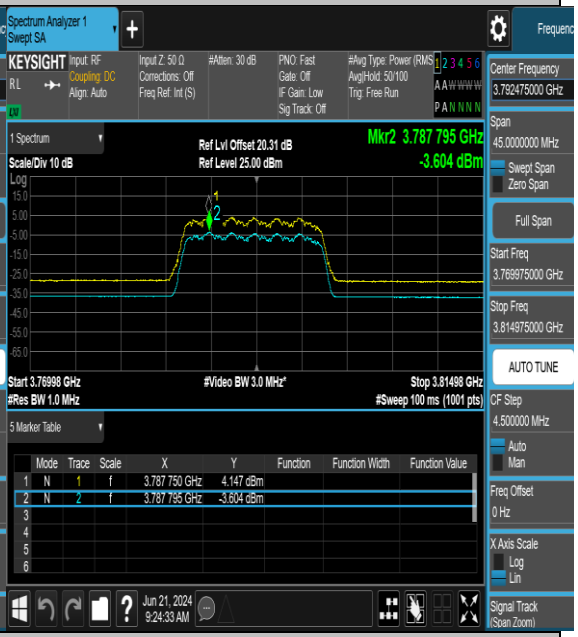

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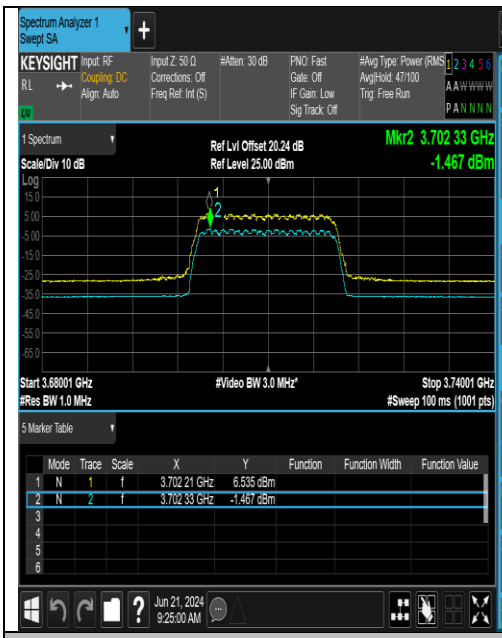

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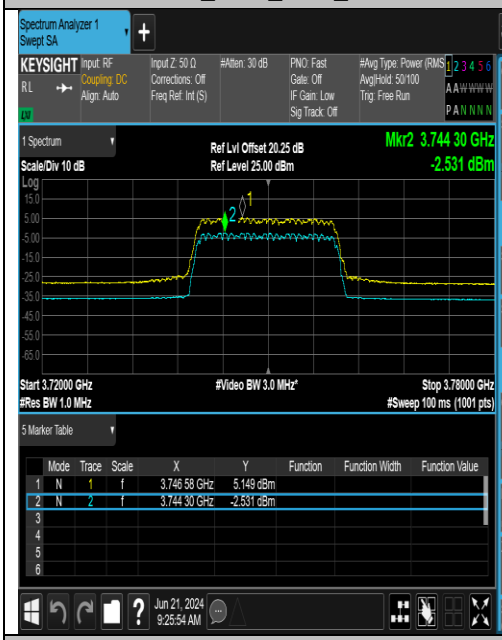

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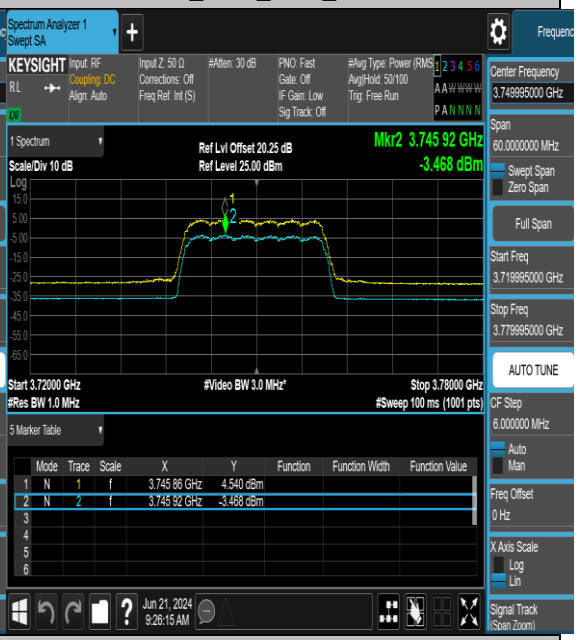

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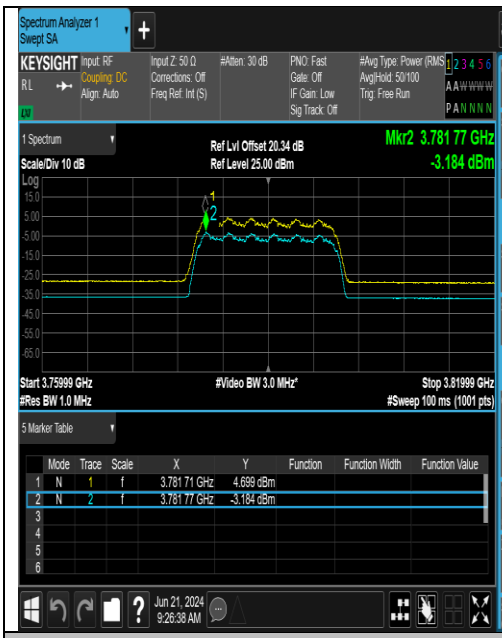

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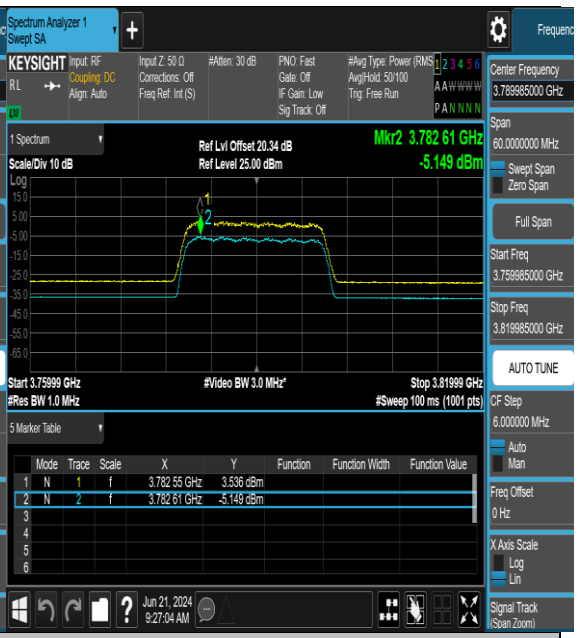

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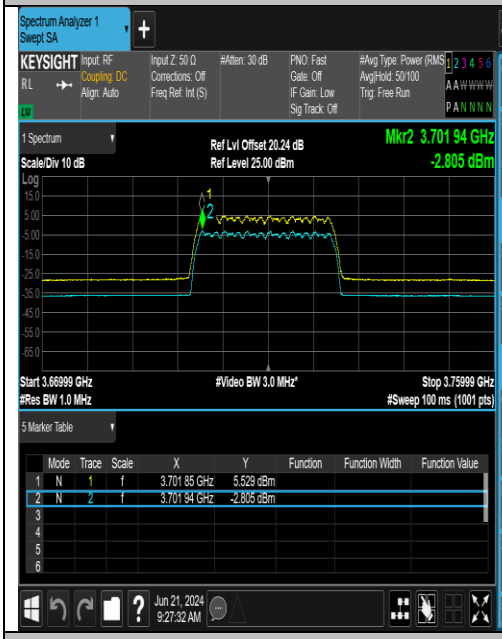

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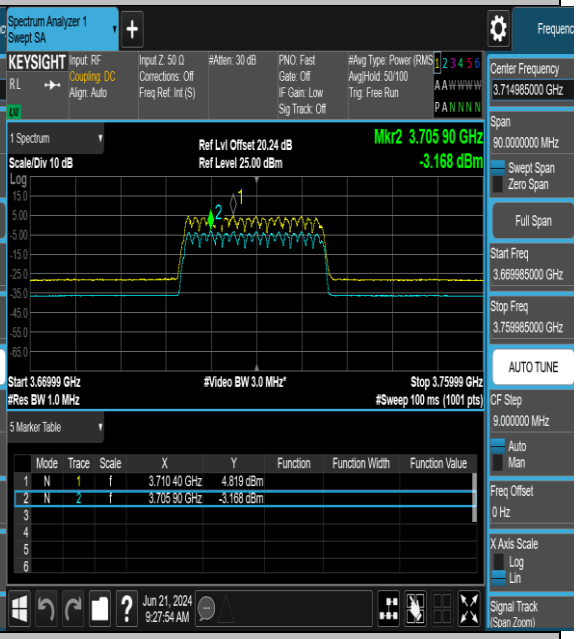

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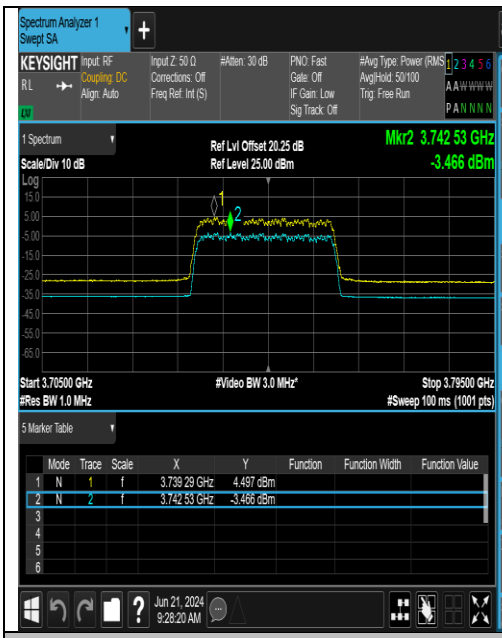

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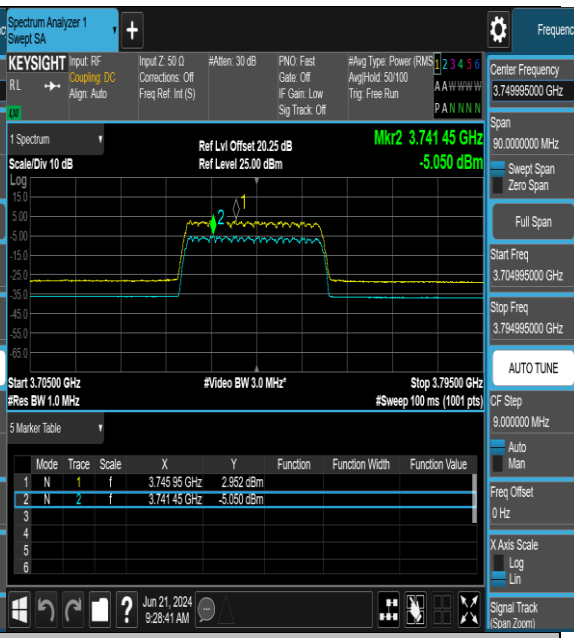

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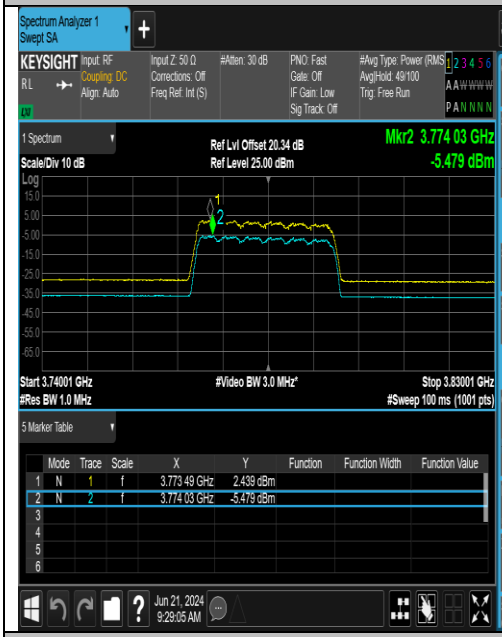

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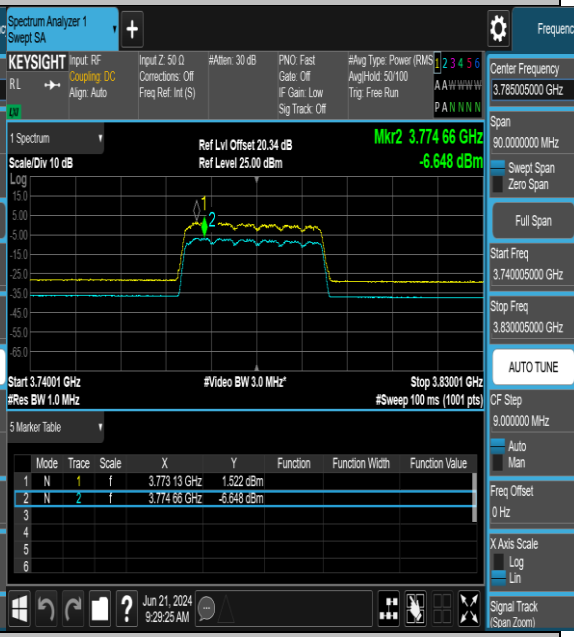

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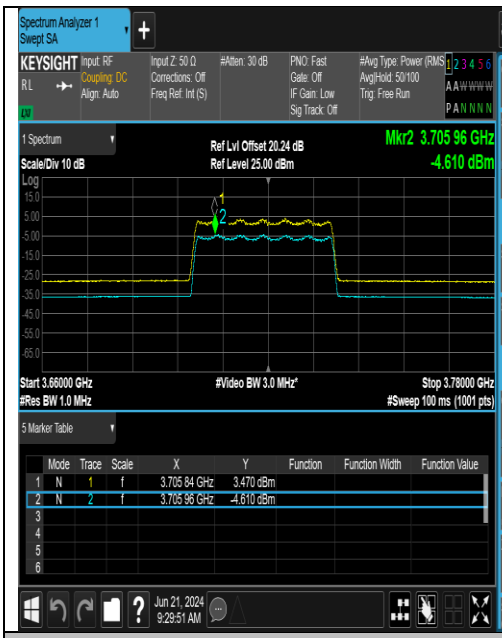

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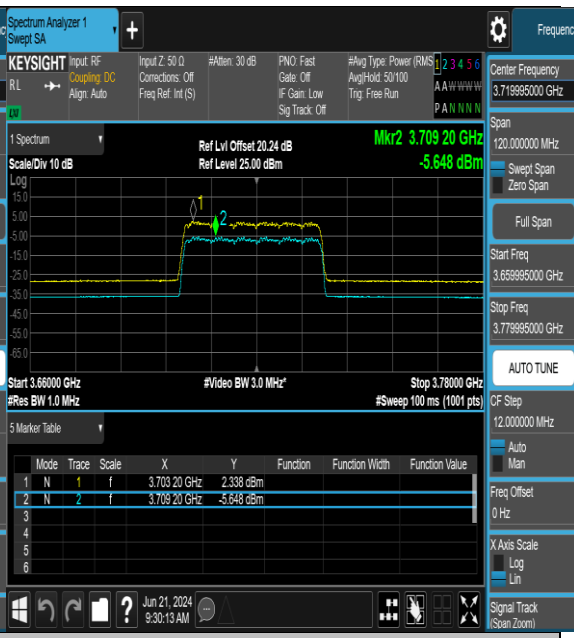

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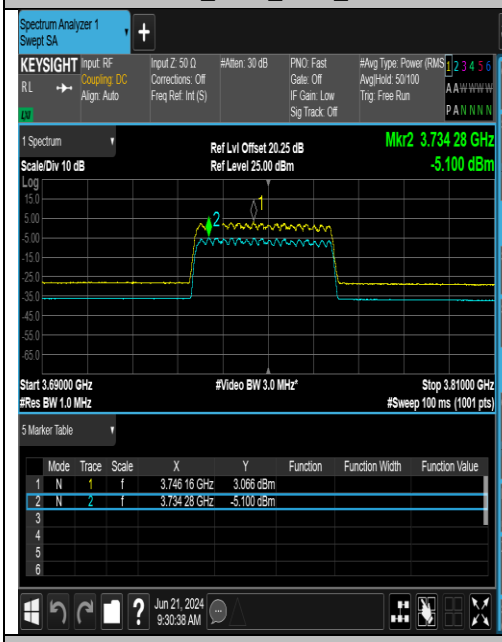

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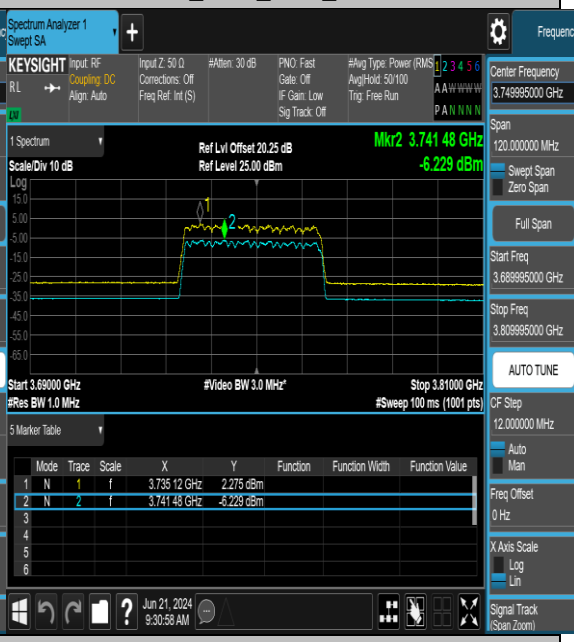

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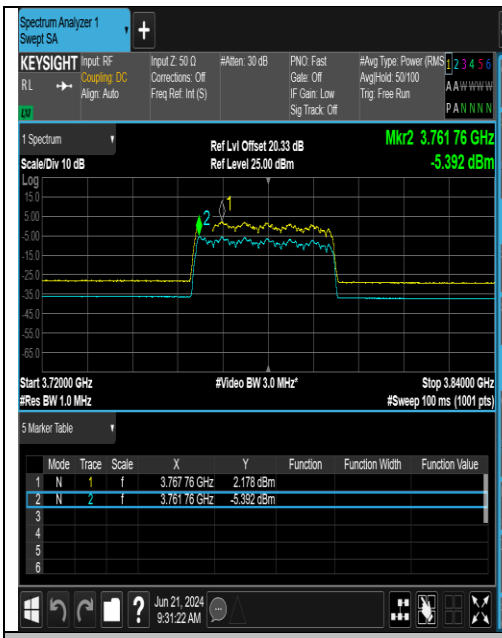

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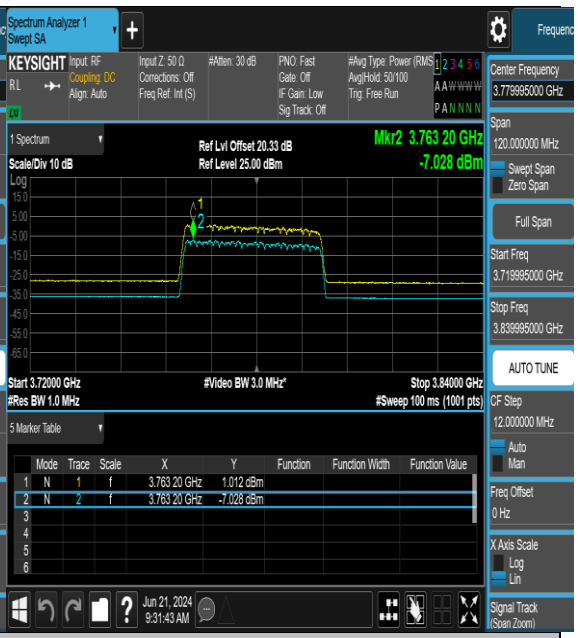

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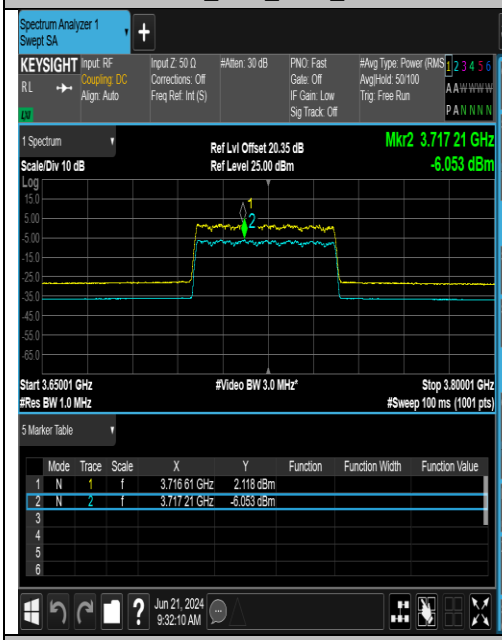

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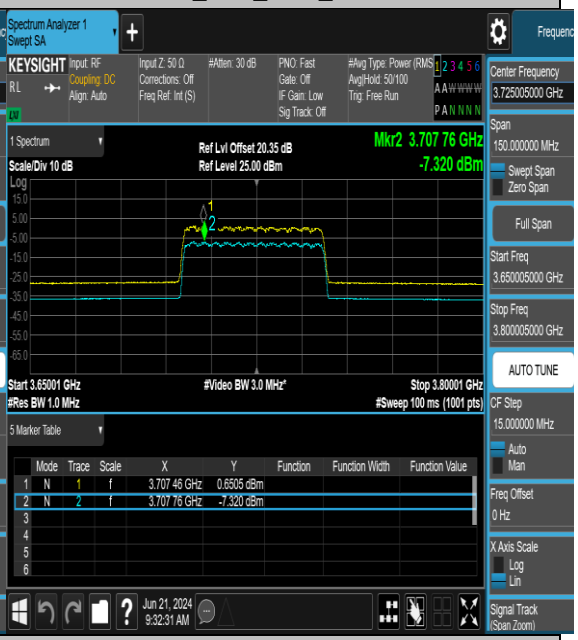

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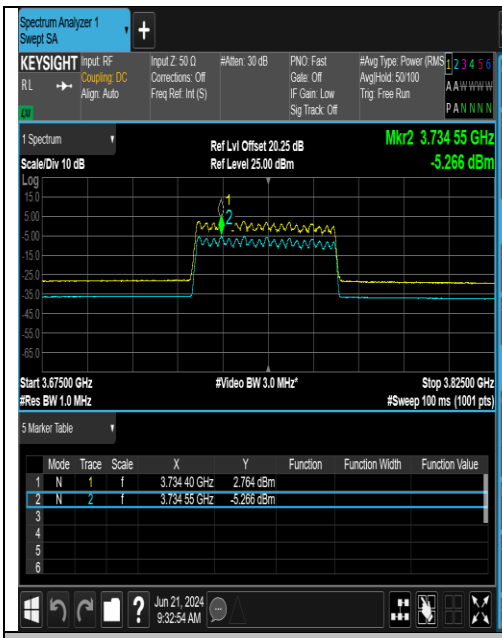

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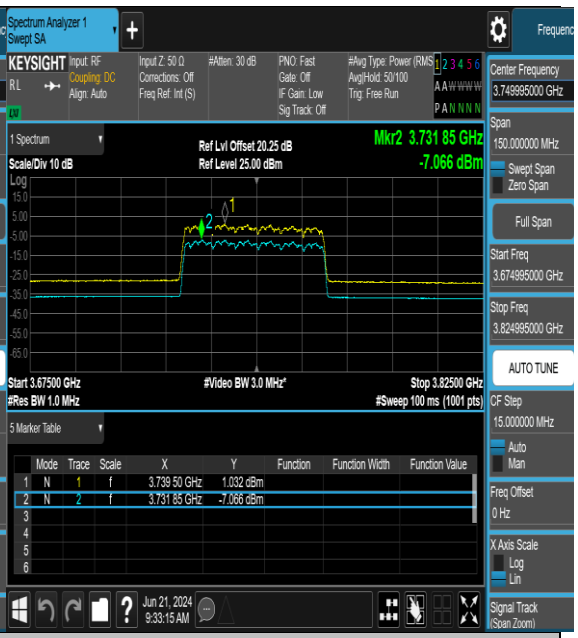

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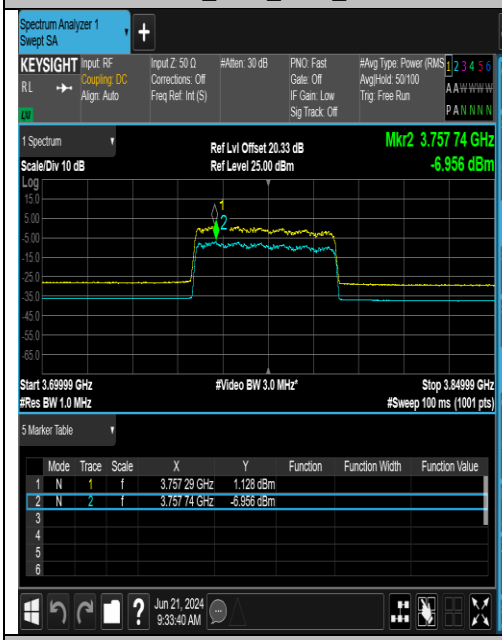

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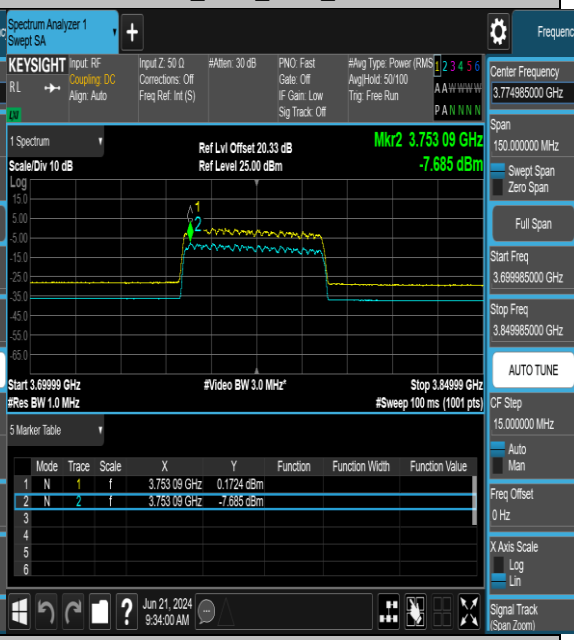

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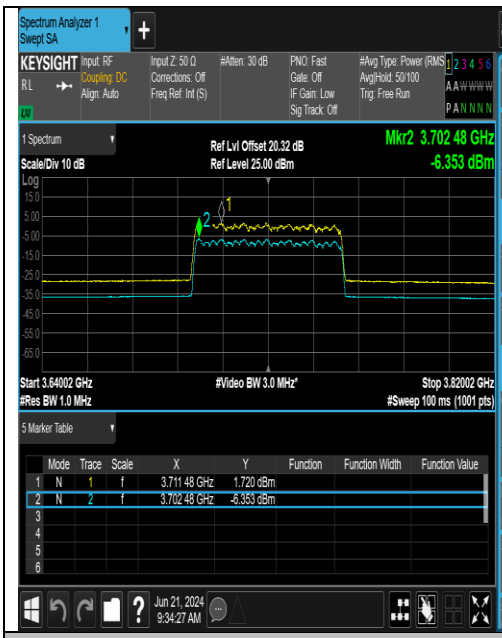

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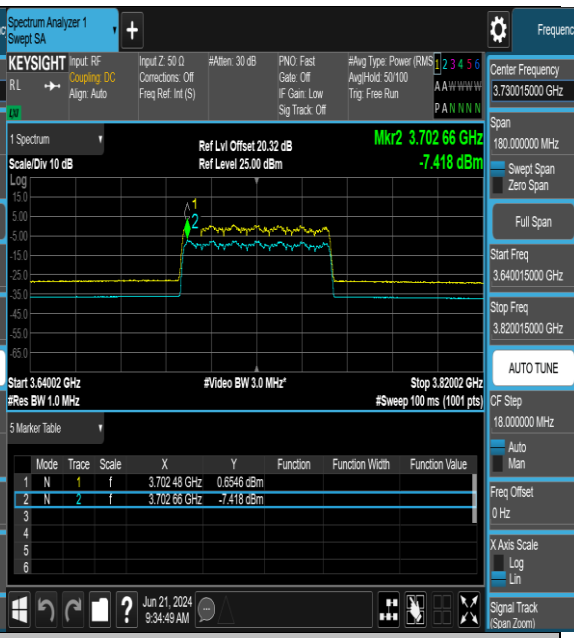

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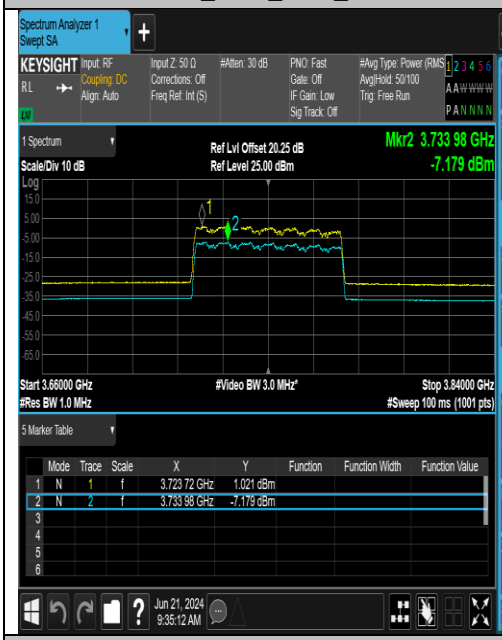

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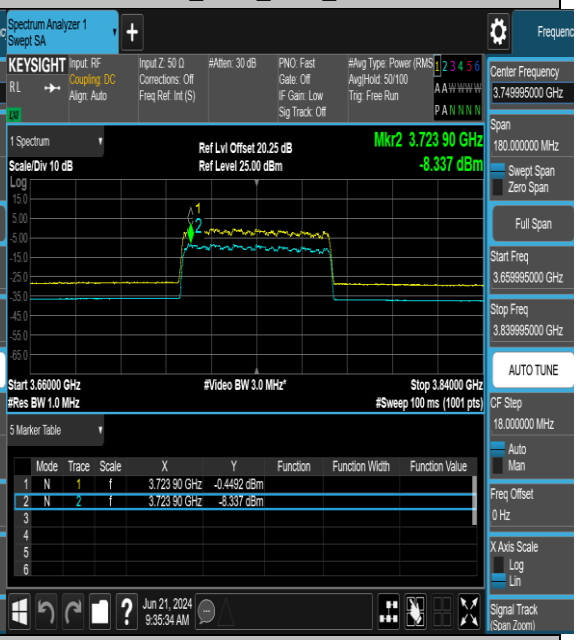

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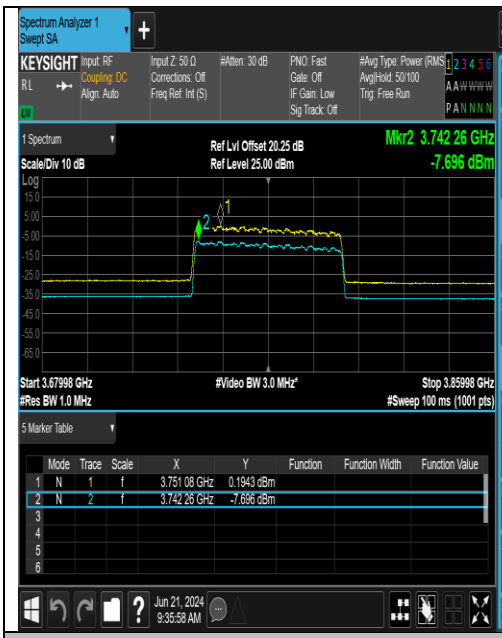

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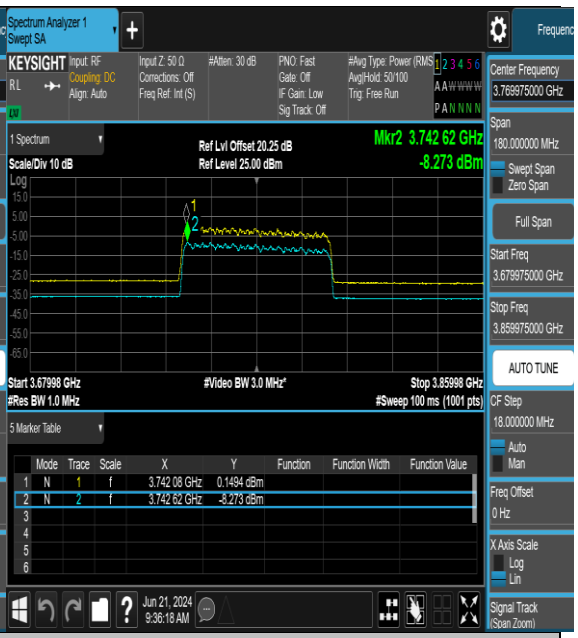

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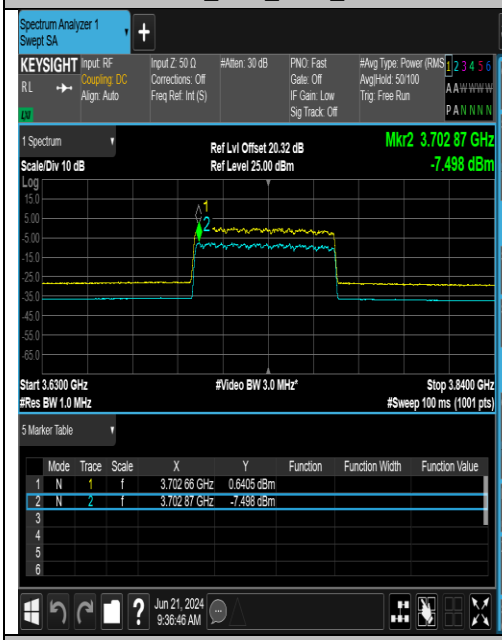

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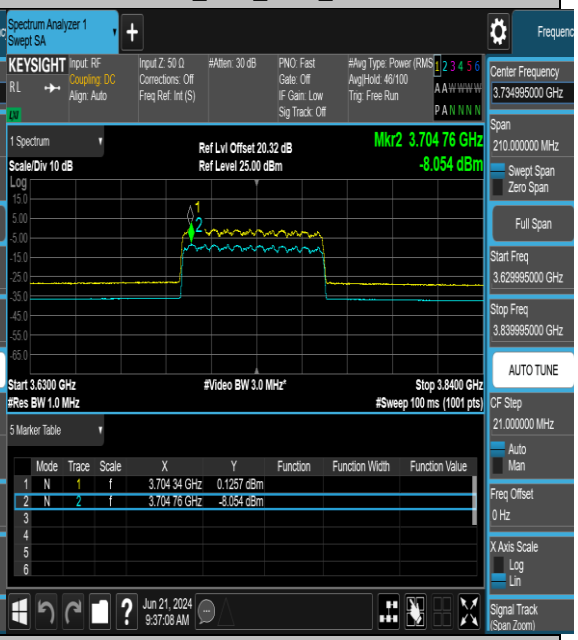

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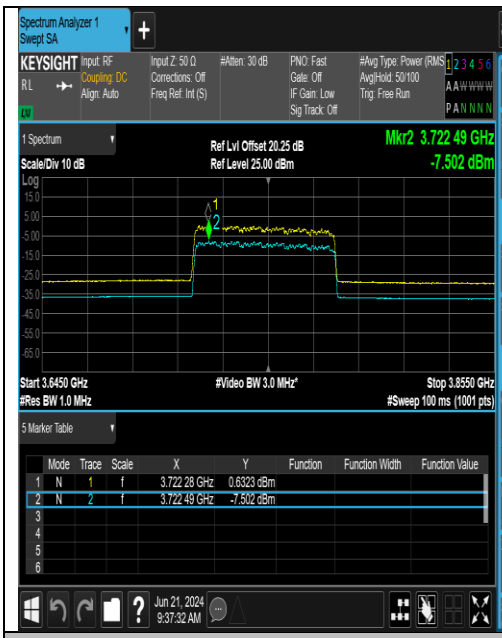

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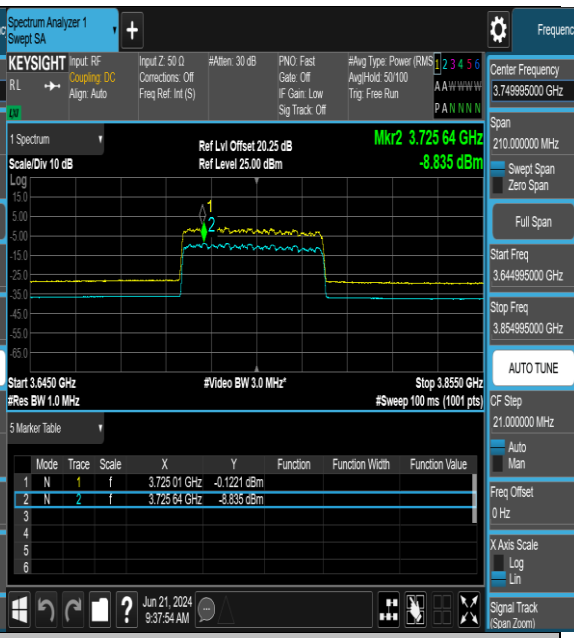

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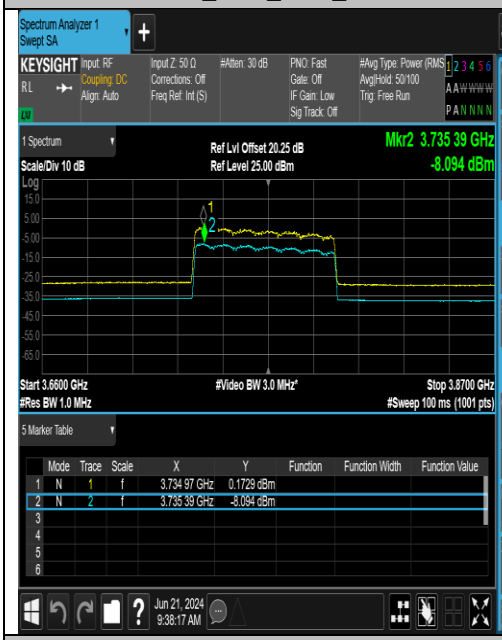

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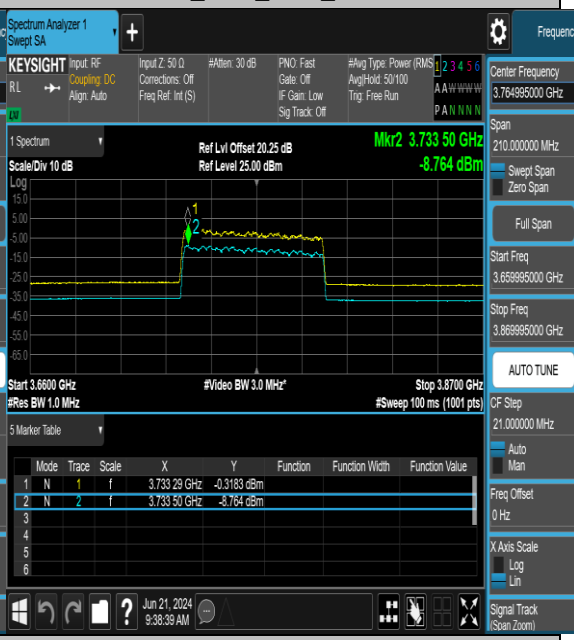

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