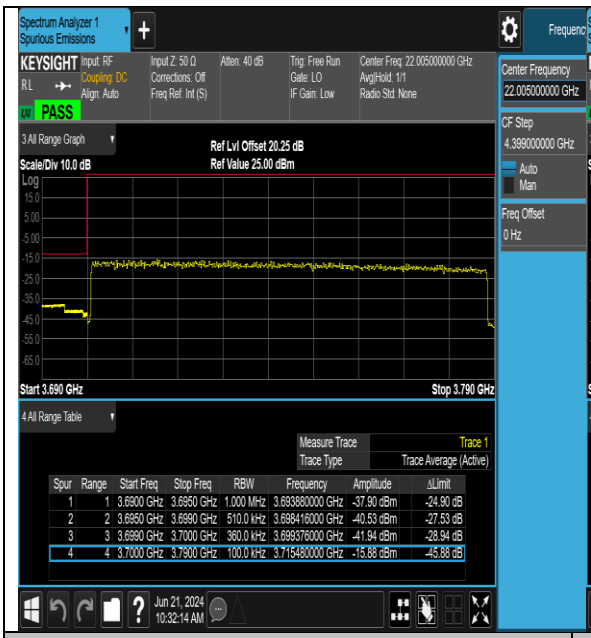

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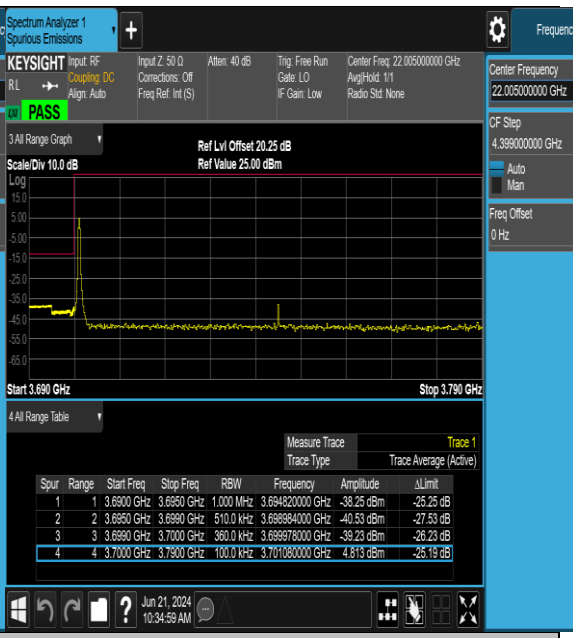

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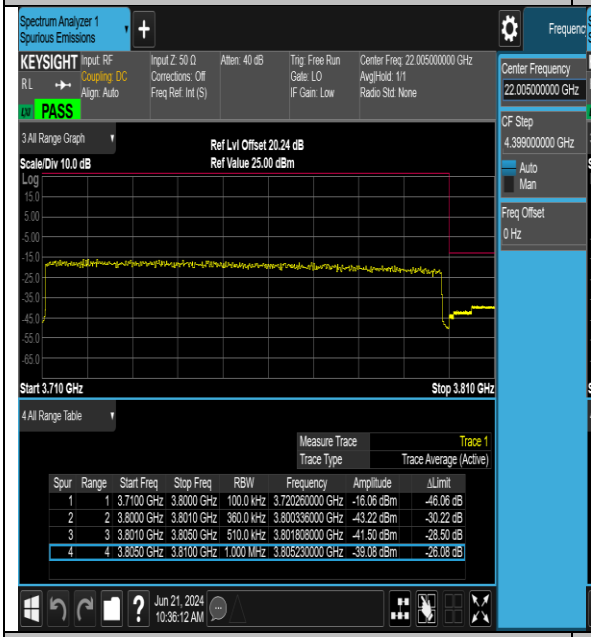

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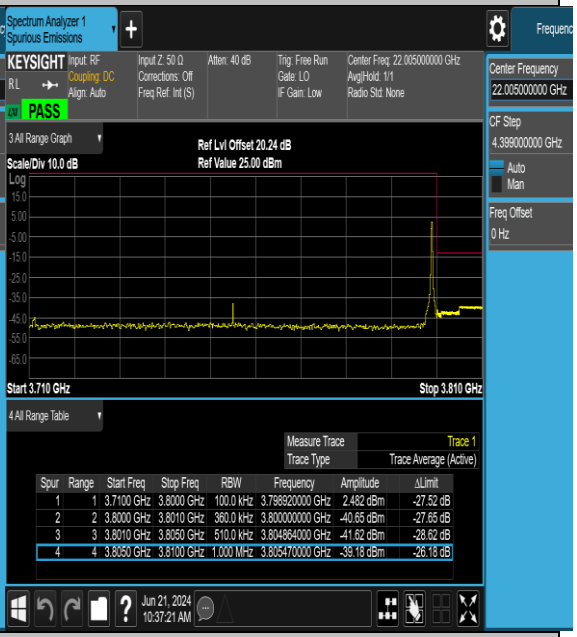

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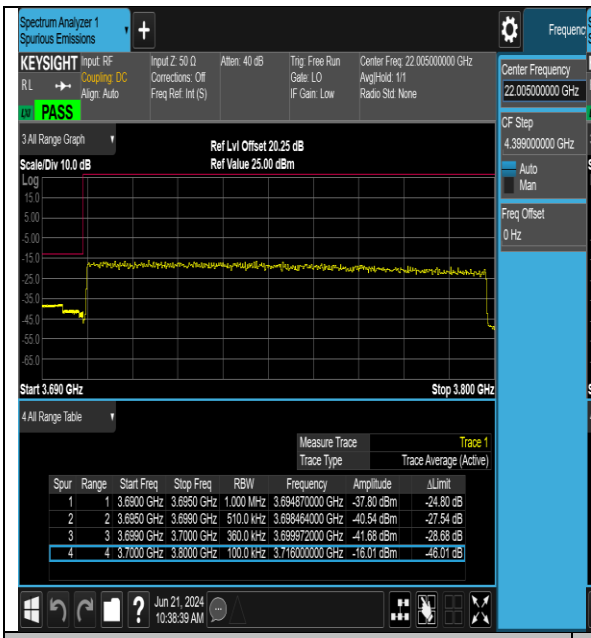

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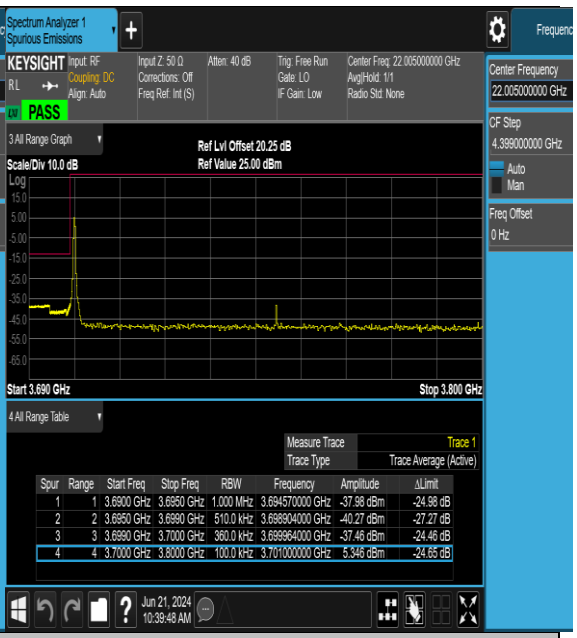

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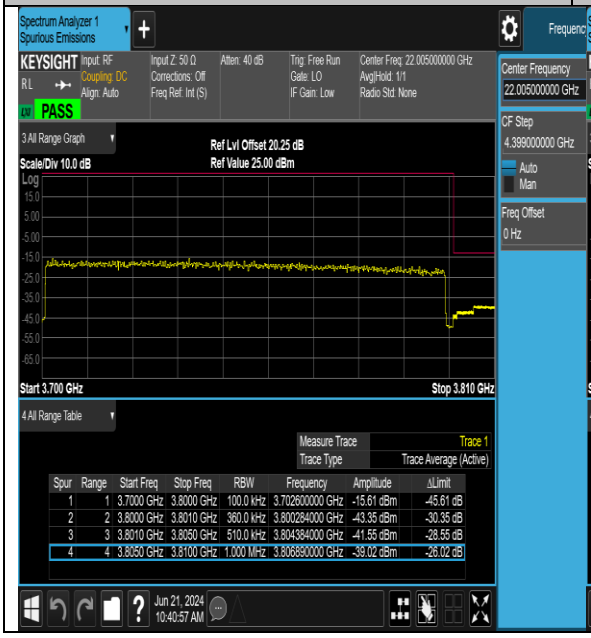

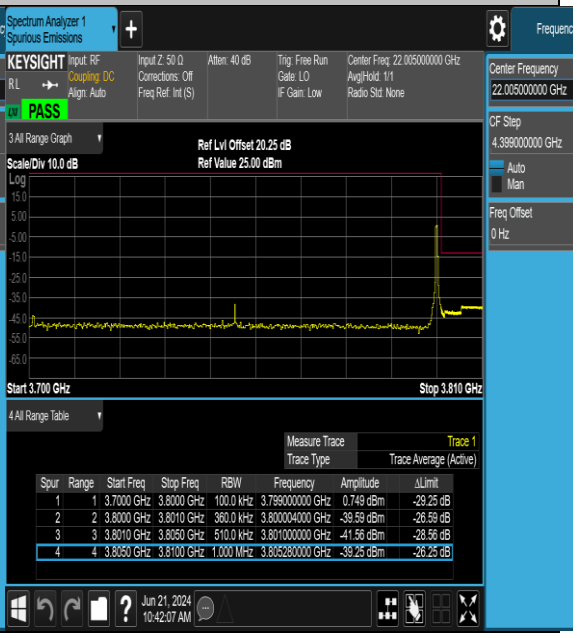

Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.56	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.94	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-54.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.01	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.35	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	18000	40000	-36.86	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.68	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.95	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-24.46	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.46	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-36.82	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.95	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.55	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.06	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.35	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-36.84	-13	PASS

DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-67.40	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.88	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-54.76	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	18000	40000	-36.92	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-66.84	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-76.15	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-50.91	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-36.92	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-66.23	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.00	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.07	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.29	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.88	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.20	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.27	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-54.58	-13	PASS

DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	18000	40000	-36.91	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.65	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.64	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.64	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-42.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-36.76	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.97	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.82	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.11	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.08	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-36.86	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.84	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.99	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-54.67	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	18000	40000	-36.96	-13	PASS

DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.11	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-75.86	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.71	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.06	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-36.87	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-66.32	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.31	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.12	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.83	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.97	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.34	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-54.52	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.31	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	18000	40000	-36.80	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.28	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.76	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.06	-13	PASS

DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.03	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-36.76	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.92	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.97	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.00	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-36.91	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-65.92	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-76.00	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-54.59	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	1000	6000	-32.98	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	18000	40000	-36.57	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-66.48	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.89	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-42.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-36.95	-13	PASS

DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-66.29	-43	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.91	-33	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.67	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.38	-13	PASS
DC_71A_n78A -3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-36.94	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.64	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.11	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-54.69	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.26	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	18000	40000	-36.86	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.37	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.84	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.39	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.54	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-36.68	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.92	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.35	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.27	-13	PASS

DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.11	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.50	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-36.83	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-66.68	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.75	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-53.89	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	18000	40000	-37.05	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-66.43	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-76.27	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-36.70	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-67.03	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.72	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.74	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-32.98	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.38	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.58	-13	PASS

DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.69	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.41	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-54.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.12	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.50	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	18000	40000	-36.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.87	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.21	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-41.95	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-36.73	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.84	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.68	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.39	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.83	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.42	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-36.57	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.62	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.71	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-54.51	-13	PASS

DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.25	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.37	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	18000	40000	-36.82	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.26	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.22	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.66	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.04	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-36.92	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-66.35	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.60	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.31	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.73	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.07	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.11	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-54.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.62	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	18000	40000	-36.86	-13	PASS

DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.66	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.78	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.22	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.42	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-36.87	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.94	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.03	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.12	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-36.93	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-66.35	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-76.96	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-54.38	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.06	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	18000	40000	-37.06	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-66.76	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.80	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-53.07	-13	PASS

DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-42.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-36.92	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-67.00	-43	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-76.04	-33	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.54	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.17	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-36.92	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.33	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.21	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-52.67	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	18000	40000	-37.05	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.30	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.40	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.78	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-36.93	-13	PASS

DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.81	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.72	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.71	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.11	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.55	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-37.08	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-67.21	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-76.20	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-54.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.04	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.42	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	18000	40000	-36.66	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-66.90	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.97	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.22	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.42	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-37.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-66.20	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.24	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.09	-13	PASS

DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.17	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.35	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.74	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.69	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.33	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-54.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.18	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	18000	40000	-36.94	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.61	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.19	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.81	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-42.32	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-37.01	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.28	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.37	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.62	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.06	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.53	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-36.74	-13	PASS

DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-66.80	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.92	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-54.35	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	18000	40000	-36.74	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.17	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.82	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.57	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.39	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-36.70	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.64	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.54	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-53.63	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.54	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.98	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.62	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.88	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-54.53	-13	PASS

DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	18000	40000	-36.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.11	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.94	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.66	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.00	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-36.76	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.96	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.13	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.17	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-36.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-66.73	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-76.28	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-54.60	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.30	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	18000	40000	-36.80	-13	PASS

DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-65.83	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-76.10	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.61	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.18	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-42.53	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-37.08	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-66.24	-43	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.85	-33	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.35	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.26	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-36.96	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.29	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.41	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-54.57	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	6000	-32.96	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.46	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	18000	40000	-36.62	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.23	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.38	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.82	-13	PASS

DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.50	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-37.07	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-67.24	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.19	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.30	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-36.81	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-66.54	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.94	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	30	1000	-54.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	18000	40000	-36.93	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-67.04	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.88	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.55	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-36.59	-13	PASS

DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-66.60	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.50	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.01	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.39	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.82	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.64	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.00	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-52.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.21	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.58	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	18000	40000	-36.76	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.51	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.07	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.11	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-42.59	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-36.83	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.81	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.37	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-47.11	-13	PASS

DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.12	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.37	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-36.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-65.99	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.37	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	30	1000	-53.81	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.17	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.37	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	18000	40000	-36.96	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.62	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.04	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-53.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.05	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-36.80	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-65.37	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.73	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.26	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.94	-13	PASS

DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.55	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.87	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-54.57	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.11	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	18000	40000	-36.73	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.29	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.16	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.46	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.18	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-36.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.11	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.15	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.01	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-36.96	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-66.84	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.80	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	30	1000	-54.46	-13	PASS

DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	18000	40000	-36.71	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-67.09	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.76	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.18	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-42.42	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-36.92	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-66.47	-43	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-76.16	-33	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.58	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-36.98	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.42	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.97	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-54.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.21	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	18000	40000	-36.61	-13	PASS

DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.48	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.38	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.57	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.28	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-36.96	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.73	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.47	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.68	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.06	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.35	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-36.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-66.21	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	0.15	30	-76.44	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	30	1000	-53.27	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.53	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	18000	40000	-36.54	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-66.19	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-76.13	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.00	-13	PASS

DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-37.01	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-66.50	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.97	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.38	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.33	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.72	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.17	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.17	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-54.66	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.11	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.30	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	18000	40000	-36.96	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.84	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.47	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.07	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-42.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-36.79	-13	PASS

DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.62	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.65	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.07	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-36.91	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-66.25	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.42	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	30	1000	-54.61	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	18000	40000	-36.96	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.91	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.36	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.62	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-36.69	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-66.49	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.46	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.01	-13	PASS

DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.30	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.91	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.81	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.73	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-54.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	18000	40000	-37.05	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-67.27	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.27	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-36.89	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.18	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.28	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.32	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.59	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-37.00	-13	PASS

DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-66.77	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	0.15	30	-76.23	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	30	1000	-54.52	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.04	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	18000	40000	-36.80	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-66.09	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-76.25	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.68	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-42.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-36.93	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-67.07	-43	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-76.28	-33	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-52.95	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-37.02	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.62	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.37	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-54.27	-13	PASS

DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.23	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	18000	40000	-36.94	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.02	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.75	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.37	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-36.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.58	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.04	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.21	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.25	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-36.95	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-66.71	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.91	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	30	1000	-54.17	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	18000	40000	-37.02	-13	PASS

DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-66.44	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.57	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-52.77	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-37.03	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-67.17	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.18	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.87	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.10	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.13	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-54.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.57	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	18000	40000	-37.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.68	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.10	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.45	-13	PASS

DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-42.31	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-37.02	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.42	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.94	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.69	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-37.00	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-66.56	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.53	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	30	1000	-54.25	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.05	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.46	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	18000	40000	-36.94	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.46	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.33	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-50.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.25	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-36.60	-13	PASS

DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-66.19	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.00	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.05	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.11	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.89	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.30	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.57	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-54.62	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.03	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.25	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	18000	40000	-36.81	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.25	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.89	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.22	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-36.67	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.40	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.09	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.64	-13	PASS

DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.50	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-36.81	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-66.43	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.67	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	30	1000	-54.28	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.05	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	18000	40000	-36.82	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-66.83	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-76.01	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-53.95	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-42.59	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-36.50	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-66.79	-43	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.95	-33	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.66	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.38	-13	PASS
DC_71A_n78A -3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-37.03	-13	PASS

DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.07	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.38	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-52.84	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.02	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.28	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	18000	40000	-36.98	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.71	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.93	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.60	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.54	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-37.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.55	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.14	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.21	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-37.08	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-66.20	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	0.15	30	-76.57	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	30	1000	-54.35	-13	PASS

DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.29	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.38	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	18000	40000	-37.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-66.38	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-76.14	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.64	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.11	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.59	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-36.94	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-66.98	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.67	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.61	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.38	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.81	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.84	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.38	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-53.91	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	18000	40000	-37.05	-13	PASS

DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.56	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.55	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-42.33	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-36.78	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.52	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.36	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.81	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-36.76	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-66.18	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	0.15	30	-75.82	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	30	1000	-54.62	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.61	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	18000	40000	-37.06	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.33	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.80	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.56	-13	PASS

DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.05	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.57	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-36.97	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-66.81	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.91	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.34	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.79	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.24	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.00	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-54.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.12	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	18000	40000	-36.81	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.98	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.35	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.54	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-36.88	-13	PASS

DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.56	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.75	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.12	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.03	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.42	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-36.71	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-66.92	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	0.15	30	-76.13	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	30	1000	-54.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.12	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	18000	40000	-36.98	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-66.97	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.84	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-42.54	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-36.84	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-66.47	-43	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-76.04	-33	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.56	-13	PASS

DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.21	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-36.77	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.15	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.15	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-54.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	18000	40000	-36.96	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.82	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.54	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.18	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-36.83	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.86	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.20	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.58	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.34	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-36.74	-13	PASS

DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-66.53	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	0.15	30	-76.43	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	30	1000	-54.21	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.11	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.38	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	18000	40000	-36.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-67.00	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.93	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-36.88	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-66.54	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.57	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.61	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.81	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.36	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.45	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-52.29	-13	PASS

DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	18000	40000	-37.03	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.53	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.22	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.59	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.23	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-42.42	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-36.99	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.35	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.07	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.83	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.24	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.57	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-37.03	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-66.71	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.33	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	30	1000	-54.58	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	18000	40000	-36.75	-13	PASS

DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.20	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.48	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-53.74	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.17	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.50	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-36.64	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-66.82	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.06	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.21	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.42	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.83	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.10	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.78	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-54.61	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	18000	40000	-37.00	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.72	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.84	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.72	-13	PASS

DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-36.96	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.94	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.14	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.07	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-36.98	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-66.43	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.93	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	30	1000	-54.38	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.08	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.52	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	18000	40000	-36.83	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-66.76	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-76.10	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.76	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-41.97	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-36.60	-13	PASS

DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-66.73	-43	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-76.72	-33	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-32.99	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-36.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.70	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.45	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-54.53	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.04	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	18000	40000	-36.89	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.95	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.11	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.54	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-37.00	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.91	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.12	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.69	-13	PASS

DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.18	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-36.91	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-67.15	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	0.15	30	-76.33	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	30	1000	-54.42	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	1000	6000	-32.91	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	18000	40000	-36.94	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-66.59	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-76.27	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-53.94	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.08	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.52	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-36.77	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-66.97	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.98	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-53.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.91	-13	PASS

DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.53	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.18	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-54.52	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.51	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	18000	40000	-37.02	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.65	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.92	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.34	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.17	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-42.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-36.95	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.79	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.93	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.37	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.99	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.54	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-36.78	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-66.58	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.21	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	30	1000	-53.94	-13	PASS

DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.26	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	18000	40000	-36.79	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.34	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.11	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.26	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.18	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-37.01	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-67.36	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-75.90	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.25	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.69	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.87	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.57	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-53.68	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.94	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.46	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	18000	40000	-36.60	-13	PASS

DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-67.11	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.88	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.59	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-37.02	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.49	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.74	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.53	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.23	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.52	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-37.05	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-66.73	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	0.15	30	-75.86	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	30	1000	-54.37	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.23	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	18000	40000	-36.60	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-67.00	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-76.13	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.36	-13	PASS

DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.18	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-42.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-36.79	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-67.10	-43	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-76.00	-33	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.21	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.46	-13	PASS
DC_71A_n78A -3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-36.91	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.00	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.82	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-54.30	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.14	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.46	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	18000	40000	-37.00	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.85	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.37	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.12	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.38	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-36.75	-13	PASS

DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.62	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.88	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.27	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.21	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.46	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-36.84	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-67.04	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	0.15	30	-75.51	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	30	1000	-54.42	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.50	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	18000	40000	-36.98	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-67.03	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.74	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.56	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-37.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-66.29	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-76.62	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.49	-13	PASS

DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.79	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.66	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.65	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-54.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	18000	40000	-36.73	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.27	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.80	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.27	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-42.53	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-36.81	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.84	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.81	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.73	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.26	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-36.85	-13	PASS

DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-66.59	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.40	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	30	1000	-54.74	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	18000	40000	-36.77	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.82	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.24	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-53.64	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.00	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.59	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-36.95	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-66.63	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.15	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.44	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.21	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.35	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.88	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.37	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.13	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-54.51	-13	PASS

DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	18000	40000	-36.64	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.68	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.37	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.68	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.16	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.37	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-36.64	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.63	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.55	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.87	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.08	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.64	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-36.65	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-66.96	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	0.15	30	-76.51	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	30	1000	-54.28	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.22	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	18000	40000	-36.87	-13	PASS

DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-66.95	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-75.65	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-53.93	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.12	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-42.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-36.92	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-66.64	-43	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-75.82	-33	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.70	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.47	-13	PASS
DC_71A_n78A -3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-37.01	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.68	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.29	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-54.45	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.04	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	18000	-42.52	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	18000	40000	-36.87	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.73	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.08	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.62	-13	PASS

DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-42.35	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-36.88	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.98	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.97	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.68	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.19	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-36.76	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-66.19	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Outer_Full	0.15	30	-76.88	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Outer_Full	30	1000	-54.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Outer_Full	1000	6000	-33.09	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Outer_Full	6000	18000	-42.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Outer_Full	18000	40000	-36.96	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-66.61	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-75.91	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-54.57	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-33.11	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	6000	18000	-42.40	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	18000	40000	-36.86	-13	PASS

DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-66.67	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-75.83	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-54.53	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-33.06	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	6000	18000	-42.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	18000	40000	-36.85	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.66	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.66	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-53.82	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.01	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	18000	-42.37	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	18000	40000	-36.97	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.56	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.01	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.48	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.17	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-36.97	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.40	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.28	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.44	-13	PASS

DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.07	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-42.46	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-37.01	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-66.55	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.15	30	-76.88	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Outer_Full	30	1000	-53.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Outer_Full	1000	6000	-33.15	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Outer_Full	6000	18000	-42.27	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Outer_Full	18000	40000	-36.72	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-66.20	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-76.27	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-54.02	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-33.17	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	6000	18000	-42.50	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	18000	40000	-36.94	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-66.66	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-76.35	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-54.66	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	6000	18000	-42.43	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	18000	40000	-36.99	-13	PASS

DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.91	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.03	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-54.59	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.13	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	18000	-42.49	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	18000	40000	-36.88	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.36	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.79	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.71	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.03	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-42.34	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-36.87	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.02	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.39	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.29	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.20	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-42.41	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-36.71	-13	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-66.88	-43	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.15	30	-76.29	-33	PASS
DC_71A_n78A -3700-3800	30	5+10 0	DFT-16QAM	M+H	Outer_Full	30	1000	-53.55	-13	PASS

DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	1000	6000	-33.10	-13	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	6000	18000	-42.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	18000	40000	-36.84	-13	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-66.43	-43	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-76.42	-33	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-54.25	-13	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-33.17	-13	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	6000	18000	-42.55	-13	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	18000	40000	-36.70	-13	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-66.20	-43	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-76.59	-33	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-54.36	-13	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-32.98	-13	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	6000	18000	-42.39	-13	PASS
DC_71A_n78A -3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	18000	40000	-36.63	-13	PASS

Test Graphs

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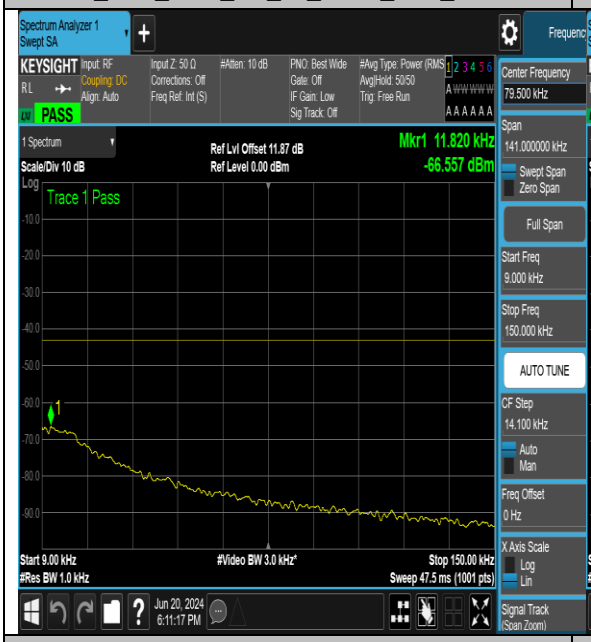

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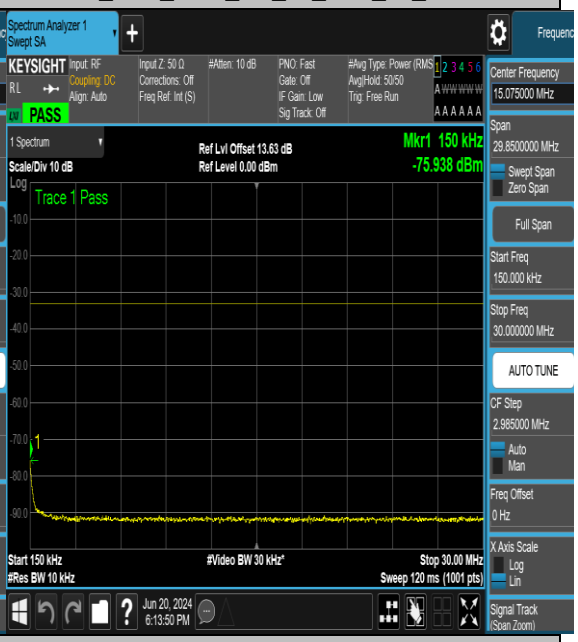

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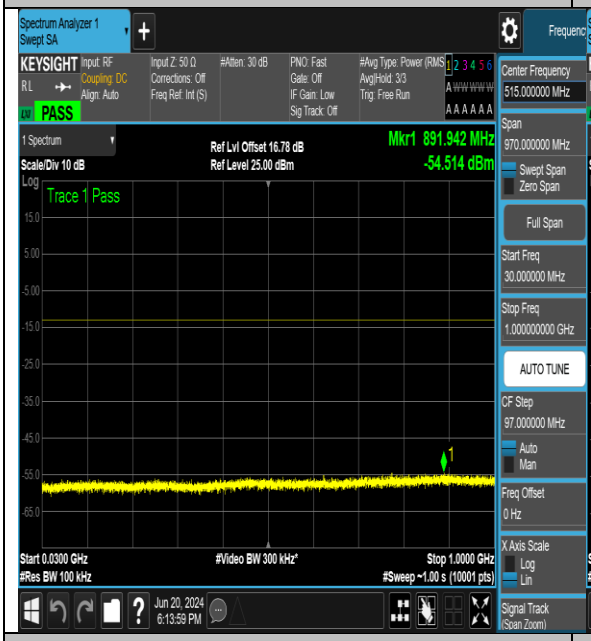

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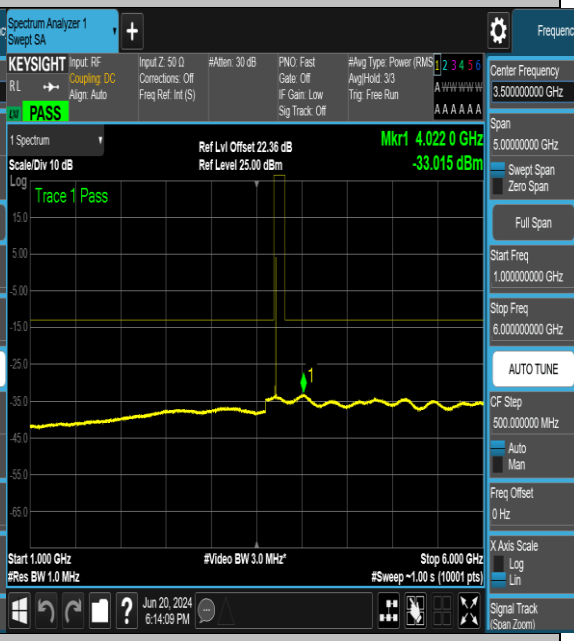

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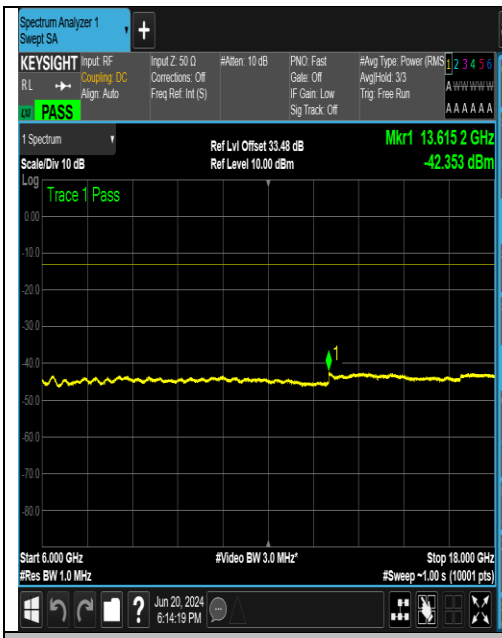

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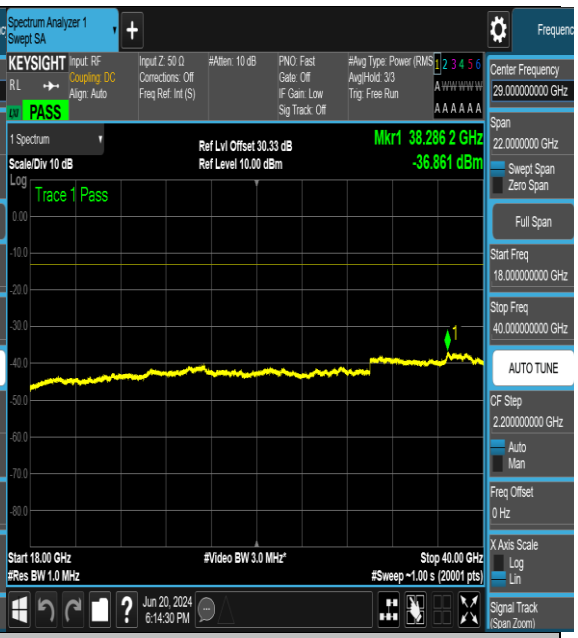

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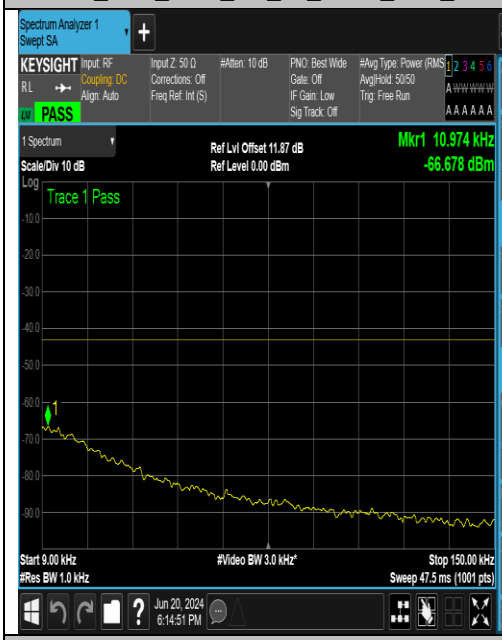

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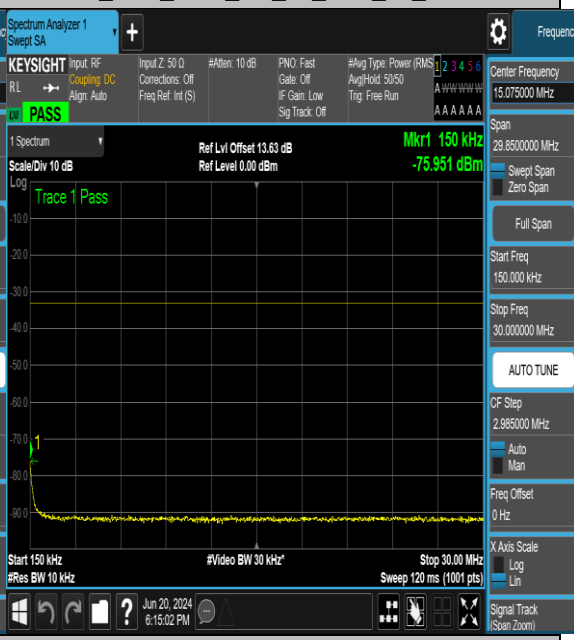

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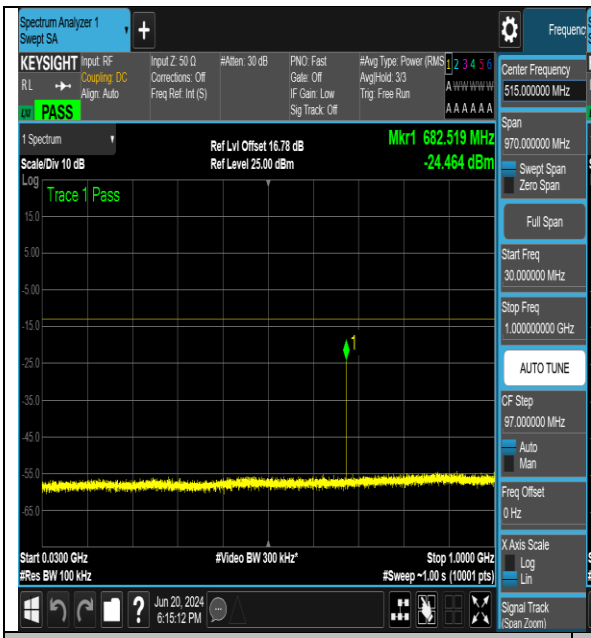

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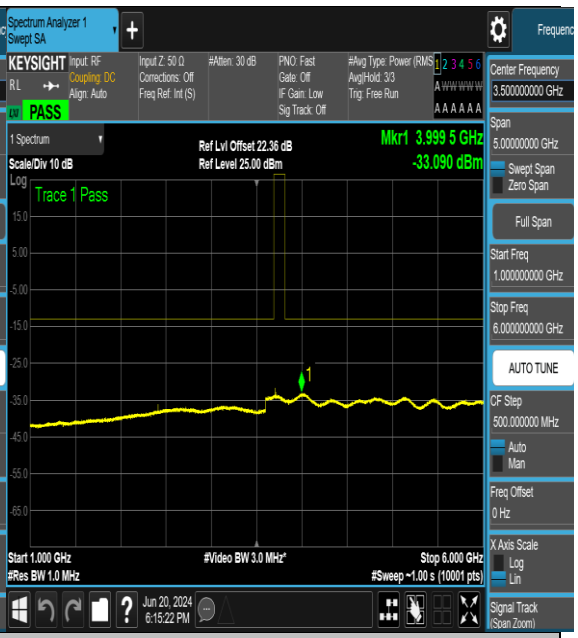

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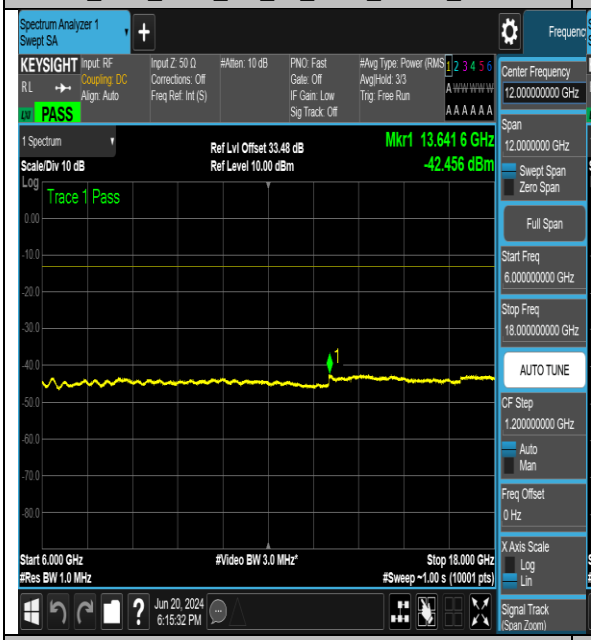

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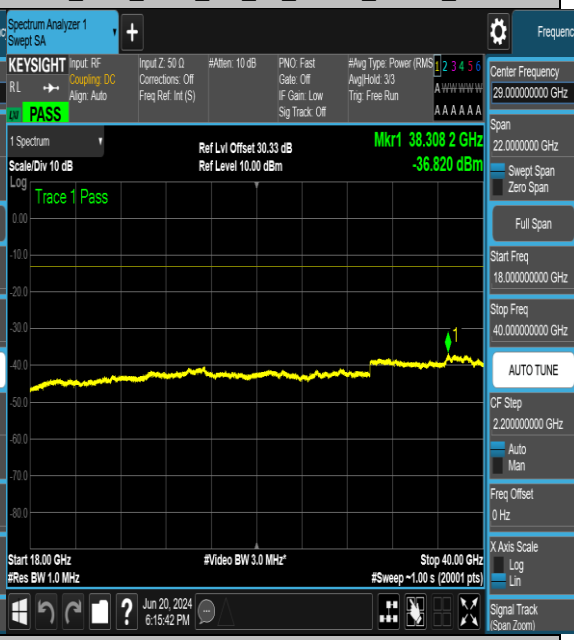

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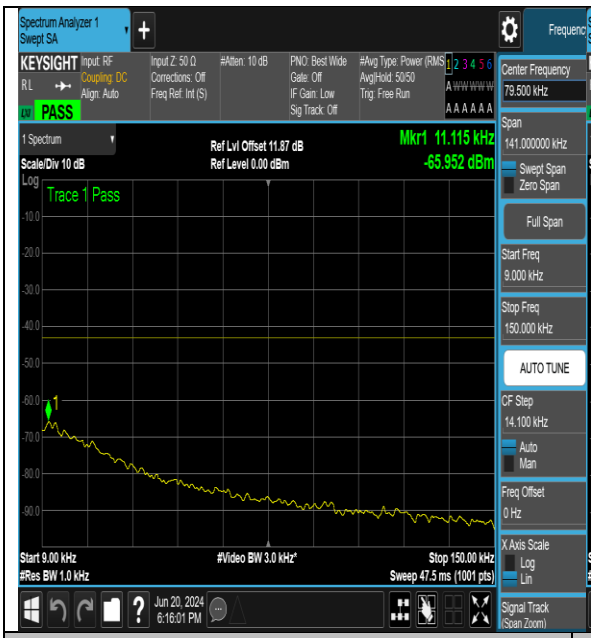

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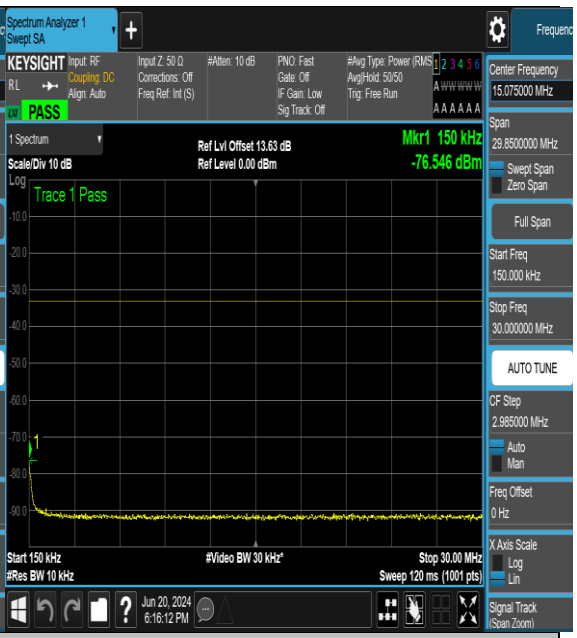

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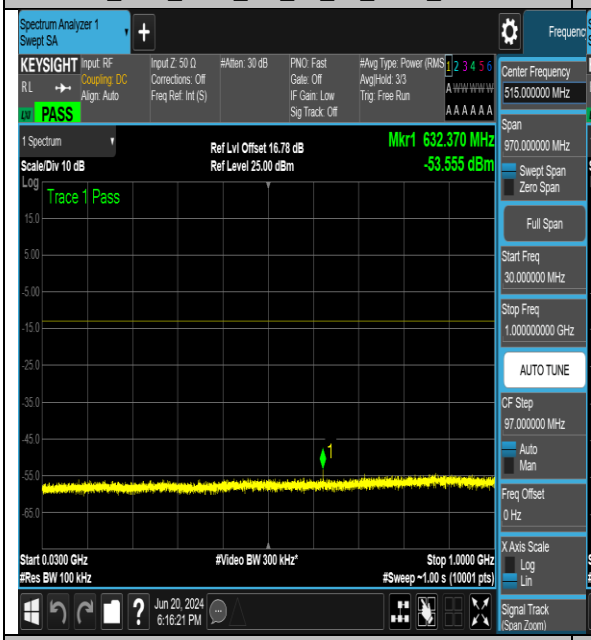

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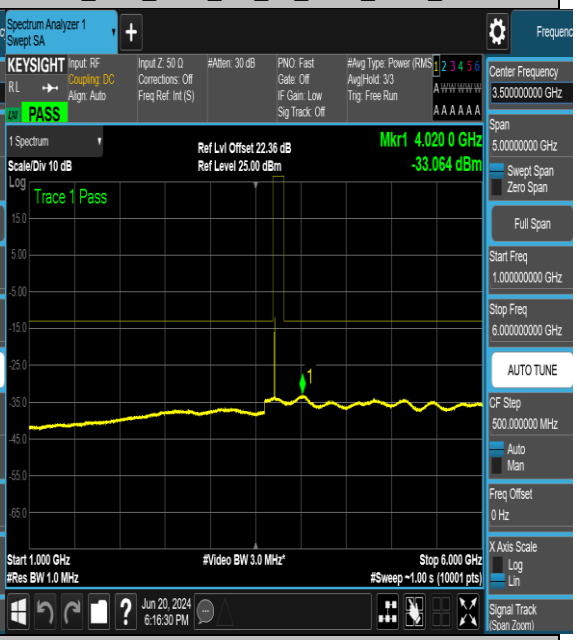

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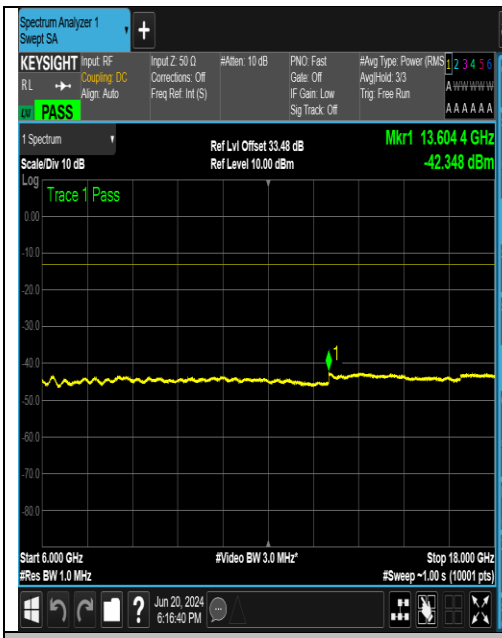

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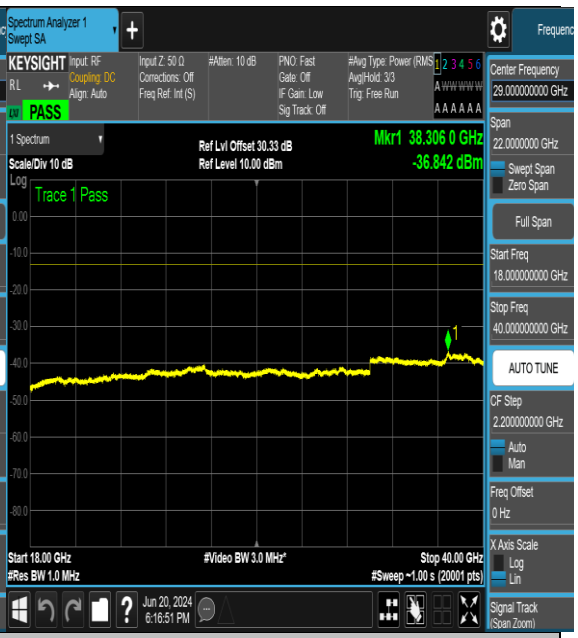

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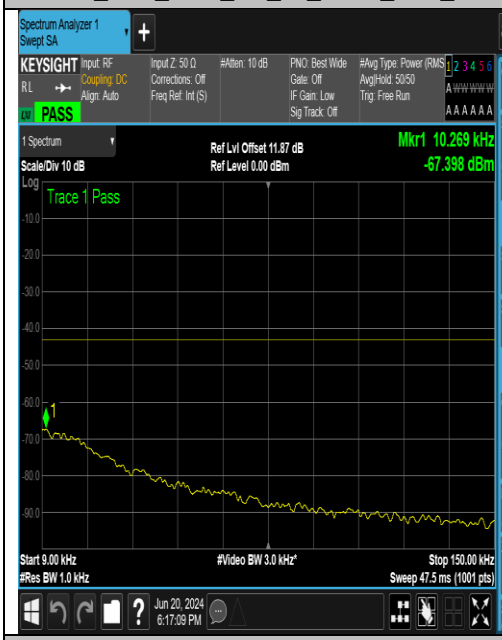

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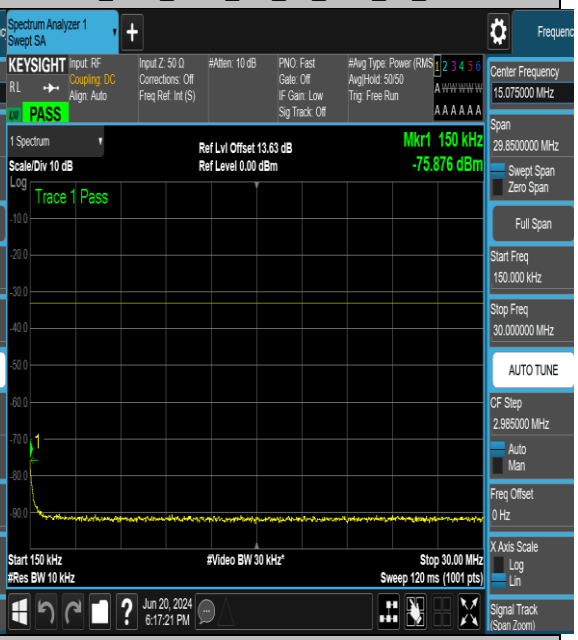

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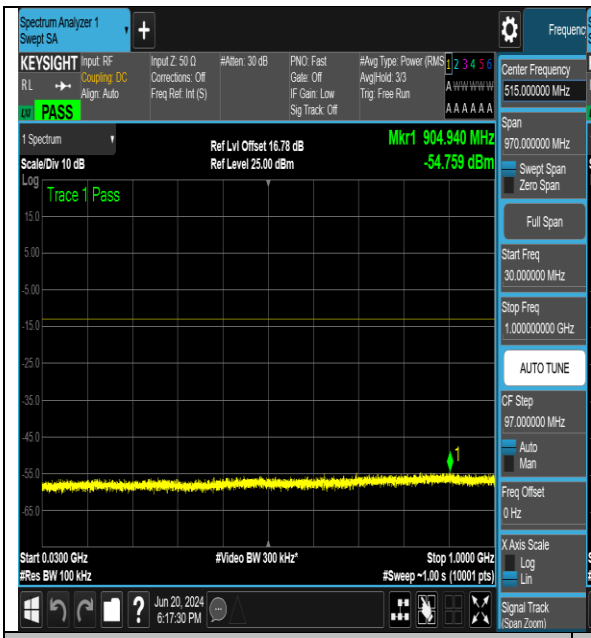

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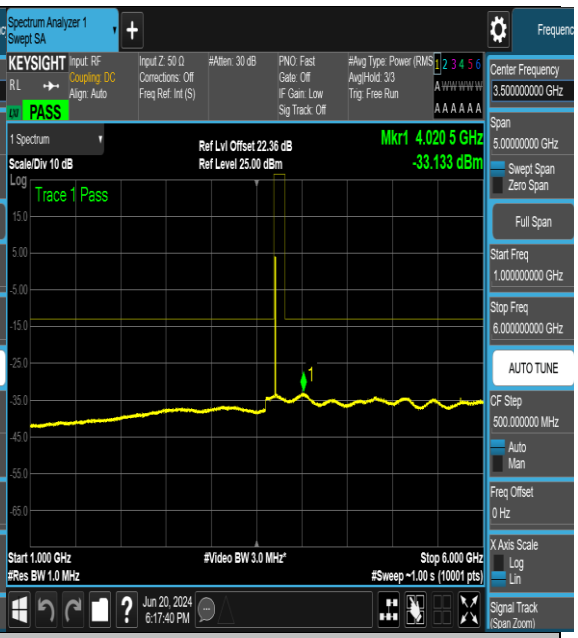

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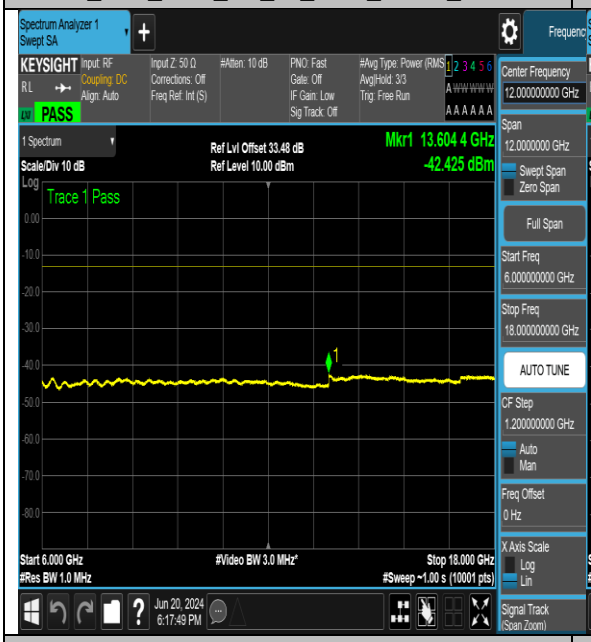

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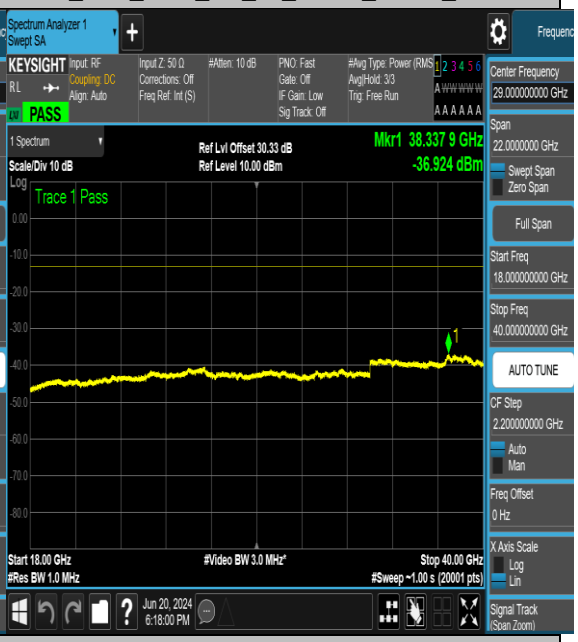

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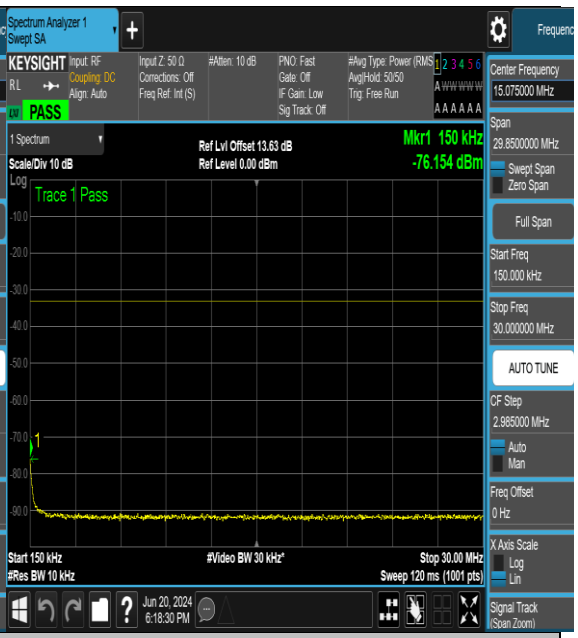

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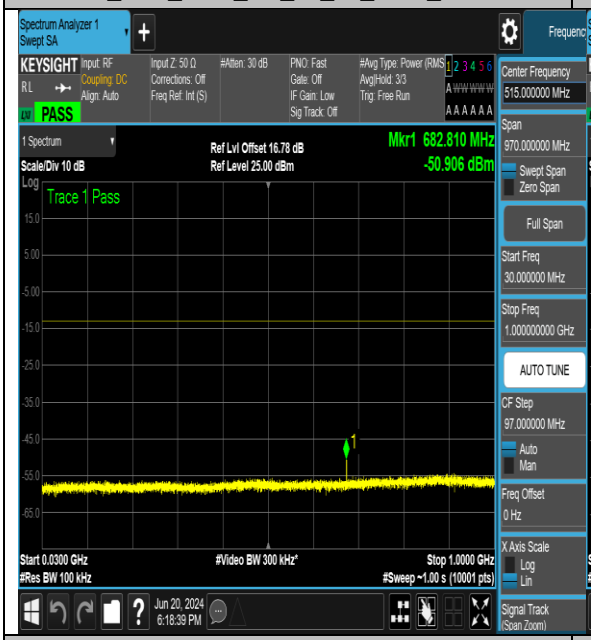

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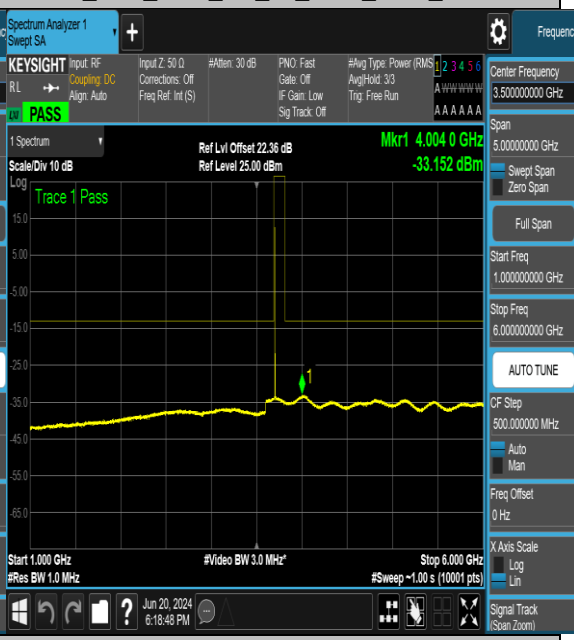

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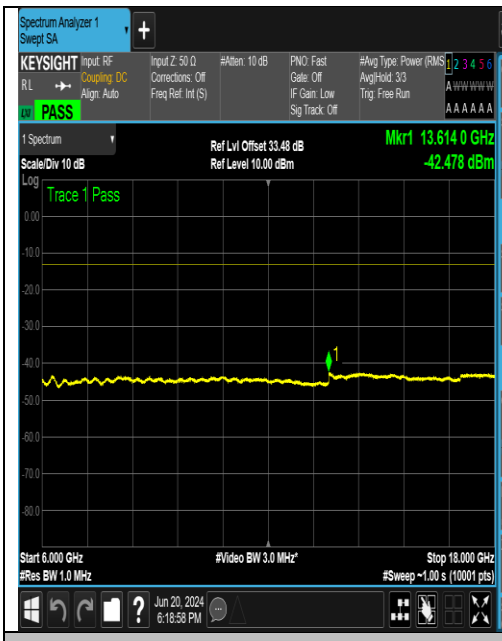

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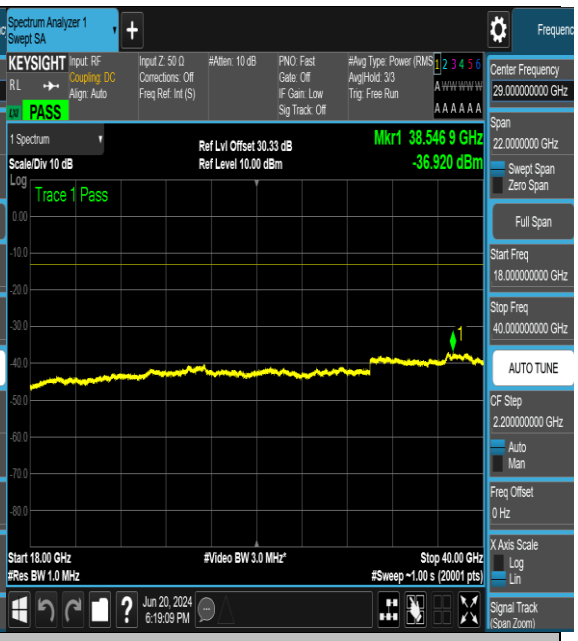

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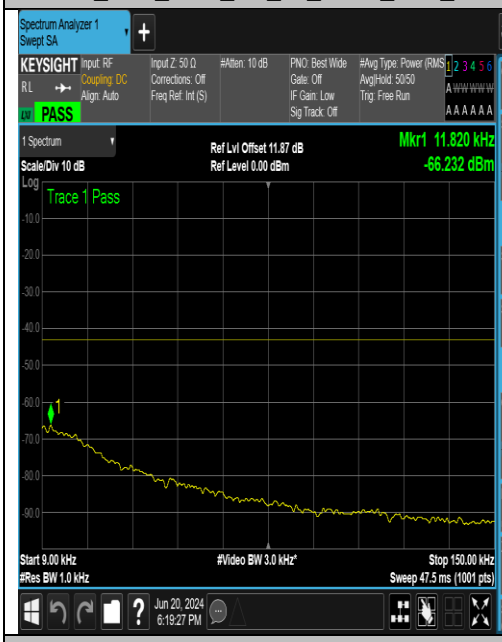

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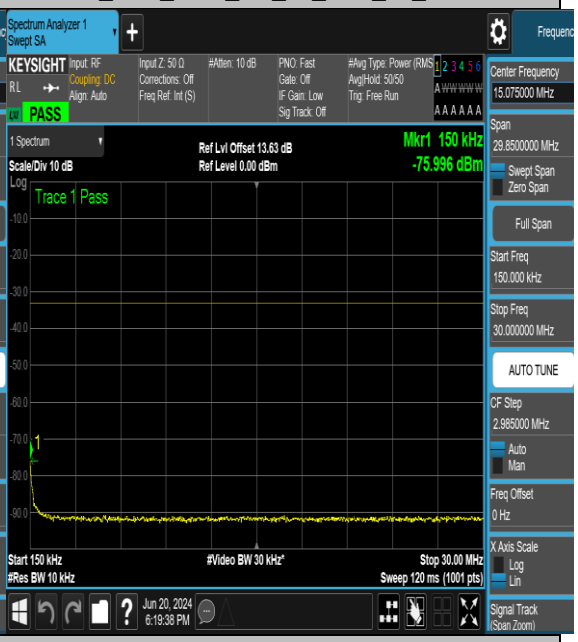

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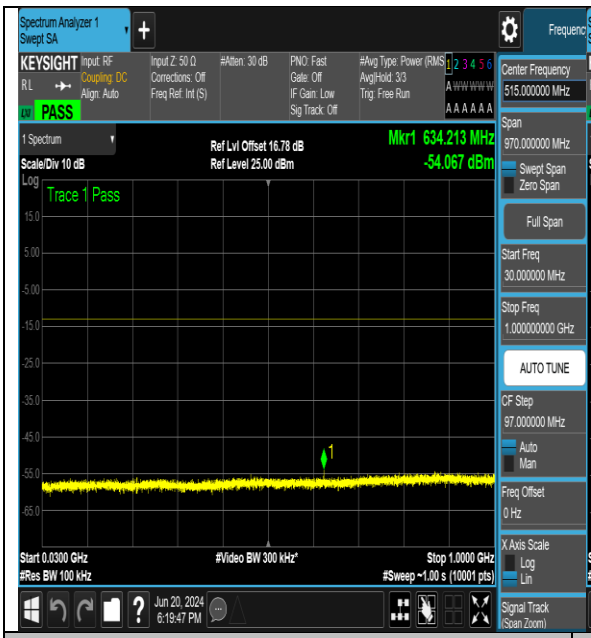

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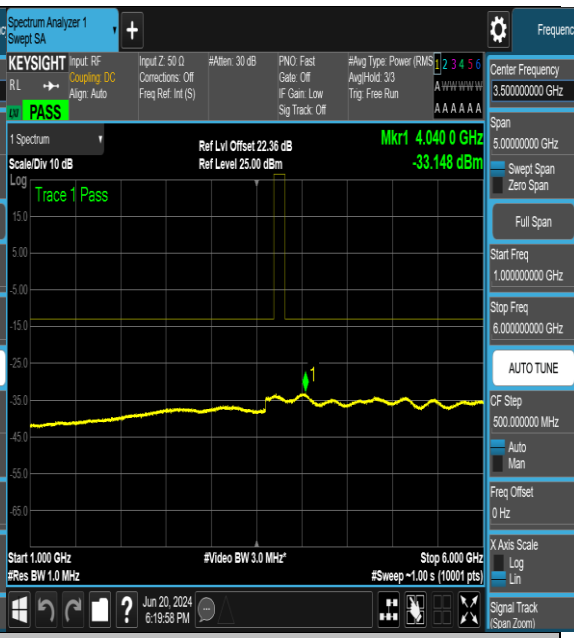

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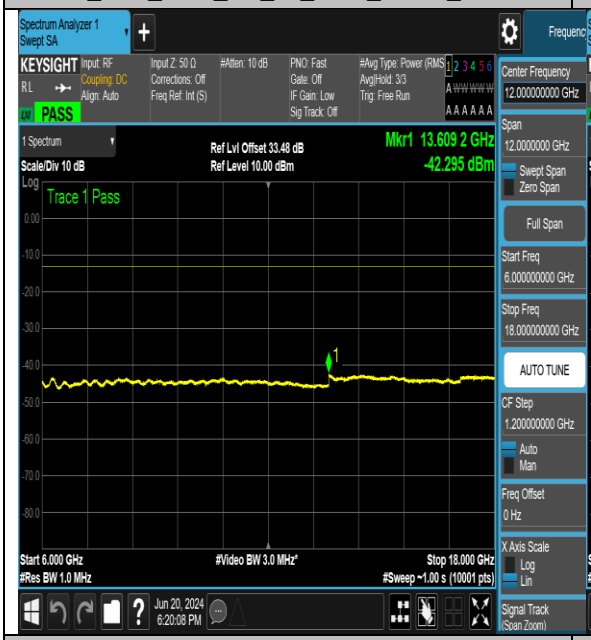

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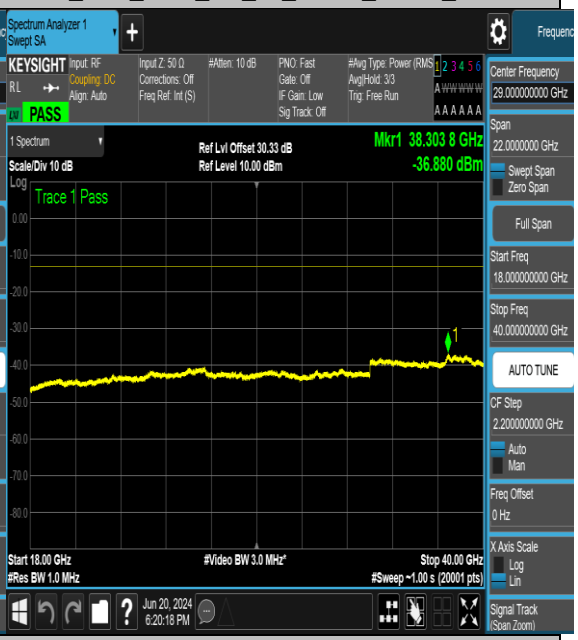

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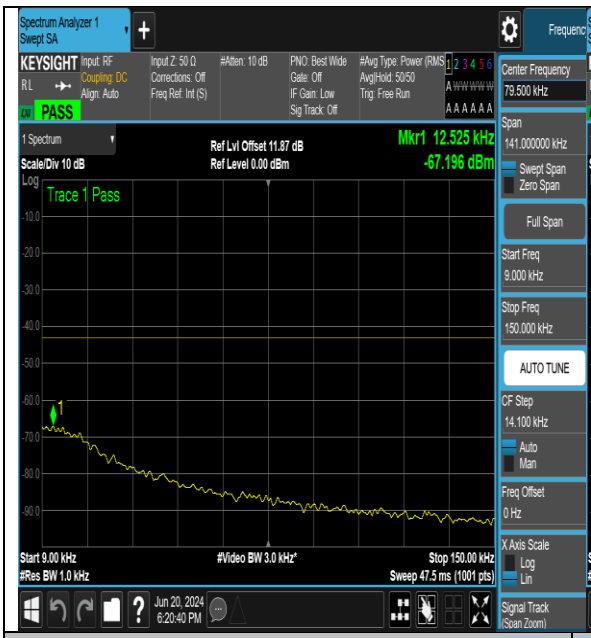

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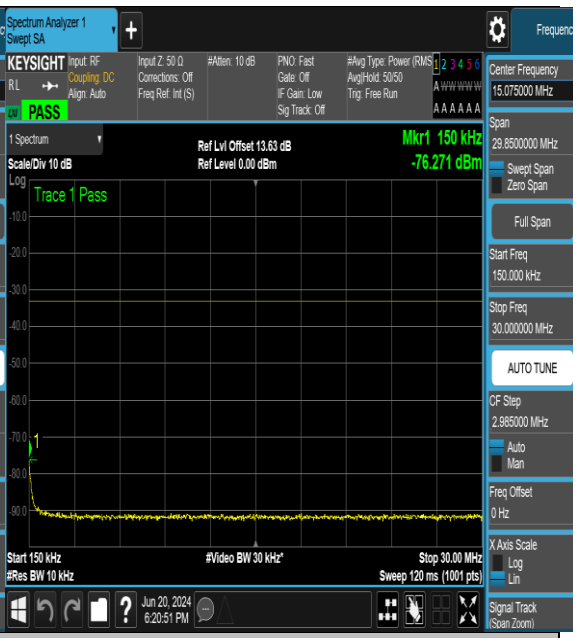

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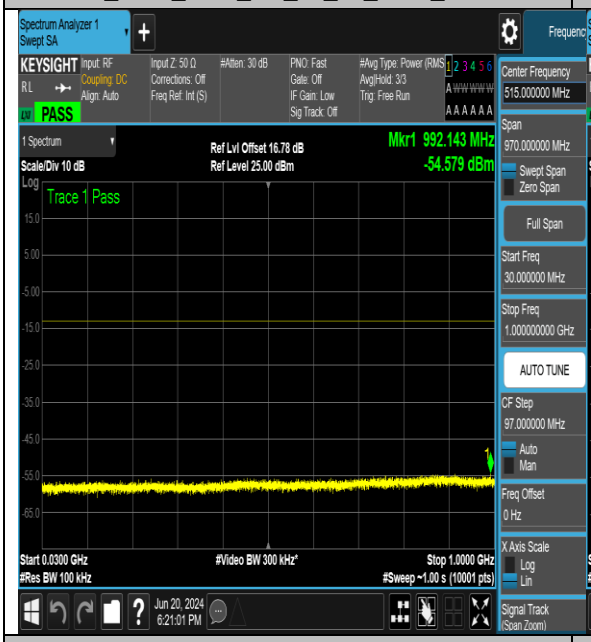

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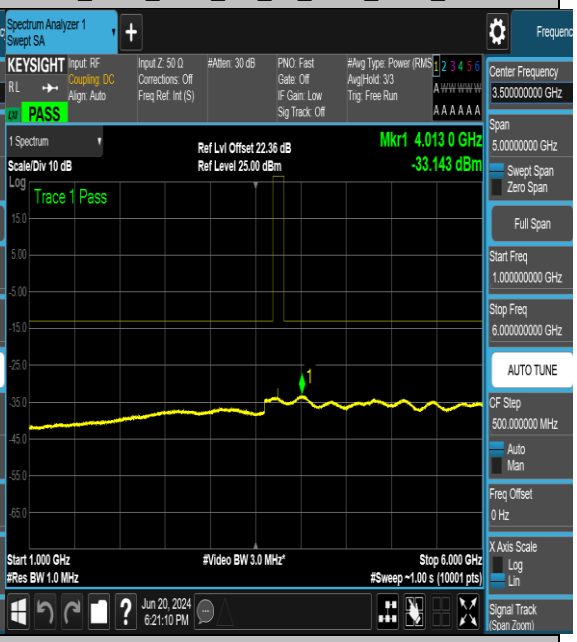

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