Appendix for N78(3700-3800MHz)

Product Name: 5G Window CPE
Model No: TITAN 5100

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N78-3700-3800	30	10	DFT-QPSK	L	Outer_Full	25.00	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Inner_Full	26.06	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Left	22.37	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Right	22.39	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	L	Outer_Full	24.00	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	L	Inner_Full	25.06	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	L	Edge_1RB_Left	22.51	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	L	Edge_1RB_Right	22.50	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Outer_Full	25.43	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Inner_Full	26.38	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Edge_1RB_Left	22.93	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Edge_1RB_Right	22.76	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	M	Outer_Full	24.41	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	M	Inner_Full	25.43	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	M	Edge_1RB_Left	23.02	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	M	Edge_1RB_Right	22.84	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Outer_Full	24.76	PC2	PASS
N78-3700	30	10	DFT-QPSK	H	Inner_Full	25.70	PC2	PASS

-3800								
N78-3700 -3800	30	10	DFT-QPSK	H	Edge_1RB_Left	22.17	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Edge_1RB_Right	22.10	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Outer_Full	23.91	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Inner_Full	24.81	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Edge_1RB_Left	22.45	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Edge_1RB_Right	22.39	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Outer_Full	25.03	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Inner_Full	26.08	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Edge_1RB_Left	22.22	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Edge_1RB_Right	22.41	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Outer_Full	24.04	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Inner_Full	24.89	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Edge_1RB_Left	22.72	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Edge_1RB_Right	22.82	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Outer_Full	25.47	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Inner_Full	26.49	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Edge_1RB_Left	22.67	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Edge_1RB_Right	22.60	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Outer_Full	24.39	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Inner_Full	25.41	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Edge_1RB_Left	22.80	PC2	PASS

N78-3700 -3800	30	15	DFT-16QAM	M	Edge_1RB_Right	22.69	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Outer_Full	24.82	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Inner_Full	25.83	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Edge_1RB_Left	22.19	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Edge_1RB_Right	22.10	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Outer_Full	23.83	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Inner_Full	24.85	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Edge_1RB_Left	22.41	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Edge_1RB_Right	22.42	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Outer_Full	25.17	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Inner_Full	26.02	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Edge_1RB_Left	22.47	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Edge_1RB_Right	22.55	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Outer_Full	24.02	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Inner_Full	25.14	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Edge_1RB_Left	22.43	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Edge_1RB_Right	22.46	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Outer_Full	25.37	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Inner_Full	26.39	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Edge_1RB_Left	23.00	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Edge_1RB_Right	22.87	PC2	PASS
N78-3700	30	20	DFT-16QAM	M	Outer_Full	24.37	PC2	PASS

-3800								
N78-3700 -3800	30	20	DFT-16QAM	M	Inner_Full	25.43	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Edge_1RB_Left	22.85	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Edge_1RB_Right	22.76	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Outer_Full	24.84	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Inner_Full	25.70	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Left	22.60	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Right	22.32	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Outer_Full	23.82	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Inner_Full	24.74	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Edge_1RB_Left	22.47	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Edge_1RB_Right	22.40	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	L	Outer_Full	25.08	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	L	Inner_Full	26.04	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	L	Edge_1RB_Left	22.66	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	L	Edge_1RB_Right	22.53	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	L	Outer_Full	24.02	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	L	Inner_Full	25.12	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	L	Edge_1RB_Left	22.87	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	L	Edge_1RB_Right	22.82	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	M	Outer_Full	25.44	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	M	Inner_Full	26.50	PC2	PASS

N78-3700 -3800	30	30	DFT-QPSK	M	Edge_1RB_Left	23.20	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	M	Edge_1RB_Right	22.95	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	M	Outer_Full	24.49	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	M	Inner_Full	25.43	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	M	Edge_1RB_Left	23.06	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	M	Edge_1RB_Right	22.87	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	H	Outer_Full	24.98	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	H	Inner_Full	25.94	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	H	Edge_1RB_Left	22.66	PC2	PASS
N78-3700 -3800	30	30	DFT-QPSK	H	Edge_1RB_Right	22.26	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	H	Outer_Full	23.93	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	H	Inner_Full	24.92	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	H	Edge_1RB_Left	22.58	PC2	PASS
N78-3700 -3800	30	30	DFT-16QAM	H	Edge_1RB_Right	22.18	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Outer_Full	25.08	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Inner_Full	26.12	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Left	22.69	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Right	22.79	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Outer_Full	24.19	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Inner_Full	25.08	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Edge_1RB_Left	23.16	PC2	PASS
N78-3700	30	40	DFT-16QAM	L	Edge_1RB_Right	23.12	PC2	PASS

-3800								
N78-3700 -3800	30	40	DFT-QPSK	M	Outer_Full	25.43	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Inner_Full	26.47	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Left	23.32	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Right	23.12	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Outer_Full	24.42	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Inner_Full	25.41	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Edge_1RB_Left	23.28	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Edge_1RB_Right	23.26	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Outer_Full	24.96	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Inner_Full	26.03	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Left	22.73	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Right	22.39	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Outer_Full	23.99	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Inner_Full	25.08	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Edge_1RB_Left	22.75	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Edge_1RB_Right	22.70	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Outer_Full	24.87	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Inner_Full	25.85	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Left	22.33	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Right	22.23	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Outer_Full	23.83	PC2	PASS

N78-3700 -3800	30	50	DFT-16QAM	L	Inner_Full	24.82	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Edge_1RB_Left	22.51	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Edge_1RB_Right	22.44	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Outer_Full	25.29	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Inner_Full	26.27	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Edge_1RB_Left	22.80	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Edge_1RB_Right	22.48	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Outer_Full	24.28	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Inner_Full	25.18	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Edge_1RB_Left	22.86	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Edge_1RB_Right	22.65	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Outer_Full	24.74	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Inner_Full	25.85	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Left	22.52	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Right	22.19	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Outer_Full	23.85	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Inner_Full	24.83	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Edge_1RB_Left	22.49	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Edge_1RB_Right	22.12	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Outer_Full	24.86	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Inner_Full	25.97	PC2	PASS
N78-3700	30	60	DFT-QPSK	L	Edge_1RB_Left	22.35	PC2	PASS

-3800								
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Right	22.21	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Outer_Full	23.98	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Inner_Full	25.01	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Edge_1RB_Left	22.36	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Edge_1RB_Right	22.31	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Outer_Full	25.16	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Inner_Full	26.24	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Left	22.86	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Right	22.61	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Outer_Full	24.16	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Inner_Full	25.20	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Edge_1RB_Left	22.83	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Edge_1RB_Right	22.61	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Outer_Full	24.80	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Inner_Full	25.88	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Left	22.55	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Right	22.14	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Outer_Full	23.75	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Inner_Full	24.86	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Edge_1RB_Left	22.59	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Edge_1RB_Right	22.18	PC2	PASS

N78-3700 -3800	30	70	DFT-QPSK	L	Outer_Full	24.92	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Inner_Full	25.93	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Edge_1RB_Left	22.34	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Edge_1RB_Right	21.94	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Outer_Full	23.82	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Inner_Full	24.99	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Edge_1RB_Left	22.51	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Edge_1RB_Right	22.19	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Outer_Full	25.16	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Inner_Full	26.17	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Edge_1RB_Left	22.81	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Edge_1RB_Right	22.22	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Outer_Full	24.12	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Inner_Full	25.22	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Edge_1RB_Left	22.76	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Edge_1RB_Right	22.51	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Outer_Full	24.78	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Inner_Full	25.87	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Edge_1RB_Left	22.50	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Edge_1RB_Right	21.93	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Outer_Full	23.87	PC2	PASS
N78-3700	30	70	DFT-16QAM	H	Inner_Full	24.89	PC2	PASS

-3800								
N78-3700 -3800	30	70	DFT-16QAM	H	Edge_1RB_Left	22.53	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Edge_1RB_Right	21.88	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Outer_Full	25.17	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Inner_Full	26.22	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Left	22.69	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Right	22.45	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Outer_Full	24.12	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Inner_Full	25.20	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Edge_1RB_Left	22.72	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Edge_1RB_Right	22.52	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Outer_Full	25.11	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Inner_Full	26.15	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Left	22.82	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Right	22.26	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Outer_Full	24.06	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Inner_Full	25.21	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Edge_1RB_Left	22.75	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Edge_1RB_Right	22.28	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Outer_Full	25.06	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Inner_Full	26.18	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Left	22.77	PC2	PASS

N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Right	22.23	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Outer_Full	24.07	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Inner_Full	25.15	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Edge_1RB_Left	22.74	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Edge_1RB_Right	22.26	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Outer_Full	25.10	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Inner_Full	26.14	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Left	22.56	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Right	22.12	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Outer_Full	24.15	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Inner_Full	25.24	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Edge_1RB_Left	22.84	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Edge_1RB_Right	22.46	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Outer_Full	25.05	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Inner_Full	26.18	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Left	22.70	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Right	22.15	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Outer_Full	24.12	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Inner_Full	25.22	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Edge_1RB_Left	22.66	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Edge_1RB_Right	22.25	PC2	PASS
N78-3700	30	90	DFT-QPSK	H	Outer_Full	25.30	PC2	PASS

-3800								
N78-3700 -3800	30	90	DFT-QPSK	H	Inner_Full	26.21	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Left	22.66	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Right	22.18	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Outer_Full	24.07	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Inner_Full	25.25	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Edge_1RB_Left	22.69	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Edge_1RB_Right	22.15	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Outer_Full	25.07	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Inner_Full	26.22	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Left	22.57	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Right	22.07	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Outer_Full	24.05	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Inner_Full	25.16	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Edge_1RB_Left	22.63	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Edge_1RB_Right	22.14	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Outer_Full	25.12	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Inner_Full	26.12	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Left	22.43	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Right	22.10	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Outer_Full	24.04	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Inner_Full	25.21	PC2	PASS

N78-3700 -3800	30	100	DFT-16QAM	M	Edge_1RB_Left	22.64	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Edge_1RB_Right	22.20	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Outer_Full	25.03	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Inner_Full	26.15	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Left	22.44	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Right	22.11	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Outer_Full	24.13	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Inner_Full	25.17	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Edge_1RB_Left	22.53	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Edge_1RB_Right	22.25	PC2	PASS

Appendix B: Peak-to-Average Ratio for SA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
N78-3700-3800	30	10	DFT-QPSK	L	Outer_Full	100%	0.00	7.76	≤13	PASS
N78-3700-3800	30	10	DFT-16QAM	L	Outer_Full	100%	0.00	8.07	≤13	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Outer_Full	100%	0.00	7.69	≤13	PASS
N78-3700-3800	30	10	DFT-16QAM	M	Outer_Full	100%	0.00	7.58	≤13	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Outer_Full	100%	0.00	7.31	≤13	PASS
N78-3700-3800	30	10	DFT-16QAM	H	Outer_Full	100%	0.00	8.22	≤13	PASS
N78-3700-3800	30	15	DFT-QPSK	L	Outer_Full	100%	0.00	7.82	≤13	PASS
N78-3700-3800	30	15	DFT-16QAM	L	Outer_Full	100%	0.00	7.84	≤13	PASS
N78-3700-3800	30	15	DFT-QPSK	M	Outer_Full	100%	0.00	8.45	≤13	PASS
N78-3700-3800	30	15	DFT-16QAM	M	Outer_Full	100%	0.00	8.50	≤13	PASS
N78-3700-3800	30	15	DFT-QPSK	H	Outer_Full	100%	0.00	8.05	≤13	PASS
N78-3700-3800	30	15	DFT-16QAM	H	Outer_Full	100%	0.00	8.20	≤13	PASS
N78-3700-3800	30	20	DFT-QPSK	L	Outer_Full	100%	0.00	8.12	≤13	PASS
N78-3700-3800	30	20	DFT-16QAM	L	Outer_Full	100%	0.00	7.83	≤13	PASS
N78-3700-3800	30	20	DFT-QPSK	M	Outer_Full	100%	0.00	7.78	≤13	PASS
N78-3700-3800	30	20	DFT-16QAM	M	Outer_Full	100%	0.00	8.19	≤13	PASS
N78-3700-3800	30	20	DFT-QPSK	H	Outer_Full	100%	0.00	8.43	≤13	PASS
N78-3700-3800	30	20	DFT-16QAM	H	Outer_Full	100%	0.00	7.78	≤13	PASS
N78-3700-3800	30	30	DFT-QPSK	L	Outer_Full	100%	0.00	8.40	≤13	PASS
N78-3700-3800	30	30	DFT-16QAM	L	Outer_Full	100%	0.00	8.17	≤13	PASS
N78-3700-3800	30	30	DFT-QPSK	M	Outer_Full	100%	0.00	8.78	≤13	PASS
N78-3700-3800	30	30	DFT-16QAM	M	Outer_Full	100%	0.00	8.45	≤13	PASS
N78-3700-3800	30	30	DFT-QPSK	H	Outer_Full	100%	0.00	8.18	≤13	PASS
N78-3700-3800	30	30	DFT-16QAM	H	Outer_Full	100%	0.00	9.00	≤13	PASS
N78-3700-3800	30	40	DFT-QPSK	L	Outer_Full	100%	0.00	8.91	≤13	PASS
N78-3700-3800	30	40	DFT-16QAM	L	Outer_Full	100%	0.00	8.95	≤13	PASS
N78-3700-3800	30	40	DFT-QPSK	M	Outer_Full	100%	0.00	8.54	≤13	PASS
N78-3700-3800	30	40	DFT-16QAM	M	Outer_Full	100%	0.00	8.17	≤13	PASS
N78-3700-3800	30	40	DFT-QPSK	H	Outer_Full	100%	0.00	8.33	≤13	PASS
N78-3700-3800	30	40	DFT-16QAM	H	Outer_Full	100%	0.00	8.84	≤13	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Outer_Full	100%	0.00	8.98	≤13	PASS
N78-3700-3800	30	50	DFT-16QAM	L	Outer_Full	100%	0.00	7.93	≤13	PASS
N78-3700-3800	30	50	DFT-QPSK	M	Outer_Full	100%	0.00	8.12	≤13	PASS
N78-3700-3800	30	50	DFT-16QAM	M	Outer_Full	100%	0.00	8.34	≤13	PASS
N78-3700-3800	30	50	DFT-QPSK	H	Outer_Full	100%	0.00	8.61	≤13	PASS

N78-3700-3800	30	50	DFT-16QAM	H	Outer_Full	100%	0.00	9.13	≤13	PASS
N78-3700-3800	30	60	DFT-QPSK	L	Outer_Full	100%	0.00	9.18	≤13	PASS
N78-3700-3800	30	60	DFT-16QAM	L	Outer_Full	100%	0.00	8.50	≤13	PASS
N78-3700-3800	30	60	DFT-QPSK	M	Outer_Full	100%	0.00	8.34	≤13	PASS
N78-3700-3800	30	60	DFT-16QAM	M	Outer_Full	100%	0.00	8.29	≤13	PASS
N78-3700-3800	30	60	DFT-QPSK	H	Outer_Full	100%	0.00	8.35	≤13	PASS
N78-3700-3800	30	60	DFT-16QAM	H	Outer_Full	100%	0.00	8.21	≤13	PASS
N78-3700-3800	30	70	DFT-QPSK	L	Outer_Full	100%	0.00	8.26	≤13	PASS
N78-3700-3800	30	70	DFT-16QAM	L	Outer_Full	100%	0.00	8.57	≤13	PASS
N78-3700-3800	30	70	DFT-QPSK	M	Outer_Full	100%	0.00	8.82	≤13	PASS
N78-3700-3800	30	70	DFT-16QAM	M	Outer_Full	100%	0.00	8.82	≤13	PASS
N78-3700-3800	30	70	DFT-QPSK	H	Outer_Full	100%	0.00	8.95	≤13	PASS
N78-3700-3800	30	70	DFT-16QAM	H	Outer_Full	100%	0.00	8.38	≤13	PASS
N78-3700-3800	30	80	DFT-QPSK	L	Outer_Full	100%	0.00	8.99	≤13	PASS
N78-3700-3800	30	80	DFT-16QAM	L	Outer_Full	100%	0.00	8.35	≤13	PASS
N78-3700-3800	30	80	DFT-QPSK	M	Outer_Full	100%	0.00	8.61	≤13	PASS
N78-3700-3800	30	80	DFT-16QAM	M	Outer_Full	100%	0.00	8.81	≤13	PASS
N78-3700-3800	30	80	DFT-QPSK	H	Outer_Full	100%	0.00	8.78	≤13	PASS
N78-3700-3800	30	80	DFT-16QAM	H	Outer_Full	100%	0.00	8.45	≤13	PASS
N78-3700-3800	30	90	DFT-QPSK	L	Outer_Full	100%	0.00	8.44	≤13	PASS
N78-3700-3800	30	90	DFT-16QAM	L	Outer_Full	100%	0.00	8.87	≤13	PASS
N78-3700-3800	30	90	DFT-QPSK	M	Outer_Full	100%	0.00	8.81	≤13	PASS
N78-3700-3800	30	90	DFT-16QAM	M	Outer_Full	100%	0.00	8.37	≤13	PASS
N78-3700-3800	30	90	DFT-QPSK	H	Outer_Full	100%	0.00	8.93	≤13	PASS
N78-3700-3800	30	90	DFT-16QAM	H	Outer_Full	100%	0.00	8.70	≤13	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	100%	0.00	8.45	≤13	PASS
N78-3700-3800	30	100	DFT-16QAM	L	Outer_Full	100%	0.00	8.27	≤13	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	100%	0.00	8.30	≤13	PASS
N78-3700-3800	30	100	DFT-16QAM	M	Outer_Full	100%	0.00	8.52	≤13	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	100%	0.00	7.94	≤13	PASS
N78-3700-3800	30	100	DFT-16QAM	H	Outer_Full	100%	0.00	8.27	≤13	PASS

Test Graphs

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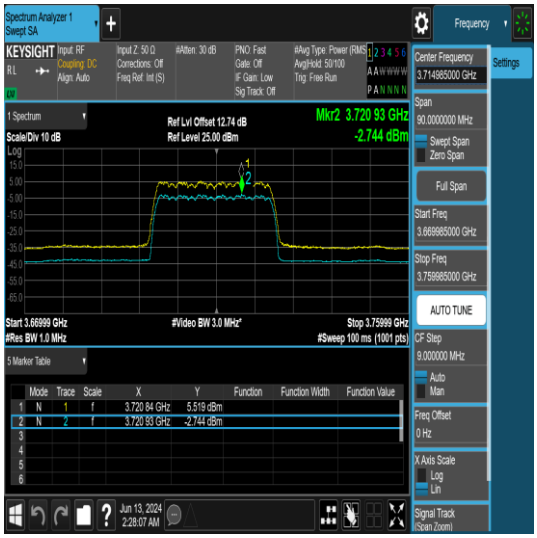

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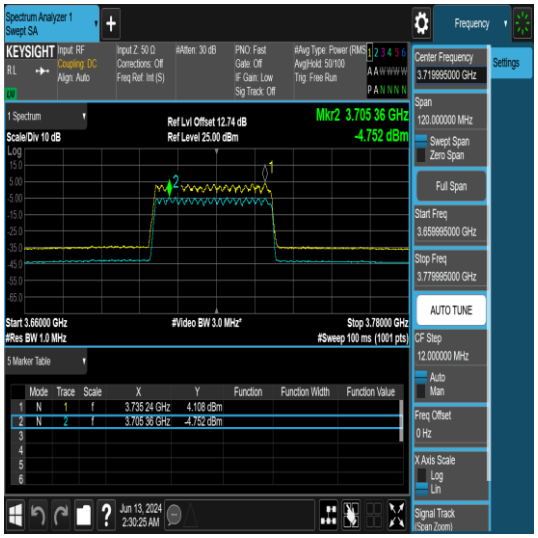

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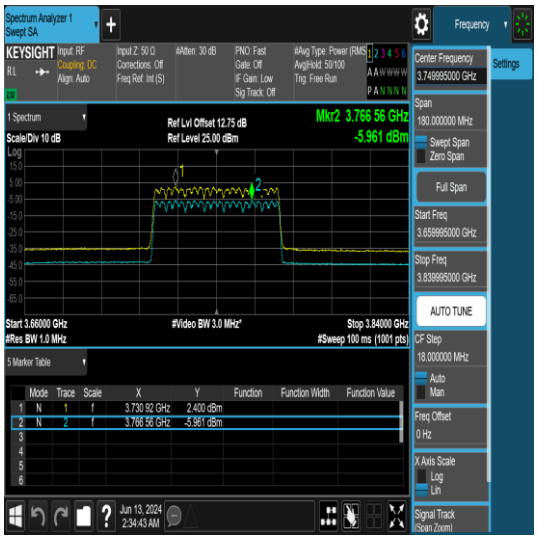

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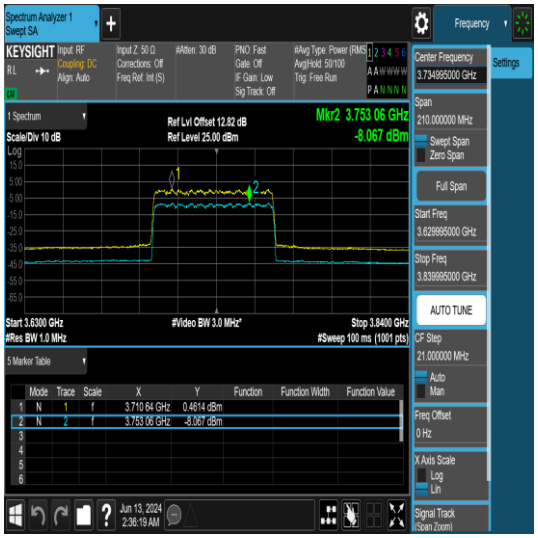

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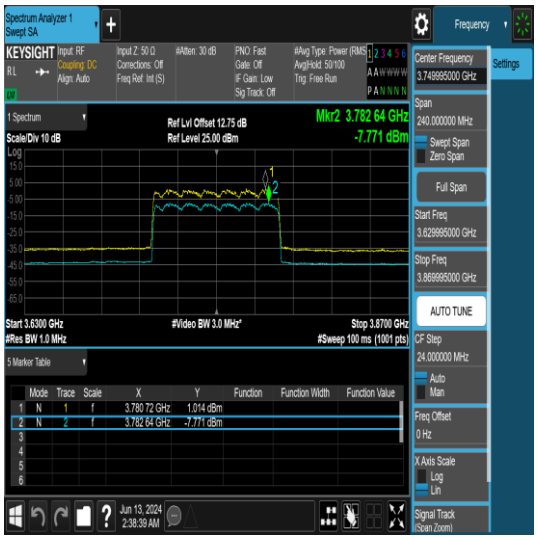

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